

# RISC-V Vector Extension Intrinsic API Reference Manual

## 1. Preface

These builtins targets on rvv 0.7.1 and trying to document rvv\_intrinsics programming model.

These rvv0.7.1 Intrinsics are designed to be compatible with rvv1.0, and the differences can be found in Incompatible With V1.0.

The toolchains may allow the specification of extensions with version numbers. To use rvv 0.7.1, please use the arch options with *'v0p7'* instead of *'v'*.

For RV32, almost all instructions and functions are available, with some exceptions that if *'* is 64 for the vector indexed load/store, the relevant instructions and functions cannot be used because the index is not supported. See *21 Vector interface not supported on rv32* for details.

This document is based on rvv-intrinsic-doc, with a number of detailed modifications and finishing touches.

This document is last modified and released by T-HEAD Semiconductor Co.,Ltd.

## Copyright Information

The RISC-V Vector Extension Intrinsic Document is

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## 2. Design Decisions and philosophy

Please see RISC-V “V” Vector Extension Intrinsics

## 3. None

Keep this chapter none to aligned to riscv-v-spec chapters

## 4. None

Keep this chapter none to aligned to riscv-v-spec chapters

## 5. General Naming Rules

Please see Naming Rules

## 6. Configuration-Setting and Utility

### Instructions

- `vsetvli`
- `vsetvl`

### 6.1. Set `vl` and `vtype`

Intrinsics function list

### 6.2. Set `vl` to `VLMAX` with specific `vtype`

Intrinsic functions list

### 6.3. Reinterpret Cast Conversion

Reinterpret the contents of a data as a different type, without changing any bits and generating any RVV instructions.

Intrinsic functions list

### 6.4. Vector Initialization

Intrinsic functions list

### 6.5. Vector LMUL Extension and Truncation

These utility functions help users to truncate or extent current LMUL under same SEW regardless of `vl`, it won't change content of `vl` register.

Intrinsic functions list

### 6.6. Read/Write URW vector CSRs

```
enum RVV_CSR {  
    RVV_VSTART = 0,  
    RVV_VXSAT,  
    RVV_VXRM,  
    RVV_VCSR,  
};
```

```
};  
  
unsigned long vread_csr(enum RVV_CSR csr);  
void vwrite_csr(enum RVV_CSR csr, unsigned long value);
```

## 7. Vector Loads and Stores

### 7.1. Vector Unit-Stride Operations

#### Instructions

- vlb.v
- vlh.v
- vlw.v
- vlbu.v
- vlhu.v
- vlwu.v
- vle<eew>.v
- vsb.v
- vsh.v
- vsw.v
- vse<eew>.v

#### Load Intrinsic functions list

#### Store Intrinsic functions list

### 7.2. Vector Strided Load/Store Operations

#### Instructions

- vlsb.v
- vlsh.v
- vlsw.v
- vlсбу.v
- vlshu.v
- vlswu.v
- vlse<eew>.v
- vssb.v
- vssh.v
- vssw.v
- vsse<eew>.v

#### Load Intrinsic functions list

#### Store Intrinsic functions list

### 7.3. Vector Indexed Load/Store Operations

#### Instructions

- vlxb.v
- vlxh.v
- vlxw.v
- vlxbu.v
- vlxhu.v
- vlxwu.v
- vlxei<eew>.v
- vsxb.v
- vsxh.v
- vsxw.v
- vsxei<eew>.v
- vsuxb.v
- vsuxh.v
- vsuxw.v
- vsuxei<eew>.v

#### Load Intrinsic functions list

#### Store Intrinsic functions list

### 7.4. Unit-stride Fault-Only-First Loads Operations

#### Instructions

- vle<eew>ff.v

#### Intrinsic functions list

#### Notes

- The unit-stride fault-only-first load instruction is used to vectorize loops with data-dependent exit conditions (while loops). These instructions execute as a regular load except that they will only take a trap on element 0. If an element > 0 raises an exception, that element and all following elements in the destination vector register are not modified, and the vector length vl is reduced to the number of elements processed without a trap.

### 7.5. Vector Load/Store Segment Operations (Zvlsseg)

#### 7.5.1. Vector Unit-Stride Segment Loads and Stores

#### Instructions

- vlseg<nf>b.v

- vlseg<nf>h.v
- vlseg<nf>w.v
- vlseg<nf>bu.v
- vlseg<nf>hu.v
- vlseg<nf>wu.v
- vlseg<nf>e<eew>.v
- vsseg<nf>b.v
- vsseg<nf>h.v
- vsseg<nf>w.v
- vsseg<nf>e<eew>.v

#### Load Intrinsic functions list

#### Store Intrinsic functions list

### 7.5.2. Vector Strided Segment Loads and Stores

#### Instructions

- vlsseg<nf>b.v
- vlsseg<nf>h.v
- vlsseg<nf>w.v
- vlsseg<nf>bu.v
- vlsseg<nf>hu.v
- vlsseg<nf>wu.v
- vlsseg<nf>e<eew>.v
- vssseg<nf>b.v
- vssseg<nf>h.v
- vssseg<nf>w.v
- vssseg<nf>e<eew>.v

#### Load Intrinsic functions list

#### Store Intrinsic functions list

### 7.5.3. Vector Indexed Segment Loads and Stores

#### Instructions

- vlxseg<nf>b.v
- vlxseg<nf>h.v
- vlxseg<nf>w.v
- vlxseg<nf>bu.v
- vlxseg<nf>hu.v
- vlxseg<nf>wu.v
- vlxseg<nf>ei<eew>.v
- vsxseg<nf>b.v

- vsxseg<nf>h.v
- vsxseg<nf>w.v
- vsxseg<nf>ei<eew>.v

**Load Intrinsic functions list**

**Store Intrinsic functions list**

## 8. None

Keep this chapter none to aligned to riscv-v-spec chapters

## 9. None

Keep this chapter none to aligned to riscv-v-spec chapters

## 10. None

Keep this chapter none to aligned to riscv-v-spec chapters

## 11. Vector Integer Arithmetic Operations

### 11.1. Vector Single-Width Integer Add and Subtract

**Instructions**

- vadd.{vv,vx,vi}
- vsub.{vv,vx}
- vrsub.{vx,vi}
- vneg.v

**Intrinsic functions list**

### 11.2. Vector Widening Integer Add/Subtract Operations

**Instructions**

- vwaddu.{vv,vx,wv,wx}
- vwsubu.{vv,vx,wv,wx}
- vwadd.{vv,vx,wv,wx}
- vwsub.{vv,vx,wv,wx}
- vwcvt.x.x.v
- vwcvtu.x.x.v

**Intrinsic functions list**

### 11.3. Vector Integer Add-with-Carry / Subtract-with-Borrow Operations

#### Instructions

- `vadc.{vvm,vxm,vim}`
- `vmadc.{vvm,vxm,vim}`
- `vsbc.{vvm,vxm}`
- `vmsbc.{vvm,vxm}`

#### Intrinsic functions list

### 11.4. Vector Bitwise Logical Operations

#### Instructions

- `vand.{vv,vx,vi}`
- `vxor.{vv,vx,vi}`
- `vor.{vv,vx,vi}`
- `vnot.v`

#### Intrinsic functions list

### 11.5. Vector Single-Width Bit Shift Operations

#### Instructions

- `vll.{vv,vx,vi}`
- `vsl.{vv,vx,vi}`
- `vsra.{vv,vx,vi}`

#### Intrinsic functions list

#### Notes

- A full complement of vector shift instructions are provided, including logical shift left, and logical (zero-extending) and arithmetic (sign-extending) shift right.

### 11.6. Vector Narrowing Integer Right Shift Operations

#### Instructions

- `vnsra.{vv,vx,vi}`
- `vnsrl.{vv,vx,vi}`
- `vncvt.x.x.w`

#### Intrinsic functions list

## 11.7. Vector Integer Comparison Operations

### Instructions

- `vmseq`.{vv,vx,vi}
- `vmsne`.{vv,vx,vi}
- `vmsltu`.{vv,vx,vi}
- `vmslt`.{vv,vx,vi}
- `vmsleu`.{vv,vx,vi}
- `vmsle`.{vv,vx,vi}
- `vmsgtu`.{vv,vx,vi}
- `vmsgt`.{vv,vx,vi}

### Intrinsic functions list

## 11.8. Vector Integer Min/Max Operations

### Instructions

- `vminu`.{vv,vx}
- `vmin`.{vv,vx}
- `vmaxu`.{vv,vx}
- `vmax`.{vv,vx}

### Intrinsic functions list

## 11.9. Vector Single-Width Integer Multiply Operations

### Instructions

- `vmul`.{vv,vx}
- `vmulh`.{vv,vx}
- `vmulhu`.{vv,vx}
- `vmulhsu`.{vv,vx}

### Intrinsic functions list

## 11.10. Vector Integer Divide Operations

### Instructions

- `vdivu`.{vv,vx}
- `vdiv`.{vv,vx}
- `vremu`.{vv,vx}
- `vrem`.{vv,vx}

### Intrinsic functions list



### 11.11. Vector Widening Integer Multiply Operations

#### Instructions

- `vwmul.{vv,vx}`
- `vwmulu.{vv,vx}`
- `vwmulsu.{vv,vx}`

#### Intrinsic functions list

### 11.12. Vector Single-Width Integer Multiply-Add Operations

#### Instructions

- `vmacc.{vv,vx}`
- `vnmsac.{vv,vx}`
- `vmadd.{vv,vx}`
- `vnmsub.{vv,vx}`

#### Intrinsic functions list

### 11.13. Vector Widening Integer Multiply-Add Operations

#### Instructions

- `vwmaccu.{vv,vx}`
- `vwmacc.{vv,vx}`
- `vwmaccsu.{vv,vx}`
- `vwmaccus.{vv,vx}`

#### Intrinsic functions list

### 11.14. Vector Integer Merge Operations

#### Instructions

- `vmerge.{vvm,vxm,vim}`

#### Intrinsic functions list

### 11.15. Vector Integer Move Operations

#### Instructions

- `vmv.v.v`
- `vmv.v.x`
- `vmv.v.i`

#### Intrinsic functions list

## 12. Vector Fixed-Point Arithmetic Operations

### 12.1. Vector Single-Width Saturating Add and Subtract

#### Instructions

- vsaddu.{vv,vx,vi}
- vsadd.{vv,vx,vi}
- vssubu.{vv,vx}
- vssub.{vv,vx}

#### Intrinsic functions list

### 12.2. Vector Single-Width Averaging Add and Subtract

#### Instructions

- vaadd.{vv,vx,vi}
- vasub.{vv,vx}

#### Intrinsic functions list

### 12.3. Vector Single-Width Fractional Multiply with Rounding and Saturation

#### Instructions

- vsmul.{vv,vx}

#### Intrinsic functions list

### 12.4. Vector Single-Width Scaling Shift Operations

#### Instructions

- vssrl.{vv,vx,vi}
- vssra.{vv,vx,vi}

#### Intrinsic functions list

### 12.5. Vector Narrowing Fixed-Point Clip Operations

#### Instructions

- vnclipu.{vx,vv,vi}
- vnclip.{vx,vv,vi}

#### Intrinsic functions list

## 13. Vector Floating-Point Operations

### 13.1. Vector Single-Width Floating-Point Add/Subtract Operations

#### Instructions

- `vfadd.{vv,vf}`
- `vfsbub.{vv,vf}`
- `vfrsub.vf`

#### Intrinsic functions list

### 13.2. Vector Widening Floating-Point Add/Subtract Operations

#### Instructions

- `vfwadd.{vv,vf,wv,wf}`
- `vfwsub.{vv,vf,wv,wf}`

#### Intrinsic functions list

### 13.3. Vector Single-Width Floating-Point Multiply/Divide Operations

#### Instructions

- `vfmul.{vv,vf}`
- `vfdiv.{vv,vf}`
- `vfrdiv.vf`

#### Intrinsic functions list

### 13.4. Vector Widening Floating-Point Multiply Operations

#### Instructions

- `vfwmul.{vv,vf}`

#### Intrinsic functions list

### 13.5. Vector Single-Width Floating-Point Fused Multiply-Add Operations

#### Instructions

- `vfmacc.{vv,vf}`
- `vfnmacc.{vv,vf}`
- `vfmnac.{vv,vf}`
- `vfnmnac.{vv,vf}`
- `vfmadd.{vv,vf}`
- `vfnmadd.{vv,vf}`

- vfmsub.{vv,vf}
- vfnmsub.{vv,vf}

#### **Intrinsic functions list**

### **13.6. Vector Widening Floating-Point Fused Multiply-Add Operations**

#### **Instructions**

- vfwacc.{vv,vf}
- vfnwacc.{vv,vf}
- vfwmsac.{vv,vf}
- vfnwmsac.{vv,vf}

#### **Intrinsic functions list**

### **13.7. Vector Floating-Point Square-Root Operations**

#### **Instructions**

- vfsqrt.v

#### **Intrinsic functions list**

### **13.8. Vector Floating-Point Reciprocal Square-Root Estimate Operations**

- vfrsqrt7.v

#### **Intrinsic functions list**

### **13.9. Vector Floating-Point Reciprocal Estimate Operations**

- vfreq7.v

#### **Intrinsic functions list**

### **13.10. Vector Floating-Point MIN/MAX Operations**

- vfmin.{vv,vf}
- vfmax.{vv,vf}

#### **Intrinsic functions list**

### 13.11. Vector Floating-Point Sign-Injection Operations

#### Instructions

- `vfsgnj`.{vv,vf}
- `vfsgnjn`.{vv,vf}
- `vfsgnjx`.{vv,vf}
- `vfneg`.v
- `vfabs`.v

#### Intrinsic functions list

### 13.12. Vector Floating-Point Compare Operations

#### Instructions

- `vmfeq`.{vv,vf}
- `vmfne`.{vv,vf}
- `vmfft`.{vv,vf}
- `vmfle`.{vv,vf}
- `vmfgt`.{vv,vf}
- `vmfge`.{vv,vf}

#### Intrinsic functions list

### 13.13. Vector Floating-Point Classify Operations

#### Instructions

- `vfclass`.v

#### Intrinsic functions list

### 13.13. Vector Floating-Point Merge Operations

#### Instructions

- `vfmerge`.vfm

#### Intrinsic functions list

### 13.15. Vector Floating-Point Move Operations

#### Instructions

- `vmv`.v.f

#### Intrinsic functions list

### 13.16. Single-Width Floating-Point/Integer Type-Convert Operations

#### Instructions

- vfcvt.xu.f.v
- vfcvt.x.f.v
- vfcvt.f.xu.v
- vfcvt.f.x.v

#### Intrinsic functions list

### 13.17. Widening Floating-Point/Integer Type-Convert Operations

#### Instructions

- vfwcvt.xu.f.v
- vfwcvt.x.f.v
- vfwcvt.f.xu.v
- vfwcvt.f.x.v
- vfwcvt.f.f.v

#### Intrinsic functions list

### 13.18. Narrowing Floating-Point/Integer Type-Convert Operations

#### Instructions

- vfnvcvt.xu.f.v
- vfnvcvt.x.f.v
- vfnvcvt.f.xu.v
- vfnvcvt.f.x.v
- vfnvcvt.f.f.v

#### Intrinsic functions list

## 14. Vector Reduction Operations

### 14.1. Vector Single-Width Integer Reduction Operations

#### Instructions

- vredsum.vs
- vredmaxu.vs
- vredmax.vs
- vredminu.vs
- vredmin.vs
- vredand.vs
- vredor.vs
- vredxor.vs

### **Intrinsic functions list**

#### **Notes**

- Reduction intrinsics will generate code using tail undisturbed policy unless `vundefined()` is passed to the `dest` argument.

### **14.2. Vector Widening Integer Reduction Operations**

#### **Instructions**

- `vwredsumu.vs`
- `vwredsum.vs`

### **Intrinsic functions list**

#### **Notes**

- Reduction intrinsics will generate code using tail undisturbed policy unless `vundefined()` is passed to the `dest` argument.

### **14.3. Vector Single-Width Floating-Point Reduction Operations**

#### **Instructions**

- `vfredosum.vs`
- `vfredsum.vs`
- `vfredmax.vs`
- `vfredmin.vs`

### **Intrinsic functions list**

#### **Notes**

- Reduction intrinsics will generate code using tail undisturbed policy unless `vundefined()` is passed to the `dest` argument.

### **14.4. Vector Widening Floating-Point Reduction Operations**

#### **Instructions**

- `vwfredosum.vs`
- `vwfredsum.vs`

### **Intrinsic functions list**

## Notes

- Reduction intrinsics will generate code using tail undisturbed policy unless `vundefined()` is passed to the `dest` argument.

## 15. Vector Mask Instructions

### 15.1. Vector Mask-Register Logical Operations

#### Instructions

- `vmand.mm`
- `vmnand.mm`
- `vmandnot.mm`
- `vmxor.mm`
- `vmor.mm`
- `vmnor.mm`
- `vmornot.mm`
- `vmxnor.mm`
- `vmcpy.m`
- `vmclr.m`
- `vmset.m`
- `vmnot.m`

#### Intrinsic functions list

### 15.2. Vector mask population count `vpop`

#### Instructions

- `vmpopc.m`

#### Intrinsic functions list

### 15.3. `vfirst` find-first-set mask bit

#### Instructions

- `vmfirst.m`

#### Intrinsic functions list

### 15.4. `vmsbf.m` set-before-first mask bit

#### Instructions

- `vmsbf.m`

#### Intrinsic functions list



### **15.5. vmsif.m set-including-first mask bit**

#### **Instructions**

- vmsif.m

#### **Intrinsic functions list**

### **15.6. vmsof.m set-only-first mask bit**

#### **Instructions**

- vmsof.m

#### **Intrinsic functions list**

### **15.7. Vector Iota Operations**

#### **Instructions**

- viota.m

#### **Intrinsic functions list**

### **15.8. Vector Element Index Operations**

#### **Instructions**

- vid.v

#### **Intrinsic functions list**

## **16. Vector Permutation Operations**

### **16.1. Integer Extract Operations**

#### **Instructions**

- vext.x.v

#### **Intrinsic functions list**

### **16.2. Integer Scalar Move Operations**

#### **Instructions**

- vmv.s.x
- vmv.x.s

#### **Intrinsic functions list**

## Notes

- `vmv.s.x` intrinsic will generate code using tail undisturbed policy unless `vundefined()` is passed to the `dest` argument.

### 16.3. Floating-Point Scalar Move Operations

#### Instructions

- `vmv.f.s`
- `vmv.s.f`

#### Intrinsic functions list

## Notes

- `vmv.s.f` intrinsic will generate code using tail undisturbed policy unless `vundefined()` is passed to the `dest` argument.

### 16.4. Vector Slide Operations

#### Instructions

- `vslideup.{vx,vi}`
- `vslidedown.{vx,vi}`
- `vslide1up.vx`
- `vslide1down.vx`

#### Intrinsic functions list

## Notes

- Unmasked `vslideup` and `vslidedown` intrinsics will generate code using tail undisturbed policy unless `vundefined()` is passed to the `dst` argument.

### 16.5. Vector Register Gather Operations

#### Instructions

- `vrgather.{vv,vx,vi}`

#### Intrinsic functions list

### 16.6. Vector Compress Operations

#### Instructions

- `vcompress.vv`

## Intrinsic functions list

### Notes

- vcompress intrinsics will generate code using tail undisturbed policy unless vundefined() is passed to the dest argument.

## 17. None

Keep this chapter none to aligned to riscv-v-spec chapters

## 18. Divided Element Extension (‘Zvediv’)

### 18.1. Vector Integer Dot-Product Operations

#### Instructions

- vdotu.vv
- vdot.vv

#### Intrinsic functions list

TODO

### 18.2. Vector Floating-Point Dot Product Operations

#### Instructions

- vfdotu.vv

#### Intrinsic functions list

TODO

## 19. RVV Intrinsic Examples

- sgemm
- saxpy

## 20. Incompatible With V1.0

### 20.1. Vector Unordered Indexed Loads

#### Instructions

- vlxei<eew>.v -> vloxei<eew>.v [RENALED]
- vsxei<eew>.v -> vsoxei<eew>.v [RENALED]
- vlxseg<nf>ei<eew>.v -> vloxseg<nf>ei<eew>.v [RENALED]
- vsxseg<nf>ei<eew>.v -> vsoxseg<nf>ei<eew>.v [RENALED]
- vluxe<eew>.v [NEW]

- `vluxseg<nf>ei<eew>.v` [NEW]

## New intrinsic functions list

### 20.2. Vector Load/Store With Extend

#### Instructions

- `vlb.v` [DELETED]
- `vlh.v` [DELETED]
- `vlw.v` [DELETED]
- `vlbu.v` [DELETED]
- `vlhu.v` [DELETED]
- `vlwu.v` [DELETED]
- `vsb.v` [DELETED]
- `vsh.v` [DELETED]
- `vsw.v` [DELETED]
- `vlsb.v` [DELETED]
- `vlsh.v` [DELETED]
- `vlsu.v` [DELETED]
- `vlsbu.v` [DELETED]
- `vlshu.v` [DELETED]
- `vlswu.v` [DELETED]
- `vssb.v` [DELETED]
- `vssh.v` [DELETED]
- `vssw.v` [DELETED]
- `vlxb.v` [DELETED]
- `vlxh.v` [DELETED]
- `vlxw.v` [DELETED]
- `vlxbu.v` [DELETED]
- `vlxhu.v` [DELETED]
- `vlxwu.v` [DELETED]
- `vsxb.v` [DELETED]
- `vsxh.v` [DELETED]

- vsxw.v [DELETED]
- vsuxb.v [DELETED]
- vsuxh.v [DELETED]
- vsuxw.v [DELETED]
- vlseg<nf>b.v [DELETED]
- vlseg<nf>h.v [DELETED]
- vlseg<nf>w.v [DELETED]
- vlseg<nf>bu.v [DELETED]
- vlseg<nf>hu.v [DELETED]
- vlseg<nf>wu.v [DELETED]
- vsseg<nf>b.v [DELETED]
- vsseg<nf>h.v [DELETED]
- vsseg<nf>w.v [DELETED]
- vlsseg<nf>b.v [DELETED]
- vlsseg<nf>h.v [DELETED]
- vlsseg<nf>w.v [DELETED]
- vlsseg<nf>bu.v [DELETED]
- vlsseg<nf>hu.v [DELETED]
- vlsseg<nf>wu.v [DELETED]
- vssseg<nf>b.v [DELETED]
- vssseg<nf>h.v [DELETED]
- vssseg<nf>w.v [DELETED]
- vlxseg<nf>b.v [DELETED]
- vlxseg<nf>h.v [DELETED]
- vlxseg<nf>w.v [DELETED]
- vlxseg<nf>bu.v [DELETED]
- vlxseg<nf>hu.v [DELETED]
- vlxseg<nf>wu.v [DELETED]
- vsxseg<nf>b.v [DELETED]
- vsxseg<nf>h.v [DELETED]
- vsxseg<nf>w.v [DELETED]

### 20.3. Vector Integer Extension Operations

#### Instructions

- `vzext.vf{2,4,8}` [NEW]
- `vsext.vf{2,4,8}` [NEW]

New intrinsic functions list

### 20.4. Vector Integer Add-to-Carry / Subtract-to-Borrow Operations

#### Instructions

- `vmadc.{vv,vx,vi}` [NEW]
- `vmsbc.{vv,vx}` [NEW]

New intrinsic functions list

### 20.5. Renamed Vector Narrowing Integer Right Shift Operations

#### Instructions

- `vnsra.{vv,vx,vi}` -> `vnsra.{wv,wx,wi}` [RENAMED]
- `vnsrl.{vv,vx,vi}` -> `vnsrl.{wv,wx,wi}` [RENAMED]

### 20.6. Renamed Vector Narrowing Fixed-Point Clip Operations

#### Instructions

- `vnclipu.{vx,vv,vi}` -> `vnclipu.{wx,wv,wi}` [RENAMED]
- `vnclip.{vx,vv,vi}` -> `vnclip.{wx,wv,wi}` [RENAMED]

### 20.7. Renamed Vector Copy Mask Operations

#### Instructions

- `vmcpy.m` -> `vmmv.m` [RENAMED]

### 20.8. Vector Floating-Point Slide Operations

#### Instructions

- `vfslide1up.vf` [NEW]
- `vfslide1down.vf` [NEW]

New intrinsic functions list

## 20.9. Vector Single-Width Averaging Add and Subtract Of Unsigned Integer Operations

### Instructions

- vaadd.vi [DELETED]
- vaaddu.{vv,vx} [NEW]
- vasubu.{vv,vx} [NEW]

### New intrinsic functions list

## 20.10. Vector Widening Saturating Scaled Multiply-Add Operations

### Instructions

- vwsmaacu.{vv, vx} [DELETED]
- vwsmaacc.{vv, vx} [DELETED]
- vwsmaaccsu.{vv, vx} [DELETED]

## 20.11. Single-Width Floating-Point/Integer Type-Convert By Truncating

### Instructions

- vfcvt.rtz.x.f.v [NEW]
- vfcvt.rtz.xu.f.v [NEW]

### New intrinsic functions list

## 20.12. Widening-Width Floating-Point/Integer Type-Convert By Truncating

### Instructions

- vfwcvt.rtz.x.f.v [NEW]
- vfwcvt.rtz.xu.f.v [NEW]

### New intrinsic functions list

## 20.13. Narrowing-Width Floating-Point/Integer Type-Convert

### Instructions

- vfncvt.xu.f.v -> vfncvt.xu.f.w [RENAMED]
- vfncvt.x.f.v -> vfncvt.x.f.w [RENAMED]
- vfncvt.rtz.xu.f.w [NEW]
- vfncvt.rtz.x.f.w [NEW]
- vfncvt.f.xu.v -> vfncvt.f.xu.w [RENAMED]
- vfncvt.f.x.v -> vfncvt.f.x.w [RENAMED]

- vfnvvt.f.f.v -> vfnvvt.f.f.w [REMOVED]
- vfnvvt.rod.f.f.w [NEW]

#### New intrinsic functions list

#### 20.14. Vector Floating-Point Ordered Compare

##### Instructions

- vmford.{vv, vf} [DELETED]

#### 20.15. Renamed Vector Mask operations

##### Instructions

- vmpopc.m -> vcpop.m [REMOVED]
- vmfirst.m -> vfirst.m [REMOVED]

#### 20.16. Vector Mask Load/Store operations

##### Instructions

- vlm.v [NEW]
- vsm.v [NEW]

#### New intrinsic functions list

#### 20.17. Vector Register Gather 16-bits Index

##### Instructions

- vrgatherei16.vv [NEW]

### 21 Vector interface not supported on rv32

##### Instructions

- vloxei64.v
- vsoxei64.v
- vloxegei64.v
- vsoxegei64.v
- vluxei64.v
- vsuxei64.v
- vluxegei64.v
- vsuxegei64.v

#### New intrinsic functions list



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### Introduction

This document introduces the intrinsics for RISC-V vector programming, including the design decision we take, the type system, the general naming rules for intrinsics, and facilities for vector users. It does not list all available intrinsics for vector programming. The full set of intrinsics will be written in another document.

### Type System

## Data Types

Encode SEW and LMUL into data types. We enforce the constraint  $LMUL \geq SEW/ELEN$  in the implementation. There are the following data types for  $ELEN = 64$ .

Types	LMUL = 1	LMUL = 2	LMUL = 4	LMUL = 8
<b>int64_t</b>	vint64m1_t	vint64m2_t	vint64m4_t	vint64m8_t
<b>uint64_t</b>	vuint64m1_t	vuint64m2_t	vuint64m4_t	vuint64m8_t
<b>int32_t</b>	vint32m1_t	vint32m2_t	vint32m4_t	vint32m8_t
<b>uint32_t</b>	vuint32m1_t	vuint32m2_t	vuint32m4_t	vuint32m8_t
<b>int16_t</b>	vint16m1_t	vint16m2_t	vint16m4_t	vint16m8_t
<b>uint16_t</b>	vuint16m1_t	vuint16m2_t	vuint16m4_t	vuint16m8_t
<b>int8_t</b>	vint8m1_t	vint8m2_t	vint8m4_t	vint8m8_t
<b>uint8_t</b>	vuint8m1_t	vuint8m2_t	vuint8m4_t	vuint8m8_t
<b>vfloat64</b>	vfloat64m1_t	vfloat64m2_t	vfloat64m4_t	vfloat64m8_t
<b>vfloat32</b>	vfloat32m1_t	vfloat32m2_t	vfloat32m4_t	vfloat32m8_t
<b>vfloat16</b>	vfloat16m1_t	vfloat16m2_t	vfloat16m4_t	vfloat16m8_t

There are the following data types for  $ELEN = 32$ .

Types	LMUL = 1	LMUL = 2	LMUL = 4	LMUL = 8
<b>int32_t</b>	vint32m1_t	vint32m2_t	vint32m4_t	vint32m8_t
<b>uint32_t</b>	vuint32m1_t	vuint32m2_t	vuint32m4_t	vuint32m8_t
<b>int16_t</b>	vint16m1_t	vint16m2_t	vint16m4_t	vint16m8_t
<b>uint16_t</b>	vuint16m1_t	vuint16m2_t	vuint16m4_t	vuint16m8_t
<b>int8_t</b>	vint8m1_t	vint8m2_t	vint8m4_t	vint8m8_t
<b>uint8_t</b>	vuint8m1_t	vuint8m2_t	vuint8m4_t	vuint8m8_t
<b>vfloat32</b>	vfloat32m1_t	vfloat32m2_t	vfloat32m4_t	vfloat32m8_t
<b>vfloat16</b>	vfloat16m1_t	vfloat16m2_t	vfloat16m4_t	vfloat16m8_t

## Mask Types

Encode the ratio of  $SEW/LMUL$  into the mask types. There are the following mask types.

$n = SEW/LMUL$

Types	n = 1	n = 2	n = 4	n = 8	n = 16	n = 32	n = 64
bool	vbool1_t	vbool2_t	vbool4_t	vbool8_t	vbool16_t	vbool32_t	vbool64_t

## Types for Segment Load/Store

Under the constraint  $LMUL * NR \leq 8$ .

```
SEGMENT_TYPE ::= 'v' TYPE LMUL 'x' NR '_t'
TYPE ::= ( 'int8' | 'int16' | 'int32' | 'int64' |
           'uint8' | 'uint16' | 'uint32' | 'uint64' |
           'float16' | 'float32' | 'float64' )
LMUL ::= ( m1 | m2 | m4 | m8 )
NR ::= ( 2 | 3 | 4 | 5 | 6 | 7 | 8 )
```

int8\_t:

NF	LMUL	LMUL = 1	LMUL = 2	LMUL = 4	LMUL = 8
<b>2</b>		vint8m1x2_t	vint8m2x2_t	vint8m4x2_t	N/A
<b>3</b>		vint8m1x3_t	vint8m2x3_t	N/A	N/A
<b>4</b>		vint8m1x4_t	vint8m2x4_t	N/A	N/A
<b>5</b>		vint8m1x5_t	N/A	N/A	N/A
<b>6</b>		vint8m1x6_t	N/A	N/A	N/A
<b>7</b>		vint8m1x7_t	N/A	N/A	N/A
<b>8</b>		vint8m1x8_t	N/A	N/A	N/A

uint8\_t:

NF	LMUL	LMUL = 1	LMUL = 2	LMUL = 4	LMUL = 8
<b>2</b>		vuint8m1x2_t	vuint8m2x2_t	vuint8m4x2_t	N/A
<b>3</b>		vuint8m1x3_t	vuint8m2x3_t	N/A	N/A
<b>4</b>		vuint8m1x4_t	vuint8m2x4_t	N/A	N/A
<b>5</b>		vuint8m1x5_t	N/A	N/A	N/A
<b>6</b>		vuint8m1x6_t	N/A	N/A	N/A
<b>7</b>		vuint8m1x7_t	N/A	N/A	N/A
<b>8</b>		vuint8m1x8_t	N/A	N/A	N/A

int16\_t:

NF	LMUL	LMUL = 1	LMUL = 2	LMUL = 4	LMUL = 8
<b>2</b>		vint16m1x2_t	vint16m2x2_t	vint16m4x2_t	N/A
<b>3</b>		vint16m1x3_t	vint16m2x3_t	N/A	N/A
<b>4</b>		vint16m1x4_t	vint16m2x4_t	N/A	N/A
<b>5</b>		vint16m1x5_t	N/A	N/A	N/A
<b>6</b>		vint16m1x6_t	N/A	N/A	N/A
<b>7</b>		vint16m1x7_t	N/A	N/A	N/A
<b>8</b>		vint16m1x8_t	N/A	N/A	N/A

uint16\_t:

NF	LMUL	LMUL = 1	LMUL = 2	LMUL = 4	LMUL = 8
<b>2</b>		vuint16m1x2_t	vuint16m2x2_t	vuint16m4x2_t	N/A
<b>3</b>		vuint16m1x3_t	vuint16m2x3_t	N/A	N/A
<b>4</b>		vuint16m1x4_t	vuint16m2x4_t	N/A	N/A
<b>5</b>		vuint16m1x5_t	N/A	N/A	N/A
<b>6</b>		vuint16m1x6_t	N/A	N/A	N/A
<b>7</b>		vuint16m1x7_t	N/A	N/A	N/A
<b>8</b>		vuint16m1x8_t	N/A	N/A	N/A

int32\_t:

NF	LMUL	LMUL = 1	LMUL = 2	LMUL = 4	LMUL = 8
<b>2</b>		vint32m1x2_t	vint32m2x2_t	vint32m4x2_t	N/A
<b>3</b>		vint32m1x3_t	vint32m2x3_t	N/A	N/A
<b>4</b>		vint32m1x4_t	vint32m2x4_t	N/A	N/A
<b>5</b>		vint32m1x5_t	N/A	N/A	N/A
<b>6</b>		vint32m1x6_t	N/A	N/A	N/A
<b>7</b>		vint32m1x7_t	N/A	N/A	N/A
<b>8</b>		vint32m1x8_t	N/A	N/A	N/A

uint32\_t:

NF	LMUL	LMUL = 1	LMUL = 2	LMUL = 4	LMUL = 8
<b>2</b>		vuint32m1x2_t	vuint32m2x2_t	vuint32m4x2_t	N/A
<b>3</b>		vuint32m1x3_t	vuint32m2x3_t	N/A	N/A
<b>4</b>		vuint32m1x4_t	vuint32m2x4_t	N/A	N/A
<b>5</b>		vuint32m1x5_t	N/A	N/A	N/A
<b>6</b>		vuint32m1x6_t	N/A	N/A	N/A
<b>7</b>		vuint32m1x7_t	N/A	N/A	N/A
<b>8</b>		vuint32m1x8_t	N/A	N/A	N/A

int64\_t:

NF	LMUL	LMUL = 1	LMUL = 2	LMUL = 4	LMUL = 8
<b>2</b>		vint64m1x2_t	vint64m2x2_t	vint64m4x2_t	N/A
<b>3</b>		vint64m1x3_t	vint64m2x3_t	N/A	N/A
<b>4</b>		vint64m1x4_t	vint64m2x4_t	N/A	N/A
<b>5</b>		vint64m1x5_t	N/A	N/A	N/A
<b>6</b>		vint64m1x6_t	N/A	N/A	N/A

NF	LMUL	LMUL = 1	LMUL = 2	LMUL = 4	LMUL = 8
<b>7</b>		vint64m1x7_t	N/A	N/A	N/A
<b>8</b>		vint64m1x8_t	N/A	N/A	N/A

uint64\_t:

NF	LMUL	LMUL = 1	LMUL = 2	LMUL = 4	LMUL = 8
<b>2</b>		vuint64m1x2_t	vuint64m2x2_t	vuint64m4x2_t	N/A
<b>3</b>		vuint64m1x3_t	vuint64m2x3_t	N/A	N/A
<b>4</b>		vuint64m1x4_t	vuint64m2x4_t	N/A	N/A
<b>5</b>		vuint64m1x5_t	N/A	N/A	N/A
<b>6</b>		vuint64m1x6_t	N/A	N/A	N/A
<b>7</b>		vuint64m1x7_t	N/A	N/A	N/A
<b>8</b>		vuint64m1x8_t	N/A	N/A	N/A

float16:

NF	LMUL	LMUL = 1	LMUL = 2	LMUL = 4	LMUL = 8
<b>2</b>		vfloat16m1x2_t	vfloat16m2x2_t	vfloat16m4x2_t	N/A
<b>3</b>		vfloat16m1x3_t	vfloat16m2x3_t	N/A	N/A
<b>4</b>		vfloat16m1x4_t	vfloat16m2x4_t	N/A	N/A
<b>5</b>		vfloat16m1x5_t	N/A	N/A	N/A
<b>6</b>		vfloat16m1x6_t	N/A	N/A	N/A
<b>7</b>		vfloat16m1x7_t	N/A	N/A	N/A
<b>8</b>		vfloat16m1x8_t	N/A	N/A	N/A

float32:

NF	LMUL	LMUL = 1	LMUL = 2	LMUL = 4	LMUL = 8
<b>2</b>		vfloat32m1x2_t	vfloat32m2x2_t	vfloat32m4x2_t	N/A
<b>3</b>		vfloat32m1x3_t	vfloat32m2x3_t	N/A	N/A
<b>4</b>		vfloat32m1x4_t	vfloat32m2x4_t	N/A	N/A
<b>5</b>		vfloat32m1x5_t	N/A	N/A	N/A
<b>6</b>		vfloat32m1x6_t	N/A	N/A	N/A
<b>7</b>		vfloat32m1x7_t	N/A	N/A	N/A
<b>8</b>		vfloat32m1x8_t	N/A	N/A	N/A

float64:

NF	LMUL	LMUL = 1	LMUL = 2	LMUL = 4	LMUL = 8
2		vfloat64m1x2_t	vfloat64m2x2_t	vfloat64m4x2_t	N/A
3		vfloat64m1x3_t	vfloat64m2x3_t	N/A	N/A
4		vfloat64m1x4_t	vfloat64m2x4_t	N/A	N/A
5		vfloat64m1x5_t	N/A	N/A	N/A
6		vfloat64m1x6_t	N/A	N/A	N/A
7		vfloat64m1x7_t	N/A	N/A	N/A
8		vfloat64m1x8_t	N/A	N/A	N/A

## Configuration-Setting and vl Argument

There are two variants of configuration setting intrinsics. `vsetvl` is used to get the active vector length (vl) according to the given application vector length(AVL), SEW and LMUL.

vl register status is not expose to C language level, so in theory you can treat `vsetvl` and `vsetvlmax` functions are return the min value for `avl` and `VLMAX`.

```
size_t vsetvl_e8m1 (size_t avl);
size_t vsetvl_e8m2 (size_t avl);
size_t vsetvl_e8m4 (size_t avl);
size_t vsetvl_e8m8 (size_t avl);
size_t vsetvlmax_e8m1 ();
size_t vsetvlmax_e8m2 ();
size_t vsetvlmax_e8m4 ();
size_t vsetvlmax_e8m8 ();
```

There is no need to specify the behavior of tail and masked-off elements being undisturbed or agnostic. The default setting is tail agnostic and masked-off undisturbed. If users do not want to keep the values in masked-off elements, they could pass `vundefined()` as the `maskedoff` value.

SEW and LMUL are a part of the naming. They are static information for the intrinsics.

All of the intrinsic functions have a `vl` argument to specify the active vector length, except a few functions operate regardless of `vl`. e.g. `vmv.x.s`, `vfmv.f.s`, `vundefined`, `vreinterpret`, `vget`, `vset` and `vcreate`.

The intrinsic functions will only operate at most `VLMAX` element if the `vl` argument are larger than `VLMAX`.

The semantics of following two snippets are equivalent. We strongly suggest the first form.

```
size_t vl = vsetvl_e8m1 (avl);
vint8m1_t va, vb, vc;
va = vadd_vv_i8m1(vb, vc, vl);
```

```
vint8m1_t va, vb, vc;  
va = vadd_vv_i8m1(vb, vc, avl);
```

## Naming Rules

Intrinsics is the interface to the low level assembly in high level programming language. The intrinsic API has the goal to make all the V-ext instructions accessible from C/C++. The intrinsic names are as close as the assembly mnemonics. Besides the basic intrinsics corresponding to assembly mnemonics, there are intrinsics close to semantic naming.

The intrinsic names will encode return type if it is appropriate. It is easier to know the output type of the intrinsics from the name. In addition, if the intrinsic call is used as the operand, having the return type is more immediate. If there is no return value, the intrinsics will encode the input value types. If the return type is the same, use exceptional rules to differentiate them. See Exceptions in Naming.

In general, the naming rule of intrinsics is

```
INTRINSIC ::= MNEMONIC '_' RET_TYPE  
MNEMONIC ::= Instruction name in v-ext specification. Replace  
            '.' with '_'.  
RET_TYPE ::= SEW LMUL  
SEW ::= ( i8 | i16 | i32 | i64 | u8 | u16 | u32 | u64 | f16 |  
          f32 | f64 )  
LMUL ::= ( m1 | m2 | m4 | m8 )
```

Example:

```
vadd.vv vd, vs2, vs1:  
vint8m1_t vadd_vv_i8m1(vint8m1_t vs2, vint8m1_t vs1, size_t vl);  
  
vwaddu.vv vd, vs2, vs1:  
vuint16m2_t vwaddu_vv_u16m2 (vuint8m1_t vs2, vuint8m1_t vs1,  
                             size_t vl);
```

All these rules are common for rvv0.7 and rvv1.0, but for compatibility, the mnemonics of rvv1.0 are used in preference to the identical function.

To find out which instructions have had mnemonics changed, please see [Incompatible With V1.0](#)

## Exceptions in Naming

If intrinsics have the same return type under different input types, we could not use general naming rules directly on these intrinsics. It will cause the same intrinsic names for different input types.

This section lists all exceptional cases for intrinsic naming.

### Vector Stores

It does not encode return type into vector store. There is no return data for store operations. Instead, use the type of store data to name the intrinsics.

Example:

```
vse8.v vs3, (rs1):  
void vse8_v_i8m1(int8_t *rs1, vint8m1_t vs3, size_t vl);
```

### Vector Integer Add-with-Carry / Subtract-with-Borrow Instructions

The result of `vmadc` and `vmsbc` is mask types. Because we use the ratio `SEW/LMUL` to name the mask types and multiple (`SEW`, `LMUL`) pairs map to the same ratio, in addition to use the return type to name the intrinsics, we also encode the input types to distinguish these intrinsics.

Example:

```
vmadc.vvm vd, vs2, vs1, v0:  
vbool8_t vmadc_vvm_i8m1_b8(vint8m1_t op1, vint8m1_t op2,  
    vbool8_t carryin, size_t vl);
```

### Comparison Instructions

The result of comparison instructions is mask types. Because we use the ratio `SEW/LMUL` to name the mask types and multiple (`SEW`, `LMUL`) pairs map to the same ratio, in addition to use the return type to name the intrinsics, we also encode the input types to distinguish these intrinsics.

Example:

```
vmseq.vv vd, vs2, vs1:  
vbool8_t vmseq_vv_i8m1_b8(vint8m1_t vs2, vint8m1_t vs1, size_t  
    vl);  
vbool8_t vmseq_vv_i16m2_b8(vint16m2_t vs2, vint16m2_t vs1,  
    size_t vl);
```

### Reduction Instructions

The scalar input and output operands are held in element 0 of a single vector register. Use `LMUL = 1` in the return type. To distinguish different intrinsics with different input types, encode the input type and the result type in the name.



Example:

```
vredsum.vs vd, vs2, vs1:
vint8m1_t vredsum_vs_i8m1_i8m1(vint8m1_t dest, vint8m1_t vs2,
    vint8m1_t vs1, size_t vl);
vint8m1_t vredsum_vs_i8m2_i8m1(vint8m1_t dest, vint8m2_t vs2,
    vint8m1_t vs1, size_t vl);
vint8m1_t vredsum_vs_i8m4_i8m1(vint8m1_t dest, vint8m4_t vs2,
    vint8m1_t vs1, size_t vl);
vint8m1_t vredsum_vs_i8m8_i8m1(vint8m1_t dest, vint8m8_t vs2,
    vint8m1_t vs1, size_t vl);
```

### **vcpop.m and vfirst.m**

The return type of `vcpop.m` and `vfirst.m` is apparently an integer. Do not encode the return type into it. Instead, encode the input type to it.

Example:

```
vcpop.m rd, vs2:
unsigned long vcpop_m_b1(vbool1_t vs2, size_t vl);
unsigned long vcpop_m_b2(vbool2_t vs2, size_t vl);
```

### **Permutation Instructions**

To move the element 0 of a vector to a scalar, encode the input vector type and the output scalar type.

Example:

```
vmv.x.s rd, vs2:
int8_t vmv_x_s_i8m1_i8 (vint8m1_t vs2, size_t vl);
int8_t vmv_x_s_i8m2_i8 (vint8m2_t vs2, size_t vl);
int8_t vmv_x_s_i8m4_i8 (vint8m4_t vs2, size_t vl);
int8_t vmv_x_s_i8m8_i8 (vint8m8_t vs2, size_t vl);
```

### **Scalar in Vector Operations**

In V specification, it defines operations between vector and scalar types. If `XLEN > SEW`, the least-significant SEW bits of the scalar register are used. If `XLEN < SEW`, the value from the scalar register is sign-extended to SEW bits.

We define arithmetic intrinsics with scalar using SEW types.

Example:

```
// Use uint8_t for op2.
```

```

vuint8m1_t vadd_vx_u8m1(vuint8m1_t op1, uint8_t op2, size_t vl);
// Use uint64_t for op2.
vuint64m1_t vadd_vx_u64m1(vuint64m1_t op1, uint64_t op2, size_t
    vl);

```

The compiler may generate multiple instructions for the intrinsics. For example, it is a little bit complicated to support `uint64_t` operations under `XLEN = 32`.

It breaks the one-to-one mapping between intrinsics and assembly mnemonics in some hardware configurations. However, it makes more sense for users to use the scalar types consistent with the SEW of vector types.

There is the same issue for `vmv.x.s`, `vmv.s.x`, `vfmv.f.s`, `vfmv.s.f`, `vslide1up.vx`, and `vslide1down.vx`. Use SEW to encode the scalar type.

Example:

```

// Use uint8_t for op2.
vuint8m1_t vslide1up_vx_u8m1(vuint8m1_t dest, vuint8m1_t op1,
    uint8_t op2);

```

## Mask in Intrinsics

RISC-V “V” extension only has “merge in output” semantic. Intrinsics with mask has two additional arguments, `mask` and `maskedoff`.

```

vd = vop(mask, maskedoff, arg1, arg2)
vd[i] = maskedoff[i], if mask[i] == 0
vd[i] = vop(arg1[i], arg2[2]), if mask[i] == 1

```

In general, the naming rule of intrinsic with mask `v0.t` is

```

INTRINSIC_WITH_MASK ::= INTRINSIC '_m'

```

Example:

```

vadd.vv vd, vs2, vs1, v0.t:
vint8m1_t vadd_vv_i8m1_m(vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t vs2, vint8m1_t vs1, size_t vl);

```

If the intrinsics are always masked, there is no need to append `_m` to the intrinsic. For example, the `vmerge` instructions are always masked.

Example:

```

vmerge.vvm vd, vs2, vs1, v0:
vint8m1_t vmerge_vvm_i8m1(vbool8_t mask, vint8m1_t vs2,
    vint8m1_t vs1, size_t vl);

```

```
vcompress.vv vd, vs2, vs1:
vint8m1_t vcompress_vm_i8m1(vbool8_t vs1, vint8m1_t maskedoff,
    vint8m1_t vs2, size_t vl);
```

There are two additional masking semantics: *zero in output* semantic and *don't care in output* semantic. Users could leverage *merge in output* intrinsics to simulate these two additional masking semantics.

Example:

```
// Don't care in output semantic
vint8m1_t vadd_vv_i8m1_m(vbool8_t mask, vundefined_i8m1(),
    vint8m1_t vs2, vint8m1_t vs1, size_t vl);
```

## Masked Intrinsics Without MaskedOff

### Vector Stores

There is no `maskedoff` argument for store operations. The value of `maskedoff` already exists in memory.

Example:

```
vse8.v vs3, (rs1), v0.t:
void vse8_v_i8m1_m(vbool8_t mask, int8_t *rs1, vint8m1_t vs3,
    size_t vl);
```

### Reduction Instructions

The result of reductions is put in element 0 of the output vector. There is no `maskedoff` argument for reduction operations.

Example:

```
vredsum.vs vd, vs2, vs1, v0.t:
vint8m1_t vredsum_vs_i8m2_i8m1_m(vbool4_t mask, vint8m1_t dest,
    vint8m2_t vs2, vint8m1_t vs1, size_t vl);
```

### Merge Instructions

The result of merge operations comes from their two source operands. Merge intrinsics have no `maskedoff` argument.

Example:

```
vmerge.vvm vd, vs2, vs1, v0:
vint8m1_t vmerge_vvm_i8m1(vbool8_t mask, vint8m1_t vs2,
    vint8m1_t vs1, size_t vl);
```

## Keep the Original Values of the Destination Vector

`vmv.s.x` and reduction operations will only modify the first element of the destination vector. Users could keep the original values of the remaining elements in the destination vector through `dest` argument in these intrinsics.

Vector slide instructions also have unchanged parts in the destination register group. Users could keep the original values of the unchanged parts in the destination vector group through `dest` argument in the intrinsics.

Example:

```
vint8m1_t vmv_s_x_i8m1(vint8m1_t dest, int8_t src, size_t vl);
vint8m1_t vredsum_vs_i8m1_i8m1(vint8m1_t dest, vint8m1_t vs2,
    vint8m1_t vs1, size_t vl);
vint8m1_t vredsum_vs_i8m2_i8m1_m(vbool4_t mask, vint8m1_t dest,
    vint8m2_t vs2, vint8m1_t vs1, size_t vl);
vuint8m1_t vslide1up_vx_u8m1(vuint8m1_t dest, vuint8m1_t op1,
    uint8_1 op2, size_t vl);
```

## SEW and LMUL of Intrinsics

SEW and LMUL are the static information for the intrinsics. The compiler will generate `vsetvli` when `vtype` is changed between operations.

Example:

```
vint8m1_t a, b, c, d;
vint16m2_t a2, b2, c2;
...
a2 = vwadd_vv_i16m2(a, b, vl);
b2 = vwadd_vv_i16m2(c, d, vl);
c2 = vadd_vv_i16m2(a2, b2, vl);
```

It will generate the following instructions.

```
vsetvli x0, vl, e8,m1
vwadd.vv a2, a, b
vwadd.vv b2, c, d
vsetvli x0, vl, e16,m2
vadd.vv c2, a2, b2
```

Be aware that when the ratio of LMUL/SEW is changed, users need to ensure the `vl` is correct for the following operations if using *implicit vl intrinsics*.

## C Operators on RISC-V Vector Types

The semantic of C builtin operators, other than simple assignment, hasn't been decided yet. Simple assignment keeps the usual C semantics of storing the value

on the right operand into the variable of the left operand.

## Utility Functions

This section lists all utility functions to help users program in V intrinsics easier.

### Vector Initialization

These utility functions are used to initialize vector values. They could be used in masking intrinsics with *don't care in output* semantics.

Example:

```
vint8m1_t vundefined_i8m1()
```

Note: Any operation with `vundefined_* ()` are undefined and unpredictable, the only recommended usage is used as maskedoff operand; an additional note is any operation with `vundefined_* ()` will got unpredictable result, e.g. `vxor(vundefined (), vundefined ())` and `vec a = vundefined(); vec b = vxor(a, a);` both are not guarantee result vector with zeros.

### Reinterpret between floating point and integer types

These utility functions help users to convert types between floating point and integer types. The reinterpreter intrinsics only change the types of underlying contents. It is a nop operation.

Example:

```
// Convert floating point to signed integer types.
vint64m1_t vreinterpret_v_f64m1_i64m1(vfloat64m1_t src)
// Convert floating point to unsigned integer types.
vuint64m1_t vreinterpret_v_f64m1_u64m1(vfloat64m1_t src);
```

### Reinterpret between signed and unsigned types

These utility functions help users to convert types between signed and unsigned types. The reinterpreter intrinsics only change the types of underlying contents. It is a nop operation.

Example:

```
// Convert signed to unsigned types.
vuint8m1_t vreinterpret_v_i8m1_u8m1(vint8m1_t src)
```

## Reinterpret between different SEW under the same LMUL

These utility functions help users to convert types between SEWs under the same LMUL, e.g., convert `vint32m1_t` to `vint64m1_t`. The reinterpreter intrinsics only change the types of underlying contents. It is a nop operation. It will generate `vsetvli` by the following vector operation for the new type.

Example:

```
// Convert SEW under the same LMUL.
vint64m1_t vreinterpret_v_i32m1_i64m1(vint32m1_t src)
```

## Vector Insertion and Extraction functions

These utility functions help users to insert or extract smaller LMUL under same SEW.

Example:

```
// Insert an smaller LMUL, vset_v_<src_lmulo>_<target_lmulo>
vint32m2_t vset_v_i32m1_i32m2 (vint32m2_t dest, size_t index,
    vint32m1_t val);
vint32m4_t vset_v_i32m1_i32m4 (vint32m4_t dest, size_t index,
    vint32m1_t val);
vint32m4_t vset_v_i32m2_i32m4 (vint32m4_t dest, size_t index,
    vint32m2_t val);

// Extract an smaller LMUL, vget_v_<src_lmulo>_<target_lmulo>
vint32m1_t vget_v_i32m2_i32m1 (vint32m2_t src, size_t index);
vint32m1_t vget_v_i32m4_i32m1 (vint32m4_t src, size_t index);
vint32m2_t vget_v_i32m4_i32m2 (vint32m4_t src, size_t index);
```

## Utility Functions for Segment Load/Store Types

These utility functions help users to create segment load/store types and insert/extract elements to/from segment load/store types.

Example:

```
// Create segment load/store types.
vint32m2x2_t vcreate_i32m2x2(vint32m2_t val1, vint32m2_t val2)
vint32m2x3_t vcreate_i32m2x3(vint32m2_t val1, vint32m2_t val2,
    vint32m2_t val3)

// Insert an element to segment load/store types.
vint32m2x2_t vset_i32m2x2(vint32m2x2_t tuple, size_t index,
    vint32m2_t value)
```

```
// Extract an element from segment load/store types.
vint32m2_t vget_i32m2x2_i32m2(vint32m2x2_t tuple, size_t index)
vint32m2_t vget_i32m2x3_i32m2(vint32m2x3_t tuple, size_t index)
```

## Switching Vtype and Keep same VL after vsetvl instruction

Compiler should guarantee the correctness of vtype setting after vsetvl instruction. For example considering the widening multiply example as below.

```
v1 = vsetvl_e16m4(n);
vfloat16m4_t vx = vle16_v_f16m4(ptr_x, v1);
// vsetvl_e32m8(v1); // No need to keep the same vl and change
    vtype manually
vfloat32m8_t vy = vle32_v_f32m8(ptr_y, v1);
// vsetvl_e16m4(v1); // No need to keep the same vl and change
    vtype manually
vfwacc_vf_f32m8(vy, 2.0, vx, v1);
// vsetvl_e32m8(v1); // No need to keep the same vl and change
    vtype manually
vse32_v_f32m8(ptr_y, vy, v1);
```

This example has a vl computed from vsetvl\_e16m4, and changing the type to vfloat32m8\_t in the middle. With compiler's helping, users don't need to change vtype manually because vfloat16m4\_t and vfloat32m8\_t have the exact same number of elements (same SEW/LMUL ratio). Noted that when using the different vtype intrinsic functions with a new SEW/LMUL ratio after vsetvl instruction, the result will raise an illegal-instruction exception.

## Programming Note

### Strided load/store with stride of 0

The V extension spec mentions that the strided load/store instruction with stride of 0 could have different instruction to perform all memory accesses or fewer memory operations. Since needing all memory accesses isn't likely to be common, the compiler implementation is allowed to generate fewer memory operations with strided load/store intrinsics. In other words, compiler does not guarantee generating the all memory accesses instruction in strided load/store intrinsics with stride of 0. If the user needs all memory accesses to be performed, they should use an indexed load/store intrinsics with all zero indices.

## RVV Intrinsic Functions List

### RVV C Type System:

#### RVV C extension types:

#### Prototypes:

```
vint8m1_t  
vint8m2_t  
vint8m4_t  
vint8m8_t  
vint16m1_t  
vint16m2_t  
vint16m4_t  
vint16m8_t  
vint32m1_t  
vint32m2_t  
vint32m4_t  
vint32m8_t  
vint64m1_t  
vint64m2_t  
vint64m4_t  
vint64m8_t  
vuint8m1_t  
vuint8m2_t  
vuint8m4_t  
vuint8m8_t  
vuint16m1_t  
vuint16m2_t  
vuint16m4_t  
vuint16m8_t  
vuint32m1_t  
vuint32m2_t  
vuint32m4_t  
vuint32m8_t  
vuint64m1_t  
vuint64m2_t  
vuint64m4_t  
vuint64m8_t  
vfloat16m1_t  
vfloat16m2_t  
vfloat16m4_t  
vfloat16m8_t  
vfloat32m1_t  
vfloat32m2_t  
vfloat32m4_t
```



```
vfloat32m8_t  
vfloat64m1_t  
vfloat64m2_t  
vfloat64m4_t  
vfloat64m8_t
```

#### **RVV C extension mask types:**

- The Syntax is `vbool<MLen>_t`
- `vbool1_t`
- `vbool2_t`
- `vbool4_t`
- `vbool8_t`
- `vbool16_t`
- `vbool32_t`
- `vbool64_t`

#### **Configuration-Setting and Utility Functions:**

##### **Set vl and vtype Functions:**

###### **Prototypes:**

```
size_t vsetvl_e8m1 (size_t avl);  
size_t vsetvl_e8m2 (size_t avl);  
size_t vsetvl_e8m4 (size_t avl);  
size_t vsetvl_e8m8 (size_t avl);  
size_t vsetvl_e16m1 (size_t avl);  
size_t vsetvl_e16m2 (size_t avl);  
size_t vsetvl_e16m4 (size_t avl);  
size_t vsetvl_e16m8 (size_t avl);  
size_t vsetvl_e32m1 (size_t avl);  
size_t vsetvl_e32m2 (size_t avl);  
size_t vsetvl_e32m4 (size_t avl);  
size_t vsetvl_e32m8 (size_t avl);  
size_t vsetvl_e64m1 (size_t avl);  
size_t vsetvl_e64m2 (size_t avl);  
size_t vsetvl_e64m4 (size_t avl);  
size_t vsetvl_e64m8 (size_t avl);
```

##### **Set the vl to VLMAX with specific vtype:**

###### **Prototypes:**

```
size_t vsetvlmax_e8m1 ();  
size_t vsetvlmax_e8m2 ();  
size_t vsetvlmax_e8m4 ();
```

```

size_t vsetvmax_e8m8 ();
size_t vsetvmax_e16m1 ();
size_t vsetvmax_e16m2 ();
size_t vsetvmax_e16m4 ();
size_t vsetvmax_e16m8 ();
size_t vsetvmax_e32m1 ();
size_t vsetvmax_e32m2 ();
size_t vsetvmax_e32m4 ();
size_t vsetvmax_e32m8 ();
size_t vsetvmax_e64m1 ();
size_t vsetvmax_e64m2 ();
size_t vsetvmax_e64m4 ();
size_t vsetvmax_e64m8 ();

```

## Vector Loads and Stores Functions:

### Vector Unit-Stride Load Functions:

#### Prototypes:

```

vint8m1_t vlb_v_i8m1 (const int8_t *a, size_t vl);
vint8m2_t vlb_v_i8m2 (const int8_t *a, size_t vl);
vint8m4_t vlb_v_i8m4 (const int8_t *a, size_t vl);
vint8m8_t vlb_v_i8m8 (const int8_t *a, size_t vl);
vint16m1_t vlb_v_i16m1 (const int16_t *a, size_t vl);
vint16m2_t vlb_v_i16m2 (const int16_t *a, size_t vl);
vint16m4_t vlb_v_i16m4 (const int16_t *a, size_t vl);
vint16m8_t vlb_v_i16m8 (const int16_t *a, size_t vl);
vint32m1_t vlb_v_i32m1 (const int32_t *a, size_t vl);
vint32m2_t vlb_v_i32m2 (const int32_t *a, size_t vl);
vint32m4_t vlb_v_i32m4 (const int32_t *a, size_t vl);
vint32m8_t vlb_v_i32m8 (const int32_t *a, size_t vl);
vint64m1_t vlb_v_i64m1 (const int64_t *a, size_t vl);
vint64m2_t vlb_v_i64m2 (const int64_t *a, size_t vl);
vint64m4_t vlb_v_i64m4 (const int64_t *a, size_t vl);
vint64m8_t vlb_v_i64m8 (const int64_t *a, size_t vl);
vint16m1_t vlh_v_i16m1 (const int16_t *a, size_t vl);
vint16m2_t vlh_v_i16m2 (const int16_t *a, size_t vl);
vint16m4_t vlh_v_i16m4 (const int16_t *a, size_t vl);
vint16m8_t vlh_v_i16m8 (const int16_t *a, size_t vl);
vint32m1_t vlh_v_i32m1 (const int32_t *a, size_t vl);
vint32m2_t vlh_v_i32m2 (const int32_t *a, size_t vl);
vint32m4_t vlh_v_i32m4 (const int32_t *a, size_t vl);
vint32m8_t vlh_v_i32m8 (const int32_t *a, size_t vl);
vint64m1_t vlh_v_i64m1 (const int64_t *a, size_t vl);
vint64m2_t vlh_v_i64m2 (const int64_t *a, size_t vl);

```

```

vint64m4_t vlh_v_i64m4 (const int64_t *a, size_t vl);
vint64m8_t vlh_v_i64m8 (const int64_t *a, size_t vl);
vint32m1_t vlw_v_i32m1 (const int32_t *a, size_t vl);
vint32m2_t vlw_v_i32m2 (const int32_t *a, size_t vl);
vint32m4_t vlw_v_i32m4 (const int32_t *a, size_t vl);
vint32m8_t vlw_v_i32m8 (const int32_t *a, size_t vl);
vint64m1_t vlw_v_i64m1 (const int64_t *a, size_t vl);
vint64m2_t vlw_v_i64m2 (const int64_t *a, size_t vl);
vint64m4_t vlw_v_i64m4 (const int64_t *a, size_t vl);
vint64m8_t vlw_v_i64m8 (const int64_t *a, size_t vl);
vuint8m1_t vlbu_v_u8m1 (const uint8_t *a, size_t vl);
vuint8m2_t vlbu_v_u8m2 (const uint8_t *a, size_t vl);
vuint8m4_t vlbu_v_u8m4 (const uint8_t *a, size_t vl);
vuint8m8_t vlbu_v_u8m8 (const uint8_t *a, size_t vl);
vuint16m1_t vlbu_v_u16m1 (const uint16_t *a, size_t vl);
vuint16m2_t vlbu_v_u16m2 (const uint16_t *a, size_t vl);
vuint16m4_t vlbu_v_u16m4 (const uint16_t *a, size_t vl);
vuint16m8_t vlbu_v_u16m8 (const uint16_t *a, size_t vl);
vuint32m1_t vlbu_v_u32m1 (const uint32_t *a, size_t vl);
vuint32m2_t vlbu_v_u32m2 (const uint32_t *a, size_t vl);
vuint32m4_t vlbu_v_u32m4 (const uint32_t *a, size_t vl);
vuint32m8_t vlbu_v_u32m8 (const uint32_t *a, size_t vl);
vuint64m1_t vlbu_v_u64m1 (const uint64_t *a, size_t vl);
vuint64m2_t vlbu_v_u64m2 (const uint64_t *a, size_t vl);
vuint64m4_t vlbu_v_u64m4 (const uint64_t *a, size_t vl);
vuint64m8_t vlbu_v_u64m8 (const uint64_t *a, size_t vl);
vuint16m1_t vlhu_v_u16m1 (const uint16_t *a, size_t vl);
vuint16m2_t vlhu_v_u16m2 (const uint16_t *a, size_t vl);
vuint16m4_t vlhu_v_u16m4 (const uint16_t *a, size_t vl);
vuint16m8_t vlhu_v_u16m8 (const uint16_t *a, size_t vl);
vuint32m1_t vlhu_v_u32m1 (const uint32_t *a, size_t vl);
vuint32m2_t vlhu_v_u32m2 (const uint32_t *a, size_t vl);
vuint32m4_t vlhu_v_u32m4 (const uint32_t *a, size_t vl);
vuint32m8_t vlhu_v_u32m8 (const uint32_t *a, size_t vl);
vuint64m1_t vlhu_v_u64m1 (const uint64_t *a, size_t vl);
vuint64m2_t vlhu_v_u64m2 (const uint64_t *a, size_t vl);
vuint64m4_t vlhu_v_u64m4 (const uint64_t *a, size_t vl);
vuint64m8_t vlhu_v_u64m8 (const uint64_t *a, size_t vl);
vuint32m1_t vlwu_v_u32m1 (const uint32_t *a, size_t vl);
vuint32m2_t vlwu_v_u32m2 (const uint32_t *a, size_t vl);
vuint32m4_t vlwu_v_u32m4 (const uint32_t *a, size_t vl);
vuint32m8_t vlwu_v_u32m8 (const uint32_t *a, size_t vl);
vuint64m1_t vlwu_v_u64m1 (const uint64_t *a, size_t vl);
vuint64m2_t vlwu_v_u64m2 (const uint64_t *a, size_t vl);
vuint64m4_t vlwu_v_u64m4 (const uint64_t *a, size_t vl);
vuint64m8_t vlwu_v_u64m8 (const uint64_t *a, size_t vl);

```

```

vint8m1_t vle8_v_i8m1 (const int8_t *base, size_t vl);
vint8m2_t vle8_v_i8m2 (const int8_t *base, size_t vl);
vint8m4_t vle8_v_i8m4 (const int8_t *base, size_t vl);
vint8m8_t vle8_v_i8m8 (const int8_t *base, size_t vl);
vint16m1_t vle16_v_i16m1 (const int16_t *base, size_t vl);
vint16m2_t vle16_v_i16m2 (const int16_t *base, size_t vl);
vint16m4_t vle16_v_i16m4 (const int16_t *base, size_t vl);
vint16m8_t vle16_v_i16m8 (const int16_t *base, size_t vl);
vint32m1_t vle32_v_i32m1 (const int32_t *base, size_t vl);
vint32m2_t vle32_v_i32m2 (const int32_t *base, size_t vl);
vint32m4_t vle32_v_i32m4 (const int32_t *base, size_t vl);
vint32m8_t vle32_v_i32m8 (const int32_t *base, size_t vl);
vint64m1_t vle64_v_i64m1 (const int64_t *base, size_t vl);
vint64m2_t vle64_v_i64m2 (const int64_t *base, size_t vl);
vint64m4_t vle64_v_i64m4 (const int64_t *base, size_t vl);
vint64m8_t vle64_v_i64m8 (const int64_t *base, size_t vl);
vuint8m1_t vle8_v_u8m1 (const uint8_t *base, size_t vl);
vuint8m2_t vle8_v_u8m2 (const uint8_t *base, size_t vl);
vuint8m4_t vle8_v_u8m4 (const uint8_t *base, size_t vl);
vuint8m8_t vle8_v_u8m8 (const uint8_t *base, size_t vl);
vuint16m1_t vle16_v_u16m1 (const uint16_t *base, size_t vl);
vuint16m2_t vle16_v_u16m2 (const uint16_t *base, size_t vl);
vuint16m4_t vle16_v_u16m4 (const uint16_t *base, size_t vl);
vuint16m8_t vle16_v_u16m8 (const uint16_t *base, size_t vl);
vuint32m1_t vle32_v_u32m1 (const uint32_t *base, size_t vl);
vuint32m2_t vle32_v_u32m2 (const uint32_t *base, size_t vl);
vuint32m4_t vle32_v_u32m4 (const uint32_t *base, size_t vl);
vuint32m8_t vle32_v_u32m8 (const uint32_t *base, size_t vl);
vuint64m1_t vle64_v_u64m1 (const uint64_t *base, size_t vl);
vuint64m2_t vle64_v_u64m2 (const uint64_t *base, size_t vl);
vuint64m4_t vle64_v_u64m4 (const uint64_t *base, size_t vl);
vuint64m8_t vle64_v_u64m8 (const uint64_t *base, size_t vl);
vfloat16m1_t vle16_v_f16m1 (const float16_t *base, size_t vl);
vfloat16m2_t vle16_v_f16m2 (const float16_t *base, size_t vl);
vfloat16m4_t vle16_v_f16m4 (const float16_t *base, size_t vl);
vfloat16m8_t vle16_v_f16m8 (const float16_t *base, size_t vl);
vfloat32m1_t vle32_v_f32m1 (const float32_t *base, size_t vl);
vfloat32m2_t vle32_v_f32m2 (const float32_t *base, size_t vl);
vfloat32m4_t vle32_v_f32m4 (const float32_t *base, size_t vl);
vfloat32m8_t vle32_v_f32m8 (const float32_t *base, size_t vl);
vfloat64m1_t vle64_v_f64m1 (const float64_t *base, size_t vl);
vfloat64m2_t vle64_v_f64m2 (const float64_t *base, size_t vl);
vfloat64m4_t vle64_v_f64m4 (const float64_t *base, size_t vl);
vfloat64m8_t vle64_v_f64m8 (const float64_t *base, size_t vl);
// masked functions

```

```

vint8m1_t vlb_v_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    const int8_t *a, size_t vl);
vint8m2_t vlb_v_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    const int8_t *a, size_t vl);
vint8m4_t vlb_v_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    const int8_t *a, size_t vl);
vint8m8_t vlb_v_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    const int8_t *a, size_t vl);
vint16m1_t vlb_v_i16m1_m (vbool16_t mask, vint16m1_t maskedoff,
    const int16_t *a, size_t vl);
vint16m2_t vlb_v_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    const int16_t *a, size_t vl);
vint16m4_t vlb_v_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    const int16_t *a, size_t vl);
vint16m8_t vlb_v_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    const int16_t *a, size_t vl);
vint32m1_t vlb_v_i32m1_m (vbool32_t mask, vint32m1_t maskedoff,
    const int32_t *a, size_t vl);
vint32m2_t vlb_v_i32m2_m (vbool16_t mask, vint32m2_t maskedoff,
    const int32_t *a, size_t vl);
vint32m4_t vlb_v_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    const int32_t *a, size_t vl);
vint32m8_t vlb_v_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    const int32_t *a, size_t vl);
vint64m1_t vlb_v_i64m1_m (vbool64_t mask, vint64m1_t maskedoff,
    const int64_t *a, size_t vl);
vint64m2_t vlb_v_i64m2_m (vbool32_t mask, vint64m2_t maskedoff,
    const int64_t *a, size_t vl);
vint64m4_t vlb_v_i64m4_m (vbool16_t mask, vint64m4_t maskedoff,
    const int64_t *a, size_t vl);
vint64m8_t vlb_v_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    const int64_t *a, size_t vl);
vint16m1_t vlh_v_i16m1_m (vbool16_t mask, vint16m1_t maskedoff,
    const int16_t *a, size_t vl);
vint16m2_t vlh_v_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    const int16_t *a, size_t vl);
vint16m4_t vlh_v_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    const int16_t *a, size_t vl);
vint16m8_t vlh_v_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    const int16_t *a, size_t vl);
vint32m1_t vlh_v_i32m1_m (vbool32_t mask, vint32m1_t maskedoff,
    const int32_t *a, size_t vl);
vint32m2_t vlh_v_i32m2_m (vbool16_t mask, vint32m2_t maskedoff,
    const int32_t *a, size_t vl);
vint32m4_t vlh_v_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    const int32_t *a, size_t vl);

```

```

vint32m8_t vlh_v_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    const int32_t *a, size_t vl);
vint64m1_t vlh_v_i64m1_m (vbool64_t mask, vint64m1_t maskedoff,
    const int64_t *a, size_t vl);
vint64m2_t vlh_v_i64m2_m (vbool32_t mask, vint64m2_t maskedoff,
    const int64_t *a, size_t vl);
vint64m4_t vlh_v_i64m4_m (vbool16_t mask, vint64m4_t maskedoff,
    const int64_t *a, size_t vl);
vint64m8_t vlh_v_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    const int64_t *a, size_t vl);
vint32m1_t vlw_v_i32m1_m (vbool32_t mask, vint32m1_t maskedoff,
    const int32_t *a, size_t vl);
vint32m2_t vlw_v_i32m2_m (vbool16_t mask, vint32m2_t maskedoff,
    const int32_t *a, size_t vl);
vint32m4_t vlw_v_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    const int32_t *a, size_t vl);
vint32m8_t vlw_v_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    const int32_t *a, size_t vl);
vint64m1_t vlw_v_i64m1_m (vbool64_t mask, vint64m1_t maskedoff,
    const int64_t *a, size_t vl);
vint64m2_t vlw_v_i64m2_m (vbool32_t mask, vint64m2_t maskedoff,
    const int64_t *a, size_t vl);
vint64m4_t vlw_v_i64m4_m (vbool16_t mask, vint64m4_t maskedoff,
    const int64_t *a, size_t vl);
vint64m8_t vlw_v_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    const int64_t *a, size_t vl);
vuint8m1_t vlbu_v_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    const uint8_t *a, size_t vl);
vuint8m2_t vlbu_v_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    const uint8_t *a, size_t vl);
vuint8m4_t vlbu_v_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    const uint8_t *a, size_t vl);
vuint8m8_t vlbu_v_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    const uint8_t *a, size_t vl);
vuint16m1_t vlbu_v_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, const uint16_t *a, size_t vl);
vuint16m2_t vlbu_v_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, const uint16_t *a, size_t vl);
vuint16m4_t vlbu_v_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, const uint16_t *a, size_t vl);
vuint16m8_t vlbu_v_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, const uint16_t *a, size_t vl);
vuint32m1_t vlbu_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, const uint32_t *a, size_t vl);
vuint32m2_t vlbu_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, const uint32_t *a, size_t vl);

```

```

vuint32m4_t vlbu_v_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, const uint32_t *a, size_t vl);
vuint32m8_t vlbu_v_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, const uint32_t *a, size_t vl);
vuint64m1_t vlbu_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, const uint64_t *a, size_t vl);
vuint64m2_t vlbu_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, const uint64_t *a, size_t vl);
vuint64m4_t vlbu_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, const uint64_t *a, size_t vl);
vuint64m8_t vlbu_v_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, const uint64_t *a, size_t vl);
vuint16m1_t vlhu_v_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, const uint16_t *a, size_t vl);
vuint16m2_t vlhu_v_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, const uint16_t *a, size_t vl);
vuint16m4_t vlhu_v_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, const uint16_t *a, size_t vl);
vuint16m8_t vlhu_v_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, const uint16_t *a, size_t vl);
vuint32m1_t vlhu_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, const uint32_t *a, size_t vl);
vuint32m2_t vlhu_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, const uint32_t *a, size_t vl);
vuint32m4_t vlhu_v_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, const uint32_t *a, size_t vl);
vuint32m8_t vlhu_v_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, const uint32_t *a, size_t vl);
vuint64m1_t vlhu_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, const uint64_t *a, size_t vl);
vuint64m2_t vlhu_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, const uint64_t *a, size_t vl);
vuint64m4_t vlhu_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, const uint64_t *a, size_t vl);
vuint64m8_t vlhu_v_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, const uint64_t *a, size_t vl);
vuint32m1_t vlwu_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, const uint32_t *a, size_t vl);
vuint32m2_t vlwu_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, const uint32_t *a, size_t vl);
vuint32m4_t vlwu_v_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, const uint32_t *a, size_t vl);
vuint32m8_t vlwu_v_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, const uint32_t *a, size_t vl);
vuint64m1_t vlwu_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, const uint64_t *a, size_t vl);

```

```

vuint64m2_t v1wu_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, const uint64_t *a, size_t vl);
vuint64m4_t v1wu_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, const uint64_t *a, size_t vl);
vuint64m8_t v1wu_v_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, const uint64_t *a, size_t vl);
vint8m1_t vle8_v_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    const int8_t *base, size_t vl);
vint8m2_t vle8_v_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    const int8_t *base, size_t vl);
vint8m4_t vle8_v_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    const int8_t *base, size_t vl);
vint8m8_t vle8_v_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    const int8_t *base, size_t vl);
vint16m1_t vle16_v_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, const int16_t *base, size_t vl);
vint16m2_t vle16_v_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    const int16_t *base, size_t vl);
vint16m4_t vle16_v_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    const int16_t *base, size_t vl);
vint16m8_t vle16_v_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    const int16_t *base, size_t vl);
vint32m1_t vle32_v_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, const int32_t *base, size_t vl);
vint32m2_t vle32_v_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, const int32_t *base, size_t vl);
vint32m4_t vle32_v_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    const int32_t *base, size_t vl);
vint32m8_t vle32_v_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    const int32_t *base, size_t vl);
vint64m1_t vle64_v_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, const int64_t *base, size_t vl);
vint64m2_t vle64_v_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, const int64_t *base, size_t vl);
vint64m4_t vle64_v_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, const int64_t *base, size_t vl);
vint64m8_t vle64_v_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    const int64_t *base, size_t vl);
vuint8m1_t vle8_v_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    const uint8_t *base, size_t vl);
vuint8m2_t vle8_v_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    const uint8_t *base, size_t vl);
vuint8m4_t vle8_v_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    const uint8_t *base, size_t vl);
vuint8m8_t vle8_v_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    const uint8_t *base, size_t vl);

```



```

vuint16m1_t vle16_v_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, const uint16_t *base, size_t vl);
vuint16m2_t vle16_v_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, const uint16_t *base, size_t vl);
vuint16m4_t vle16_v_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, const uint16_t *base, size_t vl);
vuint16m8_t vle16_v_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, const uint16_t *base, size_t vl);
vuint32m1_t vle32_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, const uint32_t *base, size_t vl);
vuint32m2_t vle32_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, const uint32_t *base, size_t vl);
vuint32m4_t vle32_v_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, const uint32_t *base, size_t vl);
vuint32m8_t vle32_v_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, const uint32_t *base, size_t vl);
vuint64m1_t vle64_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, const uint64_t *base, size_t vl);
vuint64m2_t vle64_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, const uint64_t *base, size_t vl);
vuint64m4_t vle64_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, const uint64_t *base, size_t vl);
vuint64m8_t vle64_v_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, const uint64_t *base, size_t vl);
vfloat16m1_t vle16_v_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, const float16_t *base, size_t vl);
vfloat16m2_t vle16_v_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, const float16_t *base, size_t vl);
vfloat16m4_t vle16_v_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, const float16_t *base, size_t vl);
vfloat16m8_t vle16_v_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, const float16_t *base, size_t vl);
vfloat32m1_t vle32_v_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, const float32_t *base, size_t vl);
vfloat32m2_t vle32_v_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, const float32_t *base, size_t vl);
vfloat32m4_t vle32_v_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, const float32_t *base, size_t vl);
vfloat32m8_t vle32_v_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, const float32_t *base, size_t vl);
vfloat64m1_t vle64_v_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, const float64_t *base, size_t vl);
vfloat64m2_t vle64_v_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, const float64_t *base, size_t vl);
vfloat64m4_t vle64_v_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, const float64_t *base, size_t vl);

```

```
vfloat64m8_t vle64_v_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, const float64_t *base, size_t vl);
```

## Vector Unit-Stride Store Functions:

### Prototypes:

```
void vsb_v_i8m1 (int8_t *a, vint8m1_t b, size_t vl);
void vsb_v_i8m2 (int8_t *a, vint8m2_t b, size_t vl);
void vsb_v_i8m4 (int8_t *a, vint8m4_t b, size_t vl);
void vsb_v_i8m8 (int8_t *a, vint8m8_t b, size_t vl);
void vsh_v_i16m1 (int16_t *a, vint16m1_t b, size_t vl);
void vsh_v_i16m2 (int16_t *a, vint16m2_t b, size_t vl);
void vsh_v_i16m4 (int16_t *a, vint16m4_t b, size_t vl);
void vsh_v_i16m8 (int16_t *a, vint16m8_t b, size_t vl);
void vsw_v_i32m1 (int32_t *a, vint32m1_t b, size_t vl);
void vsw_v_i32m2 (int32_t *a, vint32m2_t b, size_t vl);
void vsw_v_i32m4 (int32_t *a, vint32m4_t b, size_t vl);
void vsw_v_i32m8 (int32_t *a, vint32m8_t b, size_t vl);
void vsb_v_u8m1 (uint8_t *a, vuint8m1_t b, size_t vl);
void vsb_v_u8m2 (uint8_t *a, vuint8m2_t b, size_t vl);
void vsb_v_u8m4 (uint8_t *a, vuint8m4_t b, size_t vl);
void vsb_v_u8m8 (uint8_t *a, vuint8m8_t b, size_t vl);
void vsh_v_u16m1 (uint16_t *a, vuint16m1_t b, size_t vl);
void vsh_v_u16m2 (uint16_t *a, vuint16m2_t b, size_t vl);
void vsh_v_u16m4 (uint16_t *a, vuint16m4_t b, size_t vl);
void vsh_v_u16m8 (uint16_t *a, vuint16m8_t b, size_t vl);
void vsw_v_u32m1 (uint32_t *a, vuint32m1_t b, size_t vl);
void vsw_v_u32m2 (uint32_t *a, vuint32m2_t b, size_t vl);
void vsw_v_u32m4 (uint32_t *a, vuint32m4_t b, size_t vl);
void vsw_v_u32m8 (uint32_t *a, vuint32m8_t b, size_t vl);
void vse8_v_i8m1 (int8_t *base, vint8m1_t value, size_t vl);
void vse8_v_i8m2 (int8_t *base, vint8m2_t value, size_t vl);
void vse8_v_i8m4 (int8_t *base, vint8m4_t value, size_t vl);
void vse8_v_i8m8 (int8_t *base, vint8m8_t value, size_t vl);
void vse16_v_i16m1 (int16_t *base, vint16m1_t value, size_t vl);
void vse16_v_i16m2 (int16_t *base, vint16m2_t value, size_t vl);
void vse16_v_i16m4 (int16_t *base, vint16m4_t value, size_t vl);
void vse16_v_i16m8 (int16_t *base, vint16m8_t value, size_t vl);
void vse32_v_i32m1 (int32_t *base, vint32m1_t value, size_t vl);
void vse32_v_i32m2 (int32_t *base, vint32m2_t value, size_t vl);
void vse32_v_i32m4 (int32_t *base, vint32m4_t value, size_t vl);
void vse32_v_i32m8 (int32_t *base, vint32m8_t value, size_t vl);
void vse64_v_i64m1 (int64_t *base, vint64m1_t value, size_t vl);
void vse64_v_i64m2 (int64_t *base, vint64m2_t value, size_t vl);
void vse64_v_i64m4 (int64_t *base, vint64m4_t value, size_t vl);
```

```

void vse64_v_i64m8 (int64_t *base, vint64m8_t value, size_t vl);
void vse8_v_u8m1 (uint8_t *base, vuint8m1_t value, size_t vl);
void vse8_v_u8m2 (uint8_t *base, vuint8m2_t value, size_t vl);
void vse8_v_u8m4 (uint8_t *base, vuint8m4_t value, size_t vl);
void vse8_v_u8m8 (uint8_t *base, vuint8m8_t value, size_t vl);
void vse16_v_u16m1 (uint16_t *base, vuint16m1_t value, size_t
vl);
void vse16_v_u16m2 (uint16_t *base, vuint16m2_t value, size_t
vl);
void vse16_v_u16m4 (uint16_t *base, vuint16m4_t value, size_t
vl);
void vse16_v_u16m8 (uint16_t *base, vuint16m8_t value, size_t
vl);
void vse32_v_u32m1 (uint32_t *base, vuint32m1_t value, size_t
vl);
void vse32_v_u32m2 (uint32_t *base, vuint32m2_t value, size_t
vl);
void vse32_v_u32m4 (uint32_t *base, vuint32m4_t value, size_t
vl);
void vse32_v_u32m8 (uint32_t *base, vuint32m8_t value, size_t
vl);
void vse64_v_u64m1 (uint64_t *base, vuint64m1_t value, size_t
vl);
void vse64_v_u64m2 (uint64_t *base, vuint64m2_t value, size_t
vl);
void vse64_v_u64m4 (uint64_t *base, vuint64m4_t value, size_t
vl);
void vse64_v_u64m8 (uint64_t *base, vuint64m8_t value, size_t
vl);
void vse16_v_f16m1 (float16_t *base, vfloat16m1_t value, size_t
vl);
void vse16_v_f16m2 (float16_t *base, vfloat16m2_t value, size_t
vl);
void vse16_v_f16m4 (float16_t *base, vfloat16m4_t value, size_t
vl);
void vse16_v_f16m8 (float16_t *base, vfloat16m8_t value, size_t
vl);
void vse32_v_f32m1 (float32_t *base, vfloat32m1_t value, size_t
vl);
void vse32_v_f32m2 (float32_t *base, vfloat32m2_t value, size_t
vl);
void vse32_v_f32m4 (float32_t *base, vfloat32m4_t value, size_t
vl);
void vse32_v_f32m8 (float32_t *base, vfloat32m8_t value, size_t
vl);

```

```

void vse64_v_f64m1 (float64_t *base, vfloat64m1_t value, size_t
    vl);
void vse64_v_f64m2 (float64_t *base, vfloat64m2_t value, size_t
    vl);
void vse64_v_f64m4 (float64_t *base, vfloat64m4_t value, size_t
    vl);
void vse64_v_f64m8 (float64_t *base, vfloat64m8_t value, size_t
    vl);
// masked functions
void vsb_v_i8m1_m (vbool8_t mask, int8_t *a, vint8m1_t b, size_t
    vl);
void vsb_v_i8m2_m (vbool4_t mask, int8_t *a, vint8m2_t b, size_t
    vl);
void vsb_v_i8m4_m (vbool2_t mask, int8_t *a, vint8m4_t b, size_t
    vl);
void vsb_v_i8m8_m (vbool1_t mask, int8_t *a, vint8m8_t b, size_t
    vl);
void vsh_v_i16m1_m (vbool16_t mask, int16_t *a, vint16m1_t b,
    size_t vl);
void vsh_v_i16m2_m (vbool8_t mask, int16_t *a, vint16m2_t b,
    size_t vl);
void vsh_v_i16m4_m (vbool4_t mask, int16_t *a, vint16m4_t b,
    size_t vl);
void vsh_v_i16m8_m (vbool2_t mask, int16_t *a, vint16m8_t b,
    size_t vl);
void vsw_v_i32m1_m (vbool32_t mask, int32_t *a, vint32m1_t b,
    size_t vl);
void vsw_v_i32m2_m (vbool16_t mask, int32_t *a, vint32m2_t b,
    size_t vl);
void vsw_v_i32m4_m (vbool8_t mask, int32_t *a, vint32m4_t b,
    size_t vl);
void vsw_v_i32m8_m (vbool4_t mask, int32_t *a, vint32m8_t b,
    size_t vl);
void vsb_v_u8m1_m (vbool8_t mask, uint8_t *a, vuint8m1_t b,
    size_t vl);
void vsb_v_u8m2_m (vbool4_t mask, uint8_t *a, vuint8m2_t b,
    size_t vl);
void vsb_v_u8m4_m (vbool2_t mask, uint8_t *a, vuint8m4_t b,
    size_t vl);
void vsb_v_u8m8_m (vbool1_t mask, uint8_t *a, vuint8m8_t b,
    size_t vl);
void vsh_v_u16m1_m (vbool16_t mask, uint16_t *a, vuint16m1_t b,
    size_t vl);
void vsh_v_u16m2_m (vbool8_t mask, uint16_t *a, vuint16m2_t b,
    size_t vl);

```

```

void vsh_v_u16m4_m (vbool4_t mask, uint16_t *a, vuint16m4_t b,
    size_t vl);
void vsh_v_u16m8_m (vbool2_t mask, uint16_t *a, vuint16m8_t b,
    size_t vl);
void vsw_v_u32m1_m (vbool32_t mask, uint32_t *a, vuint32m1_t b,
    size_t vl);
void vsw_v_u32m2_m (vbool16_t mask, uint32_t *a, vuint32m2_t b,
    size_t vl);
void vsw_v_u32m4_m (vbool8_t mask, uint32_t *a, vuint32m4_t b,
    size_t vl);
void vsw_v_u32m8_m (vbool4_t mask, uint32_t *a, vuint32m8_t b,
    size_t vl);
void vse8_v_i8m1_m (vbool8_t mask, int8_t *base, vint8m1_t
    value, size_t vl);
void vse8_v_i8m2_m (vbool4_t mask, int8_t *base, vint8m2_t
    value, size_t vl);
void vse8_v_i8m4_m (vbool2_t mask, int8_t *base, vint8m4_t
    value, size_t vl);
void vse8_v_i8m8_m (vbool1_t mask, int8_t *base, vint8m8_t
    value, size_t vl);
void vse16_v_i16m1_m (vbool16_t mask, int16_t *base, vint16m1_t
    value, size_t vl);
void vse16_v_i16m2_m (vbool8_t mask, int16_t *base, vint16m2_t
    value, size_t vl);
void vse16_v_i16m4_m (vbool4_t mask, int16_t *base, vint16m4_t
    value, size_t vl);
void vse16_v_i16m8_m (vbool2_t mask, int16_t *base, vint16m8_t
    value, size_t vl);
void vse32_v_i32m1_m (vbool32_t mask, int32_t *base, vint32m1_t
    value, size_t vl);
void vse32_v_i32m2_m (vbool16_t mask, int32_t *base, vint32m2_t
    value, size_t vl);
void vse32_v_i32m4_m (vbool8_t mask, int32_t *base, vint32m4_t
    value, size_t vl);
void vse32_v_i32m8_m (vbool4_t mask, int32_t *base, vint32m8_t
    value, size_t vl);
void vse64_v_i64m1_m (vbool64_t mask, int64_t *base, vint64m1_t
    value, size_t vl);
void vse64_v_i64m2_m (vbool32_t mask, int64_t *base, vint64m2_t
    value, size_t vl);
void vse64_v_i64m4_m (vbool16_t mask, int64_t *base, vint64m4_t
    value, size_t vl);
void vse64_v_i64m8_m (vbool8_t mask, int64_t *base, vint64m8_t
    value, size_t vl);
void vse8_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    value, size_t vl);

```

```

void vse8_v_u8m2_m (vbool4_t mask, uint8_t *base, vuint8m2_t
    value, size_t vl);
void vse8_v_u8m4_m (vbool2_t mask, uint8_t *base, vuint8m4_t
    value, size_t vl);
void vse8_v_u8m8_m (vbool1_t mask, uint8_t *base, vuint8m8_t
    value, size_t vl);
void vse16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t value, size_t vl);
void vse16_v_u16m2_m (vbool8_t mask, uint16_t *base, vuint16m2_t
    value, size_t vl);
void vse16_v_u16m4_m (vbool4_t mask, uint16_t *base, vuint16m4_t
    value, size_t vl);
void vse16_v_u16m8_m (vbool2_t mask, uint16_t *base, vuint16m8_t
    value, size_t vl);
void vse32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t value, size_t vl);
void vse32_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t value, size_t vl);
void vse32_v_u32m4_m (vbool8_t mask, uint32_t *base, vuint32m4_t
    value, size_t vl);
void vse32_v_u32m8_m (vbool4_t mask, uint32_t *base, vuint32m8_t
    value, size_t vl);
void vse64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t value, size_t vl);
void vse64_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t value, size_t vl);
void vse64_v_u64m4_m (vbool16_t mask, uint64_t *base,
    vuint64m4_t value, size_t vl);
void vse64_v_u64m8_m (vbool8_t mask, uint64_t *base, vuint64m8_t
    value, size_t vl);
void vse16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vfloat16m1_t value, size_t vl);
void vse16_v_f16m2_m (vbool8_t mask, float16_t *base,
    vfloat16m2_t value, size_t vl);
void vse16_v_f16m4_m (vbool4_t mask, float16_t *base,
    vfloat16m4_t value, size_t vl);
void vse16_v_f16m8_m (vbool2_t mask, float16_t *base,
    vfloat16m8_t value, size_t vl);
void vse32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vfloat32m1_t value, size_t vl);
void vse32_v_f32m2_m (vbool16_t mask, float32_t *base,
    vfloat32m2_t value, size_t vl);
void vse32_v_f32m4_m (vbool8_t mask, float32_t *base,
    vfloat32m4_t value, size_t vl);
void vse32_v_f32m8_m (vbool4_t mask, float32_t *base,
    vfloat32m8_t value, size_t vl);

```

```

void vse64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vfloat64m1_t value, size_t vl);
void vse64_v_f64m2_m (vbool32_t mask, float64_t *base,
    vfloat64m2_t value, size_t vl);
void vse64_v_f64m4_m (vbool16_t mask, float64_t *base,
    vfloat64m4_t value, size_t vl);
void vse64_v_f64m8_m (vbool8_t mask, float64_t *base,
    vfloat64m8_t value, size_t vl);

```

### Vector Strided Load Functions:

#### Prototypes:

```

vint8m1_t vlsb_v_i8m1 (const int8_t *a, size_t stride, size_t
    vl);
vint8m2_t vlsb_v_i8m2 (const int8_t *a, size_t stride, size_t
    vl);
vint8m4_t vlsb_v_i8m4 (const int8_t *a, size_t stride, size_t
    vl);
vint8m8_t vlsb_v_i8m8 (const int8_t *a, size_t stride, size_t
    vl);
vint16m1_t vlsb_v_i16m1 (const int16_t *a, size_t stride, size_t
    vl);
vint16m2_t vlsb_v_i16m2 (const int16_t *a, size_t stride, size_t
    vl);
vint16m4_t vlsb_v_i16m4 (const int16_t *a, size_t stride, size_t
    vl);
vint16m8_t vlsb_v_i16m8 (const int16_t *a, size_t stride, size_t
    vl);
vint32m1_t vlsb_v_i32m1 (const int32_t *a, size_t stride, size_t
    vl);
vint32m2_t vlsb_v_i32m2 (const int32_t *a, size_t stride, size_t
    vl);
vint32m4_t vlsb_v_i32m4 (const int32_t *a, size_t stride, size_t
    vl);
vint32m8_t vlsb_v_i32m8 (const int32_t *a, size_t stride, size_t
    vl);
vint64m1_t vlsb_v_i64m1 (const int64_t *a, size_t stride, size_t
    vl);
vint64m2_t vlsb_v_i64m2 (const int64_t *a, size_t stride, size_t
    vl);
vint64m4_t vlsb_v_i64m4 (const int64_t *a, size_t stride, size_t
    vl);
vint64m8_t vlsb_v_i64m8 (const int64_t *a, size_t stride, size_t
    vl);

```

```

vint16m1_t vlsh_v_i16m1 (const int16_t *a, size_t stride, size_t
    vl);
vint16m2_t vlsh_v_i16m2 (const int16_t *a, size_t stride, size_t
    vl);
vint16m4_t vlsh_v_i16m4 (const int16_t *a, size_t stride, size_t
    vl);
vint16m8_t vlsh_v_i16m8 (const int16_t *a, size_t stride, size_t
    vl);
vint32m1_t vlsh_v_i32m1 (const int32_t *a, size_t stride, size_t
    vl);
vint32m2_t vlsh_v_i32m2 (const int32_t *a, size_t stride, size_t
    vl);
vint32m4_t vlsh_v_i32m4 (const int32_t *a, size_t stride, size_t
    vl);
vint32m8_t vlsh_v_i32m8 (const int32_t *a, size_t stride, size_t
    vl);
vint64m1_t vlsh_v_i64m1 (const int64_t *a, size_t stride, size_t
    vl);
vint64m2_t vlsh_v_i64m2 (const int64_t *a, size_t stride, size_t
    vl);
vint64m4_t vlsh_v_i64m4 (const int64_t *a, size_t stride, size_t
    vl);
vint64m8_t vlsh_v_i64m8 (const int64_t *a, size_t stride, size_t
    vl);
vint32m1_t vlsw_v_i32m1 (const int32_t *a, size_t stride, size_t
    vl);
vint32m2_t vlsw_v_i32m2 (const int32_t *a, size_t stride, size_t
    vl);
vint32m4_t vlsw_v_i32m4 (const int32_t *a, size_t stride, size_t
    vl);
vint32m8_t vlsw_v_i32m8 (const int32_t *a, size_t stride, size_t
    vl);
vint64m1_t vlsw_v_i64m1 (const int64_t *a, size_t stride, size_t
    vl);
vint64m2_t vlsw_v_i64m2 (const int64_t *a, size_t stride, size_t
    vl);
vint64m4_t vlsw_v_i64m4 (const int64_t *a, size_t stride, size_t
    vl);
vint64m8_t vlsw_v_i64m8 (const int64_t *a, size_t stride, size_t
    vl);
vuint8m1_t vlsbu_v_u8m1 (const uint8_t *a, size_t stride, size_t
    vl);
vuint8m2_t vlsbu_v_u8m2 (const uint8_t *a, size_t stride, size_t
    vl);
vuint8m4_t vlsbu_v_u8m4 (const uint8_t *a, size_t stride, size_t
    vl);

```



```

vuint8m8_t vlsbu_v_u8m8 (const uint8_t *a, size_t stride, size_t
    vl);
vuint16m1_t vlsbu_v_u16m1 (const uint16_t *a, size_t stride,
    size_t vl);
vuint16m2_t vlsbu_v_u16m2 (const uint16_t *a, size_t stride,
    size_t vl);
vuint16m4_t vlsbu_v_u16m4 (const uint16_t *a, size_t stride,
    size_t vl);
vuint16m8_t vlsbu_v_u16m8 (const uint16_t *a, size_t stride,
    size_t vl);
vuint32m1_t vlsbu_v_u32m1 (const uint32_t *a, size_t stride,
    size_t vl);
vuint32m2_t vlsbu_v_u32m2 (const uint32_t *a, size_t stride,
    size_t vl);
vuint32m4_t vlsbu_v_u32m4 (const uint32_t *a, size_t stride,
    size_t vl);
vuint32m8_t vlsbu_v_u32m8 (const uint32_t *a, size_t stride,
    size_t vl);
vuint64m1_t vlsbu_v_u64m1 (const uint64_t *a, size_t stride,
    size_t vl);
vuint64m2_t vlsbu_v_u64m2 (const uint64_t *a, size_t stride,
    size_t vl);
vuint64m4_t vlsbu_v_u64m4 (const uint64_t *a, size_t stride,
    size_t vl);
vuint64m8_t vlsbu_v_u64m8 (const uint64_t *a, size_t stride,
    size_t vl);
vuint16m1_t vlshu_v_u16m1 (const uint16_t *a, size_t stride,
    size_t vl);
vuint16m2_t vlshu_v_u16m2 (const uint16_t *a, size_t stride,
    size_t vl);
vuint16m4_t vlshu_v_u16m4 (const uint16_t *a, size_t stride,
    size_t vl);
vuint16m8_t vlshu_v_u16m8 (const uint16_t *a, size_t stride,
    size_t vl);
vuint32m1_t vlshu_v_u32m1 (const uint32_t *a, size_t stride,
    size_t vl);
vuint32m2_t vlshu_v_u32m2 (const uint32_t *a, size_t stride,
    size_t vl);
vuint32m4_t vlshu_v_u32m4 (const uint32_t *a, size_t stride,
    size_t vl);
vuint32m8_t vlshu_v_u32m8 (const uint32_t *a, size_t stride,
    size_t vl);
vuint64m1_t vlshu_v_u64m1 (const uint64_t *a, size_t stride,
    size_t vl);
vuint64m2_t vlshu_v_u64m2 (const uint64_t *a, size_t stride,
    size_t vl);

```

```

vuint64m4_t vlshu_v_u64m4 (const uint64_t *a, size_t stride,
    size_t vl);
vuint64m8_t vlshu_v_u64m8 (const uint64_t *a, size_t stride,
    size_t vl);
vuint32m1_t vlswu_v_u32m1 (const uint32_t *a, size_t stride,
    size_t vl);
vuint32m2_t vlswu_v_u32m2 (const uint32_t *a, size_t stride,
    size_t vl);
vuint32m4_t vlswu_v_u32m4 (const uint32_t *a, size_t stride,
    size_t vl);
vuint32m8_t vlswu_v_u32m8 (const uint32_t *a, size_t stride,
    size_t vl);
vuint64m1_t vlswu_v_u64m1 (const uint64_t *a, size_t stride,
    size_t vl);
vuint64m2_t vlswu_v_u64m2 (const uint64_t *a, size_t stride,
    size_t vl);
vuint64m4_t vlswu_v_u64m4 (const uint64_t *a, size_t stride,
    size_t vl);
vuint64m8_t vlswu_v_u64m8 (const uint64_t *a, size_t stride,
    size_t vl);
vint8m1_t vlse8_v_i8m1 (const int8_t *base, ptrdiff_t bstride,
    size_t vl);
vint8m2_t vlse8_v_i8m2 (const int8_t *base, ptrdiff_t bstride,
    size_t vl);
vint8m4_t vlse8_v_i8m4 (const int8_t *base, ptrdiff_t bstride,
    size_t vl);
vint8m8_t vlse8_v_i8m8 (const int8_t *base, ptrdiff_t bstride,
    size_t vl);
vint16m1_t vlse16_v_i16m1 (const int16_t *base, ptrdiff_t
    bstride, size_t vl);
vint16m2_t vlse16_v_i16m2 (const int16_t *base, ptrdiff_t
    bstride, size_t vl);
vint16m4_t vlse16_v_i16m4 (const int16_t *base, ptrdiff_t
    bstride, size_t vl);
vint16m8_t vlse16_v_i16m8 (const int16_t *base, ptrdiff_t
    bstride, size_t vl);
vint32m1_t vlse32_v_i32m1 (const int32_t *base, ptrdiff_t
    bstride, size_t vl);
vint32m2_t vlse32_v_i32m2 (const int32_t *base, ptrdiff_t
    bstride, size_t vl);
vint32m4_t vlse32_v_i32m4 (const int32_t *base, ptrdiff_t
    bstride, size_t vl);
vint32m8_t vlse32_v_i32m8 (const int32_t *base, ptrdiff_t
    bstride, size_t vl);
vint64m1_t vlse64_v_i64m1 (const int64_t *base, ptrdiff_t
    bstride, size_t vl);

```

```

vint64m2_t vlse64_v_i64m2 (const int64_t *base, ptrdiff_t
    bstride, size_t vl);
vint64m4_t vlse64_v_i64m4 (const int64_t *base, ptrdiff_t
    bstride, size_t vl);
vint64m8_t vlse64_v_i64m8 (const int64_t *base, ptrdiff_t
    bstride, size_t vl);
vuint8m1_t vlse8_v_u8m1 (const uint8_t *base, ptrdiff_t bstride,
    size_t vl);
vuint8m2_t vlse8_v_u8m2 (const uint8_t *base, ptrdiff_t bstride,
    size_t vl);
vuint8m4_t vlse8_v_u8m4 (const uint8_t *base, ptrdiff_t bstride,
    size_t vl);
vuint8m8_t vlse8_v_u8m8 (const uint8_t *base, ptrdiff_t bstride,
    size_t vl);
vuint16m1_t vlse16_v_u16m1 (const uint16_t *base, ptrdiff_t
    bstride, size_t vl);
vuint16m2_t vlse16_v_u16m2 (const uint16_t *base, ptrdiff_t
    bstride, size_t vl);
vuint16m4_t vlse16_v_u16m4 (const uint16_t *base, ptrdiff_t
    bstride, size_t vl);
vuint16m8_t vlse16_v_u16m8 (const uint16_t *base, ptrdiff_t
    bstride, size_t vl);
vuint32m1_t vlse32_v_u32m1 (const uint32_t *base, ptrdiff_t
    bstride, size_t vl);
vuint32m2_t vlse32_v_u32m2 (const uint32_t *base, ptrdiff_t
    bstride, size_t vl);
vuint32m4_t vlse32_v_u32m4 (const uint32_t *base, ptrdiff_t
    bstride, size_t vl);
vuint32m8_t vlse32_v_u32m8 (const uint32_t *base, ptrdiff_t
    bstride, size_t vl);
vuint64m1_t vlse64_v_u64m1 (const uint64_t *base, ptrdiff_t
    bstride, size_t vl);
vuint64m2_t vlse64_v_u64m2 (const uint64_t *base, ptrdiff_t
    bstride, size_t vl);
vuint64m4_t vlse64_v_u64m4 (const uint64_t *base, ptrdiff_t
    bstride, size_t vl);
vuint64m8_t vlse64_v_u64m8 (const uint64_t *base, ptrdiff_t
    bstride, size_t vl);
vfloat16m1_t vlse16_v_f16m1 (const float16_t *base, ptrdiff_t
    bstride, size_t vl);
vfloat16m2_t vlse16_v_f16m2 (const float16_t *base, ptrdiff_t
    bstride, size_t vl);
vfloat16m4_t vlse16_v_f16m4 (const float16_t *base, ptrdiff_t
    bstride, size_t vl);
vfloat16m8_t vlse16_v_f16m8 (const float16_t *base, ptrdiff_t
    bstride, size_t vl);

```

```

vfloat32m1_t vlse32_v_f32m1 (const float32_t *base, ptrdiff_t
    bstride, size_t vl);
vfloat32m2_t vlse32_v_f32m2 (const float32_t *base, ptrdiff_t
    bstride, size_t vl);
vfloat32m4_t vlse32_v_f32m4 (const float32_t *base, ptrdiff_t
    bstride, size_t vl);
vfloat32m8_t vlse32_v_f32m8 (const float32_t *base, ptrdiff_t
    bstride, size_t vl);
vfloat64m1_t vlse64_v_f64m1 (const float64_t *base, ptrdiff_t
    bstride, size_t vl);
vfloat64m2_t vlse64_v_f64m2 (const float64_t *base, ptrdiff_t
    bstride, size_t vl);
vfloat64m4_t vlse64_v_f64m4 (const float64_t *base, ptrdiff_t
    bstride, size_t vl);
vfloat64m8_t vlse64_v_f64m8 (const float64_t *base, ptrdiff_t
    bstride, size_t vl);
// masked functions
vint8m1_t vlse8_v_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    const int8_t *a, size_t stride, size_t vl);
vint8m2_t vlse8_v_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    const int8_t *a, size_t stride, size_t vl);
vint8m4_t vlse8_v_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    const int8_t *a, size_t stride, size_t vl);
vint8m8_t vlse8_v_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    const int8_t *a, size_t stride, size_t vl);
vint16m1_t vlse16_v_i16m1_m (vbool16_t mask, vint16m1_t maskedoff,
    const int16_t *a, size_t stride, size_t vl);
vint16m2_t vlse16_v_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    const int16_t *a, size_t stride, size_t vl);
vint16m4_t vlse16_v_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    const int16_t *a, size_t stride, size_t vl);
vint16m8_t vlse16_v_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    const int16_t *a, size_t stride, size_t vl);
vint32m1_t vlse32_v_i32m1_m (vbool32_t mask, vint32m1_t maskedoff,
    const int32_t *a, size_t stride, size_t vl);
vint32m2_t vlse32_v_i32m2_m (vbool16_t mask, vint32m2_t maskedoff,
    const int32_t *a, size_t stride, size_t vl);
vint32m4_t vlse32_v_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    const int32_t *a, size_t stride, size_t vl);
vint32m8_t vlse32_v_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    const int32_t *a, size_t stride, size_t vl);
vint64m1_t vlse64_v_i64m1_m (vbool64_t mask, vint64m1_t maskedoff,
    const int64_t *a, size_t stride, size_t vl);
vint64m2_t vlse64_v_i64m2_m (vbool32_t mask, vint64m2_t maskedoff,
    const int64_t *a, size_t stride, size_t vl);

```

```

vint64m4_t vlsb_v_i64m4_m (vbool16_t mask, vint64m4_t maskedoff,
    const int64_t *a, size_t stride, size_t vl);
vint64m8_t vlsb_v_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    const int64_t *a, size_t stride, size_t vl);
vint16m1_t vlsh_v_i16m1_m (vbool16_t mask, vint16m1_t maskedoff,
    const int16_t *a, size_t stride, size_t vl);
vint16m2_t vlsh_v_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    const int16_t *a, size_t stride, size_t vl);
vint16m4_t vlsh_v_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    const int16_t *a, size_t stride, size_t vl);
vint16m8_t vlsh_v_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    const int16_t *a, size_t stride, size_t vl);
vint32m1_t vlsh_v_i32m1_m (vbool32_t mask, vint32m1_t maskedoff,
    const int32_t *a, size_t stride, size_t vl);
vint32m2_t vlsh_v_i32m2_m (vbool16_t mask, vint32m2_t maskedoff,
    const int32_t *a, size_t stride, size_t vl);
vint32m4_t vlsh_v_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    const int32_t *a, size_t stride, size_t vl);
vint32m8_t vlsh_v_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    const int32_t *a, size_t stride, size_t vl);
vint64m1_t vlsh_v_i64m1_m (vbool64_t mask, vint64m1_t maskedoff,
    const int64_t *a, size_t stride, size_t vl);
vint64m2_t vlsh_v_i64m2_m (vbool32_t mask, vint64m2_t maskedoff,
    const int64_t *a, size_t stride, size_t vl);
vint64m4_t vlsh_v_i64m4_m (vbool16_t mask, vint64m4_t maskedoff,
    const int64_t *a, size_t stride, size_t vl);
vint64m8_t vlsh_v_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    const int64_t *a, size_t stride, size_t vl);
vint32m1_t vlsb_v_i32m1_m (vbool32_t mask, vint32m1_t maskedoff,
    const int32_t *a, size_t stride, size_t vl);
vint32m2_t vlsb_v_i32m2_m (vbool16_t mask, vint32m2_t maskedoff,
    const int32_t *a, size_t stride, size_t vl);
vint32m4_t vlsb_v_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    const int32_t *a, size_t stride, size_t vl);
vint32m8_t vlsb_v_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    const int32_t *a, size_t stride, size_t vl);
vint64m1_t vlsb_v_i64m1_m (vbool64_t mask, vint64m1_t maskedoff,
    const int64_t *a, size_t stride, size_t vl);
vint64m2_t vlsb_v_i64m2_m (vbool32_t mask, vint64m2_t maskedoff,
    const int64_t *a, size_t stride, size_t vl);
vint64m4_t vlsb_v_i64m4_m (vbool16_t mask, vint64m4_t maskedoff,
    const int64_t *a, size_t stride, size_t vl);
vint64m8_t vlsb_v_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    const int64_t *a, size_t stride, size_t vl);
vuint8m1_t vlsbu_v_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    const uint8_t *a, size_t stride, size_t vl);

```

```

vuint8m2_t vlsbu_v_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    const uint8_t *a, size_t stride, size_t vl);
vuint8m4_t vlsbu_v_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    const uint8_t *a, size_t stride, size_t vl);
vuint8m8_t vlsbu_v_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    const uint8_t *a, size_t stride, size_t vl);
vuint16m1_t vlsbu_v_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, const uint16_t *a, size_t stride, size_t vl);
vuint16m2_t vlsbu_v_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, const uint16_t *a, size_t stride, size_t vl);
vuint16m4_t vlsbu_v_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, const uint16_t *a, size_t stride, size_t vl);
vuint16m8_t vlsbu_v_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, const uint16_t *a, size_t stride, size_t vl);
vuint32m1_t vlsbu_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, const uint32_t *a, size_t stride, size_t vl);
vuint32m2_t vlsbu_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, const uint32_t *a, size_t stride, size_t vl);
vuint32m4_t vlsbu_v_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, const uint32_t *a, size_t stride, size_t vl);
vuint32m8_t vlsbu_v_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, const uint32_t *a, size_t stride, size_t vl);
vuint64m1_t vlsbu_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, const uint64_t *a, size_t stride, size_t vl);
vuint64m2_t vlsbu_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, const uint64_t *a, size_t stride, size_t vl);
vuint64m4_t vlsbu_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, const uint64_t *a, size_t stride, size_t vl);
vuint64m8_t vlsbu_v_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, const uint64_t *a, size_t stride, size_t vl);
vuint16m1_t vlshu_v_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, const uint16_t *a, size_t stride, size_t vl);
vuint16m2_t vlshu_v_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, const uint16_t *a, size_t stride, size_t vl);
vuint16m4_t vlshu_v_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, const uint16_t *a, size_t stride, size_t vl);
vuint16m8_t vlshu_v_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, const uint16_t *a, size_t stride, size_t vl);
vuint32m1_t vlshu_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, const uint32_t *a, size_t stride, size_t vl);
vuint32m2_t vlshu_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, const uint32_t *a, size_t stride, size_t vl);
vuint32m4_t vlshu_v_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, const uint32_t *a, size_t stride, size_t vl);
vuint32m8_t vlshu_v_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, const uint32_t *a, size_t stride, size_t vl);

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```

vuint64m1_t vlshu_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, const uint64_t *a, size_t stride, size_t vl);
vuint64m2_t vlshu_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, const uint64_t *a, size_t stride, size_t vl);
vuint64m4_t vlshu_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, const uint64_t *a, size_t stride, size_t vl);
vuint64m8_t vlshu_v_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, const uint64_t *a, size_t stride, size_t vl);
vuint32m1_t vlswu_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, const uint32_t *a, size_t stride, size_t vl);
vuint32m2_t vlswu_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, const uint32_t *a, size_t stride, size_t vl);
vuint32m4_t vlswu_v_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, const uint32_t *a, size_t stride, size_t vl);
vuint32m8_t vlswu_v_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, const uint32_t *a, size_t stride, size_t vl);
vuint64m1_t vlswu_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, const uint64_t *a, size_t stride, size_t vl);
vuint64m2_t vlswu_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, const uint64_t *a, size_t stride, size_t vl);
vuint64m4_t vlswu_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, const uint64_t *a, size_t stride, size_t vl);
vuint64m8_t vlswu_v_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, const uint64_t *a, size_t stride, size_t vl);
vint8m1_t vlse8_v_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    const int8_t *base, ptrdiff_t bstride, size_t vl);
vint8m2_t vlse8_v_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    const int8_t *base, ptrdiff_t bstride, size_t vl);
vint8m4_t vlse8_v_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    const int8_t *base, ptrdiff_t bstride, size_t vl);
vint8m8_t vlse8_v_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    const int8_t *base, ptrdiff_t bstride, size_t vl);
vint16m1_t vlse16_v_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, const int16_t *base, ptrdiff_t bstride, size_t
    vl);
vint16m2_t vlse16_v_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, const int16_t *base, ptrdiff_t bstride, size_t
    vl);
vint16m4_t vlse16_v_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, const int16_t *base, ptrdiff_t bstride, size_t
    vl);
vint16m8_t vlse16_v_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, const int16_t *base, ptrdiff_t bstride, size_t
    vl);
vint32m1_t vlse32_v_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, const int32_t *base, ptrdiff_t bstride, size_t

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```

    vl);
vint32m2_t vlse32_v_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, const int32_t *base, ptrdiff_t bstride, size_t
    vl);
vint32m4_t vlse32_v_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, const int32_t *base, ptrdiff_t bstride, size_t
    vl);
vint32m8_t vlse32_v_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, const int32_t *base, ptrdiff_t bstride, size_t
    vl);
vint64m1_t vlse64_v_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, const int64_t *base, ptrdiff_t bstride, size_t
    vl);
vint64m2_t vlse64_v_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, const int64_t *base, ptrdiff_t bstride, size_t
    vl);
vint64m4_t vlse64_v_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, const int64_t *base, ptrdiff_t bstride, size_t
    vl);
vint64m8_t vlse64_v_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, const int64_t *base, ptrdiff_t bstride, size_t
    vl);
vuint8m1_t vlse8_v_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    const uint8_t *base, ptrdiff_t bstride, size_t vl);
vuint8m2_t vlse8_v_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    const uint8_t *base, ptrdiff_t bstride, size_t vl);
vuint8m4_t vlse8_v_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    const uint8_t *base, ptrdiff_t bstride, size_t vl);
vuint8m8_t vlse8_v_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    const uint8_t *base, ptrdiff_t bstride, size_t vl);
vuint16m1_t vlse16_v_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, const uint16_t *base, ptrdiff_t bstride, size_t
    vl);
vuint16m2_t vlse16_v_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, const uint16_t *base, ptrdiff_t bstride, size_t
    vl);
vuint16m4_t vlse16_v_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, const uint16_t *base, ptrdiff_t bstride, size_t
    vl);
vuint16m8_t vlse16_v_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, const uint16_t *base, ptrdiff_t bstride, size_t
    vl);
vuint32m1_t vlse32_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, const uint32_t *base, ptrdiff_t bstride, size_t
    vl);

```



```

vuint32m2_t vlse32_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, const uint32_t *base, ptrdiff_t bstride, size_t
    vl);
vuint32m4_t vlse32_v_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, const uint32_t *base, ptrdiff_t bstride, size_t
    vl);
vuint32m8_t vlse32_v_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, const uint32_t *base, ptrdiff_t bstride, size_t
    vl);
vuint64m1_t vlse64_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, const uint64_t *base, ptrdiff_t bstride, size_t
    vl);
vuint64m2_t vlse64_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, const uint64_t *base, ptrdiff_t bstride, size_t
    vl);
vuint64m4_t vlse64_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, const uint64_t *base, ptrdiff_t bstride, size_t
    vl);
vuint64m8_t vlse64_v_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, const uint64_t *base, ptrdiff_t bstride, size_t
    vl);
vfloat16m1_t vlse16_v_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, const float16_t *base, ptrdiff_t bstride, size_t
    vl);
vfloat16m2_t vlse16_v_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, const float16_t *base, ptrdiff_t bstride, size_t
    vl);
vfloat16m4_t vlse16_v_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, const float16_t *base, ptrdiff_t bstride, size_t
    vl);
vfloat16m8_t vlse16_v_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, const float16_t *base, ptrdiff_t bstride, size_t
    vl);
vfloat32m1_t vlse32_v_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, const float32_t *base, ptrdiff_t bstride, size_t
    vl);
vfloat32m2_t vlse32_v_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, const float32_t *base, ptrdiff_t bstride, size_t
    vl);
vfloat32m4_t vlse32_v_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, const float32_t *base, ptrdiff_t bstride, size_t
    vl);
vfloat32m8_t vlse32_v_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, const float32_t *base, ptrdiff_t bstride, size_t
    vl);

```

```

vfloat64m1_t vlse64_v_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, const float64_t *base, ptrdiff_t bstride, size_t
    vl);
vfloat64m2_t vlse64_v_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, const float64_t *base, ptrdiff_t bstride, size_t
    vl);
vfloat64m4_t vlse64_v_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, const float64_t *base, ptrdiff_t bstride, size_t
    vl);
vfloat64m8_t vlse64_v_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, const float64_t *base, ptrdiff_t bstride, size_t
    vl);

```

### Vector Strided Store Functions:

#### Prototypes:

```

void vssb_v_i8m1 (int8_t *a, size_t stride, vint8m1_t b, size_t
    vl);
void vssb_v_i8m2 (int8_t *a, size_t stride, vint8m2_t b, size_t
    vl);
void vssb_v_i8m4 (int8_t *a, size_t stride, vint8m4_t b, size_t
    vl);
void vssb_v_i8m8 (int8_t *a, size_t stride, vint8m8_t b, size_t
    vl);
void vssh_v_i16m1 (int16_t *a, size_t stride, vint16m1_t b,
    size_t vl);
void vssh_v_i16m2 (int16_t *a, size_t stride, vint16m2_t b,
    size_t vl);
void vssh_v_i16m4 (int16_t *a, size_t stride, vint16m4_t b,
    size_t vl);
void vssh_v_i16m8 (int16_t *a, size_t stride, vint16m8_t b,
    size_t vl);
void vssw_v_i32m1 (int32_t *a, size_t stride, vint32m1_t b,
    size_t vl);
void vssw_v_i32m2 (int32_t *a, size_t stride, vint32m2_t b,
    size_t vl);
void vssw_v_i32m4 (int32_t *a, size_t stride, vint32m4_t b,
    size_t vl);
void vssw_v_i32m8 (int32_t *a, size_t stride, vint32m8_t b,
    size_t vl);
void vssb_v_u8m1 (uint8_t *a, size_t stride, vuint8m1_t b,
    size_t vl);
void vssb_v_u8m2 (uint8_t *a, size_t stride, vuint8m2_t b,
    size_t vl);

```

```

void vssb_v_u8m4 (uint8_t *a, size_t stride, vuint8m4_t b,
                 size_t vl);
void vssb_v_u8m8 (uint8_t *a, size_t stride, vuint8m8_t b,
                 size_t vl);
void vssh_v_u16m1 (uint16_t *a, size_t stride, vuint16m1_t b,
                 size_t vl);
void vssh_v_u16m2 (uint16_t *a, size_t stride, vuint16m2_t b,
                 size_t vl);
void vssh_v_u16m4 (uint16_t *a, size_t stride, vuint16m4_t b,
                 size_t vl);
void vssh_v_u16m8 (uint16_t *a, size_t stride, vuint16m8_t b,
                 size_t vl);
void vssw_v_u32m1 (uint32_t *a, size_t stride, vuint32m1_t b,
                 size_t vl);
void vssw_v_u32m2 (uint32_t *a, size_t stride, vuint32m2_t b,
                 size_t vl);
void vssw_v_u32m4 (uint32_t *a, size_t stride, vuint32m4_t b,
                 size_t vl);
void vssw_v_u32m8 (uint32_t *a, size_t stride, vuint32m8_t b,
                 size_t vl);
void vsse8_v_i8m1 (int8_t *base, ptrdiff_t bstride, vint8m1_t
                 value, size_t vl);
void vsse8_v_i8m2 (int8_t *base, ptrdiff_t bstride, vint8m2_t
                 value, size_t vl);
void vsse8_v_i8m4 (int8_t *base, ptrdiff_t bstride, vint8m4_t
                 value, size_t vl);
void vsse8_v_i8m8 (int8_t *base, ptrdiff_t bstride, vint8m8_t
                 value, size_t vl);
void vsse16_v_i16m1 (int16_t *base, ptrdiff_t bstride,
                   vint16m1_t value, size_t vl);
void vsse16_v_i16m2 (int16_t *base, ptrdiff_t bstride,
                   vint16m2_t value, size_t vl);
void vsse16_v_i16m4 (int16_t *base, ptrdiff_t bstride,
                   vint16m4_t value, size_t vl);
void vsse16_v_i16m8 (int16_t *base, ptrdiff_t bstride,
                   vint16m8_t value, size_t vl);
void vsse32_v_i32m1 (int32_t *base, ptrdiff_t bstride,
                   vint32m1_t value, size_t vl);
void vsse32_v_i32m2 (int32_t *base, ptrdiff_t bstride,
                   vint32m2_t value, size_t vl);
void vsse32_v_i32m4 (int32_t *base, ptrdiff_t bstride,
                   vint32m4_t value, size_t vl);
void vsse32_v_i32m8 (int32_t *base, ptrdiff_t bstride,
                   vint32m8_t value, size_t vl);
void vsse64_v_i64m1 (int64_t *base, ptrdiff_t bstride,
                   vint64m1_t value, size_t vl);

```

```

void vsse64_v_i64m2 (int64_t *base, ptrdiff_t bstride,
    vint64m2_t value, size_t vl);
void vsse64_v_i64m4 (int64_t *base, ptrdiff_t bstride,
    vint64m4_t value, size_t vl);
void vsse64_v_i64m8 (int64_t *base, ptrdiff_t bstride,
    vint64m8_t value, size_t vl);
void vsse8_v_u8m1 (uint8_t *base, ptrdiff_t bstride, vuint8m1_t
    value, size_t vl);
void vsse8_v_u8m2 (uint8_t *base, ptrdiff_t bstride, vuint8m2_t
    value, size_t vl);
void vsse8_v_u8m4 (uint8_t *base, ptrdiff_t bstride, vuint8m4_t
    value, size_t vl);
void vsse8_v_u8m8 (uint8_t *base, ptrdiff_t bstride, vuint8m8_t
    value, size_t vl);
void vsse16_v_u16m1 (uint16_t *base, ptrdiff_t bstride,
    vuint16m1_t value, size_t vl);
void vsse16_v_u16m2 (uint16_t *base, ptrdiff_t bstride,
    vuint16m2_t value, size_t vl);
void vsse16_v_u16m4 (uint16_t *base, ptrdiff_t bstride,
    vuint16m4_t value, size_t vl);
void vsse16_v_u16m8 (uint16_t *base, ptrdiff_t bstride,
    vuint16m8_t value, size_t vl);
void vsse32_v_u32m1 (uint32_t *base, ptrdiff_t bstride,
    vuint32m1_t value, size_t vl);
void vsse32_v_u32m2 (uint32_t *base, ptrdiff_t bstride,
    vuint32m2_t value, size_t vl);
void vsse32_v_u32m4 (uint32_t *base, ptrdiff_t bstride,
    vuint32m4_t value, size_t vl);
void vsse32_v_u32m8 (uint32_t *base, ptrdiff_t bstride,
    vuint32m8_t value, size_t vl);
void vsse64_v_u64m1 (uint64_t *base, ptrdiff_t bstride,
    vuint64m1_t value, size_t vl);
void vsse64_v_u64m2 (uint64_t *base, ptrdiff_t bstride,
    vuint64m2_t value, size_t vl);
void vsse64_v_u64m4 (uint64_t *base, ptrdiff_t bstride,
    vuint64m4_t value, size_t vl);
void vsse64_v_u64m8 (uint64_t *base, ptrdiff_t bstride,
    vuint64m8_t value, size_t vl);
void vsse16_v_f16m1 (float16_t *base, ptrdiff_t bstride,
    vfloat16m1_t value, size_t vl);
void vsse16_v_f16m2 (float16_t *base, ptrdiff_t bstride,
    vfloat16m2_t value, size_t vl);
void vsse16_v_f16m4 (float16_t *base, ptrdiff_t bstride,
    vfloat16m4_t value, size_t vl);
void vsse16_v_f16m8 (float16_t *base, ptrdiff_t bstride,
    vfloat16m8_t value, size_t vl);

```

```

void vsse32_v_f32m1 (float32_t *base, ptrdiff_t bstride,
    vfloat32m1_t value, size_t vl);
void vsse32_v_f32m2 (float32_t *base, ptrdiff_t bstride,
    vfloat32m2_t value, size_t vl);
void vsse32_v_f32m4 (float32_t *base, ptrdiff_t bstride,
    vfloat32m4_t value, size_t vl);
void vsse32_v_f32m8 (float32_t *base, ptrdiff_t bstride,
    vfloat32m8_t value, size_t vl);
void vsse64_v_f64m1 (float64_t *base, ptrdiff_t bstride,
    vfloat64m1_t value, size_t vl);
void vsse64_v_f64m2 (float64_t *base, ptrdiff_t bstride,
    vfloat64m2_t value, size_t vl);
void vsse64_v_f64m4 (float64_t *base, ptrdiff_t bstride,
    vfloat64m4_t value, size_t vl);
void vsse64_v_f64m8 (float64_t *base, ptrdiff_t bstride,
    vfloat64m8_t value, size_t vl);
// masked functions
void vssb_v_i8m1_m (vbool8_t mask, int8_t *a, size_t stride,
    vint8m1_t b, size_t vl);
void vssb_v_i8m2_m (vbool4_t mask, int8_t *a, size_t stride,
    vint8m2_t b, size_t vl);
void vssb_v_i8m4_m (vbool2_t mask, int8_t *a, size_t stride,
    vint8m4_t b, size_t vl);
void vssb_v_i8m8_m (vbool1_t mask, int8_t *a, size_t stride,
    vint8m8_t b, size_t vl);
void vssh_v_i16m1_m (vbool16_t mask, int16_t *a, size_t stride,
    vint16m1_t b, size_t vl);
void vssh_v_i16m2_m (vbool8_t mask, int16_t *a, size_t stride,
    vint16m2_t b, size_t vl);
void vssh_v_i16m4_m (vbool4_t mask, int16_t *a, size_t stride,
    vint16m4_t b, size_t vl);
void vssh_v_i16m8_m (vbool2_t mask, int16_t *a, size_t stride,
    vint16m8_t b, size_t vl);
void vssw_v_i32m1_m (vbool32_t mask, int32_t *a, size_t stride,
    vint32m1_t b, size_t vl);
void vssw_v_i32m2_m (vbool16_t mask, int32_t *a, size_t stride,
    vint32m2_t b, size_t vl);
void vssw_v_i32m4_m (vbool8_t mask, int32_t *a, size_t stride,
    vint32m4_t b, size_t vl);
void vssw_v_i32m8_m (vbool4_t mask, int32_t *a, size_t stride,
    vint32m8_t b, size_t vl);
void vssb_v_u8m1_m (vbool8_t mask, int8_t *a, size_t stride,
    vuint8m1_t b, size_t vl);
void vssb_v_u8m2_m (vbool4_t mask, int8_t *a, size_t stride,
    vuint8m2_t b, size_t vl);

```

```

void vssb_v_u8m4_m (vbool2_t mask, int8_t *a, size_t stride,
    vuint8m4_t b, size_t vl);
void vssb_v_u8m8_m (vbool1_t mask, int8_t *a, size_t stride,
    vuint8m8_t b, size_t vl);
void vssh_v_u16m1_m (vbool16_t mask, int16_t *a, size_t stride,
    vuint16m1_t b, size_t vl);
void vssh_v_u16m2_m (vbool8_t mask, int16_t *a, size_t stride,
    vuint16m2_t b, size_t vl);
void vssh_v_u16m4_m (vbool4_t mask, int16_t *a, size_t stride,
    vuint16m4_t b, size_t vl);
void vssh_v_u16m8_m (vbool2_t mask, int16_t *a, size_t stride,
    vuint16m8_t b, size_t vl);
void vssw_v_u32m1_m (vbool32_t mask, int32_t *a, size_t stride,
    vuint32m1_t b, size_t vl);
void vssw_v_u32m2_m (vbool16_t mask, int32_t *a, size_t stride,
    vuint32m2_t b, size_t vl);
void vssw_v_u32m4_m (vbool8_t mask, int32_t *a, size_t stride,
    vuint32m4_t b, size_t vl);
void vssw_v_u32m8_m (vbool4_t mask, int32_t *a, size_t stride,
    vuint32m8_t b, size_t vl);
void vsse8_v_i8m1_m (vbool8_t mask, int8_t *base, ptrdiff_t
    bstride, vint8m1_t value, size_t vl);
void vsse8_v_i8m2_m (vbool4_t mask, int8_t *base, ptrdiff_t
    bstride, vint8m2_t value, size_t vl);
void vsse8_v_i8m4_m (vbool2_t mask, int8_t *base, ptrdiff_t
    bstride, vint8m4_t value, size_t vl);
void vsse8_v_i8m8_m (vbool1_t mask, int8_t *base, ptrdiff_t
    bstride, vint8m8_t value, size_t vl);
void vsse16_v_i16m1_m (vbool16_t mask, int16_t *base, ptrdiff_t
    bstride, vint16m1_t value, size_t vl);
void vsse16_v_i16m2_m (vbool8_t mask, int16_t *base, ptrdiff_t
    bstride, vint16m2_t value, size_t vl);
void vsse16_v_i16m4_m (vbool4_t mask, int16_t *base, ptrdiff_t
    bstride, vint16m4_t value, size_t vl);
void vsse16_v_i16m8_m (vbool2_t mask, int16_t *base, ptrdiff_t
    bstride, vint16m8_t value, size_t vl);
void vsse32_v_i32m1_m (vbool32_t mask, int32_t *base, ptrdiff_t
    bstride, vint32m1_t value, size_t vl);
void vsse32_v_i32m2_m (vbool16_t mask, int32_t *base, ptrdiff_t
    bstride, vint32m2_t value, size_t vl);
void vsse32_v_i32m4_m (vbool8_t mask, int32_t *base, ptrdiff_t
    bstride, vint32m4_t value, size_t vl);
void vsse32_v_i32m8_m (vbool4_t mask, int32_t *base, ptrdiff_t
    bstride, vint32m8_t value, size_t vl);
void vsse64_v_i64m1_m (vbool64_t mask, int64_t *base, ptrdiff_t
    bstride, vint64m1_t value, size_t vl);

```

```

void vsse64_v_i64m2_m (vbool32_t mask, int64_t *base, ptrdiff_t
    bstride, vint64m2_t value, size_t vl);
void vsse64_v_i64m4_m (vbool16_t mask, int64_t *base, ptrdiff_t
    bstride, vint64m4_t value, size_t vl);
void vsse64_v_i64m8_m (vbool8_t mask, int64_t *base, ptrdiff_t
    bstride, vint64m8_t value, size_t vl);
void vsse8_v_u8m1_m (vbool8_t mask, uint8_t *base, ptrdiff_t
    bstride, vuint8m1_t value, size_t vl);
void vsse8_v_u8m2_m (vbool4_t mask, uint8_t *base, ptrdiff_t
    bstride, vuint8m2_t value, size_t vl);
void vsse8_v_u8m4_m (vbool2_t mask, uint8_t *base, ptrdiff_t
    bstride, vuint8m4_t value, size_t vl);
void vsse8_v_u8m8_m (vbool1_t mask, uint8_t *base, ptrdiff_t
    bstride, vuint8m8_t value, size_t vl);
void vsse16_v_u16m1_m (vbool16_t mask, uint16_t *base, ptrdiff_t
    bstride, vuint16m1_t value, size_t vl);
void vsse16_v_u16m2_m (vbool8_t mask, uint16_t *base, ptrdiff_t
    bstride, vuint16m2_t value, size_t vl);
void vsse16_v_u16m4_m (vbool4_t mask, uint16_t *base, ptrdiff_t
    bstride, vuint16m4_t value, size_t vl);
void vsse16_v_u16m8_m (vbool2_t mask, uint16_t *base, ptrdiff_t
    bstride, vuint16m8_t value, size_t vl);
void vsse32_v_u32m1_m (vbool32_t mask, uint32_t *base, ptrdiff_t
    bstride, vuint32m1_t value, size_t vl);
void vsse32_v_u32m2_m (vbool16_t mask, uint32_t *base, ptrdiff_t
    bstride, vuint32m2_t value, size_t vl);
void vsse32_v_u32m4_m (vbool8_t mask, uint32_t *base, ptrdiff_t
    bstride, vuint32m4_t value, size_t vl);
void vsse32_v_u32m8_m (vbool4_t mask, uint32_t *base, ptrdiff_t
    bstride, vuint32m8_t value, size_t vl);
void vsse64_v_u64m1_m (vbool64_t mask, uint64_t *base, ptrdiff_t
    bstride, vuint64m1_t value, size_t vl);
void vsse64_v_u64m2_m (vbool32_t mask, uint64_t *base, ptrdiff_t
    bstride, vuint64m2_t value, size_t vl);
void vsse64_v_u64m4_m (vbool16_t mask, uint64_t *base, ptrdiff_t
    bstride, vuint64m4_t value, size_t vl);
void vsse64_v_u64m8_m (vbool8_t mask, uint64_t *base, ptrdiff_t
    bstride, vuint64m8_t value, size_t vl);
void vsse16_v_f16m1_m (vbool16_t mask, float16_t *base,
    ptrdiff_t bstride, vfloat16m1_t value, size_t vl);
void vsse16_v_f16m2_m (vbool8_t mask, float16_t *base, ptrdiff_t
    bstride, vfloat16m2_t value, size_t vl);
void vsse16_v_f16m4_m (vbool4_t mask, float16_t *base, ptrdiff_t
    bstride, vfloat16m4_t value, size_t vl);
void vsse16_v_f16m8_m (vbool2_t mask, float16_t *base, ptrdiff_t
    bstride, vfloat16m8_t value, size_t vl);

```

```

void vsse32_v_f32m1_m (vbool32_t mask, float32_t *base,
    ptrdiff_t bstride, vfloat32m1_t value, size_t vl);
void vsse32_v_f32m2_m (vbool16_t mask, float32_t *base,
    ptrdiff_t bstride, vfloat32m2_t value, size_t vl);
void vsse32_v_f32m4_m (vbool8_t mask, float32_t *base, ptrdiff_t
    bstride, vfloat32m4_t value, size_t vl);
void vsse32_v_f32m8_m (vbool4_t mask, float32_t *base, ptrdiff_t
    bstride, vfloat32m8_t value, size_t vl);
void vsse64_v_f64m1_m (vbool64_t mask, float64_t *base,
    ptrdiff_t bstride, vfloat64m1_t value, size_t vl);
void vsse64_v_f64m2_m (vbool32_t mask, float64_t *base,
    ptrdiff_t bstride, vfloat64m2_t value, size_t vl);
void vsse64_v_f64m4_m (vbool16_t mask, float64_t *base,
    ptrdiff_t bstride, vfloat64m4_t value, size_t vl);
void vsse64_v_f64m8_m (vbool8_t mask, float64_t *base, ptrdiff_t
    bstride, vfloat64m8_t value, size_t vl);

```

## Vector Indexed Load Functions:

### Prototypes:

```

vint8m1_t vlxv_v_i8m1 (const int8_t *a, vuint8m1_t indexed,
    size_t vl);
vint8m2_t vlxv_v_i8m2 (const int8_t *a, vuint8m2_t indexed,
    size_t vl);
vint8m4_t vlxv_v_i8m4 (const int8_t *a, vuint8m4_t indexed,
    size_t vl);
vint8m8_t vlxv_v_i8m8 (const int8_t *a, vuint8m8_t indexed,
    size_t vl);
vint16m1_t vlxv_v_i16m1 (const int16_t *a, vuint16m1_t indexed,
    size_t vl);
vint16m2_t vlxv_v_i16m2 (const int16_t *a, vuint16m2_t indexed,
    size_t vl);
vint16m4_t vlxv_v_i16m4 (const int16_t *a, vuint16m4_t indexed,
    size_t vl);
vint16m8_t vlxv_v_i16m8 (const int16_t *a, vuint16m8_t indexed,
    size_t vl);
vint32m1_t vlxv_v_i32m1 (const int32_t *a, vuint32m1_t indexed,
    size_t vl);
vint32m2_t vlxv_v_i32m2 (const int32_t *a, vuint32m2_t indexed,
    size_t vl);
vint32m4_t vlxv_v_i32m4 (const int32_t *a, vuint32m4_t indexed,
    size_t vl);
vint32m8_t vlxv_v_i32m8 (const int32_t *a, vuint32m8_t indexed,
    size_t vl);

```



```

vint64m1_t vlxv_v_i64m1 (const int64_t *a, vuint64m1_t indexed,
    size_t vl);
vint64m2_t vlxv_v_i64m2 (const int64_t *a, vuint64m2_t indexed,
    size_t vl);
vint64m4_t vlxv_v_i64m4 (const int64_t *a, vuint64m4_t indexed,
    size_t vl);
vint64m8_t vlxv_v_i64m8 (const int64_t *a, vuint64m8_t indexed,
    size_t vl);
vint16m1_t vlxh_v_i16m1 (const int16_t *a, vuint16m1_t indexed,
    size_t vl);
vint16m2_t vlxh_v_i16m2 (const int16_t *a, vuint16m2_t indexed,
    size_t vl);
vint16m4_t vlxh_v_i16m4 (const int16_t *a, vuint16m4_t indexed,
    size_t vl);
vint16m8_t vlxh_v_i16m8 (const int16_t *a, vuint16m8_t indexed,
    size_t vl);
vint32m1_t vlxh_v_i32m1 (const int32_t *a, vuint32m1_t indexed,
    size_t vl);
vint32m2_t vlxh_v_i32m2 (const int32_t *a, vuint32m2_t indexed,
    size_t vl);
vint32m4_t vlxh_v_i32m4 (const int32_t *a, vuint32m4_t indexed,
    size_t vl);
vint32m8_t vlxh_v_i32m8 (const int32_t *a, vuint32m8_t indexed,
    size_t vl);
vint64m1_t vlxh_v_i64m1 (const int64_t *a, vuint64m1_t indexed,
    size_t vl);
vint64m2_t vlxh_v_i64m2 (const int64_t *a, vuint64m2_t indexed,
    size_t vl);
vint64m4_t vlxh_v_i64m4 (const int64_t *a, vuint64m4_t indexed,
    size_t vl);
vint64m8_t vlxh_v_i64m8 (const int64_t *a, vuint64m8_t indexed,
    size_t vl);
vint32m1_t vlxw_v_i32m1 (const int32_t *a, vuint32m1_t indexed,
    size_t vl);
vint32m2_t vlxw_v_i32m2 (const int32_t *a, vuint32m2_t indexed,
    size_t vl);
vint32m4_t vlxw_v_i32m4 (const int32_t *a, vuint32m4_t indexed,
    size_t vl);
vint32m8_t vlxw_v_i32m8 (const int32_t *a, vuint32m8_t indexed,
    size_t vl);
vint64m1_t vlxw_v_i64m1 (const int64_t *a, vuint64m1_t indexed,
    size_t vl);
vint64m2_t vlxw_v_i64m2 (const int64_t *a, vuint64m2_t indexed,
    size_t vl);
vint64m4_t vlxw_v_i64m4 (const int64_t *a, vuint64m4_t indexed,
    size_t vl);

```

```

vuint64m8_t vlxw_v_i64m8 (const int64_t *a, vuint64m8_t indexed,
    size_t vl);
vuint8m1_t vlxbu_v_u8m1 (const uint8_t *a, vuint8m1_t indexed,
    size_t vl);
vuint8m2_t vlxbu_v_u8m2 (const uint8_t *a, vuint8m2_t indexed,
    size_t vl);
vuint8m4_t vlxbu_v_u8m4 (const uint8_t *a, vuint8m4_t indexed,
    size_t vl);
vuint8m8_t vlxbu_v_u8m8 (const uint8_t *a, vuint8m8_t indexed,
    size_t vl);
vuint16m1_t vlxbu_v_u16m1 (const uint16_t *a, vuint16m1_t
    indexed, size_t vl);
vuint16m2_t vlxbu_v_u16m2 (const uint16_t *a, vuint16m2_t
    indexed, size_t vl);
vuint16m4_t vlxbu_v_u16m4 (const uint16_t *a, vuint16m4_t
    indexed, size_t vl);
vuint16m8_t vlxbu_v_u16m8 (const uint16_t *a, vuint16m8_t
    indexed, size_t vl);
vuint32m1_t vlxbu_v_u32m1 (const uint32_t *a, vuint32m1_t
    indexed, size_t vl);
vuint32m2_t vlxbu_v_u32m2 (const uint32_t *a, vuint32m2_t
    indexed, size_t vl);
vuint32m4_t vlxbu_v_u32m4 (const uint32_t *a, vuint32m4_t
    indexed, size_t vl);
vuint32m8_t vlxbu_v_u32m8 (const uint32_t *a, vuint32m8_t
    indexed, size_t vl);
vuint64m1_t vlxbu_v_u64m1 (const uint64_t *a, vuint64m1_t
    indexed, size_t vl);
vuint64m2_t vlxbu_v_u64m2 (const uint64_t *a, vuint64m2_t
    indexed, size_t vl);
vuint64m4_t vlxbu_v_u64m4 (const uint64_t *a, vuint64m4_t
    indexed, size_t vl);
vuint64m8_t vlxbu_v_u64m8 (const uint64_t *a, vuint64m8_t
    indexed, size_t vl);
vuint16m1_t vlxhu_v_u16m1 (const uint16_t *a, vuint16m1_t
    indexed, size_t vl);
vuint16m2_t vlxhu_v_u16m2 (const uint16_t *a, vuint16m2_t
    indexed, size_t vl);
vuint16m4_t vlxhu_v_u16m4 (const uint16_t *a, vuint16m4_t
    indexed, size_t vl);
vuint16m8_t vlxhu_v_u16m8 (const uint16_t *a, vuint16m8_t
    indexed, size_t vl);
vuint32m1_t vlxhu_v_u32m1 (const uint32_t *a, vuint32m1_t
    indexed, size_t vl);
vuint32m2_t vlxhu_v_u32m2 (const uint32_t *a, vuint32m2_t
    indexed, size_t vl);

```

```

vuint32m4_t vlxhu_v_u32m4 (const uint32_t *a, vuint32m4_t
    indexed, size_t vl);
vuint32m8_t vlxhu_v_u32m8 (const uint32_t *a, vuint32m8_t
    indexed, size_t vl);
vuint64m1_t vlxhu_v_u64m1 (const uint64_t *a, vuint64m1_t
    indexed, size_t vl);
vuint64m2_t vlxhu_v_u64m2 (const uint64_t *a, vuint64m2_t
    indexed, size_t vl);
vuint64m4_t vlxhu_v_u64m4 (const uint64_t *a, vuint64m4_t
    indexed, size_t vl);
vuint64m8_t vlxhu_v_u64m8 (const uint64_t *a, vuint64m8_t
    indexed, size_t vl);
vuint32m1_t vlxwu_v_u32m1 (const uint32_t *a, vuint32m1_t
    indexed, size_t vl);
vuint32m2_t vlxwu_v_u32m2 (const uint32_t *a, vuint32m2_t
    indexed, size_t vl);
vuint32m4_t vlxwu_v_u32m4 (const uint32_t *a, vuint32m4_t
    indexed, size_t vl);
vuint32m8_t vlxwu_v_u32m8 (const uint32_t *a, vuint32m8_t
    indexed, size_t vl);
vuint64m1_t vlxwu_v_u64m1 (const uint64_t *a, vuint64m1_t
    indexed, size_t vl);
vuint64m2_t vlxwu_v_u64m2 (const uint64_t *a, vuint64m2_t
    indexed, size_t vl);
vuint64m4_t vlxwu_v_u64m4 (const uint64_t *a, vuint64m4_t
    indexed, size_t vl);
vuint64m8_t vlxwu_v_u64m8 (const uint64_t *a, vuint64m8_t
    indexed, size_t vl);
vint8m1_t vloxei8_v_i8m1 (const int8_t *base, vuint8m1_t bindex,
    size_t vl);
vint8m2_t vloxei8_v_i8m2 (const int8_t *base, vuint8m2_t bindex,
    size_t vl);
vint8m4_t vloxei8_v_i8m4 (const int8_t *base, vuint8m4_t bindex,
    size_t vl);
vint8m8_t vloxei8_v_i8m8 (const int8_t *base, vuint8m8_t bindex,
    size_t vl);
vint16m1_t vloxei16_v_i16m1 (const int16_t *base, vuint16m1_t
    bindex, size_t vl);
vint16m2_t vloxei16_v_i16m2 (const int16_t *base, vuint16m2_t
    bindex, size_t vl);
vint16m4_t vloxei16_v_i16m4 (const int16_t *base, vuint16m4_t
    bindex, size_t vl);
vint16m8_t vloxei16_v_i16m8 (const int16_t *base, vuint16m8_t
    bindex, size_t vl);
vint32m1_t vloxei32_v_i32m1 (const int32_t *base, vuint32m1_t
    bindex, size_t vl);

```

```

vint32m2_t vloxei32_v_i32m2 (const int32_t *base, vuint32m2_t
    bindex, size_t vl);
vint32m4_t vloxei32_v_i32m4 (const int32_t *base, vuint32m4_t
    bindex, size_t vl);
vint32m8_t vloxei32_v_i32m8 (const int32_t *base, vuint32m8_t
    bindex, size_t vl);
vint64m1_t vloxei64_v_i64m1 (const int64_t *base, vuint64m1_t
    bindex, size_t vl);
vint64m2_t vloxei64_v_i64m2 (const int64_t *base, vuint64m2_t
    bindex, size_t vl);
vint64m4_t vloxei64_v_i64m4 (const int64_t *base, vuint64m4_t
    bindex, size_t vl);
vint64m8_t vloxei64_v_i64m8 (const int64_t *base, vuint64m8_t
    bindex, size_t vl);
vuint8m1_t vloxei8_v_u8m1 (const uint8_t *base, vuint8m1_t
    bindex, size_t vl);
vuint8m2_t vloxei8_v_u8m2 (const uint8_t *base, vuint8m2_t
    bindex, size_t vl);
vuint8m4_t vloxei8_v_u8m4 (const uint8_t *base, vuint8m4_t
    bindex, size_t vl);
vuint8m8_t vloxei8_v_u8m8 (const uint8_t *base, vuint8m8_t
    bindex, size_t vl);
vuint16m1_t vloxei16_v_u16m1 (const uint16_t *base, vuint16m1_t
    bindex, size_t vl);
vuint16m2_t vloxei16_v_u16m2 (const uint16_t *base, vuint16m2_t
    bindex, size_t vl);
vuint16m4_t vloxei16_v_u16m4 (const uint16_t *base, vuint16m4_t
    bindex, size_t vl);
vuint16m8_t vloxei16_v_u16m8 (const uint16_t *base, vuint16m8_t
    bindex, size_t vl);
vuint32m1_t vloxei32_v_u32m1 (const uint32_t *base, vuint32m1_t
    bindex, size_t vl);
vuint32m2_t vloxei32_v_u32m2 (const uint32_t *base, vuint32m2_t
    bindex, size_t vl);
vuint32m4_t vloxei32_v_u32m4 (const uint32_t *base, vuint32m4_t
    bindex, size_t vl);
vuint32m8_t vloxei32_v_u32m8 (const uint32_t *base, vuint32m8_t
    bindex, size_t vl);
vuint64m1_t vloxei64_v_u64m1 (const uint64_t *base, vuint64m1_t
    bindex, size_t vl);
vuint64m2_t vloxei64_v_u64m2 (const uint64_t *base, vuint64m2_t
    bindex, size_t vl);
vuint64m4_t vloxei64_v_u64m4 (const uint64_t *base, vuint64m4_t
    bindex, size_t vl);
vuint64m8_t vloxei64_v_u64m8 (const uint64_t *base, vuint64m8_t
    bindex, size_t vl);

```

```

vfloat16m1_t vloxei16_v_f16m1 (const float16_t *base,
    uint16m1_t bindex, size_t vl);
vfloat16m2_t vloxei16_v_f16m2 (const float16_t *base,
    uint16m2_t bindex, size_t vl);
vfloat16m4_t vloxei16_v_f16m4 (const float16_t *base,
    uint16m4_t bindex, size_t vl);
vfloat16m8_t vloxei16_v_f16m8 (const float16_t *base,
    uint16m8_t bindex, size_t vl);
vfloat32m1_t vloxei32_v_f32m1 (const float32_t *base,
    uint32m1_t bindex, size_t vl);
vfloat32m2_t vloxei32_v_f32m2 (const float32_t *base,
    uint32m2_t bindex, size_t vl);
vfloat32m4_t vloxei32_v_f32m4 (const float32_t *base,
    uint32m4_t bindex, size_t vl);
vfloat32m8_t vloxei32_v_f32m8 (const float32_t *base,
    uint32m8_t bindex, size_t vl);
vfloat64m1_t vloxei64_v_f64m1 (const float64_t *base,
    uint64m1_t bindex, size_t vl);
vfloat64m2_t vloxei64_v_f64m2 (const float64_t *base,
    uint64m2_t bindex, size_t vl);
vfloat64m4_t vloxei64_v_f64m4 (const float64_t *base,
    uint64m4_t bindex, size_t vl);
vfloat64m8_t vloxei64_v_f64m8 (const float64_t *base,
    uint64m8_t bindex, size_t vl);
// masked functions
vint8m1_t vlxv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    const int8_t *a, uint8m1_t indexed, size_t vl);
vint8m2_t vlxv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    const int8_t *a, uint8m2_t indexed, size_t vl);
vint8m4_t vlxv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    const int8_t *a, uint8m4_t indexed, size_t vl);
vint8m8_t vlxv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    const int8_t *a, uint8m8_t indexed, size_t vl);
vint16m1_t vlxv_i16m1_m (vbool16_t mask, vint16m1_t maskedoff,
    const int16_t *a, uint16m1_t indexed, size_t vl);
vint16m2_t vlxv_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    const int16_t *a, uint16m2_t indexed, size_t vl);
vint16m4_t vlxv_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    const int16_t *a, uint16m4_t indexed, size_t vl);
vint16m8_t vlxv_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    const int16_t *a, uint16m8_t indexed, size_t vl);
vint32m1_t vlxv_i32m1_m (vbool32_t mask, vint32m1_t maskedoff,
    const int32_t *a, uint32m1_t indexed, size_t vl);
vint32m2_t vlxv_i32m2_m (vbool16_t mask, vint32m2_t maskedoff,
    const int32_t *a, uint32m2_t indexed, size_t vl);

```

```

vint32m4_t vlxv_v_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    const int32_t *a, vuint32m4_t indexed, size_t vl);
vint32m8_t vlxv_v_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    const int32_t *a, vuint32m8_t indexed, size_t vl);
vint64m1_t vlxv_v_i64m1_m (vbool64_t mask, vint64m1_t maskedoff,
    const int64_t *a, vuint64m1_t indexed, size_t vl);
vint64m2_t vlxv_v_i64m2_m (vbool32_t mask, vint64m2_t maskedoff,
    const int64_t *a, vuint64m2_t indexed, size_t vl);
vint64m4_t vlxv_v_i64m4_m (vbool16_t mask, vint64m4_t maskedoff,
    const int64_t *a, vuint64m4_t indexed, size_t vl);
vint64m8_t vlxv_v_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    const int64_t *a, vuint64m8_t indexed, size_t vl);
vint16m1_t vlxh_v_i16m1_m (vbool16_t mask, vint16m1_t maskedoff,
    const int16_t *a, vuint16m1_t indexed, size_t vl);
vint16m2_t vlxh_v_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    const int16_t *a, vuint16m2_t indexed, size_t vl);
vint16m4_t vlxh_v_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    const int16_t *a, vuint16m4_t indexed, size_t vl);
vint16m8_t vlxh_v_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    const int16_t *a, vuint16m8_t indexed, size_t vl);
vint32m1_t vlxh_v_i32m1_m (vbool32_t mask, vint32m1_t maskedoff,
    const int32_t *a, vuint32m1_t indexed, size_t vl);
vint32m2_t vlxh_v_i32m2_m (vbool16_t mask, vint32m2_t maskedoff,
    const int32_t *a, vuint32m2_t indexed, size_t vl);
vint32m4_t vlxh_v_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    const int32_t *a, vuint32m4_t indexed, size_t vl);
vint32m8_t vlxh_v_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    const int32_t *a, vuint32m8_t indexed, size_t vl);
vint64m1_t vlxh_v_i64m1_m (vbool64_t mask, vint64m1_t maskedoff,
    const int64_t *a, vuint64m1_t indexed, size_t vl);
vint64m2_t vlxh_v_i64m2_m (vbool32_t mask, vint64m2_t maskedoff,
    const int64_t *a, vuint64m2_t indexed, size_t vl);
vint64m4_t vlxh_v_i64m4_m (vbool16_t mask, vint64m4_t maskedoff,
    const int64_t *a, vuint64m4_t indexed, size_t vl);
vint64m8_t vlxh_v_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    const int64_t *a, vuint64m8_t indexed, size_t vl);
vint32m1_t vlxw_v_i32m1_m (vbool32_t mask, vint32m1_t maskedoff,
    const int32_t *a, vuint32m1_t indexed, size_t vl);
vint32m2_t vlxw_v_i32m2_m (vbool16_t mask, vint32m2_t maskedoff,
    const int32_t *a, vuint32m2_t indexed, size_t vl);
vint32m4_t vlxw_v_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    const int32_t *a, vuint32m4_t indexed, size_t vl);
vint32m8_t vlxw_v_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    const int32_t *a, vuint32m8_t indexed, size_t vl);
vint64m1_t vlxw_v_i64m1_m (vbool64_t mask, vint64m1_t maskedoff,
    const int64_t *a, vuint64m1_t indexed, size_t vl);

```

```

vint64m2_t vlxw_v_i64m2_m (vbool32_t mask, vint64m2_t maskedoff,
    const int64_t *a, vuint64m2_t indexed, size_t vl);
vint64m4_t vlxw_v_i64m4_m (vbool16_t mask, vint64m4_t maskedoff,
    const int64_t *a, vuint64m4_t indexed, size_t vl);
vint64m8_t vlxw_v_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    const int64_t *a, vuint64m8_t indexed, size_t vl);
vuint8m1_t vlxbu_v_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    const uint8_t *a, vuint8m1_t indexed, size_t vl);
vuint8m2_t vlxbu_v_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    const uint8_t *a, vuint8m2_t indexed, size_t vl);
vuint8m4_t vlxbu_v_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    const uint8_t *a, vuint8m4_t indexed, size_t vl);
vuint8m8_t vlxbu_v_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    const uint8_t *a, vuint8m8_t indexed, size_t vl);
vuint16m1_t vlxbu_v_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, const uint16_t *a, vuint16m1_t indexed, size_t
    vl);
vuint16m2_t vlxbu_v_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, const uint16_t *a, vuint16m2_t indexed, size_t
    vl);
vuint16m4_t vlxbu_v_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, const uint16_t *a, vuint16m4_t indexed, size_t
    vl);
vuint16m8_t vlxbu_v_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, const uint16_t *a, vuint16m8_t indexed, size_t
    vl);
vuint32m1_t vlxbu_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, const uint32_t *a, vuint32m1_t indexed, size_t
    vl);
vuint32m2_t vlxbu_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, const uint32_t *a, vuint32m2_t indexed, size_t
    vl);
vuint32m4_t vlxbu_v_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, const uint32_t *a, vuint32m4_t indexed, size_t
    vl);
vuint32m8_t vlxbu_v_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, const uint32_t *a, vuint32m8_t indexed, size_t
    vl);
vuint64m1_t vlxbu_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, const uint64_t *a, vuint64m1_t indexed, size_t
    vl);
vuint64m2_t vlxbu_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, const uint64_t *a, vuint64m2_t indexed, size_t
    vl);
vuint64m4_t vlxbu_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, const uint64_t *a, vuint64m4_t indexed, size_t

```

```

    vl);
vuint64m8_t vlxbu_v_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, const uint64_t *a, vuint64m8_t indexed, size_t
    vl);
vuint16m1_t vlxhu_v_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, const uint16_t *a, vuint16m1_t indexed, size_t
    vl);
vuint16m2_t vlxhu_v_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, const uint16_t *a, vuint16m2_t indexed, size_t
    vl);
vuint16m4_t vlxhu_v_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, const uint16_t *a, vuint16m4_t indexed, size_t
    vl);
vuint16m8_t vlxhu_v_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, const uint16_t *a, vuint16m8_t indexed, size_t
    vl);
vuint32m1_t vlxhu_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, const uint32_t *a, vuint32m1_t indexed, size_t
    vl);
vuint32m2_t vlxhu_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, const uint32_t *a, vuint32m2_t indexed, size_t
    vl);
vuint32m4_t vlxhu_v_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, const uint32_t *a, vuint32m4_t indexed, size_t
    vl);
vuint32m8_t vlxhu_v_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, const uint32_t *a, vuint32m8_t indexed, size_t
    vl);
vuint64m1_t vlxhu_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, const uint64_t *a, vuint64m1_t indexed, size_t
    vl);
vuint64m2_t vlxhu_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, const uint64_t *a, vuint64m2_t indexed, size_t
    vl);
vuint64m4_t vlxhu_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, const uint64_t *a, vuint64m4_t indexed, size_t
    vl);
vuint64m8_t vlxhu_v_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, const uint64_t *a, vuint64m8_t indexed, size_t
    vl);
vuint32m1_t vlxwu_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, const uint32_t *a, vuint32m1_t indexed, size_t
    vl);
vuint32m2_t vlxwu_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, const uint32_t *a, vuint32m2_t indexed, size_t
    vl);

```



```

vuint32m4_t vlxwu_v_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, const uint32_t *a, vuint32m4_t indexed, size_t
    vl);
vuint32m8_t vlxwu_v_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, const uint32_t *a, vuint32m8_t indexed, size_t
    vl);
vuint64m1_t vlxwu_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, const uint64_t *a, vuint64m1_t indexed, size_t
    vl);
vuint64m2_t vlxwu_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, const uint64_t *a, vuint64m2_t indexed, size_t
    vl);
vuint64m4_t vlxwu_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, const uint64_t *a, vuint64m4_t indexed, size_t
    vl);
vuint64m8_t vlxwu_v_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, const uint64_t *a, vuint64m8_t indexed, size_t
    vl);
vint8m1_t vloxei8_v_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    const int8_t *base, vuint8m1_t bindex, size_t vl);
vint8m2_t vloxei8_v_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    const int8_t *base, vuint8m2_t bindex, size_t vl);
vint8m4_t vloxei8_v_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    const int8_t *base, vuint8m4_t bindex, size_t vl);
vint8m8_t vloxei8_v_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    const int8_t *base, vuint8m8_t bindex, size_t vl);
vint16m1_t vloxei16_v_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, const int16_t *base, vuint16m1_t bindex, size_t
    vl);
vint16m2_t vloxei16_v_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, const int16_t *base, vuint16m2_t bindex, size_t
    vl);
vint16m4_t vloxei16_v_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, const int16_t *base, vuint16m4_t bindex, size_t
    vl);
vint16m8_t vloxei16_v_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, const int16_t *base, vuint16m8_t bindex, size_t
    vl);
vint32m1_t vloxei32_v_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, const int32_t *base, vuint32m1_t bindex, size_t
    vl);
vint32m2_t vloxei32_v_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, const int32_t *base, vuint32m2_t bindex, size_t
    vl);
vint32m4_t vloxei32_v_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, const int32_t *base, vuint32m4_t bindex, size_t

```

```

    vl);
vint32m8_t vloxei32_v_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, const int32_t *base, vuint32m8_t bindex, size_t
    vl);
vint64m1_t vloxei64_v_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, const int64_t *base, vuint64m1_t bindex, size_t
    vl);
vint64m2_t vloxei64_v_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, const int64_t *base, vuint64m2_t bindex, size_t
    vl);
vint64m4_t vloxei64_v_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, const int64_t *base, vuint64m4_t bindex, size_t
    vl);
vint64m8_t vloxei64_v_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, const int64_t *base, vuint64m8_t bindex, size_t
    vl);
vuint8m1_t vloxei8_v_u8m1_m (vbool8_t mask, vuint8m1_t
    maskedoff, const uint8_t *base, vuint8m1_t bindex, size_t
    vl);
vuint8m2_t vloxei8_v_u8m2_m (vbool4_t mask, vuint8m2_t
    maskedoff, const uint8_t *base, vuint8m2_t bindex, size_t
    vl);
vuint8m4_t vloxei8_v_u8m4_m (vbool2_t mask, vuint8m4_t
    maskedoff, const uint8_t *base, vuint8m4_t bindex, size_t
    vl);
vuint8m8_t vloxei8_v_u8m8_m (vbool1_t mask, vuint8m8_t
    maskedoff, const uint8_t *base, vuint8m8_t bindex, size_t
    vl);
vuint16m1_t vloxei16_v_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, const uint16_t *base, vuint16m1_t bindex, size_t
    vl);
vuint16m2_t vloxei16_v_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, const uint16_t *base, vuint16m2_t bindex, size_t
    vl);
vuint16m4_t vloxei16_v_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, const uint16_t *base, vuint16m4_t bindex, size_t
    vl);
vuint16m8_t vloxei16_v_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, const uint16_t *base, vuint16m8_t bindex, size_t
    vl);
vuint32m1_t vloxei32_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, const uint32_t *base, vuint32m1_t bindex, size_t
    vl);
vuint32m2_t vloxei32_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, const uint32_t *base, vuint32m2_t bindex, size_t
    vl);

```

```

vuint32m4_t vloxei32_v_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, const uint32_t *base, vuint32m4_t bindex, size_t
    vl);
vuint32m8_t vloxei32_v_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, const uint32_t *base, vuint32m8_t bindex, size_t
    vl);
vuint64m1_t vloxei64_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, const uint64_t *base, vuint64m1_t bindex, size_t
    vl);
vuint64m2_t vloxei64_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, const uint64_t *base, vuint64m2_t bindex, size_t
    vl);
vuint64m4_t vloxei64_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, const uint64_t *base, vuint64m4_t bindex, size_t
    vl);
vuint64m8_t vloxei64_v_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, const uint64_t *base, vuint64m8_t bindex, size_t
    vl);
vfloat16m1_t vloxei16_v_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, const float16_t *base, vuint16m1_t bindex, size_t
    vl);
vfloat16m2_t vloxei16_v_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, const float16_t *base, vuint16m2_t bindex, size_t
    vl);
vfloat16m4_t vloxei16_v_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, const float16_t *base, vuint16m4_t bindex, size_t
    vl);
vfloat16m8_t vloxei16_v_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, const float16_t *base, vuint16m8_t bindex, size_t
    vl);
vfloat32m1_t vloxei32_v_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, const float32_t *base, vuint32m1_t bindex, size_t
    vl);
vfloat32m2_t vloxei32_v_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, const float32_t *base, vuint32m2_t bindex, size_t
    vl);
vfloat32m4_t vloxei32_v_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, const float32_t *base, vuint32m4_t bindex, size_t
    vl);
vfloat32m8_t vloxei32_v_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, const float32_t *base, vuint32m8_t bindex, size_t
    vl);
vfloat64m1_t vloxei64_v_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, const float64_t *base, vuint64m1_t bindex, size_t
    vl);

```

```

vfloat64m2_t vloxei64_v_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, const float64_t *base, vuint64m2_t bindex, size_t
    vl);
vfloat64m4_t vloxei64_v_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, const float64_t *base, vuint64m4_t bindex, size_t
    vl);
vfloat64m8_t vloxei64_v_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, const float64_t *base, vuint64m8_t bindex, size_t
    vl);

```

## Vector Indexed Store Functions:

### Prototypes:

```

void vsxb_v_i8m1 (int8_t *a, vuint8m1_t indexed, vint8m1_t
    value, size_t vl);
void vsxb_v_i8m2 (int8_t *a, vuint8m2_t indexed, vint8m2_t
    value, size_t vl);
void vsxb_v_i8m4 (int8_t *a, vuint8m4_t indexed, vint8m4_t
    value, size_t vl);
void vsxb_v_i8m8 (int8_t *a, vuint8m8_t indexed, vint8m8_t
    value, size_t vl);
void vsxh_v_i16m1 (int16_t *a, vuint16m1_t indexed, vint16m1_t
    value, size_t vl);
void vsxh_v_i16m2 (int16_t *a, vuint16m2_t indexed, vint16m2_t
    value, size_t vl);
void vsxh_v_i16m4 (int16_t *a, vuint16m4_t indexed, vint16m4_t
    value, size_t vl);
void vsxh_v_i16m8 (int16_t *a, vuint16m8_t indexed, vint16m8_t
    value, size_t vl);
void vsxw_v_i32m1 (int32_t *a, vuint32m1_t indexed, vint32m1_t
    value, size_t vl);
void vsxw_v_i32m2 (int32_t *a, vuint32m2_t indexed, vint32m2_t
    value, size_t vl);
void vsxw_v_i32m4 (int32_t *a, vuint32m4_t indexed, vint32m4_t
    value, size_t vl);
void vsxw_v_i32m8 (int32_t *a, vuint32m8_t indexed, vint32m8_t
    value, size_t vl);
void vsxb_v_u8m1 (uint8_t *a, vuint8m1_t indexed, vuint8m1_t
    value, size_t vl);
void vsxb_v_u8m2 (uint8_t *a, vuint8m2_t indexed, vuint8m2_t
    value, size_t vl);
void vsxb_v_u8m4 (uint8_t *a, vuint8m4_t indexed, vuint8m4_t
    value, size_t vl);
void vsxb_v_u8m8 (uint8_t *a, vuint8m8_t indexed, vuint8m8_t
    value, size_t vl);

```

```

void vsxh_v_u16m1 (uint16_t *a, vuint16m1_t indexed, vuint16m1_t
    value, size_t vl);
void vsxh_v_u16m2 (uint16_t *a, vuint16m2_t indexed, vuint16m2_t
    value, size_t vl);
void vsxh_v_u16m4 (uint16_t *a, vuint16m4_t indexed, vuint16m4_t
    value, size_t vl);
void vsxh_v_u16m8 (uint16_t *a, vuint16m8_t indexed, vuint16m8_t
    value, size_t vl);
void vsxw_v_u32m1 (uint32_t *a, vuint32m1_t indexed, vuint32m1_t
    value, size_t vl);
void vsxw_v_u32m2 (uint32_t *a, vuint32m2_t indexed, vuint32m2_t
    value, size_t vl);
void vsxw_v_u32m4 (uint32_t *a, vuint32m4_t indexed, vuint32m4_t
    value, size_t vl);
void vsxw_v_u32m8 (uint32_t *a, vuint32m8_t indexed, vuint32m8_t
    value, size_t vl);
void vsuxb_v_i8m1 (int8_t *a, vuint8m1_t indexed, vint8m1_t
    value, size_t vl);
void vsuxb_v_i8m2 (int8_t *a, vuint8m2_t indexed, vint8m2_t
    value, size_t vl);
void vsuxb_v_i8m4 (int8_t *a, vuint8m4_t indexed, vint8m4_t
    value, size_t vl);
void vsuxb_v_i8m8 (int8_t *a, vuint8m8_t indexed, vint8m8_t
    value, size_t vl);
void vsuxh_v_i16m1 (int16_t *a, vuint16m1_t indexed, vint16m1_t
    value, size_t vl);
void vsuxh_v_i16m2 (int16_t *a, vuint16m2_t indexed, vint16m2_t
    value, size_t vl);
void vsuxh_v_i16m4 (int16_t *a, vuint16m4_t indexed, vint16m4_t
    value, size_t vl);
void vsuxh_v_i16m8 (int16_t *a, vuint16m8_t indexed, vint16m8_t
    value, size_t vl);
void vsuxw_v_i32m1 (int32_t *a, vuint32m1_t indexed, vint32m1_t
    value, size_t vl);
void vsuxw_v_i32m2 (int32_t *a, vuint32m2_t indexed, vint32m2_t
    value, size_t vl);
void vsuxw_v_i32m4 (int32_t *a, vuint32m4_t indexed, vint32m4_t
    value, size_t vl);
void vsuxw_v_i32m8 (int32_t *a, vuint32m8_t indexed, vint32m8_t
    value, size_t vl);
void vsuxb_v_u8m1 (uint8_t *a, vuint8m1_t indexed, vuint8m1_t
    value, size_t vl);
void vsuxb_v_u8m2 (uint8_t *a, vuint8m2_t indexed, vuint8m2_t
    value, size_t vl);
void vsuxb_v_u8m4 (uint8_t *a, vuint8m4_t indexed, vuint8m4_t
    value, size_t vl);

```

```

void vsuxb_v_u8m8 (uint8_t *a, vuint8m8_t indexed, vuint8m8_t
    value, size_t vl);
void vsuxh_v_u16m1 (uint16_t *a, vuint16m1_t indexed,
    vuint16m1_t value, size_t vl);
void vsuxh_v_u16m2 (uint16_t *a, vuint16m2_t indexed,
    vuint16m2_t value, size_t vl);
void vsuxh_v_u16m4 (uint16_t *a, vuint16m4_t indexed,
    vuint16m4_t value, size_t vl);
void vsuxh_v_u16m8 (uint16_t *a, vuint16m8_t indexed,
    vuint16m8_t value, size_t vl);
void vsuxw_v_u32m1 (uint32_t *a, vuint32m1_t indexed,
    vuint32m1_t value, size_t vl);
void vsuxw_v_u32m2 (uint32_t *a, vuint32m2_t indexed,
    vuint32m2_t value, size_t vl);
void vsuxw_v_u32m4 (uint32_t *a, vuint32m4_t indexed,
    vuint32m4_t value, size_t vl);
void vsuxw_v_u32m8 (uint32_t *a, vuint32m8_t indexed,
    vuint32m8_t value, size_t vl);
void vsoxei8_v_i8m1 (int8_t *base, vuint8m1_t bindex, vint8m1_t
    value, size_t vl);
void vsoxei8_v_i8m2 (int8_t *base, vuint8m2_t bindex, vint8m2_t
    value, size_t vl);
void vsoxei8_v_i8m4 (int8_t *base, vuint8m4_t bindex, vint8m4_t
    value, size_t vl);
void vsoxei8_v_i8m8 (int8_t *base, vuint8m8_t bindex, vint8m8_t
    value, size_t vl);
void vsoxei16_v_i16m1 (int16_t *base, vuint16m1_t bindex,
    vint16m1_t value, size_t vl);
void vsoxei16_v_i16m2 (int16_t *base, vuint16m2_t bindex,
    vint16m2_t value, size_t vl);
void vsoxei16_v_i16m4 (int16_t *base, vuint16m4_t bindex,
    vint16m4_t value, size_t vl);
void vsoxei16_v_i16m8 (int16_t *base, vuint16m8_t bindex,
    vint16m8_t value, size_t vl);
void vsoxei32_v_i32m1 (int32_t *base, vuint32m1_t bindex,
    vint32m1_t value, size_t vl);
void vsoxei32_v_i32m2 (int32_t *base, vuint32m2_t bindex,
    vint32m2_t value, size_t vl);
void vsoxei32_v_i32m4 (int32_t *base, vuint32m4_t bindex,
    vint32m4_t value, size_t vl);
void vsoxei32_v_i32m8 (int32_t *base, vuint32m8_t bindex,
    vint32m8_t value, size_t vl);
void vsoxei64_v_i64m1 (int64_t *base, vuint64m1_t bindex,
    vint64m1_t value, size_t vl);
void vsoxei64_v_i64m2 (int64_t *base, vuint64m2_t bindex,
    vint64m2_t value, size_t vl);

```

```

void vsoxei64_v_i64m4 (int64_t *base, vuint64m4_t bindex,
    vint64m4_t value, size_t vl);
void vsoxei64_v_i64m8 (int64_t *base, vuint64m8_t bindex,
    vint64m8_t value, size_t vl);
void vsoxei8_v_u8m1 (uint8_t *base, vuint8m1_t bindex,
    vuint8m1_t value, size_t vl);
void vsoxei8_v_u8m2 (uint8_t *base, vuint8m2_t bindex,
    vuint8m2_t value, size_t vl);
void vsoxei8_v_u8m4 (uint8_t *base, vuint8m4_t bindex,
    vuint8m4_t value, size_t vl);
void vsoxei8_v_u8m8 (uint8_t *base, vuint8m8_t bindex,
    vuint8m8_t value, size_t vl);
void vsoxei16_v_u16m1 (uint16_t *base, vuint16m1_t bindex,
    vuint16m1_t value, size_t vl);
void vsoxei16_v_u16m2 (uint16_t *base, vuint16m2_t bindex,
    vuint16m2_t value, size_t vl);
void vsoxei16_v_u16m4 (uint16_t *base, vuint16m4_t bindex,
    vuint16m4_t value, size_t vl);
void vsoxei16_v_u16m8 (uint16_t *base, vuint16m8_t bindex,
    vuint16m8_t value, size_t vl);
void vsoxei32_v_u32m1 (uint32_t *base, vuint32m1_t bindex,
    vuint32m1_t value, size_t vl);
void vsoxei32_v_u32m2 (uint32_t *base, vuint32m2_t bindex,
    vuint32m2_t value, size_t vl);
void vsoxei32_v_u32m4 (uint32_t *base, vuint32m4_t bindex,
    vuint32m4_t value, size_t vl);
void vsoxei32_v_u32m8 (uint32_t *base, vuint32m8_t bindex,
    vuint32m8_t value, size_t vl);
void vsoxei64_v_u64m1 (uint64_t *base, vuint64m1_t bindex,
    vuint64m1_t value, size_t vl);
void vsoxei64_v_u64m2 (uint64_t *base, vuint64m2_t bindex,
    vuint64m2_t value, size_t vl);
void vsoxei64_v_u64m4 (uint64_t *base, vuint64m4_t bindex,
    vuint64m4_t value, size_t vl);
void vsoxei64_v_u64m8 (uint64_t *base, vuint64m8_t bindex,
    vuint64m8_t value, size_t vl);
void vsoxei16_v_f16m1 (float16_t *base, vuint16m1_t bindex,
    vfloat16m1_t value, size_t vl);
void vsoxei16_v_f16m2 (float16_t *base, vuint16m2_t bindex,
    vfloat16m2_t value, size_t vl);
void vsoxei16_v_f16m4 (float16_t *base, vuint16m4_t bindex,
    vfloat16m4_t value, size_t vl);
void vsoxei16_v_f16m8 (float16_t *base, vuint16m8_t bindex,
    vfloat16m8_t value, size_t vl);
void vsoxei32_v_f32m1 (float32_t *base, vuint32m1_t bindex,
    vfloat32m1_t value, size_t vl);

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void vsoxei32_v_f32m2 (float32_t *base, vuint32m2_t bindex,
    vfloat32m2_t value, size_t vl);
void vsoxei32_v_f32m4 (float32_t *base, vuint32m4_t bindex,
    vfloat32m4_t value, size_t vl);
void vsoxei32_v_f32m8 (float32_t *base, vuint32m8_t bindex,
    vfloat32m8_t value, size_t vl);
void vsoxei64_v_f64m1 (float64_t *base, vuint64m1_t bindex,
    vfloat64m1_t value, size_t vl);
void vsoxei64_v_f64m2 (float64_t *base, vuint64m2_t bindex,
    vfloat64m2_t value, size_t vl);
void vsoxei64_v_f64m4 (float64_t *base, vuint64m4_t bindex,
    vfloat64m4_t value, size_t vl);
void vsoxei64_v_f64m8 (float64_t *base, vuint64m8_t bindex,
    vfloat64m8_t value, size_t vl);
void vsuxei8_v_i8m1 (int8_t *base, vuint8m1_t bindex, vint8m1_t
    value, size_t vl);
void vsuxei8_v_i8m2 (int8_t *base, vuint8m2_t bindex, vint8m2_t
    value, size_t vl);
void vsuxei8_v_i8m4 (int8_t *base, vuint8m4_t bindex, vint8m4_t
    value, size_t vl);
void vsuxei8_v_i8m8 (int8_t *base, vuint8m8_t bindex, vint8m8_t
    value, size_t vl);
void vsuxei16_v_i16m1 (int16_t *base, vuint16m1_t bindex,
    vint16m1_t value, size_t vl);
void vsuxei16_v_i16m2 (int16_t *base, vuint16m2_t bindex,
    vint16m2_t value, size_t vl);
void vsuxei16_v_i16m4 (int16_t *base, vuint16m4_t bindex,
    vint16m4_t value, size_t vl);
void vsuxei16_v_i16m8 (int16_t *base, vuint16m8_t bindex,
    vint16m8_t value, size_t vl);
void vsuxei32_v_i32m1 (int32_t *base, vuint32m1_t bindex,
    vint32m1_t value, size_t vl);
void vsuxei32_v_i32m2 (int32_t *base, vuint32m2_t bindex,
    vint32m2_t value, size_t vl);
void vsuxei32_v_i32m4 (int32_t *base, vuint32m4_t bindex,
    vint32m4_t value, size_t vl);
void vsuxei32_v_i32m8 (int32_t *base, vuint32m8_t bindex,
    vint32m8_t value, size_t vl);
void vsuxei64_v_i64m1 (int64_t *base, vuint64m1_t bindex,
    vint64m1_t value, size_t vl);
void vsuxei64_v_i64m2 (int64_t *base, vuint64m2_t bindex,
    vint64m2_t value, size_t vl);
void vsuxei64_v_i64m4 (int64_t *base, vuint64m4_t bindex,
    vint64m4_t value, size_t vl);
void vsuxei64_v_i64m8 (int64_t *base, vuint64m8_t bindex,
    vint64m8_t value, size_t vl);

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void vsuxei8_v_u8m1 (uint8_t *base, vuint8m1_t bindex,
    vuint8m1_t value, size_t vl);
void vsuxei8_v_u8m2 (uint8_t *base, vuint8m2_t bindex,
    vuint8m2_t value, size_t vl);
void vsuxei8_v_u8m4 (uint8_t *base, vuint8m4_t bindex,
    vuint8m4_t value, size_t vl);
void vsuxei8_v_u8m8 (uint8_t *base, vuint8m8_t bindex,
    vuint8m8_t value, size_t vl);
void vsuxei16_v_u16m1 (uint16_t *base, vuint16m1_t bindex,
    vuint16m1_t value, size_t vl);
void vsuxei16_v_u16m2 (uint16_t *base, vuint16m2_t bindex,
    vuint16m2_t value, size_t vl);
void vsuxei16_v_u16m4 (uint16_t *base, vuint16m4_t bindex,
    vuint16m4_t value, size_t vl);
void vsuxei16_v_u16m8 (uint16_t *base, vuint16m8_t bindex,
    vuint16m8_t value, size_t vl);
void vsuxei32_v_u32m1 (uint32_t *base, vuint32m1_t bindex,
    vuint32m1_t value, size_t vl);
void vsuxei32_v_u32m2 (uint32_t *base, vuint32m2_t bindex,
    vuint32m2_t value, size_t vl);
void vsuxei32_v_u32m4 (uint32_t *base, vuint32m4_t bindex,
    vuint32m4_t value, size_t vl);
void vsuxei32_v_u32m8 (uint32_t *base, vuint32m8_t bindex,
    vuint32m8_t value, size_t vl);
void vsuxei64_v_u64m1 (uint64_t *base, vuint64m1_t bindex,
    vuint64m1_t value, size_t vl);
void vsuxei64_v_u64m2 (uint64_t *base, vuint64m2_t bindex,
    vuint64m2_t value, size_t vl);
void vsuxei64_v_u64m4 (uint64_t *base, vuint64m4_t bindex,
    vuint64m4_t value, size_t vl);
void vsuxei64_v_u64m8 (uint64_t *base, vuint64m8_t bindex,
    vuint64m8_t value, size_t vl);
void vsuxei16_v_f16m1 (float16_t *base, vuint16m1_t bindex,
    vfloat16m1_t value, size_t vl);
void vsuxei16_v_f16m2 (float16_t *base, vuint16m2_t bindex,
    vfloat16m2_t value, size_t vl);
void vsuxei16_v_f16m4 (float16_t *base, vuint16m4_t bindex,
    vfloat16m4_t value, size_t vl);
void vsuxei16_v_f16m8 (float16_t *base, vuint16m8_t bindex,
    vfloat16m8_t value, size_t vl);
void vsuxei32_v_f32m1 (float32_t *base, vuint32m1_t bindex,
    vfloat32m1_t value, size_t vl);
void vsuxei32_v_f32m2 (float32_t *base, vuint32m2_t bindex,
    vfloat32m2_t value, size_t vl);
void vsuxei32_v_f32m4 (float32_t *base, vuint32m4_t bindex,
    vfloat32m4_t value, size_t vl);

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void vsuxei32_v_f32m8 (float32_t *base, vuint32m8_t bindex,
    vfloat32m8_t value, size_t vl);
void vsuxei64_v_f64m1 (float64_t *base, vuint64m1_t bindex,
    vfloat64m1_t value, size_t vl);
void vsuxei64_v_f64m2 (float64_t *base, vuint64m2_t bindex,
    vfloat64m2_t value, size_t vl);
void vsuxei64_v_f64m4 (float64_t *base, vuint64m4_t bindex,
    vfloat64m4_t value, size_t vl);
void vsuxei64_v_f64m8 (float64_t *base, vuint64m8_t bindex,
    vfloat64m8_t value, size_t vl);
// masked functions
void vsxb_v_i8m1_m (vbool8_t mask, int8_t *a, vuint8m1_t
    indexed, vint8m1_t value, size_t vl);
void vsxb_v_i8m2_m (vbool4_t mask, int8_t *a, vuint8m2_t
    indexed, vint8m2_t value, size_t vl);
void vsxb_v_i8m4_m (vbool2_t mask, int8_t *a, vuint8m4_t
    indexed, vint8m4_t value, size_t vl);
void vsxb_v_i8m8_m (vbool1_t mask, int8_t *a, vuint8m8_t
    indexed, vint8m8_t value, size_t vl);
void vsxh_v_i16m1_m (vbool16_t mask, int16_t *a, vuint16m1_t
    indexed, vint16m1_t value, size_t vl);
void vsxh_v_i16m2_m (vbool8_t mask, int16_t *a, vuint16m2_t
    indexed, vint16m2_t value, size_t vl);
void vsxh_v_i16m4_m (vbool4_t mask, int16_t *a, vuint16m4_t
    indexed, vint16m4_t value, size_t vl);
void vsxh_v_i16m8_m (vbool2_t mask, int16_t *a, vuint16m8_t
    indexed, vint16m8_t value, size_t vl);
void vsxw_v_i32m1_m (vbool32_t mask, int32_t *a, vuint32m1_t
    indexed, vint32m1_t value, size_t vl);
void vsxw_v_i32m2_m (vbool16_t mask, int32_t *a, vuint32m2_t
    indexed, vint32m2_t value, size_t vl);
void vsxw_v_i32m4_m (vbool8_t mask, int32_t *a, vuint32m4_t
    indexed, vint32m4_t value, size_t vl);
void vsxw_v_i32m8_m (vbool4_t mask, int32_t *a, vuint32m8_t
    indexed, vint32m8_t value, size_t vl);
void vsxb_v_u8m1_m (vbool8_t mask, uint8_t *a, vuint8m1_t
    indexed, vuint8m1_t value, size_t vl);
void vsxb_v_u8m2_m (vbool4_t mask, uint8_t *a, vuint8m2_t
    indexed, vuint8m2_t value, size_t vl);
void vsxb_v_u8m4_m (vbool2_t mask, uint8_t *a, vuint8m4_t
    indexed, vuint8m4_t value, size_t vl);
void vsxb_v_u8m8_m (vbool1_t mask, uint8_t *a, vuint8m8_t
    indexed, vuint8m8_t value, size_t vl);
void vsxh_v_u16m1_m (vbool16_t mask, uint16_t *a, vuint16m1_t
    indexed, vuint16m1_t value, size_t vl);

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```

void vsxh_v_u16m2_m (vbool8_t mask, uint16_t *a, vuint16m2_t
    indexed, vuint16m2_t value, size_t vl);
void vsxh_v_u16m4_m (vbool4_t mask, uint16_t *a, vuint16m4_t
    indexed, vuint16m4_t value, size_t vl);
void vsxh_v_u16m8_m (vbool2_t mask, uint16_t *a, vuint16m8_t
    indexed, vuint16m8_t value, size_t vl);
void vsxw_v_u32m1_m (vbool32_t mask, uint32_t *a, vuint32m1_t
    indexed, vuint32m1_t value, size_t vl);
void vsxw_v_u32m2_m (vbool16_t mask, uint32_t *a, vuint32m2_t
    indexed, vuint32m2_t value, size_t vl);
void vsxw_v_u32m4_m (vbool8_t mask, uint32_t *a, vuint32m4_t
    indexed, vuint32m4_t value, size_t vl);
void vsxw_v_u32m8_m (vbool4_t mask, uint32_t *a, vuint32m8_t
    indexed, vuint32m8_t value, size_t vl);
void vsuxb_v_i8m1_m (vbool8_t mask, int8_t *a, vuint8m1_t
    indexed, vint8m1_t value, size_t vl);
void vsuxb_v_i8m2_m (vbool4_t mask, int8_t *a, vuint8m2_t
    indexed, vint8m2_t value, size_t vl);
void vsuxb_v_i8m4_m (vbool2_t mask, int8_t *a, vuint8m4_t
    indexed, vint8m4_t value, size_t vl);
void vsuxb_v_i8m8_m (vbool1_t mask, int8_t *a, vuint8m8_t
    indexed, vint8m8_t value, size_t vl);
void vsuxh_v_i16m1_m (vbool16_t mask, int16_t *a, vuint16m1_t
    indexed, vint16m1_t value, size_t vl);
void vsuxh_v_i16m2_m (vbool8_t mask, int16_t *a, vuint16m2_t
    indexed, vint16m2_t value, size_t vl);
void vsuxh_v_i16m4_m (vbool4_t mask, int16_t *a, vuint16m4_t
    indexed, vint16m4_t value, size_t vl);
void vsuxh_v_i16m8_m (vbool2_t mask, int16_t *a, vuint16m8_t
    indexed, vint16m8_t value, size_t vl);
void vsuxw_v_i32m1_m (vbool32_t mask, int32_t *a, vuint32m1_t
    indexed, vint32m1_t value, size_t vl);
void vsuxw_v_i32m2_m (vbool16_t mask, int32_t *a, vuint32m2_t
    indexed, vint32m2_t value, size_t vl);
void vsuxw_v_i32m4_m (vbool8_t mask, int32_t *a, vuint32m4_t
    indexed, vint32m4_t value, size_t vl);
void vsuxw_v_i32m8_m (vbool4_t mask, int32_t *a, vuint32m8_t
    indexed, vint32m8_t value, size_t vl);
void vsuxb_v_u8m1_m (vbool8_t mask, uint8_t *a, vuint8m1_t
    indexed, vuint8m1_t value, size_t vl);
void vsuxb_v_u8m2_m (vbool4_t mask, uint8_t *a, vuint8m2_t
    indexed, vuint8m2_t value, size_t vl);
void vsuxb_v_u8m4_m (vbool2_t mask, uint8_t *a, vuint8m4_t
    indexed, vuint8m4_t value, size_t vl);
void vsuxb_v_u8m8_m (vbool1_t mask, uint8_t *a, vuint8m8_t
    indexed, vuint8m8_t value, size_t vl);

```

```

void vsuxh_v_u16m1_m (vbool16_t mask, uint16_t *a, vuint16m1_t
    indexed, vuint16m1_t value, size_t vl);
void vsuxh_v_u16m2_m (vbool8_t mask, uint16_t *a, vuint16m2_t
    indexed, vuint16m2_t value, size_t vl);
void vsuxh_v_u16m4_m (vbool4_t mask, uint16_t *a, vuint16m4_t
    indexed, vuint16m4_t value, size_t vl);
void vsuxh_v_u16m8_m (vbool2_t mask, uint16_t *a, vuint16m8_t
    indexed, vuint16m8_t value, size_t vl);
void vsuxw_v_u32m1_m (vbool32_t mask, uint32_t *a, vuint32m1_t
    indexed, vuint32m1_t value, size_t vl);
void vsuxw_v_u32m2_m (vbool16_t mask, uint32_t *a, vuint32m2_t
    indexed, vuint32m2_t value, size_t vl);
void vsuxw_v_u32m4_m (vbool8_t mask, uint32_t *a, vuint32m4_t
    indexed, vuint32m4_t value, size_t vl);
void vsuxw_v_u32m8_m (vbool4_t mask, uint32_t *a, vuint32m8_t
    indexed, vuint32m8_t value, size_t vl);
void vsoxei8_v_i8m1_m (vbool8_t mask, int8_t *base, vuint8m1_t
    bindex, vint8m1_t value, size_t vl);
void vsoxei8_v_i8m2_m (vbool4_t mask, int8_t *base, vuint8m2_t
    bindex, vint8m2_t value, size_t vl);
void vsoxei8_v_i8m4_m (vbool2_t mask, int8_t *base, vuint8m4_t
    bindex, vint8m4_t value, size_t vl);
void vsoxei8_v_i8m8_m (vbool1_t mask, int8_t *base, vuint8m8_t
    bindex, vint8m8_t value, size_t vl);
void vsoxei16_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t bindex, vint16m1_t value, size_t vl);
void vsoxei16_v_i16m2_m (vbool8_t mask, int16_t *base,
    vuint16m2_t bindex, vint16m2_t value, size_t vl);
void vsoxei16_v_i16m4_m (vbool4_t mask, int16_t *base,
    vuint16m4_t bindex, vint16m4_t value, size_t vl);
void vsoxei16_v_i16m8_m (vbool2_t mask, int16_t *base,
    vuint16m8_t bindex, vint16m8_t value, size_t vl);
void vsoxei32_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t bindex, vint32m1_t value, size_t vl);
void vsoxei32_v_i32m2_m (vbool16_t mask, int32_t *base,
    vuint32m2_t bindex, vint32m2_t value, size_t vl);
void vsoxei32_v_i32m4_m (vbool8_t mask, int32_t *base,
    vuint32m4_t bindex, vint32m4_t value, size_t vl);
void vsoxei32_v_i32m8_m (vbool4_t mask, int32_t *base,
    vuint32m8_t bindex, vint32m8_t value, size_t vl);
void vsoxei64_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t bindex, vint64m1_t value, size_t vl);
void vsoxei64_v_i64m2_m (vbool32_t mask, int64_t *base,
    vuint64m2_t bindex, vint64m2_t value, size_t vl);
void vsoxei64_v_i64m4_m (vbool16_t mask, int64_t *base,
    vuint64m4_t bindex, vint64m4_t value, size_t vl);

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```

void vsoxei64_v_i64m8_m (vbool8_t mask, int64_t *base,
    vuint64m8_t bindex, vint64m8_t value, size_t vl);
void vsoxei8_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    bindex, vuint8m1_t value, size_t vl);
void vsoxei8_v_u8m2_m (vbool4_t mask, uint8_t *base, vuint8m2_t
    bindex, vuint8m2_t value, size_t vl);
void vsoxei8_v_u8m4_m (vbool2_t mask, uint8_t *base, vuint8m4_t
    bindex, vuint8m4_t value, size_t vl);
void vsoxei8_v_u8m8_m (vbool1_t mask, uint8_t *base, vuint8m8_t
    bindex, vuint8m8_t value, size_t vl);
void vsoxei16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t bindex, vuint16m1_t value, size_t vl);
void vsoxei16_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t bindex, vuint16m2_t value, size_t vl);
void vsoxei16_v_u16m4_m (vbool4_t mask, uint16_t *base,
    vuint16m4_t bindex, vuint16m4_t value, size_t vl);
void vsoxei16_v_u16m8_m (vbool2_t mask, uint16_t *base,
    vuint16m8_t bindex, vuint16m8_t value, size_t vl);
void vsoxei32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t bindex, vuint32m1_t value, size_t vl);
void vsoxei32_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t bindex, vuint32m2_t value, size_t vl);
void vsoxei32_v_u32m4_m (vbool8_t mask, uint32_t *base,
    vuint32m4_t bindex, vuint32m4_t value, size_t vl);
void vsoxei32_v_u32m8_m (vbool4_t mask, uint32_t *base,
    vuint32m8_t bindex, vuint32m8_t value, size_t vl);
void vsoxei64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t bindex, vuint64m1_t value, size_t vl);
void vsoxei64_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t bindex, vuint64m2_t value, size_t vl);
void vsoxei64_v_u64m4_m (vbool16_t mask, uint64_t *base,
    vuint64m4_t bindex, vuint64m4_t value, size_t vl);
void vsoxei64_v_u64m8_m (vbool8_t mask, uint64_t *base,
    vuint64m8_t bindex, vuint64m8_t value, size_t vl);
void vsoxei16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vuint16m1_t bindex, vfloat16m1_t value, size_t vl);
void vsoxei16_v_f16m2_m (vbool8_t mask, float16_t *base,
    vuint16m2_t bindex, vfloat16m2_t value, size_t vl);
void vsoxei16_v_f16m4_m (vbool4_t mask, float16_t *base,
    vuint16m4_t bindex, vfloat16m4_t value, size_t vl);
void vsoxei16_v_f16m8_m (vbool2_t mask, float16_t *base,
    vuint16m8_t bindex, vfloat16m8_t value, size_t vl);
void vsoxei32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vuint32m1_t bindex, vfloat32m1_t value, size_t vl);
void vsoxei32_v_f32m2_m (vbool16_t mask, float32_t *base,
    vuint32m2_t bindex, vfloat32m2_t value, size_t vl);

```

```

void vsoxei32_v_f32m4_m (vbool8_t mask, float32_t *base,
    vuint32m4_t bindex, vfloat32m4_t value, size_t vl);
void vsoxei32_v_f32m8_m (vbool4_t mask, float32_t *base,
    vuint32m8_t bindex, vfloat32m8_t value, size_t vl);
void vsoxei64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vuint64m1_t bindex, vfloat64m1_t value, size_t vl);
void vsoxei64_v_f64m2_m (vbool32_t mask, float64_t *base,
    vuint64m2_t bindex, vfloat64m2_t value, size_t vl);
void vsoxei64_v_f64m4_m (vbool16_t mask, float64_t *base,
    vuint64m4_t bindex, vfloat64m4_t value, size_t vl);
void vsoxei64_v_f64m8_m (vbool8_t mask, float64_t *base,
    vuint64m8_t bindex, vfloat64m8_t value, size_t vl);
void vsuxei8_v_i8m1_m (vbool8_t mask, int8_t *base, vuint8m1_t
    bindex, vint8m1_t value, size_t vl);
void vsuxei8_v_i8m2_m (vbool4_t mask, int8_t *base, vuint8m2_t
    bindex, vint8m2_t value, size_t vl);
void vsuxei8_v_i8m4_m (vbool2_t mask, int8_t *base, vuint8m4_t
    bindex, vint8m4_t value, size_t vl);
void vsuxei8_v_i8m8_m (vbool1_t mask, int8_t *base, vuint8m8_t
    bindex, vint8m8_t value, size_t vl);
void vsuxei16_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t bindex, vint16m1_t value, size_t vl);
void vsuxei16_v_i16m2_m (vbool8_t mask, int16_t *base,
    vuint16m2_t bindex, vint16m2_t value, size_t vl);
void vsuxei16_v_i16m4_m (vbool4_t mask, int16_t *base,
    vuint16m4_t bindex, vint16m4_t value, size_t vl);
void vsuxei16_v_i16m8_m (vbool2_t mask, int16_t *base,
    vuint16m8_t bindex, vint16m8_t value, size_t vl);
void vsuxei32_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t bindex, vint32m1_t value, size_t vl);
void vsuxei32_v_i32m2_m (vbool16_t mask, int32_t *base,
    vuint32m2_t bindex, vint32m2_t value, size_t vl);
void vsuxei32_v_i32m4_m (vbool8_t mask, int32_t *base,
    vuint32m4_t bindex, vint32m4_t value, size_t vl);
void vsuxei32_v_i32m8_m (vbool4_t mask, int32_t *base,
    vuint32m8_t bindex, vint32m8_t value, size_t vl);
void vsuxei64_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t bindex, vint64m1_t value, size_t vl);
void vsuxei64_v_i64m2_m (vbool32_t mask, int64_t *base,
    vuint64m2_t bindex, vint64m2_t value, size_t vl);
void vsuxei64_v_i64m4_m (vbool16_t mask, int64_t *base,
    vuint64m4_t bindex, vint64m4_t value, size_t vl);
void vsuxei64_v_i64m8_m (vbool8_t mask, int64_t *base,
    vuint64m8_t bindex, vint64m8_t value, size_t vl);
void vsuxei8_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    bindex, vuint8m1_t value, size_t vl);

```

```

void vsuxei8_v_u8m2_m (vbool4_t mask, uint8_t *base, vuint8m2_t
    bindex, vuint8m2_t value, size_t vl);
void vsuxei8_v_u8m4_m (vbool2_t mask, uint8_t *base, vuint8m4_t
    bindex, vuint8m4_t value, size_t vl);
void vsuxei8_v_u8m8_m (vbool1_t mask, uint8_t *base, vuint8m8_t
    bindex, vuint8m8_t value, size_t vl);
void vsuxei16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t bindex, vuint16m1_t value, size_t vl);
void vsuxei16_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t bindex, vuint16m2_t value, size_t vl);
void vsuxei16_v_u16m4_m (vbool4_t mask, uint16_t *base,
    vuint16m4_t bindex, vuint16m4_t value, size_t vl);
void vsuxei16_v_u16m8_m (vbool2_t mask, uint16_t *base,
    vuint16m8_t bindex, vuint16m8_t value, size_t vl);
void vsuxei32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t bindex, vuint32m1_t value, size_t vl);
void vsuxei32_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t bindex, vuint32m2_t value, size_t vl);
void vsuxei32_v_u32m4_m (vbool8_t mask, uint32_t *base,
    vuint32m4_t bindex, vuint32m4_t value, size_t vl);
void vsuxei32_v_u32m8_m (vbool4_t mask, uint32_t *base,
    vuint32m8_t bindex, vuint32m8_t value, size_t vl);
void vsuxei64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t bindex, vuint64m1_t value, size_t vl);
void vsuxei64_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t bindex, vuint64m2_t value, size_t vl);
void vsuxei64_v_u64m4_m (vbool16_t mask, uint64_t *base,
    vuint64m4_t bindex, vuint64m4_t value, size_t vl);
void vsuxei64_v_u64m8_m (vbool8_t mask, uint64_t *base,
    vuint64m8_t bindex, vuint64m8_t value, size_t vl);
void vsuxei16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vuint16m1_t bindex, vfloat16m1_t value, size_t vl);
void vsuxei16_v_f16m2_m (vbool8_t mask, float16_t *base,
    vuint16m2_t bindex, vfloat16m2_t value, size_t vl);
void vsuxei16_v_f16m4_m (vbool4_t mask, float16_t *base,
    vuint16m4_t bindex, vfloat16m4_t value, size_t vl);
void vsuxei16_v_f16m8_m (vbool2_t mask, float16_t *base,
    vuint16m8_t bindex, vfloat16m8_t value, size_t vl);
void vsuxei32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vuint32m1_t bindex, vfloat32m1_t value, size_t vl);
void vsuxei32_v_f32m2_m (vbool16_t mask, float32_t *base,
    vuint32m2_t bindex, vfloat32m2_t value, size_t vl);
void vsuxei32_v_f32m4_m (vbool8_t mask, float32_t *base,
    vuint32m4_t bindex, vfloat32m4_t value, size_t vl);
void vsuxei32_v_f32m8_m (vbool4_t mask, float32_t *base,
    vuint32m8_t bindex, vfloat32m8_t value, size_t vl);

```

```

void vsuxei64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vuint64m1_t bindex, vfloat64m1_t value, size_t vl);
void vsuxei64_v_f64m2_m (vbool32_t mask, float64_t *base,
    vuint64m2_t bindex, vfloat64m2_t value, size_t vl);
void vsuxei64_v_f64m4_m (vbool16_t mask, float64_t *base,
    vuint64m4_t bindex, vfloat64m4_t value, size_t vl);
void vsuxei64_v_f64m8_m (vbool8_t mask, float64_t *base,
    vuint64m8_t bindex, vfloat64m8_t value, size_t vl);

```

### Unit-stride Fault-Only-First Loads Functions:

#### Prototypes:

```

vint8m1_t vle8ff_v_i8m1 (const int8_t *base, size_t *new_vl,
    size_t vl);
vint8m2_t vle8ff_v_i8m2 (const int8_t *base, size_t *new_vl,
    size_t vl);
vint8m4_t vle8ff_v_i8m4 (const int8_t *base, size_t *new_vl,
    size_t vl);
vint8m8_t vle8ff_v_i8m8 (const int8_t *base, size_t *new_vl,
    size_t vl);
vint16m1_t vle16ff_v_i16m1 (const int16_t *base, size_t *new_vl,
    size_t vl);
vint16m2_t vle16ff_v_i16m2 (const int16_t *base, size_t *new_vl,
    size_t vl);
vint16m4_t vle16ff_v_i16m4 (const int16_t *base, size_t *new_vl,
    size_t vl);
vint16m8_t vle16ff_v_i16m8 (const int16_t *base, size_t *new_vl,
    size_t vl);
vint32m1_t vle32ff_v_i32m1 (const int32_t *base, size_t *new_vl,
    size_t vl);
vint32m2_t vle32ff_v_i32m2 (const int32_t *base, size_t *new_vl,
    size_t vl);
vint32m4_t vle32ff_v_i32m4 (const int32_t *base, size_t *new_vl,
    size_t vl);
vint32m8_t vle32ff_v_i32m8 (const int32_t *base, size_t *new_vl,
    size_t vl);
vint64m1_t vle64ff_v_i64m1 (const int64_t *base, size_t *new_vl,
    size_t vl);
vint64m2_t vle64ff_v_i64m2 (const int64_t *base, size_t *new_vl,
    size_t vl);
vint64m4_t vle64ff_v_i64m4 (const int64_t *base, size_t *new_vl,
    size_t vl);
vint64m8_t vle64ff_v_i64m8 (const int64_t *base, size_t *new_vl,
    size_t vl);

```



```

vuint8m1_t vle8ff_v_u8m1 (const uint8_t *base, size_t *new_vl,
    size_t vl);
vuint8m2_t vle8ff_v_u8m2 (const uint8_t *base, size_t *new_vl,
    size_t vl);
vuint8m4_t vle8ff_v_u8m4 (const uint8_t *base, size_t *new_vl,
    size_t vl);
vuint8m8_t vle8ff_v_u8m8 (const uint8_t *base, size_t *new_vl,
    size_t vl);
vuint16m1_t vle16ff_v_u16m1 (const uint16_t *base, size_t
    *new_vl, size_t vl);
vuint16m2_t vle16ff_v_u16m2 (const uint16_t *base, size_t
    *new_vl, size_t vl);
vuint16m4_t vle16ff_v_u16m4 (const uint16_t *base, size_t
    *new_vl, size_t vl);
vuint16m8_t vle16ff_v_u16m8 (const uint16_t *base, size_t
    *new_vl, size_t vl);
vuint32m1_t vle32ff_v_u32m1 (const uint32_t *base, size_t
    *new_vl, size_t vl);
vuint32m2_t vle32ff_v_u32m2 (const uint32_t *base, size_t
    *new_vl, size_t vl);
vuint32m4_t vle32ff_v_u32m4 (const uint32_t *base, size_t
    *new_vl, size_t vl);
vuint32m8_t vle32ff_v_u32m8 (const uint32_t *base, size_t
    *new_vl, size_t vl);
vuint64m1_t vle64ff_v_u64m1 (const uint64_t *base, size_t
    *new_vl, size_t vl);
vuint64m2_t vle64ff_v_u64m2 (const uint64_t *base, size_t
    *new_vl, size_t vl);
vuint64m4_t vle64ff_v_u64m4 (const uint64_t *base, size_t
    *new_vl, size_t vl);
vuint64m8_t vle64ff_v_u64m8 (const uint64_t *base, size_t
    *new_vl, size_t vl);
vfloat16m1_t vle16ff_v_f16m1 (const float16_t *base, size_t
    *new_vl, size_t vl);
vfloat16m2_t vle16ff_v_f16m2 (const float16_t *base, size_t
    *new_vl, size_t vl);
vfloat16m4_t vle16ff_v_f16m4 (const float16_t *base, size_t
    *new_vl, size_t vl);
vfloat16m8_t vle16ff_v_f16m8 (const float16_t *base, size_t
    *new_vl, size_t vl);
vfloat32m1_t vle32ff_v_f32m1 (const float32_t *base, size_t
    *new_vl, size_t vl);
vfloat32m2_t vle32ff_v_f32m2 (const float32_t *base, size_t
    *new_vl, size_t vl);
vfloat32m4_t vle32ff_v_f32m4 (const float32_t *base, size_t
    *new_vl, size_t vl);

```

```

vfloat32m8_t vle32ff_v_f32m8 (const float32_t *base, size_t
    *new_vl, size_t vl);
vfloat64m1_t vle64ff_v_f64m1 (const float64_t *base, size_t
    *new_vl, size_t vl);
vfloat64m2_t vle64ff_v_f64m2 (const float64_t *base, size_t
    *new_vl, size_t vl);
vfloat64m4_t vle64ff_v_f64m4 (const float64_t *base, size_t
    *new_vl, size_t vl);
vfloat64m8_t vle64ff_v_f64m8 (const float64_t *base, size_t
    *new_vl, size_t vl);
// masked functions
vint8m1_t vle8ff_v_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    const int8_t *base, size_t *new_vl, size_t vl);
vint8m2_t vle8ff_v_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    const int8_t *base, size_t *new_vl, size_t vl);
vint8m4_t vle8ff_v_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    const int8_t *base, size_t *new_vl, size_t vl);
vint8m8_t vle8ff_v_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    const int8_t *base, size_t *new_vl, size_t vl);
vint16m1_t vle16ff_v_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, const int16_t *base, size_t *new_vl, size_t vl);
vint16m2_t vle16ff_v_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, const int16_t *base, size_t *new_vl, size_t vl);
vint16m4_t vle16ff_v_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, const int16_t *base, size_t *new_vl, size_t vl);
vint16m8_t vle16ff_v_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, const int16_t *base, size_t *new_vl, size_t vl);
vint32m1_t vle32ff_v_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, const int32_t *base, size_t *new_vl, size_t vl);
vint32m2_t vle32ff_v_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, const int32_t *base, size_t *new_vl, size_t vl);
vint32m4_t vle32ff_v_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, const int32_t *base, size_t *new_vl, size_t vl);
vint32m8_t vle32ff_v_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, const int32_t *base, size_t *new_vl, size_t vl);
vint64m1_t vle64ff_v_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, const int64_t *base, size_t *new_vl, size_t vl);
vint64m2_t vle64ff_v_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, const int64_t *base, size_t *new_vl, size_t vl);
vint64m4_t vle64ff_v_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, const int64_t *base, size_t *new_vl, size_t vl);
vint64m8_t vle64ff_v_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, const int64_t *base, size_t *new_vl, size_t vl);
vuint8m1_t vle8ff_v_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    const uint8_t *base, size_t *new_vl, size_t vl);

```

```

vuint8m2_t vle8ff_v_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    const uint8_t *base, size_t *new_vl, size_t vl);
vuint8m4_t vle8ff_v_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    const uint8_t *base, size_t *new_vl, size_t vl);
vuint8m8_t vle8ff_v_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    const uint8_t *base, size_t *new_vl, size_t vl);
vuint16m1_t vle16ff_v_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, const uint16_t *base, size_t *new_vl, size_t vl);
vuint16m2_t vle16ff_v_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, const uint16_t *base, size_t *new_vl, size_t vl);
vuint16m4_t vle16ff_v_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, const uint16_t *base, size_t *new_vl, size_t vl);
vuint16m8_t vle16ff_v_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, const uint16_t *base, size_t *new_vl, size_t vl);
vuint32m1_t vle32ff_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, const uint32_t *base, size_t *new_vl, size_t vl);
vuint32m2_t vle32ff_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, const uint32_t *base, size_t *new_vl, size_t vl);
vuint32m4_t vle32ff_v_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, const uint32_t *base, size_t *new_vl, size_t vl);
vuint32m8_t vle32ff_v_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, const uint32_t *base, size_t *new_vl, size_t vl);
vuint64m1_t vle64ff_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, const uint64_t *base, size_t *new_vl, size_t vl);
vuint64m2_t vle64ff_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, const uint64_t *base, size_t *new_vl, size_t vl);
vuint64m4_t vle64ff_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, const uint64_t *base, size_t *new_vl, size_t vl);
vuint64m8_t vle64ff_v_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, const uint64_t *base, size_t *new_vl, size_t vl);
vfloat16m1_t vle16ff_v_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, const float16_t *base, size_t *new_vl, size_t vl);
vfloat16m2_t vle16ff_v_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, const float16_t *base, size_t *new_vl, size_t vl);
vfloat16m4_t vle16ff_v_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, const float16_t *base, size_t *new_vl, size_t vl);
vfloat16m8_t vle16ff_v_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, const float16_t *base, size_t *new_vl, size_t vl);
vfloat32m1_t vle32ff_v_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, const float32_t *base, size_t *new_vl, size_t vl);
vfloat32m2_t vle32ff_v_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, const float32_t *base, size_t *new_vl, size_t vl);
vfloat32m4_t vle32ff_v_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, const float32_t *base, size_t *new_vl, size_t vl);
vfloat32m8_t vle32ff_v_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, const float32_t *base, size_t *new_vl, size_t vl);

```

```

vfloat64m1_t vle64ff_v_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, const float64_t *base, size_t *new_vl, size_t vl);
vfloat64m2_t vle64ff_v_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, const float64_t *base, size_t *new_vl, size_t vl);
vfloat64m4_t vle64ff_v_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, const float64_t *base, size_t *new_vl, size_t vl);
vfloat64m8_t vle64ff_v_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, const float64_t *base, size_t *new_vl, size_t vl);

```

## Vector Load/Store Segment Instructions (Zvlsseg):

### Vector Unit-Stride Segment Load Functions:

#### Prototypes:

```

void vlseg2e8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, const int8_t
    *base, size_t vl);
void vlseg3e8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, const int8_t *base, size_t vl);
void vlseg4e8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, const int8_t *base, size_t vl);
void vlseg5e8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, const int8_t *base,
    size_t vl);
void vlseg6e8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, const
    int8_t *base, size_t vl);
void vlseg7e8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
    *v6, const int8_t *base, size_t vl);
void vlseg8e8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
    *v6, vint8m1_t *v7, const int8_t *base, size_t vl);
void vlseg2e8_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, const int8_t
    *base, size_t vl);
void vlseg3e8_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, const int8_t *base, size_t vl);
void vlseg4e8_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, vint8m2_t *v3, const int8_t *base, size_t vl);
void vlseg2e8_v_i8m4 (vint8m4_t *v0, vint8m4_t *v1, const int8_t
    *base, size_t vl);
void vlseg2e16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, const
    int16_t *base, size_t vl);
void vlseg3e16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, const int16_t *base, size_t vl);

```

```

void vlseg4e16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, const int16_t *base, size_t
    vl);
void vlseg5e16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, const
    int16_t *base, size_t vl);
void vlseg6e16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, const int16_t *base, size_t vl);
void vlseg7e16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, const int16_t *base, size_t vl);
void vlseg8e16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, vint16m1_t *v7, const int16_t *base,
    size_t vl);
void vlseg2e16_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1, const
    int16_t *base, size_t vl);
void vlseg3e16_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, const int16_t *base, size_t vl);
void vlseg4e16_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vint16m2_t *v3, const int16_t *base, size_t
    vl);
void vlseg2e16_v_i16m4 (vint16m4_t *v0, vint16m4_t *v1, const
    int16_t *base, size_t vl);
void vlseg2e32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, const
    int32_t *base, size_t vl);
void vlseg3e32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, const int32_t *base, size_t vl);
void vlseg4e32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, const int32_t *base, size_t
    vl);
void vlseg5e32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, const
    int32_t *base, size_t vl);
void vlseg6e32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, const int32_t *base, size_t vl);
void vlseg7e32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, const int32_t *base, size_t vl);
void vlseg8e32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, const int32_t *base,
    size_t vl);

```

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void vlseg2e32_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, const
    int32_t *base, size_t vl);
void vlseg3e32_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, const int32_t *base, size_t vl);
void vlseg4e32_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, const int32_t *base, size_t
    vl);
void vlseg2e32_v_i32m4 (vint32m4_t *v0, vint32m4_t *v1, const
    int32_t *base, size_t vl);
void vlseg2e64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, const
    int64_t *base, size_t vl);
void vlseg3e64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, const int64_t *base, size_t vl);
void vlseg4e64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, const int64_t *base, size_t
    vl);
void vlseg5e64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, const
    int64_t *base, size_t vl);
void vlseg6e64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, const int64_t *base, size_t vl);
void vlseg7e64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, const int64_t *base, size_t vl);
void vlseg8e64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vint64m1_t *v7, const int64_t *base,
    size_t vl);
void vlseg2e64_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, const
    int64_t *base, size_t vl);
void vlseg3e64_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, const int64_t *base, size_t vl);
void vlseg4e64_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, const int64_t *base, size_t
    vl);
void vlseg2e64_v_i64m4 (vint64m4_t *v0, vint64m4_t *v1, const
    int64_t *base, size_t vl);
void vlseg2e8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, const
    uint8_t *base, size_t vl);
void vlseg3e8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, vuint8m1_t
    *v2, const uint8_t *base, size_t vl);
void vlseg4e8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, vuint8m1_t
    *v2, vuint8m1_t *v3, const uint8_t *base, size_t vl);
void vlseg5e8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, vuint8m1_t
    *v2, vuint8m1_t *v3, vuint8m1_t *v4, const uint8_t *base,

```

```

    size_t vl);
void vlseg6e8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, vuint8m1_t
    *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t *v5, const
    uint8_t *base, size_t vl);
void vlseg7e8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, vuint8m1_t
    *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t *v5,
    vuint8m1_t *v6, const uint8_t *base, size_t vl);
void vlseg8e8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, vuint8m1_t
    *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t *v5,
    vuint8m1_t *v6, vuint8m1_t *v7, const uint8_t *base, size_t
    vl);
void vlseg2e8_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1, const
    uint8_t *base, size_t vl);
void vlseg3e8_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1, vuint8m2_t
    *v2, const uint8_t *base, size_t vl);
void vlseg4e8_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1, vuint8m2_t
    *v2, vuint8m2_t *v3, const uint8_t *base, size_t vl);
void vlseg2e8_v_u8m4 (vuint8m4_t *v0, vuint8m4_t *v1, const
    uint8_t *base, size_t vl);
void vlseg2e16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1, const
    uint16_t *base, size_t vl);
void vlseg3e16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, const uint16_t *base, size_t vl);
void vlseg4e16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, const uint16_t *base,
    size_t vl);
void vlseg5e16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, const
    uint16_t *base, size_t vl);
void vlseg6e16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, const uint16_t *base, size_t vl);
void vlseg7e16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, const uint16_t *base,
    size_t vl);
void vlseg8e16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, const
    uint16_t *base, size_t vl);
void vlseg2e16_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1, const
    uint16_t *base, size_t vl);
void vlseg3e16_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, const uint16_t *base, size_t vl);
void vlseg4e16_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vuint16m2_t *v3, const uint16_t *base,

```

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    size_t vl);
void vlseg2e16_v_u16m4 (vuint16m4_t *v0, vuint16m4_t *v1, const
    uint16_t *base, size_t vl);
void vlseg2e32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1, const
    uint32_t *base, size_t vl);
void vlseg3e32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, const uint32_t *base, size_t vl);
void vlseg4e32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, const uint32_t *base,
    size_t vl);
void vlseg5e32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, const
    uint32_t *base, size_t vl);
void vlseg6e32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, const uint32_t *base, size_t vl);
void vlseg7e32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, const uint32_t *base,
    size_t vl);
void vlseg8e32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, const
    uint32_t *base, size_t vl);
void vlseg2e32_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1, const
    uint32_t *base, size_t vl);
void vlseg3e32_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, const uint32_t *base, size_t vl);
void vlseg4e32_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, const uint32_t *base,
    size_t vl);
void vlseg2e32_v_u32m4 (vuint32m4_t *v0, vuint32m4_t *v1, const
    uint32_t *base, size_t vl);
void vlseg2e64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1, const
    uint64_t *base, size_t vl);
void vlseg3e64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, const uint64_t *base, size_t vl);
void vlseg4e64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, const uint64_t *base,
    size_t vl);
void vlseg5e64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, const
    uint64_t *base, size_t vl);
void vlseg6e64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, const uint64_t *base, size_t vl);

```



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void vlseg7e64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, const uint64_t *base,
    size_t vl);
void vlseg8e64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, const
    uint64_t *base, size_t vl);
void vlseg2e64_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1, const
    uint64_t *base, size_t vl);
void vlseg3e64_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, const uint64_t *base, size_t vl);
void vlseg4e64_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, const uint64_t *base,
    size_t vl);
void vlseg2e64_v_u64m4 (vuint64m4_t *v0, vuint64m4_t *v1, const
    uint64_t *base, size_t vl);
void vlseg2e16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    const float16_t *base, size_t vl);
void vlseg3e16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, const float16_t *base, size_t vl);
void vlseg4e16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, const float16_t *base,
    size_t vl);
void vlseg5e16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4, const
    float16_t *base, size_t vl);
void vlseg6e16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, const float16_t *base, size_t vl);
void vlseg7e16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, vfloat16m1_t *v6, const float16_t *base,
    size_t vl);
void vlseg8e16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, vfloat16m1_t *v6, vfloat16m1_t *v7, const
    float16_t *base, size_t vl);
void vlseg2e16_v_f16m2 (vfloat16m2_t *v0, vfloat16m2_t *v1,
    const float16_t *base, size_t vl);
void vlseg3e16_v_f16m2 (vfloat16m2_t *v0, vfloat16m2_t *v1,
    vfloat16m2_t *v2, const float16_t *base, size_t vl);
void vlseg4e16_v_f16m2 (vfloat16m2_t *v0, vfloat16m2_t *v1,
    vfloat16m2_t *v2, vfloat16m2_t *v3, const float16_t *base,
    size_t vl);

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void vlseg2e16_v_f16m4 (vfloat16m4_t *v0, vfloat16m4_t *v1,
    const float16_t *base, size_t vl);
void vlseg2e32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    const float32_t *base, size_t vl);
void vlseg3e32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, const float32_t *base, size_t vl);
void vlseg4e32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, const float32_t *base,
    size_t vl);
void vlseg5e32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4, const
    float32_t *base, size_t vl);
void vlseg6e32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, const float32_t *base, size_t vl);
void vlseg7e32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, vfloat32m1_t *v6, const float32_t *base,
    size_t vl);
void vlseg8e32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, vfloat32m1_t *v6, vfloat32m1_t *v7, const
    float32_t *base, size_t vl);
void vlseg2e32_v_f32m2 (vfloat32m2_t *v0, vfloat32m2_t *v1,
    const float32_t *base, size_t vl);
void vlseg3e32_v_f32m2 (vfloat32m2_t *v0, vfloat32m2_t *v1,
    vfloat32m2_t *v2, const float32_t *base, size_t vl);
void vlseg4e32_v_f32m2 (vfloat32m2_t *v0, vfloat32m2_t *v1,
    vfloat32m2_t *v2, vfloat32m2_t *v3, const float32_t *base,
    size_t vl);
void vlseg2e32_v_f32m4 (vfloat32m4_t *v0, vfloat32m4_t *v1,
    const float32_t *base, size_t vl);
void vlseg2e64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    const float64_t *base, size_t vl);
void vlseg3e64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, const float64_t *base, size_t vl);
void vlseg4e64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, const float64_t *base,
    size_t vl);
void vlseg5e64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4, const
    float64_t *base, size_t vl);
void vlseg6e64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, const float64_t *base, size_t vl);

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void vlseg7e64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, vfloat64m1_t *v6, const float64_t *base,
    size_t vl);
void vlseg8e64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, vfloat64m1_t *v6, vfloat64m1_t *v7, const
    float64_t *base, size_t vl);
void vlseg2e64_v_f64m2 (vfloat64m2_t *v0, vfloat64m2_t *v1,
    const float64_t *base, size_t vl);
void vlseg3e64_v_f64m2 (vfloat64m2_t *v0, vfloat64m2_t *v1,
    vfloat64m2_t *v2, const float64_t *base, size_t vl);
void vlseg4e64_v_f64m2 (vfloat64m2_t *v0, vfloat64m2_t *v1,
    vfloat64m2_t *v2, vfloat64m2_t *v3, const float64_t *base,
    size_t vl);
void vlseg2e64_v_f64m4 (vfloat64m4_t *v0, vfloat64m4_t *v1,
    const float64_t *base, size_t vl);
void vlseg2e8ff_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, const
    int8_t *base, size_t *new_vl, size_t vl);
void vlseg3e8ff_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, const int8_t *base, size_t *new_vl, size_t vl);
void vlseg4e8ff_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, const int8_t *base, size_t *new_vl,
    size_t vl);
void vlseg5e8ff_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, const int8_t *base,
    size_t *new_vl, size_t vl);
void vlseg6e8ff_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, const
    int8_t *base, size_t *new_vl, size_t vl);
void vlseg7e8ff_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
    *v6, const int8_t *base, size_t *new_vl, size_t vl);
void vlseg8e8ff_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
    *v6, vint8m1_t *v7, const int8_t *base, size_t *new_vl,
    size_t vl);
void vlseg2e8ff_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, const
    int8_t *base, size_t *new_vl, size_t vl);
void vlseg3e8ff_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, const int8_t *base, size_t *new_vl, size_t vl);
void vlseg4e8ff_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, vint8m2_t *v3, const int8_t *base, size_t *new_vl,
    size_t vl);
void vlseg2e8ff_v_i8m4 (vint8m4_t *v0, vint8m4_t *v1, const
    int8_t *base, size_t *new_vl, size_t vl);

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void vlseg2e16ff_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, const
    int16_t *base, size_t *new_vl, size_t vl);
void vlseg3e16ff_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, const int16_t *base, size_t *new_vl, size_t
    vl);
void vlseg4e16ff_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, const int16_t *base, size_t
    *new_vl, size_t vl);
void vlseg5e16ff_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, const
    int16_t *base, size_t *new_vl, size_t vl);
void vlseg6e16ff_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, const int16_t *base, size_t *new_vl, size_t vl);
void vlseg7e16ff_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, const int16_t *base, size_t *new_vl,
    size_t vl);
void vlseg8e16ff_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, vint16m1_t *v7, const int16_t *base,
    size_t *new_vl, size_t vl);
void vlseg2e16ff_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1, const
    int16_t *base, size_t *new_vl, size_t vl);
void vlseg3e16ff_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, const int16_t *base, size_t *new_vl, size_t
    vl);
void vlseg4e16ff_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vint16m2_t *v3, const int16_t *base, size_t
    *new_vl, size_t vl);
void vlseg2e16ff_v_i16m4 (vint16m4_t *v0, vint16m4_t *v1, const
    int16_t *base, size_t *new_vl, size_t vl);
void vlseg2e32ff_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, const
    int32_t *base, size_t *new_vl, size_t vl);
void vlseg3e32ff_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, const int32_t *base, size_t *new_vl, size_t
    vl);
void vlseg4e32ff_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, const int32_t *base, size_t
    *new_vl, size_t vl);
void vlseg5e32ff_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, const
    int32_t *base, size_t *new_vl, size_t vl);
void vlseg6e32ff_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, const int32_t *base, size_t *new_vl, size_t vl);

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void vlseg7e32ff_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, const int32_t *base, size_t *new_vl,
    size_t vl);
void vlseg8e32ff_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, const int32_t *base,
    size_t *new_vl, size_t vl);
void vlseg2e32ff_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, const
    int32_t *base, size_t *new_vl, size_t vl);
void vlseg3e32ff_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, const int32_t *base, size_t *new_vl, size_t
    vl);
void vlseg4e32ff_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, const int32_t *base, size_t
    *new_vl, size_t vl);
void vlseg2e32ff_v_i32m4 (vint32m4_t *v0, vint32m4_t *v1, const
    int32_t *base, size_t *new_vl, size_t vl);
void vlseg2e64ff_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, const
    int64_t *base, size_t *new_vl, size_t vl);
void vlseg3e64ff_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, const int64_t *base, size_t *new_vl, size_t
    vl);
void vlseg4e64ff_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, const int64_t *base, size_t
    *new_vl, size_t vl);
void vlseg5e64ff_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, const
    int64_t *base, size_t *new_vl, size_t vl);
void vlseg6e64ff_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, const int64_t *base, size_t *new_vl, size_t vl);
void vlseg7e64ff_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, const int64_t *base, size_t *new_vl,
    size_t vl);
void vlseg8e64ff_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vint64m1_t *v7, const int64_t *base,
    size_t *new_vl, size_t vl);
void vlseg2e64ff_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, const
    int64_t *base, size_t *new_vl, size_t vl);
void vlseg3e64ff_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, const int64_t *base, size_t *new_vl, size_t
    vl);

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void vlseg4e64ff_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, const int64_t *base, size_t
    *new_vl, size_t vl);
void vlseg2e64ff_v_i64m4 (vint64m4_t *v0, vint64m4_t *v1, const
    int64_t *base, size_t *new_vl, size_t vl);
void vlseg2e8ff_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, const
    uint8_t *base, size_t *new_vl, size_t vl);
void vlseg3e8ff_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, const uint8_t *base, size_t *new_vl, size_t
    vl);
void vlseg4e8ff_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, const uint8_t *base, size_t
    *new_vl, size_t vl);
void vlseg5e8ff_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, const
    uint8_t *base, size_t *new_vl, size_t vl);
void vlseg6e8ff_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, const uint8_t *base, size_t *new_vl, size_t vl);
void vlseg7e8ff_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, const uint8_t *base, size_t *new_vl,
    size_t vl);
void vlseg8e8ff_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, vuint8m1_t *v7, const uint8_t *base,
    size_t *new_vl, size_t vl);
void vlseg2e8ff_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1, const
    uint8_t *base, size_t *new_vl, size_t vl);
void vlseg3e8ff_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, const uint8_t *base, size_t *new_vl, size_t
    vl);
void vlseg4e8ff_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, vuint8m2_t *v3, const uint8_t *base, size_t
    *new_vl, size_t vl);
void vlseg2e8ff_v_u8m4 (vuint8m4_t *v0, vuint8m4_t *v1, const
    uint8_t *base, size_t *new_vl, size_t vl);
void vlseg2e16ff_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    const uint16_t *base, size_t *new_vl, size_t vl);
void vlseg3e16ff_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, const uint16_t *base, size_t *new_vl,
    size_t vl);
void vlseg4e16ff_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, const uint16_t *base,
    size_t *new_vl, size_t vl);

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void vlseg5e16ff_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, const
    uint16_t *base, size_t *new_vl, size_t vl);
void vlseg6e16ff_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, const uint16_t *base, size_t *new_vl,
    size_t vl);
void vlseg7e16ff_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, const uint16_t *base,
    size_t *new_vl, size_t vl);
void vlseg8e16ff_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, const
    uint16_t *base, size_t *new_vl, size_t vl);
void vlseg2e16ff_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    const uint16_t *base, size_t *new_vl, size_t vl);
void vlseg3e16ff_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, const uint16_t *base, size_t *new_vl,
    size_t vl);
void vlseg4e16ff_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vuint16m2_t *v3, const uint16_t *base,
    size_t *new_vl, size_t vl);
void vlseg2e16ff_v_u16m4 (vuint16m4_t *v0, vuint16m4_t *v1,
    const uint16_t *base, size_t *new_vl, size_t vl);
void vlseg2e32ff_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    const uint32_t *base, size_t *new_vl, size_t vl);
void vlseg3e32ff_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, const uint32_t *base, size_t *new_vl,
    size_t vl);
void vlseg4e32ff_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, const uint32_t *base,
    size_t *new_vl, size_t vl);
void vlseg5e32ff_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, const
    uint32_t *base, size_t *new_vl, size_t vl);
void vlseg6e32ff_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, const uint32_t *base, size_t *new_vl,
    size_t vl);
void vlseg7e32ff_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, const uint32_t *base,
    size_t *new_vl, size_t vl);
void vlseg8e32ff_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,

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    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, const
    uint32_t *base, size_t *new_vl, size_t vl);
void vlseg2e32ff_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    const uint32_t *base, size_t *new_vl, size_t vl);
void vlseg3e32ff_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, const uint32_t *base, size_t *new_vl,
    size_t vl);
void vlseg4e32ff_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, const uint32_t *base,
    size_t *new_vl, size_t vl);
void vlseg2e32ff_v_u32m4 (vuint32m4_t *v0, vuint32m4_t *v1,
    const uint32_t *base, size_t *new_vl, size_t vl);
void vlseg2e64ff_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    const uint64_t *base, size_t *new_vl, size_t vl);
void vlseg3e64ff_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, const uint64_t *base, size_t *new_vl,
    size_t vl);
void vlseg4e64ff_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, const uint64_t *base,
    size_t *new_vl, size_t vl);
void vlseg5e64ff_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, const
    uint64_t *base, size_t *new_vl, size_t vl);
void vlseg6e64ff_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, const uint64_t *base, size_t *new_vl,
    size_t vl);
void vlseg7e64ff_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, const uint64_t *base,
    size_t *new_vl, size_t vl);
void vlseg8e64ff_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, const
    uint64_t *base, size_t *new_vl, size_t vl);
void vlseg2e64ff_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    const uint64_t *base, size_t *new_vl, size_t vl);
void vlseg3e64ff_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, const uint64_t *base, size_t *new_vl,
    size_t vl);
void vlseg4e64ff_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, const uint64_t *base,
    size_t *new_vl, size_t vl);
void vlseg2e64ff_v_u64m4 (vuint64m4_t *v0, vuint64m4_t *v1,
    const uint64_t *base, size_t *new_vl, size_t vl);

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void vlseg2e16ff_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    const float16_t *base, size_t *new_vl, size_t vl);
void vlseg3e16ff_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, const float16_t *base, size_t *new_vl,
    size_t vl);
void vlseg4e16ff_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, const float16_t *base,
    size_t *new_vl, size_t vl);
void vlseg5e16ff_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4, const
    float16_t *base, size_t *new_vl, size_t vl);
void vlseg6e16ff_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, const float16_t *base, size_t *new_vl,
    size_t vl);
void vlseg7e16ff_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, vfloat16m1_t *v6, const float16_t *base,
    size_t *new_vl, size_t vl);
void vlseg8e16ff_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, vfloat16m1_t *v6, vfloat16m1_t *v7, const
    float16_t *base, size_t *new_vl, size_t vl);
void vlseg2e16ff_v_f16m2 (vfloat16m2_t *v0, vfloat16m2_t *v1,
    const float16_t *base, size_t *new_vl, size_t vl);
void vlseg3e16ff_v_f16m2 (vfloat16m2_t *v0, vfloat16m2_t *v1,
    vfloat16m2_t *v2, const float16_t *base, size_t *new_vl,
    size_t vl);
void vlseg4e16ff_v_f16m2 (vfloat16m2_t *v0, vfloat16m2_t *v1,
    vfloat16m2_t *v2, vfloat16m2_t *v3, const float16_t *base,
    size_t *new_vl, size_t vl);
void vlseg2e16ff_v_f16m4 (vfloat16m4_t *v0, vfloat16m4_t *v1,
    const float16_t *base, size_t *new_vl, size_t vl);
void vlseg2e32ff_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    const float32_t *base, size_t *new_vl, size_t vl);
void vlseg3e32ff_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, const float32_t *base, size_t *new_vl,
    size_t vl);
void vlseg4e32ff_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, const float32_t *base,
    size_t *new_vl, size_t vl);
void vlseg5e32ff_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4, const
    float32_t *base, size_t *new_vl, size_t vl);
void vlseg6e32ff_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,

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    vfloat32m1_t *v5, const float32_t *base, size_t *new_vl,
    size_t vl);
void vlseg7e32ff_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, vfloat32m1_t *v6, const float32_t *base,
    size_t *new_vl, size_t vl);
void vlseg8e32ff_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, vfloat32m1_t *v6, vfloat32m1_t *v7, const
    float32_t *base, size_t *new_vl, size_t vl);
void vlseg2e32ff_v_f32m2 (vfloat32m2_t *v0, vfloat32m2_t *v1,
    const float32_t *base, size_t *new_vl, size_t vl);
void vlseg3e32ff_v_f32m2 (vfloat32m2_t *v0, vfloat32m2_t *v1,
    vfloat32m2_t *v2, const float32_t *base, size_t *new_vl,
    size_t vl);
void vlseg4e32ff_v_f32m2 (vfloat32m2_t *v0, vfloat32m2_t *v1,
    vfloat32m2_t *v2, vfloat32m2_t *v3, const float32_t *base,
    size_t *new_vl, size_t vl);
void vlseg2e32ff_v_f32m4 (vfloat32m4_t *v0, vfloat32m4_t *v1,
    const float32_t *base, size_t *new_vl, size_t vl);
void vlseg2e64ff_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    const float64_t *base, size_t *new_vl, size_t vl);
void vlseg3e64ff_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, const float64_t *base, size_t *new_vl,
    size_t vl);
void vlseg4e64ff_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, const float64_t *base,
    size_t *new_vl, size_t vl);
void vlseg5e64ff_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4, const
    float64_t *base, size_t *new_vl, size_t vl);
void vlseg6e64ff_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, const float64_t *base, size_t *new_vl,
    size_t vl);
void vlseg7e64ff_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, vfloat64m1_t *v6, const float64_t *base,
    size_t *new_vl, size_t vl);
void vlseg8e64ff_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, vfloat64m1_t *v6, vfloat64m1_t *v7, const
    float64_t *base, size_t *new_vl, size_t vl);
void vlseg2e64ff_v_f64m2 (vfloat64m2_t *v0, vfloat64m2_t *v1,
    const float64_t *base, size_t *new_vl, size_t vl);

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void vlseg3e64ff_v_f64m2 (vfloat64m2_t *v0, vfloat64m2_t *v1,
    vfloat64m2_t *v2, const float64_t *base, size_t *new_vl,
    size_t vl);
void vlseg4e64ff_v_f64m2 (vfloat64m2_t *v0, vfloat64m2_t *v1,
    vfloat64m2_t *v2, vfloat64m2_t *v3, const float64_t *base,
    size_t *new_vl, size_t vl);
void vlseg2e64ff_v_f64m4 (vfloat64m4_t *v0, vfloat64m4_t *v1,
    const float64_t *base, size_t *new_vl, size_t vl);
// masked functions
void vlseg2e8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vbool8_t
    mask, vint8m1_t maskedoff0, vint8m1_t maskedoff1, const
    int8_t *base, size_t vl);
void vlseg3e8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vbool8_t mask, vint8m1_t maskedoff0, vint8m1_t
    maskedoff1, vint8m1_t maskedoff2, const int8_t *base, size_t
    vl);
void vlseg4e8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vbool8_t mask, vint8m1_t maskedoff0,
    vint8m1_t maskedoff1, vint8m1_t maskedoff2, vint8m1_t
    maskedoff3, const int8_t *base, size_t vl);
void vlseg5e8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vbool8_t mask, vint8m1_t
    maskedoff0, vint8m1_t maskedoff1, vint8m1_t maskedoff2,
    vint8m1_t maskedoff3, vint8m1_t maskedoff4, const int8_t
    *base, size_t vl);
void vlseg6e8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vbool8_t
    mask, vint8m1_t maskedoff0, vint8m1_t maskedoff1, vint8m1_t
    maskedoff2, vint8m1_t maskedoff3, vint8m1_t maskedoff4,
    vint8m1_t maskedoff5, const int8_t *base, size_t vl);
void vlseg7e8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
    *v6, vbool8_t mask, vint8m1_t maskedoff0, vint8m1_t
    maskedoff1, vint8m1_t maskedoff2, vint8m1_t maskedoff3,
    vint8m1_t maskedoff4, vint8m1_t maskedoff5, vint8m1_t
    maskedoff6, const int8_t *base, size_t vl);
void vlseg8e8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
    *v6, vint8m1_t *v7, vbool8_t mask, vint8m1_t maskedoff0,
    vint8m1_t maskedoff1, vint8m1_t maskedoff2, vint8m1_t
    maskedoff3, vint8m1_t maskedoff4, vint8m1_t maskedoff5,
    vint8m1_t maskedoff6, vint8m1_t maskedoff7, const int8_t
    *base, size_t vl);
void vlseg2e8_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1, vbool4_t
    mask, vint8m2_t maskedoff0, vint8m2_t maskedoff1, const
    int8_t *base, size_t vl);

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void vlseg3e8_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
*v2, vbool4_t mask, vint8m2_t maskedoff0, vint8m2_t
maskedoff1, vint8m2_t maskedoff2, const int8_t *base, size_t
vl);
void vlseg4e8_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
*v2, vint8m2_t *v3, vbool4_t mask, vint8m2_t maskedoff0,
vint8m2_t maskedoff1, vint8m2_t maskedoff2, vint8m2_t
maskedoff3, const int8_t *base, size_t vl);
void vlseg2e8_v_i8m4_m (vint8m4_t *v0, vint8m4_t *v1, vbool2_t
mask, vint8m4_t maskedoff0, vint8m4_t maskedoff1, const
int8_t *base, size_t vl);
void vlseg2e16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
maskedoff1, const int16_t *base, size_t vl);
void vlseg3e16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vbool16_t mask, vint16m1_t maskedoff0,
vint16m1_t maskedoff1, vint16m1_t maskedoff2, const int16_t
*base, size_t vl);
void vlseg4e16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vbool16_t mask, vint16m1_t
maskedoff0, vint16m1_t maskedoff1, vint16m1_t maskedoff2,
vint16m1_t maskedoff3, const int16_t *base, size_t vl);
void vlseg5e16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vbool16_t
mask, vint16m1_t maskedoff0, vint16m1_t maskedoff1,
vint16m1_t maskedoff2, vint16m1_t maskedoff3, vint16m1_t
maskedoff4, const int16_t *base, size_t vl);
void vlseg6e16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
maskedoff1, vint16m1_t maskedoff2, vint16m1_t maskedoff3,
vint16m1_t maskedoff4, vint16m1_t maskedoff5, const int16_t
*base, size_t vl);
void vlseg7e16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vint16m1_t *v6, vbool16_t mask, vint16m1_t maskedoff0,
vint16m1_t maskedoff1, vint16m1_t maskedoff2, vint16m1_t
maskedoff3, vint16m1_t maskedoff4, vint16m1_t maskedoff5,
vint16m1_t maskedoff6, const int16_t *base, size_t vl);
void vlseg8e16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vint16m1_t *v6, vint16m1_t *v7, vbool16_t mask,
vint16m1_t maskedoff0, vint16m1_t maskedoff1, vint16m1_t
maskedoff2, vint16m1_t maskedoff3, vint16m1_t maskedoff4,
vint16m1_t maskedoff5, vint16m1_t maskedoff6, vint16m1_t
maskedoff7, const int16_t *base, size_t vl);

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void vlseg2e16_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
    vbool8_t mask, vint16m2_t maskedoff0, vint16m2_t maskedoff1,
    const int16_t *base, size_t vl);
void vlseg3e16_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vbool8_t mask, vint16m2_t maskedoff0,
    vint16m2_t maskedoff1, vint16m2_t maskedoff2, const int16_t
    *base, size_t vl);
void vlseg4e16_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vint16m2_t *v3, vbool8_t mask, vint16m2_t
    maskedoff0, vint16m2_t maskedoff1, vint16m2_t maskedoff2,
    vint16m2_t maskedoff3, const int16_t *base, size_t vl);
void vlseg2e16_v_i16m4_m (vint16m4_t *v0, vint16m4_t *v1,
    vbool4_t mask, vint16m4_t maskedoff0, vint16m4_t maskedoff1,
    const int16_t *base, size_t vl);
void vlseg2e32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, const int32_t *base, size_t vl);
void vlseg3e32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, const int32_t
    *base, size_t vl);
void vlseg4e32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vbool32_t mask, vint32m1_t
    maskedoff0, vint32m1_t maskedoff1, vint32m1_t maskedoff2,
    vint32m1_t maskedoff3, const int32_t *base, size_t vl);
void vlseg5e32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vbool32_t
    mask, vint32m1_t maskedoff0, vint32m1_t maskedoff1,
    vint32m1_t maskedoff2, vint32m1_t maskedoff3, vint32m1_t
    maskedoff4, const int32_t *base, size_t vl);
void vlseg6e32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, vint32m1_t maskedoff2, vint32m1_t maskedoff3,
    vint32m1_t maskedoff4, vint32m1_t maskedoff5, const int32_t
    *base, size_t vl);
void vlseg7e32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, vint32m1_t
    maskedoff3, vint32m1_t maskedoff4, vint32m1_t maskedoff5,
    vint32m1_t maskedoff6, const int32_t *base, size_t vl);
void vlseg8e32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, vbool32_t mask,
    vint32m1_t maskedoff0, vint32m1_t maskedoff1, vint32m1_t

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maskedoff2, vint32m1_t maskedoff3, vint32m1_t maskedoff4,
vint32m1_t maskedoff5, vint32m1_t maskedoff6, vint32m1_t
maskedoff7, const int32_t *base, size_t vl);
void vlseg2e32_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
vbool16_t mask, vint32m2_t maskedoff0, vint32m2_t
maskedoff1, const int32_t *base, size_t vl);
void vlseg3e32_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
vint32m2_t *v2, vbool16_t mask, vint32m2_t maskedoff0,
vint32m2_t maskedoff1, vint32m2_t maskedoff2, const int32_t
*base, size_t vl);
void vlseg4e32_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
vint32m2_t *v2, vint32m2_t *v3, vbool16_t mask, vint32m2_t
maskedoff0, vint32m2_t maskedoff1, vint32m2_t maskedoff2,
vint32m2_t maskedoff3, const int32_t *base, size_t vl);
void vlseg2e32_v_i32m4_m (vint32m4_t *v0, vint32m4_t *v1,
vbool8_t mask, vint32m4_t maskedoff0, vint32m4_t maskedoff1,
const int32_t *base, size_t vl);
void vlseg2e64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
maskedoff1, const int64_t *base, size_t vl);
void vlseg3e64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vbool64_t mask, vint64m1_t maskedoff0,
vint64m1_t maskedoff1, vint64m1_t maskedoff2, const int64_t
*base, size_t vl);
void vlseg4e64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vbool64_t mask, vint64m1_t
maskedoff0, vint64m1_t maskedoff1, vint64m1_t maskedoff2,
vint64m1_t maskedoff3, const int64_t *base, size_t vl);
void vlseg5e64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vbool64_t
mask, vint64m1_t maskedoff0, vint64m1_t maskedoff1,
vint64m1_t maskedoff2, vint64m1_t maskedoff3, vint64m1_t
maskedoff4, const int64_t *base, size_t vl);
void vlseg6e64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
*v5, vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
maskedoff1, vint64m1_t maskedoff2, vint64m1_t maskedoff3,
vint64m1_t maskedoff4, vint64m1_t maskedoff5, const int64_t
*base, size_t vl);
void vlseg7e64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
*v5, vint64m1_t *v6, vbool64_t mask, vint64m1_t maskedoff0,
vint64m1_t maskedoff1, vint64m1_t maskedoff2, vint64m1_t
maskedoff3, vint64m1_t maskedoff4, vint64m1_t maskedoff5,
vint64m1_t maskedoff6, const int64_t *base, size_t vl);

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void vlseg8e64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vint64m1_t *v7, vbool64_t mask,
    vint64m1_t maskedoff0, vint64m1_t maskedoff1, vint64m1_t
    maskedoff2, vint64m1_t maskedoff3, vint64m1_t maskedoff4,
    vint64m1_t maskedoff5, vint64m1_t maskedoff6, vint64m1_t
    maskedoff7, const int64_t *base, size_t vl);
void vlseg2e64_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vbool32_t mask, vint64m2_t maskedoff0, vint64m2_t
    maskedoff1, const int64_t *base, size_t vl);
void vlseg3e64_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vbool32_t mask, vint64m2_t maskedoff0,
    vint64m2_t maskedoff1, vint64m2_t maskedoff2, const int64_t
    *base, size_t vl);
void vlseg4e64_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, vbool32_t mask, vint64m2_t
    maskedoff0, vint64m2_t maskedoff1, vint64m2_t maskedoff2,
    vint64m2_t maskedoff3, const int64_t *base, size_t vl);
void vlseg2e64_v_i64m4_m (vint64m4_t *v0, vint64m4_t *v1,
    vbool16_t mask, vint64m4_t maskedoff0, vint64m4_t
    maskedoff1, const int64_t *base, size_t vl);
void vlseg2e8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1, vbool8_t
    mask, vuint8m1_t maskedoff0, vuint8m1_t maskedoff1, const
    uint8_t *base, size_t vl);
void vlseg3e8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vbool8_t mask, vuint8m1_t maskedoff0,
    vuint8m1_t maskedoff1, vuint8m1_t maskedoff2, const uint8_t
    *base, size_t vl);
void vlseg4e8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vbool8_t mask, vuint8m1_t
    maskedoff0, vuint8m1_t maskedoff1, vuint8m1_t maskedoff2,
    vuint8m1_t maskedoff3, const uint8_t *base, size_t vl);
void vlseg5e8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vbool8_t
    mask, vuint8m1_t maskedoff0, vuint8m1_t maskedoff1,
    vuint8m1_t maskedoff2, vuint8m1_t maskedoff3, vuint8m1_t
    maskedoff4, const uint8_t *base, size_t vl);
void vlseg6e8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vbool8_t mask, vuint8m1_t maskedoff0, vuint8m1_t
    maskedoff1, vuint8m1_t maskedoff2, vuint8m1_t maskedoff3,
    vuint8m1_t maskedoff4, vuint8m1_t maskedoff5, const uint8_t
    *base, size_t vl);
void vlseg7e8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, vbool8_t mask, vuint8m1_t maskedoff0,

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vuint8m1_t maskedoff1, vuint8m1_t maskedoff2, vuint8m1_t
maskedoff3, vuint8m1_t maskedoff4, vuint8m1_t maskedoff5,
vuint8m1_t maskedoff6, const uint8_t *base, size_t vl);
void vlseg8e8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
*v5, vuint8m1_t *v6, vuint8m1_t *v7, vbool8_t mask,
vuint8m1_t maskedoff0, vuint8m1_t maskedoff1, vuint8m1_t
maskedoff2, vuint8m1_t maskedoff3, vuint8m1_t maskedoff4,
vuint8m1_t maskedoff5, vuint8m1_t maskedoff6, vuint8m1_t
maskedoff7, const uint8_t *base, size_t vl);
void vlseg2e8_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1, vbool4_t
mask, vuint8m2_t maskedoff0, vuint8m2_t maskedoff1, const
uint8_t *base, size_t vl);
void vlseg3e8_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
vuint8m2_t *v2, vbool4_t mask, vuint8m2_t maskedoff0,
vuint8m2_t maskedoff1, vuint8m2_t maskedoff2, const uint8_t
*base, size_t vl);
void vlseg4e8_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
vuint8m2_t *v2, vuint8m2_t *v3, vbool4_t mask, vuint8m2_t
maskedoff0, vuint8m2_t maskedoff1, vuint8m2_t maskedoff2,
vuint8m2_t maskedoff3, const uint8_t *base, size_t vl);
void vlseg2e8_v_u8m4_m (vuint8m4_t *v0, vuint8m4_t *v1, vbool2_t
mask, vuint8m4_t maskedoff0, vuint8m4_t maskedoff1, const
uint8_t *base, size_t vl);
void vlseg2e16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vbool16_t mask, vuint16m1_t maskedoff0, vuint16m1_t
maskedoff1, const uint16_t *base, size_t vl);
void vlseg3e16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vbool16_t mask, vuint16m1_t maskedoff0,
vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, const
uint16_t *base, size_t vl);
void vlseg4e16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vbool16_t mask,
vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
maskedoff2, vuint16m1_t maskedoff3, const uint16_t *base,
size_t vl);
void vlseg5e16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, vbool16_t
mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
maskedoff4, const uint16_t *base, size_t vl);
void vlseg6e16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vbool16_t mask, vuint16m1_t maskedoff0,
vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, vuint16m1_t
maskedoff3, vuint16m1_t maskedoff4, vuint16m1_t maskedoff5,

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    const uint16_t *base, size_t vl);
void vlseg7e16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, vbool16_t mask,
    vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
    maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t maskedoff4,
    vuint16m1_t maskedoff5, vuint16m1_t maskedoff6, const
    uint16_t *base, size_t vl);
void vlseg8e16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, vbool16_t
    mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
    vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
    maskedoff4, vuint16m1_t maskedoff5, vuint16m1_t maskedoff6,
    vuint16m1_t maskedoff7, const uint16_t *base, size_t vl);
void vlseg2e16_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vbool8_t mask, vuint16m2_t maskedoff0, vuint16m2_t
    maskedoff1, const uint16_t *base, size_t vl);
void vlseg3e16_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vbool8_t mask, vuint16m2_t maskedoff0,
    vuint16m2_t maskedoff1, vuint16m2_t maskedoff2, const
    uint16_t *base, size_t vl);
void vlseg4e16_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vuint16m2_t *v3, vbool8_t mask, vuint16m2_t
    maskedoff0, vuint16m2_t maskedoff1, vuint16m2_t maskedoff2,
    vuint16m2_t maskedoff3, const uint16_t *base, size_t vl);
void vlseg2e16_v_u16m4_m (vuint16m4_t *v0, vuint16m4_t *v1,
    vbool4_t mask, vuint16m4_t maskedoff0, vuint16m4_t
    maskedoff1, const uint16_t *base, size_t vl);
void vlseg2e32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vbool32_t mask, vuint32m1_t maskedoff0, vuint32m1_t
    maskedoff1, const uint32_t *base, size_t vl);
void vlseg3e32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, const
    uint32_t *base, size_t vl);
void vlseg4e32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, const uint32_t *base,
    size_t vl);
void vlseg5e32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, const uint32_t *base, size_t vl);

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void vlseg6e32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, vuint32m1_t
    maskedoff3, vuint32m1_t maskedoff4, vuint32m1_t maskedoff5,
    const uint32_t *base, size_t vl);
void vlseg7e32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t maskedoff4,
    vuint32m1_t maskedoff5, vuint32m1_t maskedoff6, const
    uint32_t *base, size_t vl);
void vlseg8e32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, vuint32m1_t maskedoff5, vuint32m1_t maskedoff6,
    vuint32m1_t maskedoff7, const uint32_t *base, size_t vl);
void vlseg2e32_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vbool16_t mask, vuint32m2_t maskedoff0, vuint32m2_t
    maskedoff1, const uint32_t *base, size_t vl);
void vlseg3e32_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vbool16_t mask, vuint32m2_t maskedoff0,
    vuint32m2_t maskedoff1, vuint32m2_t maskedoff2, const
    uint32_t *base, size_t vl);
void vlseg4e32_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, vbool16_t mask,
    vuint32m2_t maskedoff0, vuint32m2_t maskedoff1, vuint32m2_t
    maskedoff2, vuint32m2_t maskedoff3, const uint32_t *base,
    size_t vl);
void vlseg2e32_v_u32m4_m (vuint32m4_t *v0, vuint32m4_t *v1,
    vbool8_t mask, vuint32m4_t maskedoff0, vuint32m4_t
    maskedoff1, const uint32_t *base, size_t vl);
void vlseg2e64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vbool64_t mask, vuint64m1_t maskedoff0, vuint64m1_t
    maskedoff1, const uint64_t *base, size_t vl);
void vlseg3e64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, const
    uint64_t *base, size_t vl);
void vlseg4e64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, const uint64_t *base,

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    size_t vl);
void vlseg5e64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, const uint64_t *base, size_t vl);
void vlseg6e64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, vuint64m1_t
    maskedoff3, vuint64m1_t maskedoff4, vuint64m1_t maskedoff5,
    const uint64_t *base, size_t vl);
void vlseg7e64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t maskedoff4,
    vuint64m1_t maskedoff5, vuint64m1_t maskedoff6, const
    uint64_t *base, size_t vl);
void vlseg8e64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, vuint64m1_t maskedoff5, vuint64m1_t maskedoff6,
    vuint64m1_t maskedoff7, const uint64_t *base, size_t vl);
void vlseg2e64_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vbool32_t mask, vuint64m2_t maskedoff0, vuint64m2_t
    maskedoff1, const uint64_t *base, size_t vl);
void vlseg3e64_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vbool32_t mask, vuint64m2_t maskedoff0,
    vuint64m2_t maskedoff1, vuint64m2_t maskedoff2, const
    uint64_t *base, size_t vl);
void vlseg4e64_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, vbool32_t mask,
    vuint64m2_t maskedoff0, vuint64m2_t maskedoff1, vuint64m2_t
    maskedoff2, vuint64m2_t maskedoff3, const uint64_t *base,
    size_t vl);
void vlseg2e64_v_u64m4_m (vuint64m4_t *v0, vuint64m4_t *v1,
    vbool16_t mask, vuint64m4_t maskedoff0, vuint64m4_t
    maskedoff1, const uint64_t *base, size_t vl);
void vlseg2e16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vbool16_t mask, vfloat16m1_t maskedoff0, vfloat16m1_t
    maskedoff1, const float16_t *base, size_t vl);
void vlseg3e16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vbool16_t mask, vfloat16m1_t maskedoff0,

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    vfloat16m1_t maskedoff1, vfloat16m1_t maskedoff2, const
    float16_t *base, size_t vl);
void vlseg4e16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vbool16_t mask,
    vfloat16m1_t maskedoff0, vfloat16m1_t maskedoff1,
    vfloat16m1_t maskedoff2, vfloat16m1_t maskedoff3, const
    float16_t *base, size_t vl);
void vlseg5e16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vbool16_t mask, vfloat16m1_t maskedoff0, vfloat16m1_t
    maskedoff1, vfloat16m1_t maskedoff2, vfloat16m1_t
    maskedoff3, vfloat16m1_t maskedoff4, const float16_t *base,
    size_t vl);
void vlseg6e16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, vbool16_t mask, vfloat16m1_t maskedoff0,
    vfloat16m1_t maskedoff1, vfloat16m1_t maskedoff2,
    vfloat16m1_t maskedoff3, vfloat16m1_t maskedoff4,
    vfloat16m1_t maskedoff5, const float16_t *base, size_t vl);
void vlseg7e16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, vfloat16m1_t *v6, vbool16_t mask,
    vfloat16m1_t maskedoff0, vfloat16m1_t maskedoff1,
    vfloat16m1_t maskedoff2, vfloat16m1_t maskedoff3,
    vfloat16m1_t maskedoff4, vfloat16m1_t maskedoff5,
    vfloat16m1_t maskedoff6, const float16_t *base, size_t vl);
void vlseg8e16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, vfloat16m1_t *v6, vfloat16m1_t *v7,
    vbool16_t mask, vfloat16m1_t maskedoff0, vfloat16m1_t
    maskedoff1, vfloat16m1_t maskedoff2, vfloat16m1_t
    maskedoff3, vfloat16m1_t maskedoff4, vfloat16m1_t
    maskedoff5, vfloat16m1_t maskedoff6, vfloat16m1_t
    maskedoff7, const float16_t *base, size_t vl);
void vlseg2e16_v_f16m2_m (vfloat16m2_t *v0, vfloat16m2_t *v1,
    vbool8_t mask, vfloat16m2_t maskedoff0, vfloat16m2_t
    maskedoff1, const float16_t *base, size_t vl);
void vlseg3e16_v_f16m2_m (vfloat16m2_t *v0, vfloat16m2_t *v1,
    vfloat16m2_t *v2, vbool8_t mask, vfloat16m2_t maskedoff0,
    vfloat16m2_t maskedoff1, vfloat16m2_t maskedoff2, const
    float16_t *base, size_t vl);
void vlseg4e16_v_f16m2_m (vfloat16m2_t *v0, vfloat16m2_t *v1,
    vfloat16m2_t *v2, vfloat16m2_t *v3, vbool8_t mask,
    vfloat16m2_t maskedoff0, vfloat16m2_t maskedoff1,
    vfloat16m2_t maskedoff2, vfloat16m2_t maskedoff3, const
    float16_t *base, size_t vl);

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void vlseg2e16_v_f16m4_m (vfloat16m4_t *v0, vfloat16m4_t *v1,
    vbool4_t mask, vfloat16m4_t maskedoff0, vfloat16m4_t
    maskedoff1, const float16_t *base, size_t vl);
void vlseg2e32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vbool32_t mask, vfloat32m1_t maskedoff0, vfloat32m1_t
    maskedoff1, const float32_t *base, size_t vl);
void vlseg3e32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vbool32_t mask, vfloat32m1_t maskedoff0,
    vfloat32m1_t maskedoff1, vfloat32m1_t maskedoff2, const
    float32_t *base, size_t vl);
void vlseg4e32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vbool32_t mask,
    vfloat32m1_t maskedoff0, vfloat32m1_t maskedoff1,
    vfloat32m1_t maskedoff2, vfloat32m1_t maskedoff3, const
    float32_t *base, size_t vl);
void vlseg5e32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vbool32_t mask, vfloat32m1_t maskedoff0, vfloat32m1_t
    maskedoff1, vfloat32m1_t maskedoff2, vfloat32m1_t
    maskedoff3, vfloat32m1_t maskedoff4, const float32_t *base,
    size_t vl);
void vlseg6e32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, vbool32_t mask, vfloat32m1_t maskedoff0,
    vfloat32m1_t maskedoff1, vfloat32m1_t maskedoff2,
    vfloat32m1_t maskedoff3, vfloat32m1_t maskedoff4,
    vfloat32m1_t maskedoff5, const float32_t *base, size_t vl);
void vlseg7e32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, vfloat32m1_t *v6, vbool32_t mask,
    vfloat32m1_t maskedoff0, vfloat32m1_t maskedoff1,
    vfloat32m1_t maskedoff2, vfloat32m1_t maskedoff3,
    vfloat32m1_t maskedoff4, vfloat32m1_t maskedoff5,
    vfloat32m1_t maskedoff6, const float32_t *base, size_t vl);
void vlseg8e32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, vfloat32m1_t *v6, vfloat32m1_t *v7,
    vbool32_t mask, vfloat32m1_t maskedoff0, vfloat32m1_t
    maskedoff1, vfloat32m1_t maskedoff2, vfloat32m1_t
    maskedoff3, vfloat32m1_t maskedoff4, vfloat32m1_t
    maskedoff5, vfloat32m1_t maskedoff6, vfloat32m1_t
    maskedoff7, const float32_t *base, size_t vl);
void vlseg2e32_v_f32m2_m (vfloat32m2_t *v0, vfloat32m2_t *v1,
    vbool16_t mask, vfloat32m2_t maskedoff0, vfloat32m2_t
    maskedoff1, const float32_t *base, size_t vl);

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void vlseg3e32_v_f32m2_m (vfloat32m2_t *v0, vfloat32m2_t *v1,
    vfloat32m2_t *v2, vbool16_t mask, vfloat32m2_t maskedoff0,
    vfloat32m2_t maskedoff1, vfloat32m2_t maskedoff2, const
    float32_t *base, size_t vl);
void vlseg4e32_v_f32m2_m (vfloat32m2_t *v0, vfloat32m2_t *v1,
    vfloat32m2_t *v2, vfloat32m2_t *v3, vbool16_t mask,
    vfloat32m2_t maskedoff0, vfloat32m2_t maskedoff1,
    vfloat32m2_t maskedoff2, vfloat32m2_t maskedoff3, const
    float32_t *base, size_t vl);
void vlseg2e32_v_f32m4_m (vfloat32m4_t *v0, vfloat32m4_t *v1,
    vbool8_t mask, vfloat32m4_t maskedoff0, vfloat32m4_t
    maskedoff1, const float32_t *base, size_t vl);
void vlseg2e64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vbool64_t mask, vfloat64m1_t maskedoff0, vfloat64m1_t
    maskedoff1, const float64_t *base, size_t vl);
void vlseg3e64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vbool64_t mask, vfloat64m1_t maskedoff0,
    vfloat64m1_t maskedoff1, vfloat64m1_t maskedoff2, const
    float64_t *base, size_t vl);
void vlseg4e64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vbool64_t mask,
    vfloat64m1_t maskedoff0, vfloat64m1_t maskedoff1,
    vfloat64m1_t maskedoff2, vfloat64m1_t maskedoff3, const
    float64_t *base, size_t vl);
void vlseg5e64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vbool64_t mask, vfloat64m1_t maskedoff0, vfloat64m1_t
    maskedoff1, vfloat64m1_t maskedoff2, vfloat64m1_t
    maskedoff3, vfloat64m1_t maskedoff4, const float64_t *base,
    size_t vl);
void vlseg6e64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, vbool64_t mask, vfloat64m1_t maskedoff0,
    vfloat64m1_t maskedoff1, vfloat64m1_t maskedoff2,
    vfloat64m1_t maskedoff3, vfloat64m1_t maskedoff4,
    vfloat64m1_t maskedoff5, const float64_t *base, size_t vl);
void vlseg7e64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, vfloat64m1_t *v6, vbool64_t mask,
    vfloat64m1_t maskedoff0, vfloat64m1_t maskedoff1,
    vfloat64m1_t maskedoff2, vfloat64m1_t maskedoff3,
    vfloat64m1_t maskedoff4, vfloat64m1_t maskedoff5,
    vfloat64m1_t maskedoff6, const float64_t *base, size_t vl);
void vlseg8e64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, vfloat64m1_t *v6, vfloat64m1_t *v7,

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vbool64_t mask, vfloat64m1_t maskedoff0, vfloat64m1_t
maskedoff1, vfloat64m1_t maskedoff2, vfloat64m1_t
maskedoff3, vfloat64m1_t maskedoff4, vfloat64m1_t
maskedoff5, vfloat64m1_t maskedoff6, vfloat64m1_t
maskedoff7, const float64_t *base, size_t vl);
void vlseg2e64_v_f64m2_m (vfloat64m2_t *v0, vfloat64m2_t *v1,
vbool32_t mask, vfloat64m2_t maskedoff0, vfloat64m2_t
maskedoff1, const float64_t *base, size_t vl);
void vlseg3e64_v_f64m2_m (vfloat64m2_t *v0, vfloat64m2_t *v1,
vfloat64m2_t *v2, vbool32_t mask, vfloat64m2_t maskedoff0,
vfloat64m2_t maskedoff1, vfloat64m2_t maskedoff2, const
float64_t *base, size_t vl);
void vlseg4e64_v_f64m2_m (vfloat64m2_t *v0, vfloat64m2_t *v1,
vfloat64m2_t *v2, vfloat64m2_t *v3, vbool32_t mask,
vfloat64m2_t maskedoff0, vfloat64m2_t maskedoff1,
vfloat64m2_t maskedoff2, vfloat64m2_t maskedoff3, const
float64_t *base, size_t vl);
void vlseg2e64_v_f64m4_m (vfloat64m4_t *v0, vfloat64m4_t *v1,
vbool16_t mask, vfloat64m4_t maskedoff0, vfloat64m4_t
maskedoff1, const float64_t *base, size_t vl);
void vlseg2e8ff_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vbool8_t
mask, vint8m1_t maskedoff0, vint8m1_t maskedoff1, const
int8_t *base, size_t *new_vl, size_t vl);
void vlseg3e8ff_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1,
vint8m1_t *v2, vbool8_t mask, vint8m1_t maskedoff0,
vint8m1_t maskedoff1, vint8m1_t maskedoff2, const int8_t
*base, size_t *new_vl, size_t vl);
void vlseg4e8ff_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1,
vint8m1_t *v2, vint8m1_t *v3, vbool8_t mask, vint8m1_t
maskedoff0, vint8m1_t maskedoff1, vint8m1_t maskedoff2,
vint8m1_t maskedoff3, const int8_t *base, size_t *new_vl,
size_t vl);
void vlseg5e8ff_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1,
vint8m1_t *v2, vint8m1_t *v3, vint8m1_t *v4, vbool8_t mask,
vint8m1_t maskedoff0, vint8m1_t maskedoff1, vint8m1_t
maskedoff2, vint8m1_t maskedoff3, vint8m1_t maskedoff4,
const int8_t *base, size_t *new_vl, size_t vl);
void vlseg6e8ff_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1,
vint8m1_t *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5,
vbool8_t mask, vint8m1_t maskedoff0, vint8m1_t maskedoff1,
vint8m1_t maskedoff2, vint8m1_t maskedoff3, vint8m1_t
maskedoff4, vint8m1_t maskedoff5, const int8_t *base, size_t
*new_vl, size_t vl);
void vlseg7e8ff_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1,
vint8m1_t *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5,
vint8m1_t *v6, vbool8_t mask, vint8m1_t maskedoff0,

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vint8m1_t maskedoff1, vint8m1_t maskedoff2, vint8m1_t
maskedoff3, vint8m1_t maskedoff4, vint8m1_t maskedoff5,
vint8m1_t maskedoff6, const int8_t *base, size_t *new_vl,
size_t vl);
void vlseg8e8ff_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1,
vint8m1_t *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5,
vint8m1_t *v6, vint8m1_t *v7, vbool8_t mask, vint8m1_t
maskedoff0, vint8m1_t maskedoff1, vint8m1_t maskedoff2,
vint8m1_t maskedoff3, vint8m1_t maskedoff4, vint8m1_t
maskedoff5, vint8m1_t maskedoff6, vint8m1_t maskedoff7,
const int8_t *base, size_t *new_vl, size_t vl);
void vlseg2e8ff_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1, vbool4_t
mask, vint8m2_t maskedoff0, vint8m2_t maskedoff1, const
int8_t *base, size_t *new_vl, size_t vl);
void vlseg3e8ff_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1,
vint8m2_t *v2, vbool4_t mask, vint8m2_t maskedoff0,
vint8m2_t maskedoff1, vint8m2_t maskedoff2, const int8_t
*base, size_t *new_vl, size_t vl);
void vlseg4e8ff_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1,
vint8m2_t *v2, vint8m2_t *v3, vbool4_t mask, vint8m2_t
maskedoff0, vint8m2_t maskedoff1, vint8m2_t maskedoff2,
vint8m2_t maskedoff3, const int8_t *base, size_t *new_vl,
size_t vl);
void vlseg2e8ff_v_i8m4_m (vint8m4_t *v0, vint8m4_t *v1, vbool2_t
mask, vint8m4_t maskedoff0, vint8m4_t maskedoff1, const
int8_t *base, size_t *new_vl, size_t vl);
void vlseg2e16ff_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
maskedoff1, const int16_t *base, size_t *new_vl, size_t vl);
void vlseg3e16ff_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vbool16_t mask, vint16m1_t maskedoff0,
vint16m1_t maskedoff1, vint16m1_t maskedoff2, const int16_t
*base, size_t *new_vl, size_t vl);
void vlseg4e16ff_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vbool16_t mask, vint16m1_t
maskedoff0, vint16m1_t maskedoff1, vint16m1_t maskedoff2,
vint16m1_t maskedoff3, const int16_t *base, size_t *new_vl,
size_t vl);
void vlseg5e16ff_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vbool16_t
mask, vint16m1_t maskedoff0, vint16m1_t maskedoff1,
vint16m1_t maskedoff2, vint16m1_t maskedoff3, vint16m1_t
maskedoff4, const int16_t *base, size_t *new_vl, size_t vl);
void vlseg6e16ff_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t

```



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maskedoff1, vint16m1_t maskedoff2, vint16m1_t maskedoff3,
vint16m1_t maskedoff4, vint16m1_t maskedoff5, const int16_t
*base, size_t *new_vl, size_t vl);
void vlseg7e16ff_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vint16m1_t *v6, vbool16_t mask, vint16m1_t maskedoff0,
vint16m1_t maskedoff1, vint16m1_t maskedoff2, vint16m1_t
maskedoff3, vint16m1_t maskedoff4, vint16m1_t maskedoff5,
vint16m1_t maskedoff6, const int16_t *base, size_t *new_vl,
size_t vl);
void vlseg8e16ff_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vint16m1_t *v6, vint16m1_t *v7, vbool16_t mask,
vint16m1_t maskedoff0, vint16m1_t maskedoff1, vint16m1_t
maskedoff2, vint16m1_t maskedoff3, vint16m1_t maskedoff4,
vint16m1_t maskedoff5, vint16m1_t maskedoff6, vint16m1_t
maskedoff7, const int16_t *base, size_t *new_vl, size_t vl);
void vlseg2e16ff_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
vbool8_t mask, vint16m2_t maskedoff0, vint16m2_t maskedoff1,
const int16_t *base, size_t *new_vl, size_t vl);
void vlseg3e16ff_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
vint16m2_t *v2, vbool8_t mask, vint16m2_t maskedoff0,
vint16m2_t maskedoff1, vint16m2_t maskedoff2, const int16_t
*base, size_t *new_vl, size_t vl);
void vlseg4e16ff_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
vint16m2_t *v2, vint16m2_t *v3, vbool8_t mask, vint16m2_t
maskedoff0, vint16m2_t maskedoff1, vint16m2_t maskedoff2,
vint16m2_t maskedoff3, const int16_t *base, size_t *new_vl,
size_t vl);
void vlseg2e16ff_v_i16m4_m (vint16m4_t *v0, vint16m4_t *v1,
vbool4_t mask, vint16m4_t maskedoff0, vint16m4_t maskedoff1,
const int16_t *base, size_t *new_vl, size_t vl);
void vlseg2e32ff_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
maskedoff1, const int32_t *base, size_t *new_vl, size_t vl);
void vlseg3e32ff_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vbool32_t mask, vint32m1_t maskedoff0,
vint32m1_t maskedoff1, vint32m1_t maskedoff2, const int32_t
*base, size_t *new_vl, size_t vl);
void vlseg4e32ff_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vbool32_t mask, vint32m1_t
maskedoff0, vint32m1_t maskedoff1, vint32m1_t maskedoff2,
vint32m1_t maskedoff3, const int32_t *base, size_t *new_vl,
size_t vl);
void vlseg5e32ff_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vbool32_t

```

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mask, vint32m1_t maskedoff0, vint32m1_t maskedoff1,
vint32m1_t maskedoff2, vint32m1_t maskedoff3, vint32m1_t
maskedoff4, const int32_t *base, size_t *new_vl, size_t vl);
void vlseg6e32ff_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
*v5, vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
maskedoff1, vint32m1_t maskedoff2, vint32m1_t maskedoff3,
vint32m1_t maskedoff4, vint32m1_t maskedoff5, const int32_t
*base, size_t *new_vl, size_t vl);
void vlseg7e32ff_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
*v5, vint32m1_t *v6, vbool32_t mask, vint32m1_t maskedoff0,
vint32m1_t maskedoff1, vint32m1_t maskedoff2, vint32m1_t
maskedoff3, vint32m1_t maskedoff4, vint32m1_t maskedoff5,
vint32m1_t maskedoff6, const int32_t *base, size_t *new_vl,
size_t vl);
void vlseg8e32ff_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
*v5, vint32m1_t *v6, vint32m1_t *v7, vbool32_t mask,
vint32m1_t maskedoff0, vint32m1_t maskedoff1, vint32m1_t
maskedoff2, vint32m1_t maskedoff3, vint32m1_t maskedoff4,
vint32m1_t maskedoff5, vint32m1_t maskedoff6, vint32m1_t
maskedoff7, const int32_t *base, size_t *new_vl, size_t vl);
void vlseg2e32ff_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
vbool16_t mask, vint32m2_t maskedoff0, vint32m2_t
maskedoff1, const int32_t *base, size_t *new_vl, size_t vl);
void vlseg3e32ff_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
vint32m2_t *v2, vbool16_t mask, vint32m2_t maskedoff0,
vint32m2_t maskedoff1, vint32m2_t maskedoff2, const int32_t
*base, size_t *new_vl, size_t vl);
void vlseg4e32ff_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
vint32m2_t *v2, vint32m2_t *v3, vbool16_t mask, vint32m2_t
maskedoff0, vint32m2_t maskedoff1, vint32m2_t maskedoff2,
vint32m2_t maskedoff3, const int32_t *base, size_t *new_vl,
size_t vl);
void vlseg2e32ff_v_i32m4_m (vint32m4_t *v0, vint32m4_t *v1,
vbool8_t mask, vint32m4_t maskedoff0, vint32m4_t maskedoff1,
const int32_t *base, size_t *new_vl, size_t vl);
void vlseg2e64ff_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
maskedoff1, const int64_t *base, size_t *new_vl, size_t vl);
void vlseg3e64ff_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vbool64_t mask, vint64m1_t maskedoff0,
vint64m1_t maskedoff1, vint64m1_t maskedoff2, const int64_t
*base, size_t *new_vl, size_t vl);

```

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void vlseg4e64ff_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vbool64_t mask, vint64m1_t
    maskedoff0, vint64m1_t maskedoff1, vint64m1_t maskedoff2,
    vint64m1_t maskedoff3, const int64_t *base, size_t *new_vl,
    size_t vl);
void vlseg5e64ff_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vbool64_t
    mask, vint64m1_t maskedoff0, vint64m1_t maskedoff1,
    vint64m1_t maskedoff2, vint64m1_t maskedoff3, vint64m1_t
    maskedoff4, const int64_t *base, size_t *new_vl, size_t vl);
void vlseg6e64ff_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, vint64m1_t maskedoff2, vint64m1_t maskedoff3,
    vint64m1_t maskedoff4, vint64m1_t maskedoff5, const int64_t
    *base, size_t *new_vl, size_t vl);
void vlseg7e64ff_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, vint64m1_t
    maskedoff3, vint64m1_t maskedoff4, vint64m1_t maskedoff5,
    vint64m1_t maskedoff6, const int64_t *base, size_t *new_vl,
    size_t vl);
void vlseg8e64ff_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vint64m1_t *v7, vbool64_t mask,
    vint64m1_t maskedoff0, vint64m1_t maskedoff1, vint64m1_t
    maskedoff2, vint64m1_t maskedoff3, vint64m1_t maskedoff4,
    vint64m1_t maskedoff5, vint64m1_t maskedoff6, vint64m1_t
    maskedoff7, const int64_t *base, size_t *new_vl, size_t vl);
void vlseg2e64ff_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vbool32_t mask, vint64m2_t maskedoff0, vint64m2_t
    maskedoff1, const int64_t *base, size_t *new_vl, size_t vl);
void vlseg3e64ff_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vbool32_t mask, vint64m2_t maskedoff0,
    vint64m2_t maskedoff1, vint64m2_t maskedoff2, const int64_t
    *base, size_t *new_vl, size_t vl);
void vlseg4e64ff_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, vbool32_t mask, vint64m2_t
    maskedoff0, vint64m2_t maskedoff1, vint64m2_t maskedoff2,
    vint64m2_t maskedoff3, const int64_t *base, size_t *new_vl,
    size_t vl);
void vlseg2e64ff_v_i64m4_m (vint64m4_t *v0, vint64m4_t *v1,
    vbool16_t mask, vint64m4_t maskedoff0, vint64m4_t
    maskedoff1, const int64_t *base, size_t *new_vl, size_t vl);

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void vlseg2e8ff_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vbool8_t mask, vuint8m1_t maskedoff0, vuint8m1_t maskedoff1,
    const uint8_t *base, size_t *new_vl, size_t vl);
void vlseg3e8ff_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vbool8_t mask, vuint8m1_t maskedoff0,
    vuint8m1_t maskedoff1, vuint8m1_t maskedoff2, const uint8_t
    *base, size_t *new_vl, size_t vl);
void vlseg4e8ff_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vbool8_t mask, vuint8m1_t
    maskedoff0, vuint8m1_t maskedoff1, vuint8m1_t maskedoff2,
    vuint8m1_t maskedoff3, const uint8_t *base, size_t *new_vl,
    size_t vl);
void vlseg5e8ff_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vbool8_t
    mask, vuint8m1_t maskedoff0, vuint8m1_t maskedoff1,
    vuint8m1_t maskedoff2, vuint8m1_t maskedoff3, vuint8m1_t
    maskedoff4, const uint8_t *base, size_t *new_vl, size_t vl);
void vlseg6e8ff_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vbool8_t mask, vuint8m1_t maskedoff0, vuint8m1_t
    maskedoff1, vuint8m1_t maskedoff2, vuint8m1_t maskedoff3,
    vuint8m1_t maskedoff4, vuint8m1_t maskedoff5, const uint8_t
    *base, size_t *new_vl, size_t vl);
void vlseg7e8ff_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, vbool8_t mask, vuint8m1_t maskedoff0,
    vuint8m1_t maskedoff1, vuint8m1_t maskedoff2, vuint8m1_t
    maskedoff3, vuint8m1_t maskedoff4, vuint8m1_t maskedoff5,
    vuint8m1_t maskedoff6, const uint8_t *base, size_t *new_vl,
    size_t vl);
void vlseg8e8ff_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, vuint8m1_t *v7, vbool8_t mask,
    vuint8m1_t maskedoff0, vuint8m1_t maskedoff1, vuint8m1_t
    maskedoff2, vuint8m1_t maskedoff3, vuint8m1_t maskedoff4,
    vuint8m1_t maskedoff5, vuint8m1_t maskedoff6, vuint8m1_t
    maskedoff7, const uint8_t *base, size_t *new_vl, size_t vl);
void vlseg2e8ff_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
    vbool4_t mask, vuint8m2_t maskedoff0, vuint8m2_t maskedoff1,
    const uint8_t *base, size_t *new_vl, size_t vl);
void vlseg3e8ff_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, vbool4_t mask, vuint8m2_t maskedoff0,
    vuint8m2_t maskedoff1, vuint8m2_t maskedoff2, const uint8_t
    *base, size_t *new_vl, size_t vl);
void vlseg4e8ff_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, vuint8m2_t *v3, vbool4_t mask, vuint8m2_t

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maskedoff0, vuint8m2_t maskedoff1, vuint8m2_t maskedoff2,
vuint8m2_t maskedoff3, const uint8_t *base, size_t *new_vl,
size_t vl);
void vlseg2e8ff_v_u8m4_m (vuint8m4_t *v0, vuint8m4_t *v1,
vbool2_t mask, vuint8m4_t maskedoff0, vuint8m4_t maskedoff1,
const uint8_t *base, size_t *new_vl, size_t vl);
void vlseg2e16ff_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vbool16_t mask, vuint16m1_t maskedoff0, vuint16m1_t
maskedoff1, const uint16_t *base, size_t *new_vl, size_t vl);
void vlseg3e16ff_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vbool16_t mask, vuint16m1_t maskedoff0,
vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, const
uint16_t *base, size_t *new_vl, size_t vl);
void vlseg4e16ff_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vbool16_t mask,
vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
maskedoff2, vuint16m1_t maskedoff3, const uint16_t *base,
size_t *new_vl, size_t vl);
void vlseg5e16ff_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, vbool16_t
mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
maskedoff4, const uint16_t *base, size_t *new_vl, size_t vl);
void vlseg6e16ff_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vbool16_t mask, vuint16m1_t maskedoff0,
vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, vuint16m1_t
maskedoff3, vuint16m1_t maskedoff4, vuint16m1_t maskedoff5,
const uint16_t *base, size_t *new_vl, size_t vl);
void vlseg7e16ff_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vuint16m1_t *v6, vbool16_t mask,
vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t maskedoff4,
vuint16m1_t maskedoff5, vuint16m1_t maskedoff6, const
uint16_t *base, size_t *new_vl, size_t vl);
void vlseg8e16ff_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, vbool16_t
mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
maskedoff4, vuint16m1_t maskedoff5, vuint16m1_t maskedoff6,
vuint16m1_t maskedoff7, const uint16_t *base, size_t
*new_vl, size_t vl);
void vlseg2e16ff_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
vbool8_t mask, vuint16m2_t maskedoff0, vuint16m2_t

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maskedoff1, const uint16_t *base, size_t *new_vl, size_t vl);
void vlseg3e16ff_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
vuint16m2_t *v2, vbool8_t mask, vuint16m2_t maskedoff0,
vuint16m2_t maskedoff1, vuint16m2_t maskedoff2, const
uint16_t *base, size_t *new_vl, size_t vl);
void vlseg4e16ff_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
vuint16m2_t *v2, vuint16m2_t *v3, vbool8_t mask, vuint16m2_t
maskedoff0, vuint16m2_t maskedoff1, vuint16m2_t maskedoff2,
vuint16m2_t maskedoff3, const uint16_t *base, size_t
*new_vl, size_t vl);
void vlseg2e16ff_v_u16m4_m (vuint16m4_t *v0, vuint16m4_t *v1,
vbool4_t mask, vuint16m4_t maskedoff0, vuint16m4_t
maskedoff1, const uint16_t *base, size_t *new_vl, size_t vl);
void vlseg2e32ff_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vbool32_t mask, vuint32m1_t maskedoff0, vuint32m1_t
maskedoff1, const uint32_t *base, size_t *new_vl, size_t vl);
void vlseg3e32ff_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vbool32_t mask, vuint32m1_t maskedoff0,
vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, const
uint32_t *base, size_t *new_vl, size_t vl);
void vlseg4e32ff_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vbool32_t mask,
vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
maskedoff2, vuint32m1_t maskedoff3, const uint32_t *base,
size_t *new_vl, size_t vl);
void vlseg5e32ff_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, vbool32_t
mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
maskedoff4, const uint32_t *base, size_t *new_vl, size_t vl);
void vlseg6e32ff_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vbool32_t mask, vuint32m1_t maskedoff0,
vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, vuint32m1_t
maskedoff3, vuint32m1_t maskedoff4, vuint32m1_t maskedoff5,
const uint32_t *base, size_t *new_vl, size_t vl);
void vlseg7e32ff_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vuint32m1_t *v6, vbool32_t mask,
vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t maskedoff4,
vuint32m1_t maskedoff5, vuint32m1_t maskedoff6, const
uint32_t *base, size_t *new_vl, size_t vl);
void vlseg8e32ff_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, vbool32_t

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    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, vuint32m1_t maskedoff5, vuint32m1_t maskedoff6,
    vuint32m1_t maskedoff7, const uint32_t *base, size_t
    *new_vl, size_t vl);
void vlseg2e32ff_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vbool16_t mask, vuint32m2_t maskedoff0, vuint32m2_t
    maskedoff1, const uint32_t *base, size_t *new_vl, size_t vl);
void vlseg3e32ff_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vbool16_t mask, vuint32m2_t maskedoff0,
    vuint32m2_t maskedoff1, vuint32m2_t maskedoff2, const
    uint32_t *base, size_t *new_vl, size_t vl);
void vlseg4e32ff_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, vbool16_t mask,
    vuint32m2_t maskedoff0, vuint32m2_t maskedoff1, vuint32m2_t
    maskedoff2, vuint32m2_t maskedoff3, const uint32_t *base,
    size_t *new_vl, size_t vl);
void vlseg2e32ff_v_u32m4_m (vuint32m4_t *v0, vuint32m4_t *v1,
    vbool8_t mask, vuint32m4_t maskedoff0, vuint32m4_t
    maskedoff1, const uint32_t *base, size_t *new_vl, size_t vl);
void vlseg2e64ff_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vbool64_t mask, vuint64m1_t maskedoff0, vuint64m1_t
    maskedoff1, const uint64_t *base, size_t *new_vl, size_t vl);
void vlseg3e64ff_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, const
    uint64_t *base, size_t *new_vl, size_t vl);
void vlseg4e64ff_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, const uint64_t *base,
    size_t *new_vl, size_t vl);
void vlseg5e64ff_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, const uint64_t *base, size_t *new_vl, size_t vl);
void vlseg6e64ff_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, vuint64m1_t
    maskedoff3, vuint64m1_t maskedoff4, vuint64m1_t maskedoff5,
    const uint64_t *base, size_t *new_vl, size_t vl);
void vlseg7e64ff_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vbool64_t mask,

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    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t maskedoff4,
    vuint64m1_t maskedoff5, vuint64m1_t maskedoff6, const
    uint64_t *base, size_t *new_vl, size_t vl);
void vlseg8e64ff_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, vuint64m1_t maskedoff5, vuint64m1_t maskedoff6,
    vuint64m1_t maskedoff7, const uint64_t *base, size_t
    *new_vl, size_t vl);
void vlseg2e64ff_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vbool32_t mask, vuint64m2_t maskedoff0, vuint64m2_t
    maskedoff1, const uint64_t *base, size_t *new_vl, size_t vl);
void vlseg3e64ff_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vbool32_t mask, vuint64m2_t maskedoff0,
    vuint64m2_t maskedoff1, vuint64m2_t maskedoff2, const
    uint64_t *base, size_t *new_vl, size_t vl);
void vlseg4e64ff_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, vbool32_t mask,
    vuint64m2_t maskedoff0, vuint64m2_t maskedoff1, vuint64m2_t
    maskedoff2, vuint64m2_t maskedoff3, const uint64_t *base,
    size_t *new_vl, size_t vl);
void vlseg2e64ff_v_u64m4_m (vuint64m4_t *v0, vuint64m4_t *v1,
    vbool16_t mask, vuint64m4_t maskedoff0, vuint64m4_t
    maskedoff1, const uint64_t *base, size_t *new_vl, size_t vl);
void vlseg2e16ff_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vbool16_t mask, vfloat16m1_t maskedoff0, vfloat16m1_t
    maskedoff1, const float16_t *base, size_t *new_vl, size_t
    vl);
void vlseg3e16ff_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vbool16_t mask, vfloat16m1_t maskedoff0,
    vfloat16m1_t maskedoff1, vfloat16m1_t maskedoff2, const
    float16_t *base, size_t *new_vl, size_t vl);
void vlseg4e16ff_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vbool16_t mask,
    vfloat16m1_t maskedoff0, vfloat16m1_t maskedoff1,
    vfloat16m1_t maskedoff2, vfloat16m1_t maskedoff3, const
    float16_t *base, size_t *new_vl, size_t vl);
void vlseg5e16ff_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vbool16_t mask, vfloat16m1_t maskedoff0, vfloat16m1_t
    maskedoff1, vfloat16m1_t maskedoff2, vfloat16m1_t
    maskedoff3, vfloat16m1_t maskedoff4, const float16_t *base,
    size_t *new_vl, size_t vl);

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void vlseg6e16ff_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
vfloat16m1_t *v5, vbool16_t mask, vfloat16m1_t maskedoff0,
vfloat16m1_t maskedoff1, vfloat16m1_t maskedoff2,
vfloat16m1_t maskedoff3, vfloat16m1_t maskedoff4,
vfloat16m1_t maskedoff5, const float16_t *base, size_t
*new_vl, size_t vl);
void vlseg7e16ff_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
vfloat16m1_t *v5, vfloat16m1_t *v6, vbool16_t mask,
vfloat16m1_t maskedoff0, vfloat16m1_t maskedoff1,
vfloat16m1_t maskedoff2, vfloat16m1_t maskedoff3,
vfloat16m1_t maskedoff4, vfloat16m1_t maskedoff5,
vfloat16m1_t maskedoff6, const float16_t *base, size_t
*new_vl, size_t vl);
void vlseg8e16ff_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
vfloat16m1_t *v5, vfloat16m1_t *v6, vfloat16m1_t *v7,
vbool16_t mask, vfloat16m1_t maskedoff0, vfloat16m1_t
maskedoff1, vfloat16m1_t maskedoff2, vfloat16m1_t
maskedoff3, vfloat16m1_t maskedoff4, vfloat16m1_t
maskedoff5, vfloat16m1_t maskedoff6, vfloat16m1_t
maskedoff7, const float16_t *base, size_t *new_vl, size_t
vl);
void vlseg2e16ff_v_f16m2_m (vfloat16m2_t *v0, vfloat16m2_t *v1,
vbool8_t mask, vfloat16m2_t maskedoff0, vfloat16m2_t
maskedoff1, const float16_t *base, size_t *new_vl, size_t
vl);
void vlseg3e16ff_v_f16m2_m (vfloat16m2_t *v0, vfloat16m2_t *v1,
vfloat16m2_t *v2, vbool8_t mask, vfloat16m2_t maskedoff0,
vfloat16m2_t maskedoff1, vfloat16m2_t maskedoff2, const
float16_t *base, size_t *new_vl, size_t vl);
void vlseg4e16ff_v_f16m2_m (vfloat16m2_t *v0, vfloat16m2_t *v1,
vfloat16m2_t *v2, vfloat16m2_t *v3, vbool8_t mask,
vfloat16m2_t maskedoff0, vfloat16m2_t maskedoff1,
vfloat16m2_t maskedoff2, vfloat16m2_t maskedoff3, const
float16_t *base, size_t *new_vl, size_t vl);
void vlseg2e16ff_v_f16m4_m (vfloat16m4_t *v0, vfloat16m4_t *v1,
vbool4_t mask, vfloat16m4_t maskedoff0, vfloat16m4_t
maskedoff1, const float16_t *base, size_t *new_vl, size_t
vl);
void vlseg2e32ff_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
vbool32_t mask, vfloat32m1_t maskedoff0, vfloat32m1_t
maskedoff1, const float32_t *base, size_t *new_vl, size_t
vl);

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void vlseg3e32ff_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vbool32_t mask, vfloat32m1_t maskedoff0,
    vfloat32m1_t maskedoff1, vfloat32m1_t maskedoff2, const
    float32_t *base, size_t *new_vl, size_t vl);
void vlseg4e32ff_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vbool32_t mask,
    vfloat32m1_t maskedoff0, vfloat32m1_t maskedoff1,
    vfloat32m1_t maskedoff2, vfloat32m1_t maskedoff3, const
    float32_t *base, size_t *new_vl, size_t vl);
void vlseg5e32ff_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vbool32_t mask, vfloat32m1_t maskedoff0, vfloat32m1_t
    maskedoff1, vfloat32m1_t maskedoff2, vfloat32m1_t
    maskedoff3, vfloat32m1_t maskedoff4, const float32_t *base,
    size_t *new_vl, size_t vl);
void vlseg6e32ff_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, vbool32_t mask, vfloat32m1_t maskedoff0,
    vfloat32m1_t maskedoff1, vfloat32m1_t maskedoff2,
    vfloat32m1_t maskedoff3, vfloat32m1_t maskedoff4,
    vfloat32m1_t maskedoff5, const float32_t *base, size_t
    *new_vl, size_t vl);
void vlseg7e32ff_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, vfloat32m1_t *v6, vbool32_t mask,
    vfloat32m1_t maskedoff0, vfloat32m1_t maskedoff1,
    vfloat32m1_t maskedoff2, vfloat32m1_t maskedoff3,
    vfloat32m1_t maskedoff4, vfloat32m1_t maskedoff5,
    vfloat32m1_t maskedoff6, const float32_t *base, size_t
    *new_vl, size_t vl);
void vlseg8e32ff_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, vfloat32m1_t *v6, vfloat32m1_t *v7,
    vbool32_t mask, vfloat32m1_t maskedoff0, vfloat32m1_t
    maskedoff1, vfloat32m1_t maskedoff2, vfloat32m1_t
    maskedoff3, vfloat32m1_t maskedoff4, vfloat32m1_t
    maskedoff5, vfloat32m1_t maskedoff6, vfloat32m1_t
    maskedoff7, const float32_t *base, size_t *new_vl, size_t
    vl);
void vlseg2e32ff_v_f32m2_m (vfloat32m2_t *v0, vfloat32m2_t *v1,
    vbool16_t mask, vfloat32m2_t maskedoff0, vfloat32m2_t
    maskedoff1, const float32_t *base, size_t *new_vl, size_t
    vl);
void vlseg3e32ff_v_f32m2_m (vfloat32m2_t *v0, vfloat32m2_t *v1,
    vfloat32m2_t *v2, vbool16_t mask, vfloat32m2_t maskedoff0,
    vfloat32m2_t maskedoff1, vfloat32m2_t maskedoff2, const

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float32_t *base, size_t *new_vl, size_t vl);
void vlseg4e32ff_v_f32m2_m (vfloat32m2_t *v0, vfloat32m2_t *v1,
vfloat32m2_t *v2, vfloat32m2_t *v3, vbool16_t mask,
vfloat32m2_t maskedoff0, vfloat32m2_t maskedoff1,
vfloat32m2_t maskedoff2, vfloat32m2_t maskedoff3, const
float32_t *base, size_t *new_vl, size_t vl);
void vlseg2e32ff_v_f32m4_m (vfloat32m4_t *v0, vfloat32m4_t *v1,
vbool8_t mask, vfloat32m4_t maskedoff0, vfloat32m4_t
maskedoff1, const float32_t *base, size_t *new_vl, size_t
vl);
void vlseg2e64ff_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
vbool64_t mask, vfloat64m1_t maskedoff0, vfloat64m1_t
maskedoff1, const float64_t *base, size_t *new_vl, size_t
vl);
void vlseg3e64ff_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
vfloat64m1_t *v2, vbool64_t mask, vfloat64m1_t maskedoff0,
vfloat64m1_t maskedoff1, vfloat64m1_t maskedoff2, const
float64_t *base, size_t *new_vl, size_t vl);
void vlseg4e64ff_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
vfloat64m1_t *v2, vfloat64m1_t *v3, vbool64_t mask,
vfloat64m1_t maskedoff0, vfloat64m1_t maskedoff1,
vfloat64m1_t maskedoff2, vfloat64m1_t maskedoff3, const
float64_t *base, size_t *new_vl, size_t vl);
void vlseg5e64ff_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
vbool64_t mask, vfloat64m1_t maskedoff0, vfloat64m1_t
maskedoff1, vfloat64m1_t maskedoff2, vfloat64m1_t
maskedoff3, vfloat64m1_t maskedoff4, const float64_t *base,
size_t *new_vl, size_t vl);
void vlseg6e64ff_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
vfloat64m1_t *v5, vbool64_t mask, vfloat64m1_t maskedoff0,
vfloat64m1_t maskedoff1, vfloat64m1_t maskedoff2,
vfloat64m1_t maskedoff3, vfloat64m1_t maskedoff4,
vfloat64m1_t maskedoff5, const float64_t *base, size_t
*new_vl, size_t vl);
void vlseg7e64ff_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
vfloat64m1_t *v5, vfloat64m1_t *v6, vbool64_t mask,
vfloat64m1_t maskedoff0, vfloat64m1_t maskedoff1,
vfloat64m1_t maskedoff2, vfloat64m1_t maskedoff3,
vfloat64m1_t maskedoff4, vfloat64m1_t maskedoff5,
vfloat64m1_t maskedoff6, const float64_t *base, size_t
*new_vl, size_t vl);
void vlseg8e64ff_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,

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vfloat64m1_t *v5, vfloat64m1_t *v6, vfloat64m1_t *v7,
vbool64_t mask, vfloat64m1_t maskedoff0, vfloat64m1_t
maskedoff1, vfloat64m1_t maskedoff2, vfloat64m1_t
maskedoff3, vfloat64m1_t maskedoff4, vfloat64m1_t
maskedoff5, vfloat64m1_t maskedoff6, vfloat64m1_t
maskedoff7, const float64_t *base, size_t *new_vl, size_t
vl);
void vlseg2e64ff_v_f64m2_m (vfloat64m2_t *v0, vfloat64m2_t *v1,
vbool32_t mask, vfloat64m2_t maskedoff0, vfloat64m2_t
maskedoff1, const float64_t *base, size_t *new_vl, size_t
vl);
void vlseg3e64ff_v_f64m2_m (vfloat64m2_t *v0, vfloat64m2_t *v1,
vfloat64m2_t *v2, vbool32_t mask, vfloat64m2_t maskedoff0,
vfloat64m2_t maskedoff1, vfloat64m2_t maskedoff2, const
float64_t *base, size_t *new_vl, size_t vl);
void vlseg4e64ff_v_f64m2_m (vfloat64m2_t *v0, vfloat64m2_t *v1,
vfloat64m2_t *v2, vfloat64m2_t *v3, vbool32_t mask,
vfloat64m2_t maskedoff0, vfloat64m2_t maskedoff1,
vfloat64m2_t maskedoff2, vfloat64m2_t maskedoff3, const
float64_t *base, size_t *new_vl, size_t vl);
void vlseg2e64ff_v_f64m4_m (vfloat64m4_t *v0, vfloat64m4_t *v1,
vbool16_t mask, vfloat64m4_t maskedoff0, vfloat64m4_t
maskedoff1, const float64_t *base, size_t *new_vl, size_t
vl);

```

## Vector Unit-Stride Segment Store Functions:

### Prototypes:

```

void vsseg2e8_v_i8m1 (int8_t *base, vint8m1_t v0, vint8m1_t v1,
size_t vl);
void vsseg3e8_v_i8m1 (int8_t *base, vint8m1_t v0, vint8m1_t v1,
vint8m1_t v2, size_t vl);
void vsseg4e8_v_i8m1 (int8_t *base, vint8m1_t v0, vint8m1_t v1,
vint8m1_t v2, vint8m1_t v3, size_t vl);
void vsseg5e8_v_i8m1 (int8_t *base, vint8m1_t v0, vint8m1_t v1,
vint8m1_t v2, vint8m1_t v3, vint8m1_t v4, size_t vl);
void vsseg6e8_v_i8m1 (int8_t *base, vint8m1_t v0, vint8m1_t v1,
vint8m1_t v2, vint8m1_t v3, vint8m1_t v4, vint8m1_t v5,
size_t vl);
void vsseg7e8_v_i8m1 (int8_t *base, vint8m1_t v0, vint8m1_t v1,
vint8m1_t v2, vint8m1_t v3, vint8m1_t v4, vint8m1_t v5,
vint8m1_t v6, size_t vl);
void vsseg8e8_v_i8m1 (int8_t *base, vint8m1_t v0, vint8m1_t v1,
vint8m1_t v2, vint8m1_t v3, vint8m1_t v4, vint8m1_t v5,
vint8m1_t v6, vint8m1_t v7, size_t vl);

```

```

void vsseg2e8_v_i8m2 (int8_t *base, vint8m2_t v0, vint8m2_t v1,
    size_t vl);
void vsseg3e8_v_i8m2 (int8_t *base, vint8m2_t v0, vint8m2_t v1,
    vint8m2_t v2, size_t vl);
void vsseg4e8_v_i8m2 (int8_t *base, vint8m2_t v0, vint8m2_t v1,
    vint8m2_t v2, vint8m2_t v3, size_t vl);
void vsseg2e8_v_i8m4 (int8_t *base, vint8m4_t v0, vint8m4_t v1,
    size_t vl);
void vsseg2e16_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, size_t vl);
void vsseg3e16_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, size_t vl);
void vsseg4e16_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, vint16m1_t v3, size_t vl);
void vsseg5e16_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, vint16m1_t v3, vint16m1_t v4, size_t vl);
void vsseg6e16_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t
    v5, size_t vl);
void vsseg7e16_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t
    v5, vint16m1_t v6, size_t vl);
void vsseg8e16_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t
    v5, vint16m1_t v6, vint16m1_t v7, size_t vl);
void vsseg2e16_v_i16m2 (int16_t *base, vint16m2_t v0, vint16m2_t
    v1, size_t vl);
void vsseg3e16_v_i16m2 (int16_t *base, vint16m2_t v0, vint16m2_t
    v1, vint16m2_t v2, size_t vl);
void vsseg4e16_v_i16m2 (int16_t *base, vint16m2_t v0, vint16m2_t
    v1, vint16m2_t v2, vint16m2_t v3, size_t vl);
void vsseg2e16_v_i16m4 (int16_t *base, vint16m4_t v0, vint16m4_t
    v1, size_t vl);
void vsseg2e32_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, size_t vl);
void vsseg3e32_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, size_t vl);
void vsseg4e32_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, size_t vl);
void vsseg5e32_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, vint32m1_t v4, size_t vl);
void vsseg6e32_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t
    v5, size_t vl);
void vsseg7e32_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t

```

```

    v5, vint32m1_t v6, size_t vl);
void vsseg8e32_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t
    v5, vint32m1_t v6, vint32m1_t v7, size_t vl);
void vsseg2e32_v_i32m2 (int32_t *base, vint32m2_t v0, vint32m2_t
    v1, size_t vl);
void vsseg3e32_v_i32m2 (int32_t *base, vint32m2_t v0, vint32m2_t
    v1, vint32m2_t v2, size_t vl);
void vsseg4e32_v_i32m2 (int32_t *base, vint32m2_t v0, vint32m2_t
    v1, vint32m2_t v2, vint32m2_t v3, size_t vl);
void vsseg2e32_v_i32m4 (int32_t *base, vint32m4_t v0, vint32m4_t
    v1, size_t vl);
void vsseg2e64_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, size_t vl);
void vsseg3e64_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, size_t vl);
void vsseg4e64_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, size_t vl);
void vsseg5e64_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, vint64m1_t v4, size_t vl);
void vsseg6e64_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t
    v5, size_t vl);
void vsseg7e64_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t
    v5, vint64m1_t v6, size_t vl);
void vsseg8e64_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t
    v5, vint64m1_t v6, vint64m1_t v7, size_t vl);
void vsseg2e64_v_i64m2 (int64_t *base, vint64m2_t v0, vint64m2_t
    v1, size_t vl);
void vsseg3e64_v_i64m2 (int64_t *base, vint64m2_t v0, vint64m2_t
    v1, vint64m2_t v2, size_t vl);
void vsseg4e64_v_i64m2 (int64_t *base, vint64m2_t v0, vint64m2_t
    v1, vint64m2_t v2, vint64m2_t v3, size_t vl);
void vsseg2e64_v_i64m4 (int64_t *base, vint64m4_t v0, vint64m4_t
    v1, size_t vl);
void vsseg2e8_v_u8m1 (uint8_t *base, vuint8m1_t v0, vuint8m1_t
    v1, size_t vl);
void vsseg3e8_v_u8m1 (uint8_t *base, vuint8m1_t v0, vuint8m1_t
    v1, vuint8m1_t v2, size_t vl);
void vsseg4e8_v_u8m1 (uint8_t *base, vuint8m1_t v0, vuint8m1_t
    v1, vuint8m1_t v2, vuint8m1_t v3, size_t vl);
void vsseg5e8_v_u8m1 (uint8_t *base, vuint8m1_t v0, vuint8m1_t
    v1, vuint8m1_t v2, vuint8m1_t v3, vuint8m1_t v4, size_t vl);

```

```

void vsseg6e8_v_u8m1 (uint8_t *base, vuint8m1_t v0, vuint8m1_t
    v1, vuint8m1_t v2, vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t
    v5, size_t vl);
void vsseg7e8_v_u8m1 (uint8_t *base, vuint8m1_t v0, vuint8m1_t
    v1, vuint8m1_t v2, vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t
    v5, vuint8m1_t v6, size_t vl);
void vsseg8e8_v_u8m1 (uint8_t *base, vuint8m1_t v0, vuint8m1_t
    v1, vuint8m1_t v2, vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t
    v5, vuint8m1_t v6, vuint8m1_t v7, size_t vl);
void vsseg2e8_v_u8m2 (uint8_t *base, vuint8m2_t v0, vuint8m2_t
    v1, size_t vl);
void vsseg3e8_v_u8m2 (uint8_t *base, vuint8m2_t v0, vuint8m2_t
    v1, vuint8m2_t v2, size_t vl);
void vsseg4e8_v_u8m2 (uint8_t *base, vuint8m2_t v0, vuint8m2_t
    v1, vuint8m2_t v2, vuint8m2_t v3, size_t vl);
void vsseg2e8_v_u8m4 (uint8_t *base, vuint8m4_t v0, vuint8m4_t
    v1, size_t vl);
void vsseg2e16_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, size_t vl);
void vsseg3e16_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, size_t vl);
void vsseg4e16_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t v3, size_t vl);
void vsseg5e16_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t
    v4, size_t vl);
void vsseg6e16_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t
    v4, vuint16m1_t v5, size_t vl);
void vsseg7e16_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t
    v4, vuint16m1_t v5, vuint16m1_t v6, size_t vl);
void vsseg8e16_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t
    v4, vuint16m1_t v5, vuint16m1_t v6, vuint16m1_t v7, size_t
    vl);
void vsseg2e16_v_u16m2 (uint16_t *base, vuint16m2_t v0,
    vuint16m2_t v1, size_t vl);
void vsseg3e16_v_u16m2 (uint16_t *base, vuint16m2_t v0,
    vuint16m2_t v1, vuint16m2_t v2, size_t vl);
void vsseg4e16_v_u16m2 (uint16_t *base, vuint16m2_t v0,
    vuint16m2_t v1, vuint16m2_t v2, vuint16m2_t v3, size_t vl);
void vsseg2e16_v_u16m4 (uint16_t *base, vuint16m4_t v0,
    vuint16m4_t v1, size_t vl);
void vsseg2e32_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, size_t vl);

```

```

void vsseg3e32_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, size_t vl);
void vsseg4e32_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, size_t vl);
void vsseg5e32_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t
    v4, size_t vl);
void vsseg6e32_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t
    v4, vuint32m1_t v5, size_t vl);
void vsseg7e32_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t
    v4, vuint32m1_t v5, vuint32m1_t v6, size_t vl);
void vsseg8e32_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t
    v4, vuint32m1_t v5, vuint32m1_t v6, vuint32m1_t v7, size_t
    vl);
void vsseg2e32_v_u32m2 (uint32_t *base, vuint32m2_t v0,
    vuint32m2_t v1, size_t vl);
void vsseg3e32_v_u32m2 (uint32_t *base, vuint32m2_t v0,
    vuint32m2_t v1, vuint32m2_t v2, size_t vl);
void vsseg4e32_v_u32m2 (uint32_t *base, vuint32m2_t v0,
    vuint32m2_t v1, vuint32m2_t v2, vuint32m2_t v3, size_t vl);
void vsseg2e32_v_u32m4 (uint32_t *base, vuint32m4_t v0,
    vuint32m4_t v1, size_t vl);
void vsseg2e64_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, size_t vl);
void vsseg3e64_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, size_t vl);
void vsseg4e64_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, size_t vl);
void vsseg5e64_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t
    v4, size_t vl);
void vsseg6e64_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t
    v4, vuint64m1_t v5, size_t vl);
void vsseg7e64_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t
    v4, vuint64m1_t v5, vuint64m1_t v6, size_t vl);
void vsseg8e64_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t
    v4, vuint64m1_t v5, vuint64m1_t v6, vuint64m1_t v7, size_t
    vl);
void vsseg2e64_v_u64m2 (uint64_t *base, vuint64m2_t v0,
    vuint64m2_t v1, size_t vl);

```



```

void vsseg3e64_v_u64m2 (uint64_t *base, vuint64m2_t v0,
    vuint64m2_t v1, vuint64m2_t v2, size_t vl);
void vsseg4e64_v_u64m2 (uint64_t *base, vuint64m2_t v0,
    vuint64m2_t v1, vuint64m2_t v2, vuint64m2_t v3, size_t vl);
void vsseg2e64_v_u64m4 (uint64_t *base, vuint64m4_t v0,
    vuint64m4_t v1, size_t vl);
void vsseg2e16_v_f16m1 (float16_t *base, vfloat16m1_t v0,
    vfloat16m1_t v1, size_t vl);
void vsseg3e16_v_f16m1 (float16_t *base, vfloat16m1_t v0,
    vfloat16m1_t v1, vfloat16m1_t v2, size_t vl);
void vsseg4e16_v_f16m1 (float16_t *base, vfloat16m1_t v0,
    vfloat16m1_t v1, vfloat16m1_t v2, vfloat16m1_t v3, size_t
    vl);
void vsseg5e16_v_f16m1 (float16_t *base, vfloat16m1_t v0,
    vfloat16m1_t v1, vfloat16m1_t v2, vfloat16m1_t v3,
    vfloat16m1_t v4, size_t vl);
void vsseg6e16_v_f16m1 (float16_t *base, vfloat16m1_t v0,
    vfloat16m1_t v1, vfloat16m1_t v2, vfloat16m1_t v3,
    vfloat16m1_t v4, vfloat16m1_t v5, size_t vl);
void vsseg7e16_v_f16m1 (float16_t *base, vfloat16m1_t v0,
    vfloat16m1_t v1, vfloat16m1_t v2, vfloat16m1_t v3,
    vfloat16m1_t v4, vfloat16m1_t v5, vfloat16m1_t v6, size_t
    vl);
void vsseg8e16_v_f16m1 (float16_t *base, vfloat16m1_t v0,
    vfloat16m1_t v1, vfloat16m1_t v2, vfloat16m1_t v3,
    vfloat16m1_t v4, vfloat16m1_t v5, vfloat16m1_t v6,
    vfloat16m1_t v7, size_t vl);
void vsseg2e16_v_f16m2 (float16_t *base, vfloat16m2_t v0,
    vfloat16m2_t v1, size_t vl);
void vsseg3e16_v_f16m2 (float16_t *base, vfloat16m2_t v0,
    vfloat16m2_t v1, vfloat16m2_t v2, size_t vl);
void vsseg4e16_v_f16m2 (float16_t *base, vfloat16m2_t v0,
    vfloat16m2_t v1, vfloat16m2_t v2, vfloat16m2_t v3, size_t
    vl);
void vsseg2e16_v_f16m4 (float16_t *base, vfloat16m4_t v0,
    vfloat16m4_t v1, size_t vl);
void vsseg2e32_v_f32m1 (float32_t *base, vfloat32m1_t v0,
    vfloat32m1_t v1, size_t vl);
void vsseg3e32_v_f32m1 (float32_t *base, vfloat32m1_t v0,
    vfloat32m1_t v1, vfloat32m1_t v2, size_t vl);
void vsseg4e32_v_f32m1 (float32_t *base, vfloat32m1_t v0,
    vfloat32m1_t v1, vfloat32m1_t v2, vfloat32m1_t v3, size_t
    vl);
void vsseg5e32_v_f32m1 (float32_t *base, vfloat32m1_t v0,
    vfloat32m1_t v1, vfloat32m1_t v2, vfloat32m1_t v3,
    vfloat32m1_t v4, size_t vl);

```

```

void vsseg6e32_v_f32m1 (float32_t *base, vfloat32m1_t v0,
    vfloat32m1_t v1, vfloat32m1_t v2, vfloat32m1_t v3,
    vfloat32m1_t v4, vfloat32m1_t v5, size_t vl);
void vsseg7e32_v_f32m1 (float32_t *base, vfloat32m1_t v0,
    vfloat32m1_t v1, vfloat32m1_t v2, vfloat32m1_t v3,
    vfloat32m1_t v4, vfloat32m1_t v5, vfloat32m1_t v6, size_t
    vl);
void vsseg8e32_v_f32m1 (float32_t *base, vfloat32m1_t v0,
    vfloat32m1_t v1, vfloat32m1_t v2, vfloat32m1_t v3,
    vfloat32m1_t v4, vfloat32m1_t v5, vfloat32m1_t v6,
    vfloat32m1_t v7, size_t vl);
void vsseg2e32_v_f32m2 (float32_t *base, vfloat32m2_t v0,
    vfloat32m2_t v1, size_t vl);
void vsseg3e32_v_f32m2 (float32_t *base, vfloat32m2_t v0,
    vfloat32m2_t v1, vfloat32m2_t v2, size_t vl);
void vsseg4e32_v_f32m2 (float32_t *base, vfloat32m2_t v0,
    vfloat32m2_t v1, vfloat32m2_t v2, vfloat32m2_t v3, size_t
    vl);
void vsseg2e32_v_f32m4 (float32_t *base, vfloat32m4_t v0,
    vfloat32m4_t v1, size_t vl);
void vsseg2e64_v_f64m1 (float64_t *base, vfloat64m1_t v0,
    vfloat64m1_t v1, size_t vl);
void vsseg3e64_v_f64m1 (float64_t *base, vfloat64m1_t v0,
    vfloat64m1_t v1, vfloat64m1_t v2, size_t vl);
void vsseg4e64_v_f64m1 (float64_t *base, vfloat64m1_t v0,
    vfloat64m1_t v1, vfloat64m1_t v2, vfloat64m1_t v3, size_t
    vl);
void vsseg5e64_v_f64m1 (float64_t *base, vfloat64m1_t v0,
    vfloat64m1_t v1, vfloat64m1_t v2, vfloat64m1_t v3,
    vfloat64m1_t v4, size_t vl);
void vsseg6e64_v_f64m1 (float64_t *base, vfloat64m1_t v0,
    vfloat64m1_t v1, vfloat64m1_t v2, vfloat64m1_t v3,
    vfloat64m1_t v4, vfloat64m1_t v5, size_t vl);
void vsseg7e64_v_f64m1 (float64_t *base, vfloat64m1_t v0,
    vfloat64m1_t v1, vfloat64m1_t v2, vfloat64m1_t v3,
    vfloat64m1_t v4, vfloat64m1_t v5, vfloat64m1_t v6, size_t
    vl);
void vsseg8e64_v_f64m1 (float64_t *base, vfloat64m1_t v0,
    vfloat64m1_t v1, vfloat64m1_t v2, vfloat64m1_t v3,
    vfloat64m1_t v4, vfloat64m1_t v5, vfloat64m1_t v6,
    vfloat64m1_t v7, size_t vl);
void vsseg2e64_v_f64m2 (float64_t *base, vfloat64m2_t v0,
    vfloat64m2_t v1, size_t vl);
void vsseg3e64_v_f64m2 (float64_t *base, vfloat64m2_t v0,
    vfloat64m2_t v1, vfloat64m2_t v2, size_t vl);

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void vsseg4e64_v_f64m2 (float64_t *base, vfloat64m2_t v0,
    vfloat64m2_t v1, vfloat64m2_t v2, vfloat64m2_t v3, size_t
    vl);
void vsseg2e64_v_f64m4 (float64_t *base, vfloat64m4_t v0,
    vfloat64m4_t v1, size_t vl);
// masked functions
void vsseg2e8_v_i8m1_m (vbool8_t mask, int8_t *base, vint8m1_t
    v0, vint8m1_t v1, size_t vl);
void vsseg3e8_v_i8m1_m (vbool8_t mask, int8_t *base, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, size_t vl);
void vsseg4e8_v_i8m1_m (vbool8_t mask, int8_t *base, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, size_t vl);
void vsseg5e8_v_i8m1_m (vbool8_t mask, int8_t *base, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, vint8m1_t v4,
    size_t vl);
void vsseg6e8_v_i8m1_m (vbool8_t mask, int8_t *base, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, vint8m1_t v4,
    vint8m1_t v5, size_t vl);
void vsseg7e8_v_i8m1_m (vbool8_t mask, int8_t *base, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, vint8m1_t v4,
    vint8m1_t v5, vint8m1_t v6, size_t vl);
void vsseg8e8_v_i8m1_m (vbool8_t mask, int8_t *base, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, vint8m1_t v4,
    vint8m1_t v5, vint8m1_t v6, vint8m1_t v7, size_t vl);
void vsseg2e8_v_i8m2_m (vbool4_t mask, int8_t *base, vint8m2_t
    v0, vint8m2_t v1, size_t vl);
void vsseg3e8_v_i8m2_m (vbool4_t mask, int8_t *base, vint8m2_t
    v0, vint8m2_t v1, vint8m2_t v2, size_t vl);
void vsseg4e8_v_i8m2_m (vbool4_t mask, int8_t *base, vint8m2_t
    v0, vint8m2_t v1, vint8m2_t v2, vint8m2_t v3, size_t vl);
void vsseg2e8_v_i8m4_m (vbool2_t mask, int8_t *base, vint8m4_t
    v0, vint8m4_t v1, size_t vl);
void vsseg2e16_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, size_t vl);
void vsseg3e16_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, size_t vl);
void vsseg4e16_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    size_t vl);
void vsseg5e16_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, size_t vl);
void vsseg6e16_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, size_t vl);

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void vsseg7e16_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, size_t vl);
void vsseg8e16_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, vint16m1_t v7,
    size_t vl);
void vsseg2e16_v_i16m2_m (vbool8_t mask, int16_t *base,
    vint16m2_t v0, vint16m2_t v1, size_t vl);
void vsseg3e16_v_i16m2_m (vbool8_t mask, int16_t *base,
    vint16m2_t v0, vint16m2_t v1, vint16m2_t v2, size_t vl);
void vsseg4e16_v_i16m2_m (vbool8_t mask, int16_t *base,
    vint16m2_t v0, vint16m2_t v1, vint16m2_t v2, vint16m2_t v3,
    size_t vl);
void vsseg2e16_v_i16m4_m (vbool4_t mask, int16_t *base,
    vint16m4_t v0, vint16m4_t v1, size_t vl);
void vsseg2e32_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, size_t vl);
void vsseg3e32_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, size_t vl);
void vsseg4e32_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    size_t vl);
void vsseg5e32_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, size_t vl);
void vsseg6e32_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, size_t vl);
void vsseg7e32_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, size_t vl);
void vsseg8e32_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, vint32m1_t v7,
    size_t vl);
void vsseg2e32_v_i32m2_m (vbool16_t mask, int32_t *base,
    vint32m2_t v0, vint32m2_t v1, size_t vl);
void vsseg3e32_v_i32m2_m (vbool16_t mask, int32_t *base,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, size_t vl);
void vsseg4e32_v_i32m2_m (vbool16_t mask, int32_t *base,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, vint32m2_t v3,
    size_t vl);
void vsseg2e32_v_i32m4_m (vbool8_t mask, int32_t *base,
    vint32m4_t v0, vint32m4_t v1, size_t vl);

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void vsseg2e64_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, size_t vl);
void vsseg3e64_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, size_t vl);
void vsseg4e64_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    size_t vl);
void vsseg5e64_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, size_t vl);
void vsseg6e64_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, size_t vl);
void vsseg7e64_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, size_t vl);
void vsseg8e64_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, vint64m1_t v7,
    size_t vl);
void vsseg2e64_v_i64m2_m (vbool32_t mask, int64_t *base,
    vint64m2_t v0, vint64m2_t v1, size_t vl);
void vsseg3e64_v_i64m2_m (vbool32_t mask, int64_t *base,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, size_t vl);
void vsseg4e64_v_i64m2_m (vbool32_t mask, int64_t *base,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, vint64m2_t v3,
    size_t vl);
void vsseg2e64_v_i64m4_m (vbool16_t mask, int64_t *base,
    vint64m4_t v0, vint64m4_t v1, size_t vl);
void vsseg2e8_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    v0, vuint8m1_t v1, size_t vl);
void vsseg3e8_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    v0, vuint8m1_t v1, vuint8m1_t v2, size_t vl);
void vsseg4e8_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3, size_t vl);
void vsseg5e8_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3, vuint8m1_t
    v4, size_t vl);
void vsseg6e8_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3, vuint8m1_t
    v4, vuint8m1_t v5, size_t vl);
void vsseg7e8_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3, vuint8m1_t
    v4, vuint8m1_t v5, vuint8m1_t v6, size_t vl);
void vsseg8e8_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3, vuint8m1_t

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    v4, vuint8m1_t v5, vuint8m1_t v6, vuint8m1_t v7, size_t vl);
void vsseg2e8_v_u8m2_m (vbool4_t mask, uint8_t *base, vuint8m2_t
    v0, vuint8m2_t v1, size_t vl);
void vsseg3e8_v_u8m2_m (vbool4_t mask, uint8_t *base, vuint8m2_t
    v0, vuint8m2_t v1, vuint8m2_t v2, size_t vl);
void vsseg4e8_v_u8m2_m (vbool4_t mask, uint8_t *base, vuint8m2_t
    v0, vuint8m2_t v1, vuint8m2_t v2, vuint8m2_t v3, size_t vl);
void vsseg2e8_v_u8m4_m (vbool2_t mask, uint8_t *base, vuint8m4_t
    v0, vuint8m4_t v1, size_t vl);
void vsseg2e16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, size_t vl);
void vsseg3e16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, size_t vl);
void vsseg4e16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, size_t vl);
void vsseg5e16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, size_t vl);
void vsseg6e16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, size_t vl);
void vsseg7e16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6, size_t
    vl);
void vsseg8e16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6,
    vuint16m1_t v7, size_t vl);
void vsseg2e16_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t v0, vuint16m2_t v1, size_t vl);
void vsseg3e16_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, size_t vl);
void vsseg4e16_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, vuint16m2_t
    v3, size_t vl);
void vsseg2e16_v_u16m4_m (vbool4_t mask, uint16_t *base,
    vuint16m4_t v0, vuint16m4_t v1, size_t vl);
void vsseg2e32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, size_t vl);
void vsseg3e32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, size_t vl);
void vsseg4e32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, size_t vl);

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void vsseg5e32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, size_t vl);
void vsseg6e32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, size_t vl);
void vsseg7e32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6, size_t
    vl);
void vsseg8e32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6,
    vuint32m1_t v7, size_t vl);
void vsseg2e32_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t v0, vuint32m2_t v1, size_t vl);
void vsseg3e32_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, size_t vl);
void vsseg4e32_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, vuint32m2_t
    v3, size_t vl);
void vsseg2e32_v_u32m4_m (vbool8_t mask, uint32_t *base,
    vuint32m4_t v0, vuint32m4_t v1, size_t vl);
void vsseg2e64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, size_t vl);
void vsseg3e64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, size_t vl);
void vsseg4e64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, size_t vl);
void vsseg5e64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, size_t vl);
void vsseg6e64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, size_t vl);
void vsseg7e64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6, size_t
    vl);
void vsseg8e64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6,
    vuint64m1_t v7, size_t vl);
void vsseg2e64_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t v0, vuint64m2_t v1, size_t vl);

```

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void vsseg3e64_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, size_t vl);
void vsseg4e64_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, vuint64m2_t
    v3, size_t vl);
void vsseg2e64_v_u64m4_m (vbool16_t mask, uint64_t *base,
    vuint64m4_t v0, vuint64m4_t v1, size_t vl);
void vsseg2e16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vfloat16m1_t v0, vfloat16m1_t v1, size_t vl);
void vsseg3e16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2, size_t
    vl);
void vsseg4e16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2,
    vfloat16m1_t v3, size_t vl);
void vsseg5e16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2,
    vfloat16m1_t v3, vfloat16m1_t v4, size_t vl);
void vsseg6e16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2,
    vfloat16m1_t v3, vfloat16m1_t v4, vfloat16m1_t v5, size_t
    vl);
void vsseg7e16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2,
    vfloat16m1_t v3, vfloat16m1_t v4, vfloat16m1_t v5,
    vfloat16m1_t v6, size_t vl);
void vsseg8e16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2,
    vfloat16m1_t v3, vfloat16m1_t v4, vfloat16m1_t v5,
    vfloat16m1_t v6, vfloat16m1_t v7, size_t vl);
void vsseg2e16_v_f16m2_m (vbool8_t mask, float16_t *base,
    vfloat16m2_t v0, vfloat16m2_t v1, size_t vl);
void vsseg3e16_v_f16m2_m (vbool8_t mask, float16_t *base,
    vfloat16m2_t v0, vfloat16m2_t v1, vfloat16m2_t v2, size_t
    vl);
void vsseg4e16_v_f16m2_m (vbool8_t mask, float16_t *base,
    vfloat16m2_t v0, vfloat16m2_t v1, vfloat16m2_t v2,
    vfloat16m2_t v3, size_t vl);
void vsseg2e16_v_f16m4_m (vbool14_t mask, float16_t *base,
    vfloat16m4_t v0, vfloat16m4_t v1, size_t vl);
void vsseg2e32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vfloat32m1_t v0, vfloat32m1_t v1, size_t vl);
void vsseg3e32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2, size_t
    vl);

```



```

void vsseg4e32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2,
    vfloat32m1_t v3, size_t vl);
void vsseg5e32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2,
    vfloat32m1_t v3, vfloat32m1_t v4, size_t vl);
void vsseg6e32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2,
    vfloat32m1_t v3, vfloat32m1_t v4, vfloat32m1_t v5, size_t
    vl);
void vsseg7e32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2,
    vfloat32m1_t v3, vfloat32m1_t v4, vfloat32m1_t v5,
    vfloat32m1_t v6, size_t vl);
void vsseg8e32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2,
    vfloat32m1_t v3, vfloat32m1_t v4, vfloat32m1_t v5,
    vfloat32m1_t v6, vfloat32m1_t v7, size_t vl);
void vsseg2e32_v_f32m2_m (vbool16_t mask, float32_t *base,
    vfloat32m2_t v0, vfloat32m2_t v1, size_t vl);
void vsseg3e32_v_f32m2_m (vbool16_t mask, float32_t *base,
    vfloat32m2_t v0, vfloat32m2_t v1, vfloat32m2_t v2, size_t
    vl);
void vsseg4e32_v_f32m2_m (vbool16_t mask, float32_t *base,
    vfloat32m2_t v0, vfloat32m2_t v1, vfloat32m2_t v2,
    vfloat32m2_t v3, size_t vl);
void vsseg2e32_v_f32m4_m (vbool8_t mask, float32_t *base,
    vfloat32m4_t v0, vfloat32m4_t v1, size_t vl);
void vsseg2e64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vfloat64m1_t v0, vfloat64m1_t v1, size_t vl);
void vsseg3e64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2, size_t
    vl);
void vsseg4e64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2,
    vfloat64m1_t v3, size_t vl);
void vsseg5e64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2,
    vfloat64m1_t v3, vfloat64m1_t v4, size_t vl);
void vsseg6e64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2,
    vfloat64m1_t v3, vfloat64m1_t v4, vfloat64m1_t v5, size_t
    vl);
void vsseg7e64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2,
    vfloat64m1_t v3, vfloat64m1_t v4, vfloat64m1_t v5,

```

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    vfloat64m1_t v6, size_t vl);
void vsseg8e64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2,
    vfloat64m1_t v3, vfloat64m1_t v4, vfloat64m1_t v5,
    vfloat64m1_t v6, vfloat64m1_t v7, size_t vl);
void vsseg2e64_v_f64m2_m (vbool32_t mask, float64_t *base,
    vfloat64m2_t v0, vfloat64m2_t v1, size_t vl);
void vsseg3e64_v_f64m2_m (vbool32_t mask, float64_t *base,
    vfloat64m2_t v0, vfloat64m2_t v1, vfloat64m2_t v2, size_t
    vl);
void vsseg4e64_v_f64m2_m (vbool32_t mask, float64_t *base,
    vfloat64m2_t v0, vfloat64m2_t v1, vfloat64m2_t v2,
    vfloat64m2_t v3, size_t vl);
void vsseg2e64_v_f64m4_m (vbool16_t mask, float64_t *base,
    vfloat64m4_t v0, vfloat64m4_t v1, size_t vl);

```

## Vector Strided Segment Load Functions:

### Prototypes:

```

void vlsseg2e8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, const
    int8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, const int8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, const int8_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg5e8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, const int8_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg6e8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, const
    int8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg7e8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
    *v6, const int8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg8e8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
    *v6, vint8m1_t *v7, const int8_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg2e8_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, const
    int8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e8_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, const int8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e8_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, vint8m2_t *v3, const int8_t *base, ptrdiff_t bstride,

```

```

    size_t vl);
void vlsseg2e8_v_i8m4 (vint8m4_t *v0, vint8m4_t *v1, const
    int8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, const
    int16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, const int16_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, const int16_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg5e16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, const
    int16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg6e16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, const int16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg7e16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, const int16_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg8e16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, vint16m1_t *v7, const int16_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg2e16_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1, const
    int16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e16_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, const int16_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e16_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vint16m2_t *v3, const int16_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg2e16_v_i16m4 (vint16m4_t *v0, vint16m4_t *v1, const
    int16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, const
    int32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, const int32_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, const int32_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg5e32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, const
    int32_t *base, ptrdiff_t bstride, size_t vl);

```

```

void vlsseg6e32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, const int32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg7e32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, const int32_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg8e32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, const int32_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg2e32_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, const
    int32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e32_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, const int32_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e32_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, const int32_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg2e32_v_i32m4 (vint32m4_t *v0, vint32m4_t *v1, const
    int32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, const
    int64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, const int64_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, const int64_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg5e64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, const
    int64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg6e64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, const int64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg7e64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, const int64_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg8e64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vint64m1_t *v7, const int64_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg2e64_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, const
    int64_t *base, ptrdiff_t bstride, size_t vl);

```

```

void vlsseg3e64_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, const int64_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e64_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, const int64_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg2e64_v_i64m4 (vint64m4_t *v0, vint64m4_t *v1, const
    int64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, const
    uint8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, const uint8_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, const uint8_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg5e8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, const
    uint8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg6e8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, const uint8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg7e8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, const uint8_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg8e8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, vuint8m1_t *v7, const uint8_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg2e8_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1, const
    uint8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e8_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, const uint8_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e8_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, vuint8m2_t *v3, const uint8_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg2e8_v_u8m4 (vuint8m4_t *v0, vuint8m4_t *v1, const
    uint8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1, const
    uint16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, const uint16_t *base, ptrdiff_t bstride,
    size_t vl);

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void vlsseg4e16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, const uint16_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg5e16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, const
    uint16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg6e16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, const uint16_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg7e16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, const uint16_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg8e16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, const
    uint16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e16_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1, const
    uint16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e16_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, const uint16_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e16_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vuint16m2_t *v3, const uint16_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg2e16_v_u16m4 (vuint16m4_t *v0, vuint16m4_t *v1, const
    uint16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1, const
    uint32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, const uint32_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, const uint32_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg5e32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, const
    uint32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg6e32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, const uint32_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg7e32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, const uint32_t *base,

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    ptrdiff_t bstride, size_t vl);
void vlsseg8e32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, const
    uint32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e32_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1, const
    uint32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e32_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, const uint32_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e32_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, const uint32_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg2e32_v_u32m4 (vuint32m4_t *v0, vuint32m4_t *v1, const
    uint32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1, const
    uint64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, const uint64_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, const uint64_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg5e64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, const
    uint64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg6e64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, const uint64_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg7e64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, const uint64_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg8e64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, const
    uint64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e64_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1, const
    uint64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e64_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, const uint64_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e64_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, const uint64_t *base,
    ptrdiff_t bstride, size_t vl);

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void vlsseg2e64_v_u64m4 (vuint64m4_t *v0, vuint64m4_t *v1, const
    uint64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    const float16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, const float16_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, const float16_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg5e16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4, const
    float16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg6e16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, const float16_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg7e16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, vfloat16m1_t *v6, const float16_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg8e16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, vfloat16m1_t *v6, vfloat16m1_t *v7, const
    float16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e16_v_f16m2 (vfloat16m2_t *v0, vfloat16m2_t *v1,
    const float16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e16_v_f16m2 (vfloat16m2_t *v0, vfloat16m2_t *v1,
    vfloat16m2_t *v2, const float16_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e16_v_f16m2 (vfloat16m2_t *v0, vfloat16m2_t *v1,
    vfloat16m2_t *v2, vfloat16m2_t *v3, const float16_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg2e16_v_f16m4 (vfloat16m4_t *v0, vfloat16m4_t *v1,
    const float16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    const float32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, const float32_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, const float32_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg5e32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4, const
    float32_t *base, ptrdiff_t bstride, size_t vl);

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void vlsseg6e32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, const float32_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg7e32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, vfloat32m1_t *v6, const float32_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg8e32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, vfloat32m1_t *v6, vfloat32m1_t *v7, const
    float32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e32_v_f32m2 (vfloat32m2_t *v0, vfloat32m2_t *v1,
    const float32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e32_v_f32m2 (vfloat32m2_t *v0, vfloat32m2_t *v1,
    vfloat32m2_t *v2, const float32_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e32_v_f32m2 (vfloat32m2_t *v0, vfloat32m2_t *v1,
    vfloat32m2_t *v2, vfloat32m2_t *v3, const float32_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg2e32_v_f32m4 (vfloat32m4_t *v0, vfloat32m4_t *v1,
    const float32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    const float64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, const float64_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, const float64_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg5e64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4, const
    float64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg6e64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, const float64_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg7e64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, vfloat64m1_t *v6, const float64_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg8e64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, vfloat64m1_t *v6, vfloat64m1_t *v7, const
    float64_t *base, ptrdiff_t bstride, size_t vl);

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void vlsseg2e64_v_f64m2 (vfloat64m2_t *v0, vfloat64m2_t *v1,
    const float64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e64_v_f64m2 (vfloat64m2_t *v0, vfloat64m2_t *v1,
    vfloat64m2_t *v2, const float64_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg4e64_v_f64m2 (vfloat64m2_t *v0, vfloat64m2_t *v1,
    vfloat64m2_t *v2, vfloat64m2_t *v3, const float64_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg2e64_v_f64m4 (vfloat64m4_t *v0, vfloat64m4_t *v1,
    const float64_t *base, ptrdiff_t bstride, size_t vl);
// masked functions
void vlsseg2e8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vbool8_t
    mask, vint8m1_t maskedoff0, vint8m1_t maskedoff1, const
    int8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vbool8_t mask, vint8m1_t maskedoff0, vint8m1_t
    maskedoff1, vint8m1_t maskedoff2, const int8_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg4e8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vbool8_t mask, vint8m1_t maskedoff0,
    vint8m1_t maskedoff1, vint8m1_t maskedoff2, vint8m1_t
    maskedoff3, const int8_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg5e8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vbool8_t mask, vint8m1_t
    maskedoff0, vint8m1_t maskedoff1, vint8m1_t maskedoff2,
    vint8m1_t maskedoff3, vint8m1_t maskedoff4, const int8_t
    *base, ptrdiff_t bstride, size_t vl);
void vlsseg6e8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vbool8_t
    mask, vint8m1_t maskedoff0, vint8m1_t maskedoff1, vint8m1_t
    maskedoff2, vint8m1_t maskedoff3, vint8m1_t maskedoff4,
    vint8m1_t maskedoff5, const int8_t *base, ptrdiff_t bstride,
    size_t vl);
void vlsseg7e8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
    *v6, vbool8_t mask, vint8m1_t maskedoff0, vint8m1_t
    maskedoff1, vint8m1_t maskedoff2, vint8m1_t maskedoff3,
    vint8m1_t maskedoff4, vint8m1_t maskedoff5, vint8m1_t
    maskedoff6, const int8_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg8e8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
    *v6, vint8m1_t *v7, vbool8_t mask, vint8m1_t maskedoff0,
    vint8m1_t maskedoff1, vint8m1_t maskedoff2, vint8m1_t
    maskedoff3, vint8m1_t maskedoff4, vint8m1_t maskedoff5,

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    vint8m1_t maskedoff6, vint8m1_t maskedoff7, const int8_t
    *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e8_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1, vbool4_t
    mask, vint8m2_t maskedoff0, vint8m2_t maskedoff1, const
    int8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e8_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, vbool4_t mask, vint8m2_t maskedoff0, vint8m2_t
    maskedoff1, vint8m2_t maskedoff2, const int8_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg4e8_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, vint8m2_t *v3, vbool4_t mask, vint8m2_t maskedoff0,
    vint8m2_t maskedoff1, vint8m2_t maskedoff2, vint8m2_t
    maskedoff3, const int8_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg2e8_v_i8m4_m (vint8m4_t *v0, vint8m4_t *v1, vbool2_t
    mask, vint8m4_t maskedoff0, vint8m4_t maskedoff1, const
    int8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
    maskedoff1, const int16_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg3e16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vbool16_t mask, vint16m1_t maskedoff0,
    vint16m1_t maskedoff1, vint16m1_t maskedoff2, const int16_t
    *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vbool16_t mask, vint16m1_t
    maskedoff0, vint16m1_t maskedoff1, vint16m1_t maskedoff2,
    vint16m1_t maskedoff3, const int16_t *base, ptrdiff_t
    bstride, size_t vl);
void vlsseg5e16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vbool16_t
    mask, vint16m1_t maskedoff0, vint16m1_t maskedoff1,
    vint16m1_t maskedoff2, vint16m1_t maskedoff3, vint16m1_t
    maskedoff4, const int16_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg6e16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
    maskedoff1, vint16m1_t maskedoff2, vint16m1_t maskedoff3,
    vint16m1_t maskedoff4, vint16m1_t maskedoff5, const int16_t
    *base, ptrdiff_t bstride, size_t vl);
void vlsseg7e16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, vbool16_t mask, vint16m1_t maskedoff0,
    vint16m1_t maskedoff1, vint16m1_t maskedoff2, vint16m1_t

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maskedoff3, vint16m1_t maskedoff4, vint16m1_t maskedoff5,
vint16m1_t maskedoff6, const int16_t *base, ptrdiff_t
bstride, size_t vl);
void vlsseg8e16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vint16m1_t *v6, vint16m1_t *v7, vbool16_t mask,
vint16m1_t maskedoff0, vint16m1_t maskedoff1, vint16m1_t
maskedoff2, vint16m1_t maskedoff3, vint16m1_t maskedoff4,
vint16m1_t maskedoff5, vint16m1_t maskedoff6, vint16m1_t
maskedoff7, const int16_t *base, ptrdiff_t bstride, size_t
vl);
void vlsseg2e16_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
vbool8_t mask, vint16m2_t maskedoff0, vint16m2_t maskedoff1,
const int16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e16_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
vint16m2_t *v2, vbool8_t mask, vint16m2_t maskedoff0,
vint16m2_t maskedoff1, vint16m2_t maskedoff2, const int16_t
*base, ptrdiff_t bstride, size_t vl);
void vlsseg4e16_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
vint16m2_t *v2, vint16m2_t *v3, vbool8_t mask, vint16m2_t
maskedoff0, vint16m2_t maskedoff1, vint16m2_t maskedoff2,
vint16m2_t maskedoff3, const int16_t *base, ptrdiff_t
bstride, size_t vl);
void vlsseg2e16_v_i16m4_m (vint16m4_t *v0, vint16m4_t *v1,
vbool4_t mask, vint16m4_t maskedoff0, vint16m4_t maskedoff1,
const int16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
maskedoff1, const int32_t *base, ptrdiff_t bstride, size_t
vl);
void vlsseg3e32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vbool32_t mask, vint32m1_t maskedoff0,
vint32m1_t maskedoff1, vint32m1_t maskedoff2, const int32_t
*base, ptrdiff_t bstride, size_t vl);
void vlsseg4e32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vbool32_t mask, vint32m1_t
maskedoff0, vint32m1_t maskedoff1, vint32m1_t maskedoff2,
vint32m1_t maskedoff3, const int32_t *base, ptrdiff_t
bstride, size_t vl);
void vlsseg5e32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vbool32_t
mask, vint32m1_t maskedoff0, vint32m1_t maskedoff1,
vint32m1_t maskedoff2, vint32m1_t maskedoff3, vint32m1_t
maskedoff4, const int32_t *base, ptrdiff_t bstride, size_t
vl);

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void vlsseg6e32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
*v5, vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
maskedoff1, vint32m1_t maskedoff2, vint32m1_t maskedoff3,
vint32m1_t maskedoff4, vint32m1_t maskedoff5, const int32_t
*base, ptrdiff_t bstride, size_t vl);
void vlsseg7e32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
*v5, vint32m1_t *v6, vbool32_t mask, vint32m1_t maskedoff0,
vint32m1_t maskedoff1, vint32m1_t maskedoff2, vint32m1_t
maskedoff3, vint32m1_t maskedoff4, vint32m1_t maskedoff5,
vint32m1_t maskedoff6, const int32_t *base, ptrdiff_t
bstride, size_t vl);
void vlsseg8e32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
*v5, vint32m1_t *v6, vint32m1_t *v7, vbool32_t mask,
vint32m1_t maskedoff0, vint32m1_t maskedoff1, vint32m1_t
maskedoff2, vint32m1_t maskedoff3, vint32m1_t maskedoff4,
vint32m1_t maskedoff5, vint32m1_t maskedoff6, vint32m1_t
maskedoff7, const int32_t *base, ptrdiff_t bstride, size_t
vl);
void vlsseg2e32_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
vbool16_t mask, vint32m2_t maskedoff0, vint32m2_t
maskedoff1, const int32_t *base, ptrdiff_t bstride, size_t
vl);
void vlsseg3e32_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
vint32m2_t *v2, vbool16_t mask, vint32m2_t maskedoff0,
vint32m2_t maskedoff1, vint32m2_t maskedoff2, const int32_t
*base, ptrdiff_t bstride, size_t vl);
void vlsseg4e32_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
vint32m2_t *v2, vint32m2_t *v3, vbool16_t mask, vint32m2_t
maskedoff0, vint32m2_t maskedoff1, vint32m2_t maskedoff2,
vint32m2_t maskedoff3, const int32_t *base, ptrdiff_t
bstride, size_t vl);
void vlsseg2e32_v_i32m4_m (vint32m4_t *v0, vint32m4_t *v1,
vbool8_t mask, vint32m4_t maskedoff0, vint32m4_t maskedoff1,
const int32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
maskedoff1, const int64_t *base, ptrdiff_t bstride, size_t
vl);
void vlsseg3e64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vbool64_t mask, vint64m1_t maskedoff0,
vint64m1_t maskedoff1, vint64m1_t maskedoff2, const int64_t
*base, ptrdiff_t bstride, size_t vl);

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void vlsseg4e64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vbool64_t mask, vint64m1_t
    maskedoff0, vint64m1_t maskedoff1, vint64m1_t maskedoff2,
    vint64m1_t maskedoff3, const int64_t *base, ptrdiff_t
    bstride, size_t vl);
void vlsseg5e64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vbool64_t
    mask, vint64m1_t maskedoff0, vint64m1_t maskedoff1,
    vint64m1_t maskedoff2, vint64m1_t maskedoff3, vint64m1_t
    maskedoff4, const int64_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg6e64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, vint64m1_t maskedoff2, vint64m1_t maskedoff3,
    vint64m1_t maskedoff4, vint64m1_t maskedoff5, const int64_t
    *base, ptrdiff_t bstride, size_t vl);
void vlsseg7e64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, vint64m1_t
    maskedoff3, vint64m1_t maskedoff4, vint64m1_t maskedoff5,
    vint64m1_t maskedoff6, const int64_t *base, ptrdiff_t
    bstride, size_t vl);
void vlsseg8e64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vint64m1_t *v7, vbool64_t mask,
    vint64m1_t maskedoff0, vint64m1_t maskedoff1, vint64m1_t
    maskedoff2, vint64m1_t maskedoff3, vint64m1_t maskedoff4,
    vint64m1_t maskedoff5, vint64m1_t maskedoff6, vint64m1_t
    maskedoff7, const int64_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg2e64_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vbool32_t mask, vint64m2_t maskedoff0, vint64m2_t
    maskedoff1, const int64_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg3e64_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vbool32_t mask, vint64m2_t maskedoff0,
    vint64m2_t maskedoff1, vint64m2_t maskedoff2, const int64_t
    *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e64_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, vbool32_t mask, vint64m2_t
    maskedoff0, vint64m2_t maskedoff1, vint64m2_t maskedoff2,
    vint64m2_t maskedoff3, const int64_t *base, ptrdiff_t
    bstride, size_t vl);

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void vlsseg2e64_v_i64m4_m (vint64m4_t *v0, vint64m4_t *v1,
    vbool16_t mask, vint64m4_t maskedoff0, vint64m4_t
    maskedoff1, const int64_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg2e8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vbool8_t mask, vuint8m1_t maskedoff0, vuint8m1_t maskedoff1,
    const uint8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg3e8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vbool8_t mask, vuint8m1_t maskedoff0,
    vuint8m1_t maskedoff1, vuint8m1_t maskedoff2, const uint8_t
    *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vbool8_t mask, vuint8m1_t
    maskedoff0, vuint8m1_t maskedoff1, vuint8m1_t maskedoff2,
    vuint8m1_t maskedoff3, const uint8_t *base, ptrdiff_t
    bstride, size_t vl);
void vlsseg5e8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vbool8_t
    mask, vuint8m1_t maskedoff0, vuint8m1_t maskedoff1,
    vuint8m1_t maskedoff2, vuint8m1_t maskedoff3, vuint8m1_t
    maskedoff4, const uint8_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg6e8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vbool8_t mask, vuint8m1_t maskedoff0, vuint8m1_t
    maskedoff1, vuint8m1_t maskedoff2, vuint8m1_t maskedoff3,
    vuint8m1_t maskedoff4, vuint8m1_t maskedoff5, const uint8_t
    *base, ptrdiff_t bstride, size_t vl);
void vlsseg7e8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, vbool8_t mask, vuint8m1_t maskedoff0,
    vuint8m1_t maskedoff1, vuint8m1_t maskedoff2, vuint8m1_t
    maskedoff3, vuint8m1_t maskedoff4, vuint8m1_t maskedoff5,
    vuint8m1_t maskedoff6, const uint8_t *base, ptrdiff_t
    bstride, size_t vl);
void vlsseg8e8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, vuint8m1_t *v7, vbool8_t mask,
    vuint8m1_t maskedoff0, vuint8m1_t maskedoff1, vuint8m1_t
    maskedoff2, vuint8m1_t maskedoff3, vuint8m1_t maskedoff4,
    vuint8m1_t maskedoff5, vuint8m1_t maskedoff6, vuint8m1_t
    maskedoff7, const uint8_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg2e8_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
    vbool4_t mask, vuint8m2_t maskedoff0, vuint8m2_t maskedoff1,
    const uint8_t *base, ptrdiff_t bstride, size_t vl);

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void vlsseg3e8_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, vbool4_t mask, vuint8m2_t maskedoff0,
    vuint8m2_t maskedoff1, vuint8m2_t maskedoff2, const uint8_t
    *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e8_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, vuint8m2_t *v3, vbool4_t mask, vuint8m2_t
    maskedoff0, vuint8m2_t maskedoff1, vuint8m2_t maskedoff2,
    vuint8m2_t maskedoff3, const uint8_t *base, ptrdiff_t
    bstride, size_t vl);
void vlsseg2e8_v_u8m4_m (vuint8m4_t *v0, vuint8m4_t *v1,
    vbool2_t mask, vuint8m4_t maskedoff0, vuint8m4_t maskedoff1,
    const uint8_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vbool16_t mask, vuint16m1_t maskedoff0, vuint16m1_t
    maskedoff1, const uint16_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg3e16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vbool16_t mask, vuint16m1_t maskedoff0,
    vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, const
    uint16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vbool16_t mask,
    vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
    maskedoff2, vuint16m1_t maskedoff3, const uint16_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg5e16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, vbool16_t
    mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
    vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
    maskedoff4, const uint16_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg6e16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vbool16_t mask, vuint16m1_t maskedoff0,
    vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, vuint16m1_t
    maskedoff3, vuint16m1_t maskedoff4, vuint16m1_t maskedoff5,
    const uint16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg7e16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, vbool16_t mask,
    vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
    maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t maskedoff4,
    vuint16m1_t maskedoff5, vuint16m1_t maskedoff6, const
    uint16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg8e16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,

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vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, vbool16_t
mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
maskedoff4, vuint16m1_t maskedoff5, vuint16m1_t maskedoff6,
vuint16m1_t maskedoff7, const uint16_t *base, ptrdiff_t
bstride, size_t vl);
void vlsseg2e16_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
vbool8_t mask, vuint16m2_t maskedoff0, vuint16m2_t
maskedoff1, const uint16_t *base, ptrdiff_t bstride, size_t
vl);
void vlsseg3e16_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
vuint16m2_t *v2, vbool8_t mask, vuint16m2_t maskedoff0,
vuint16m2_t maskedoff1, vuint16m2_t maskedoff2, const
uint16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e16_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
vuint16m2_t *v2, vuint16m2_t *v3, vbool8_t mask, vuint16m2_t
maskedoff0, vuint16m2_t maskedoff1, vuint16m2_t maskedoff2,
vuint16m2_t maskedoff3, const uint16_t *base, ptrdiff_t
bstride, size_t vl);
void vlsseg2e16_v_u16m4_m (vuint16m4_t *v0, vuint16m4_t *v1,
vbool4_t mask, vuint16m4_t maskedoff0, vuint16m4_t
maskedoff1, const uint16_t *base, ptrdiff_t bstride, size_t
vl);
void vlsseg2e32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vbool32_t mask, vuint32m1_t maskedoff0, vuint32m1_t
maskedoff1, const uint32_t *base, ptrdiff_t bstride, size_t
vl);
void vlsseg3e32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vbool32_t mask, vuint32m1_t maskedoff0,
vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, const
uint32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vbool32_t mask,
vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
maskedoff2, vuint32m1_t maskedoff3, const uint32_t *base,
ptrdiff_t bstride, size_t vl);
void vlsseg5e32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, vbool32_t
mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
maskedoff4, const uint32_t *base, ptrdiff_t bstride, size_t
vl);
void vlsseg6e32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vbool32_t mask, vuint32m1_t maskedoff0,
vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, vuint32m1_t

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maskedoff3, vuint32m1_t maskedoff4, vuint32m1_t maskedoff5,
    const uint32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg7e32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t maskedoff4,
    vuint32m1_t maskedoff5, vuint32m1_t maskedoff6, const
    uint32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg8e32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, vuint32m1_t maskedoff5, vuint32m1_t maskedoff6,
    vuint32m1_t maskedoff7, const uint32_t *base, ptrdiff_t
    bstride, size_t vl);
void vlsseg2e32_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vbool16_t mask, vuint32m2_t maskedoff0, vuint32m2_t
    maskedoff1, const uint32_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg3e32_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vbool16_t mask, vuint32m2_t maskedoff0,
    vuint32m2_t maskedoff1, vuint32m2_t maskedoff2, const
    uint32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e32_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, vbool16_t mask,
    vuint32m2_t maskedoff0, vuint32m2_t maskedoff1, vuint32m2_t
    maskedoff2, vuint32m2_t maskedoff3, const uint32_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg2e32_v_u32m4_m (vuint32m4_t *v0, vuint32m4_t *v1,
    vbool8_t mask, vuint32m4_t maskedoff0, vuint32m4_t
    maskedoff1, const uint32_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg2e64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vbool64_t mask, vuint64m1_t maskedoff0, vuint64m1_t
    maskedoff1, const uint64_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg3e64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, const
    uint64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, const uint64_t *base,

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    ptrdiff_t bstride, size_t vl);
void vlsseg5e64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, const uint64_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg6e64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, vuint64m1_t
    maskedoff3, vuint64m1_t maskedoff4, vuint64m1_t maskedoff5,
    const uint64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg7e64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t maskedoff4,
    vuint64m1_t maskedoff5, vuint64m1_t maskedoff6, const
    uint64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg8e64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, vuint64m1_t maskedoff5, vuint64m1_t maskedoff6,
    vuint64m1_t maskedoff7, const uint64_t *base, ptrdiff_t
    bstride, size_t vl);
void vlsseg2e64_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vbool32_t mask, vuint64m2_t maskedoff0, vuint64m2_t
    maskedoff1, const uint64_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg3e64_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vbool32_t mask, vuint64m2_t maskedoff0,
    vuint64m2_t maskedoff1, vuint64m2_t maskedoff2, const
    uint64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e64_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, vbool32_t mask,
    vuint64m2_t maskedoff0, vuint64m2_t maskedoff1, vuint64m2_t
    maskedoff2, vuint64m2_t maskedoff3, const uint64_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg2e64_v_u64m4_m (vuint64m4_t *v0, vuint64m4_t *v1,
    vbool16_t mask, vuint64m4_t maskedoff0, vuint64m4_t
    maskedoff1, const uint64_t *base, ptrdiff_t bstride, size_t
    vl);

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void vlsseg2e16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vbool16_t mask, vfloat16m1_t maskedoff0, vfloat16m1_t
    maskedoff1, const float16_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg3e16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vbool16_t mask, vfloat16m1_t maskedoff0,
    vfloat16m1_t maskedoff1, vfloat16m1_t maskedoff2, const
    float16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vbool16_t mask,
    vfloat16m1_t maskedoff0, vfloat16m1_t maskedoff1,
    vfloat16m1_t maskedoff2, vfloat16m1_t maskedoff3, const
    float16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg5e16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vbool16_t mask, vfloat16m1_t maskedoff0, vfloat16m1_t
    maskedoff1, vfloat16m1_t maskedoff2, vfloat16m1_t
    maskedoff3, vfloat16m1_t maskedoff4, const float16_t *base,
    ptrdiff_t bstride, size_t vl);
void vlsseg6e16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, vbool16_t mask, vfloat16m1_t maskedoff0,
    vfloat16m1_t maskedoff1, vfloat16m1_t maskedoff2,
    vfloat16m1_t maskedoff3, vfloat16m1_t maskedoff4,
    vfloat16m1_t maskedoff5, const float16_t *base, ptrdiff_t
    bstride, size_t vl);
void vlsseg7e16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, vfloat16m1_t *v6, vbool16_t mask,
    vfloat16m1_t maskedoff0, vfloat16m1_t maskedoff1,
    vfloat16m1_t maskedoff2, vfloat16m1_t maskedoff3,
    vfloat16m1_t maskedoff4, vfloat16m1_t maskedoff5,
    vfloat16m1_t maskedoff6, const float16_t *base, ptrdiff_t
    bstride, size_t vl);
void vlsseg8e16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, vfloat16m1_t *v6, vfloat16m1_t *v7,
    vbool16_t mask, vfloat16m1_t maskedoff0, vfloat16m1_t
    maskedoff1, vfloat16m1_t maskedoff2, vfloat16m1_t
    maskedoff3, vfloat16m1_t maskedoff4, vfloat16m1_t
    maskedoff5, vfloat16m1_t maskedoff6, vfloat16m1_t
    maskedoff7, const float16_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg2e16_v_f16m2_m (vfloat16m2_t *v0, vfloat16m2_t *v1,
    vbool8_t mask, vfloat16m2_t maskedoff0, vfloat16m2_t
    maskedoff1, const float16_t *base, ptrdiff_t bstride, size_t

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    vl);
void vlsseg3e16_v_f16m2_m (vfloat16m2_t *v0, vfloat16m2_t *v1,
vfloat16m2_t *v2, vbool8_t mask, vfloat16m2_t maskedoff0,
vfloat16m2_t maskedoff1, vfloat16m2_t maskedoff2, const
float16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e16_v_f16m2_m (vfloat16m2_t *v0, vfloat16m2_t *v1,
vfloat16m2_t *v2, vfloat16m2_t *v3, vbool8_t mask,
vfloat16m2_t maskedoff0, vfloat16m2_t maskedoff1,
vfloat16m2_t maskedoff2, vfloat16m2_t maskedoff3, const
float16_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e16_v_f16m4_m (vfloat16m4_t *v0, vfloat16m4_t *v1,
vbool4_t mask, vfloat16m4_t maskedoff0, vfloat16m4_t
maskedoff1, const float16_t *base, ptrdiff_t bstride, size_t
vl);
void vlsseg2e32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
vbool32_t mask, vfloat32m1_t maskedoff0, vfloat32m1_t
maskedoff1, const float32_t *base, ptrdiff_t bstride, size_t
vl);
void vlsseg3e32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
vfloat32m1_t *v2, vbool32_t mask, vfloat32m1_t maskedoff0,
vfloat32m1_t maskedoff1, vfloat32m1_t maskedoff2, const
float32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
vfloat32m1_t *v2, vfloat32m1_t *v3, vbool32_t mask,
vfloat32m1_t maskedoff0, vfloat32m1_t maskedoff1,
vfloat32m1_t maskedoff2, vfloat32m1_t maskedoff3, const
float32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg5e32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
vbool32_t mask, vfloat32m1_t maskedoff0, vfloat32m1_t
maskedoff1, vfloat32m1_t maskedoff2, vfloat32m1_t
maskedoff3, vfloat32m1_t maskedoff4, const float32_t *base,
ptrdiff_t bstride, size_t vl);
void vlsseg6e32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
vfloat32m1_t *v5, vbool32_t mask, vfloat32m1_t maskedoff0,
vfloat32m1_t maskedoff1, vfloat32m1_t maskedoff2,
vfloat32m1_t maskedoff3, vfloat32m1_t maskedoff4,
vfloat32m1_t maskedoff5, const float32_t *base, ptrdiff_t
bstride, size_t vl);
void vlsseg7e32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
vfloat32m1_t *v5, vfloat32m1_t *v6, vbool32_t mask,
vfloat32m1_t maskedoff0, vfloat32m1_t maskedoff1,
vfloat32m1_t maskedoff2, vfloat32m1_t maskedoff3,
vfloat32m1_t maskedoff4, vfloat32m1_t maskedoff5,

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    vfloat32m1_t maskedoff6, const float32_t *base, ptrdiff_t
    bstride, size_t vl);
void vlsseg8e32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, vfloat32m1_t *v6, vfloat32m1_t *v7,
    vbool32_t mask, vfloat32m1_t maskedoff0, vfloat32m1_t
    maskedoff1, vfloat32m1_t maskedoff2, vfloat32m1_t
    maskedoff3, vfloat32m1_t maskedoff4, vfloat32m1_t
    maskedoff5, vfloat32m1_t maskedoff6, vfloat32m1_t
    maskedoff7, const float32_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg2e32_v_f32m2_m (vfloat32m2_t *v0, vfloat32m2_t *v1,
    vbool16_t mask, vfloat32m2_t maskedoff0, vfloat32m2_t
    maskedoff1, const float32_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg3e32_v_f32m2_m (vfloat32m2_t *v0, vfloat32m2_t *v1,
    vfloat32m2_t *v2, vbool16_t mask, vfloat32m2_t maskedoff0,
    vfloat32m2_t maskedoff1, vfloat32m2_t maskedoff2, const
    float32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e32_v_f32m2_m (vfloat32m2_t *v0, vfloat32m2_t *v1,
    vfloat32m2_t *v2, vfloat32m2_t *v3, vbool16_t mask,
    vfloat32m2_t maskedoff0, vfloat32m2_t maskedoff1,
    vfloat32m2_t maskedoff2, vfloat32m2_t maskedoff3, const
    float32_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e32_v_f32m4_m (vfloat32m4_t *v0, vfloat32m4_t *v1,
    vbool8_t mask, vfloat32m4_t maskedoff0, vfloat32m4_t
    maskedoff1, const float32_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg2e64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vbool64_t mask, vfloat64m1_t maskedoff0, vfloat64m1_t
    maskedoff1, const float64_t *base, ptrdiff_t bstride, size_t
    vl);
void vlsseg3e64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vbool64_t mask, vfloat64m1_t maskedoff0,
    vfloat64m1_t maskedoff1, vfloat64m1_t maskedoff2, const
    float64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vbool64_t mask,
    vfloat64m1_t maskedoff0, vfloat64m1_t maskedoff1,
    vfloat64m1_t maskedoff2, vfloat64m1_t maskedoff3, const
    float64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg5e64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vbool64_t mask, vfloat64m1_t maskedoff0, vfloat64m1_t
    maskedoff1, vfloat64m1_t maskedoff2, vfloat64m1_t
    maskedoff3, vfloat64m1_t maskedoff4, const float64_t *base,

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    ptrdiff_t bstride, size_t vl);
void vlsseg6e64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
vfloat64m1_t *v5, vbool64_t mask, vfloat64m1_t maskedoff0,
vfloat64m1_t maskedoff1, vfloat64m1_t maskedoff2,
vfloat64m1_t maskedoff3, vfloat64m1_t maskedoff4,
vfloat64m1_t maskedoff5, const float64_t *base, ptrdiff_t
bstride, size_t vl);
void vlsseg7e64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
vfloat64m1_t *v5, vfloat64m1_t *v6, vbool64_t mask,
vfloat64m1_t maskedoff0, vfloat64m1_t maskedoff1,
vfloat64m1_t maskedoff2, vfloat64m1_t maskedoff3,
vfloat64m1_t maskedoff4, vfloat64m1_t maskedoff5,
vfloat64m1_t maskedoff6, const float64_t *base, ptrdiff_t
bstride, size_t vl);
void vlsseg8e64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
vfloat64m1_t *v5, vfloat64m1_t *v6, vfloat64m1_t *v7,
vbool64_t mask, vfloat64m1_t maskedoff0, vfloat64m1_t
maskedoff1, vfloat64m1_t maskedoff2, vfloat64m1_t
maskedoff3, vfloat64m1_t maskedoff4, vfloat64m1_t
maskedoff5, vfloat64m1_t maskedoff6, vfloat64m1_t
maskedoff7, const float64_t *base, ptrdiff_t bstride, size_t
vl);
void vlsseg2e64_v_f64m2_m (vfloat64m2_t *v0, vfloat64m2_t *v1,
vbool32_t mask, vfloat64m2_t maskedoff0, vfloat64m2_t
maskedoff1, const float64_t *base, ptrdiff_t bstride, size_t
vl);
void vlsseg3e64_v_f64m2_m (vfloat64m2_t *v0, vfloat64m2_t *v1,
vfloat64m2_t *v2, vbool32_t mask, vfloat64m2_t maskedoff0,
vfloat64m2_t maskedoff1, vfloat64m2_t maskedoff2, const
float64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg4e64_v_f64m2_m (vfloat64m2_t *v0, vfloat64m2_t *v1,
vfloat64m2_t *v2, vfloat64m2_t *v3, vbool32_t mask,
vfloat64m2_t maskedoff0, vfloat64m2_t maskedoff1,
vfloat64m2_t maskedoff2, vfloat64m2_t maskedoff3, const
float64_t *base, ptrdiff_t bstride, size_t vl);
void vlsseg2e64_v_f64m4_m (vfloat64m4_t *v0, vfloat64m4_t *v1,
vbool16_t mask, vfloat64m4_t maskedoff0, vfloat64m4_t
maskedoff1, const float64_t *base, ptrdiff_t bstride, size_t
vl);

```

**Vector Strided Segment Store Functions:**

**Prototypes:**

```

void vssseg2e8_v_i8m1 (int8_t *base, ptrdiff_t bstride,
    vint8m1_t v0, vint8m1_t v1, size_t vl);
void vssseg3e8_v_i8m1 (int8_t *base, ptrdiff_t bstride,
    vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, size_t vl);
void vssseg4e8_v_i8m1 (int8_t *base, ptrdiff_t bstride,
    vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3,
    size_t vl);
void vssseg5e8_v_i8m1 (int8_t *base, ptrdiff_t bstride,
    vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3,
    vint8m1_t v4, size_t vl);
void vssseg6e8_v_i8m1 (int8_t *base, ptrdiff_t bstride,
    vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3,
    vint8m1_t v4, vint8m1_t v5, size_t vl);
void vssseg7e8_v_i8m1 (int8_t *base, ptrdiff_t bstride,
    vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3,
    vint8m1_t v4, vint8m1_t v5, vint8m1_t v6, size_t vl);
void vssseg8e8_v_i8m1 (int8_t *base, ptrdiff_t bstride,
    vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3,
    vint8m1_t v4, vint8m1_t v5, vint8m1_t v6, vint8m1_t v7,
    size_t vl);
void vssseg2e8_v_i8m2 (int8_t *base, ptrdiff_t bstride,
    vint8m2_t v0, vint8m2_t v1, size_t vl);
void vssseg3e8_v_i8m2 (int8_t *base, ptrdiff_t bstride,
    vint8m2_t v0, vint8m2_t v1, vint8m2_t v2, size_t vl);
void vssseg4e8_v_i8m2 (int8_t *base, ptrdiff_t bstride,
    vint8m2_t v0, vint8m2_t v1, vint8m2_t v2, vint8m2_t v3,
    size_t vl);
void vssseg2e8_v_i8m4 (int8_t *base, ptrdiff_t bstride,
    vint8m4_t v0, vint8m4_t v1, size_t vl);
void vssseg2e16_v_i16m1 (int16_t *base, ptrdiff_t bstride,
    vint16m1_t v0, vint16m1_t v1, size_t vl);
void vssseg3e16_v_i16m1 (int16_t *base, ptrdiff_t bstride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, size_t vl);
void vssseg4e16_v_i16m1 (int16_t *base, ptrdiff_t bstride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    size_t vl);
void vssseg5e16_v_i16m1 (int16_t *base, ptrdiff_t bstride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, size_t vl);
void vssseg6e16_v_i16m1 (int16_t *base, ptrdiff_t bstride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, size_t vl);
void vssseg7e16_v_i16m1 (int16_t *base, ptrdiff_t bstride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, size_t vl);

```



```

void vssseg8e16_v_i16m1 (int16_t *base, ptrdiff_t bstride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, vint16m1_t v7,
    size_t vl);
void vssseg2e16_v_i16m2 (int16_t *base, ptrdiff_t bstride,
    vint16m2_t v0, vint16m2_t v1, size_t vl);
void vssseg3e16_v_i16m2 (int16_t *base, ptrdiff_t bstride,
    vint16m2_t v0, vint16m2_t v1, vint16m2_t v2, size_t vl);
void vssseg4e16_v_i16m2 (int16_t *base, ptrdiff_t bstride,
    vint16m2_t v0, vint16m2_t v1, vint16m2_t v2, vint16m2_t v3,
    size_t vl);
void vssseg2e16_v_i16m4 (int16_t *base, ptrdiff_t bstride,
    vint16m4_t v0, vint16m4_t v1, size_t vl);
void vssseg2e32_v_i32m1 (int32_t *base, ptrdiff_t bstride,
    vint32m1_t v0, vint32m1_t v1, size_t vl);
void vssseg3e32_v_i32m1 (int32_t *base, ptrdiff_t bstride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, size_t vl);
void vssseg4e32_v_i32m1 (int32_t *base, ptrdiff_t bstride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    size_t vl);
void vssseg5e32_v_i32m1 (int32_t *base, ptrdiff_t bstride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, size_t vl);
void vssseg6e32_v_i32m1 (int32_t *base, ptrdiff_t bstride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, size_t vl);
void vssseg7e32_v_i32m1 (int32_t *base, ptrdiff_t bstride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, size_t vl);
void vssseg8e32_v_i32m1 (int32_t *base, ptrdiff_t bstride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, vint32m1_t v7,
    size_t vl);
void vssseg2e32_v_i32m2 (int32_t *base, ptrdiff_t bstride,
    vint32m2_t v0, vint32m2_t v1, size_t vl);
void vssseg3e32_v_i32m2 (int32_t *base, ptrdiff_t bstride,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, size_t vl);
void vssseg4e32_v_i32m2 (int32_t *base, ptrdiff_t bstride,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, vint32m2_t v3,
    size_t vl);
void vssseg2e32_v_i32m4 (int32_t *base, ptrdiff_t bstride,
    vint32m4_t v0, vint32m4_t v1, size_t vl);
void vssseg2e64_v_i64m1 (int64_t *base, ptrdiff_t bstride,
    vint64m1_t v0, vint64m1_t v1, size_t vl);
void vssseg3e64_v_i64m1 (int64_t *base, ptrdiff_t bstride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, size_t vl);

```

```

void vssseg4e64_v_i64m1 (int64_t *base, ptrdiff_t bstride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    size_t vl);
void vssseg5e64_v_i64m1 (int64_t *base, ptrdiff_t bstride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, size_t vl);
void vssseg6e64_v_i64m1 (int64_t *base, ptrdiff_t bstride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, size_t vl);
void vssseg7e64_v_i64m1 (int64_t *base, ptrdiff_t bstride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, size_t vl);
void vssseg8e64_v_i64m1 (int64_t *base, ptrdiff_t bstride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, vint64m1_t v7,
    size_t vl);
void vssseg2e64_v_i64m2 (int64_t *base, ptrdiff_t bstride,
    vint64m2_t v0, vint64m2_t v1, size_t vl);
void vssseg3e64_v_i64m2 (int64_t *base, ptrdiff_t bstride,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, size_t vl);
void vssseg4e64_v_i64m2 (int64_t *base, ptrdiff_t bstride,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, vint64m2_t v3,
    size_t vl);
void vssseg2e64_v_i64m4 (int64_t *base, ptrdiff_t bstride,
    vint64m4_t v0, vint64m4_t v1, size_t vl);
void vssseg2e8_v_u8m1 (uint8_t *base, ptrdiff_t bstride,
    vuint8m1_t v0, vuint8m1_t v1, size_t vl);
void vssseg3e8_v_u8m1 (uint8_t *base, ptrdiff_t bstride,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, size_t vl);
void vssseg4e8_v_u8m1 (uint8_t *base, ptrdiff_t bstride,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    size_t vl);
void vssseg5e8_v_u8m1 (uint8_t *base, ptrdiff_t bstride,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    vuint8m1_t v4, size_t vl);
void vssseg6e8_v_u8m1 (uint8_t *base, ptrdiff_t bstride,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    vuint8m1_t v4, vuint8m1_t v5, size_t vl);
void vssseg7e8_v_u8m1 (uint8_t *base, ptrdiff_t bstride,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    vuint8m1_t v4, vuint8m1_t v5, vuint8m1_t v6, size_t vl);
void vssseg8e8_v_u8m1 (uint8_t *base, ptrdiff_t bstride,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    vuint8m1_t v4, vuint8m1_t v5, vuint8m1_t v6, vuint8m1_t v7,
    size_t vl);

```

```

void vssseg2e8_v_u8m2 (uint8_t *base, ptrdiff_t bstride,
    vuint8m2_t v0, vuint8m2_t v1, size_t vl);
void vssseg3e8_v_u8m2 (uint8_t *base, ptrdiff_t bstride,
    vuint8m2_t v0, vuint8m2_t v1, vuint8m2_t v2, size_t vl);
void vssseg4e8_v_u8m2 (uint8_t *base, ptrdiff_t bstride,
    vuint8m2_t v0, vuint8m2_t v1, vuint8m2_t v2, vuint8m2_t v3,
    size_t vl);
void vssseg2e8_v_u8m4 (uint8_t *base, ptrdiff_t bstride,
    vuint8m4_t v0, vuint8m4_t v1, size_t vl);
void vssseg2e16_v_u16m1 (uint16_t *base, ptrdiff_t bstride,
    vuint16m1_t v0, vuint16m1_t v1, size_t vl);
void vssseg3e16_v_u16m1 (uint16_t *base, ptrdiff_t bstride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, size_t vl);
void vssseg4e16_v_u16m1 (uint16_t *base, ptrdiff_t bstride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, size_t vl);
void vssseg5e16_v_u16m1 (uint16_t *base, ptrdiff_t bstride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, size_t vl);
void vssseg6e16_v_u16m1 (uint16_t *base, ptrdiff_t bstride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, size_t vl);
void vssseg7e16_v_u16m1 (uint16_t *base, ptrdiff_t bstride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6, size_t
    vl);
void vssseg8e16_v_u16m1 (uint16_t *base, ptrdiff_t bstride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6,
    vuint16m1_t v7, size_t vl);
void vssseg2e16_v_u16m2 (uint16_t *base, ptrdiff_t bstride,
    vuint16m2_t v0, vuint16m2_t v1, size_t vl);
void vssseg3e16_v_u16m2 (uint16_t *base, ptrdiff_t bstride,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, size_t vl);
void vssseg4e16_v_u16m2 (uint16_t *base, ptrdiff_t bstride,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, vuint16m2_t
    v3, size_t vl);
void vssseg2e16_v_u16m4 (uint16_t *base, ptrdiff_t bstride,
    vuint16m4_t v0, vuint16m4_t v1, size_t vl);
void vssseg2e32_v_u32m1 (uint32_t *base, ptrdiff_t bstride,
    vuint32m1_t v0, vuint32m1_t v1, size_t vl);
void vssseg3e32_v_u32m1 (uint32_t *base, ptrdiff_t bstride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, size_t vl);
void vssseg4e32_v_u32m1 (uint32_t *base, ptrdiff_t bstride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, size_t vl);

```

```

void vssseg5e32_v_u32m1 (uint32_t *base, ptrdiff_t bstride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, size_t vl);
void vssseg6e32_v_u32m1 (uint32_t *base, ptrdiff_t bstride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, size_t vl);
void vssseg7e32_v_u32m1 (uint32_t *base, ptrdiff_t bstride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6, size_t
    vl);
void vssseg8e32_v_u32m1 (uint32_t *base, ptrdiff_t bstride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6,
    vuint32m1_t v7, size_t vl);
void vssseg2e32_v_u32m2 (uint32_t *base, ptrdiff_t bstride,
    vuint32m2_t v0, vuint32m2_t v1, size_t vl);
void vssseg3e32_v_u32m2 (uint32_t *base, ptrdiff_t bstride,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, size_t vl);
void vssseg4e32_v_u32m2 (uint32_t *base, ptrdiff_t bstride,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, vuint32m2_t
    v3, size_t vl);
void vssseg2e32_v_u32m4 (uint32_t *base, ptrdiff_t bstride,
    vuint32m4_t v0, vuint32m4_t v1, size_t vl);
void vssseg2e64_v_u64m1 (uint64_t *base, ptrdiff_t bstride,
    vuint64m1_t v0, vuint64m1_t v1, size_t vl);
void vssseg3e64_v_u64m1 (uint64_t *base, ptrdiff_t bstride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, size_t vl);
void vssseg4e64_v_u64m1 (uint64_t *base, ptrdiff_t bstride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, size_t vl);
void vssseg5e64_v_u64m1 (uint64_t *base, ptrdiff_t bstride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, size_t vl);
void vssseg6e64_v_u64m1 (uint64_t *base, ptrdiff_t bstride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, size_t vl);
void vssseg7e64_v_u64m1 (uint64_t *base, ptrdiff_t bstride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6, size_t
    vl);
void vssseg8e64_v_u64m1 (uint64_t *base, ptrdiff_t bstride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6,
    vuint64m1_t v7, size_t vl);
void vssseg2e64_v_u64m2 (uint64_t *base, ptrdiff_t bstride,
    vuint64m2_t v0, vuint64m2_t v1, size_t vl);

```

```

void vssseg3e64_v_u64m2 (uint64_t *base, ptrdiff_t bstride,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, size_t vl);
void vssseg4e64_v_u64m2 (uint64_t *base, ptrdiff_t bstride,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, vuint64m2_t
    v3, size_t vl);
void vssseg2e64_v_u64m4 (uint64_t *base, ptrdiff_t bstride,
    vuint64m4_t v0, vuint64m4_t v1, size_t vl);
void vssseg2e16_v_f16m1 (float16_t *base, ptrdiff_t bstride,
    vfloat16m1_t v0, vfloat16m1_t v1, size_t vl);
void vssseg3e16_v_f16m1 (float16_t *base, ptrdiff_t bstride,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2, size_t
    vl);
void vssseg4e16_v_f16m1 (float16_t *base, ptrdiff_t bstride,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2,
    vfloat16m1_t v3, size_t vl);
void vssseg5e16_v_f16m1 (float16_t *base, ptrdiff_t bstride,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2,
    vfloat16m1_t v3, vfloat16m1_t v4, size_t vl);
void vssseg6e16_v_f16m1 (float16_t *base, ptrdiff_t bstride,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2,
    vfloat16m1_t v3, vfloat16m1_t v4, vfloat16m1_t v5, size_t
    vl);
void vssseg7e16_v_f16m1 (float16_t *base, ptrdiff_t bstride,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2,
    vfloat16m1_t v3, vfloat16m1_t v4, vfloat16m1_t v5,
    vfloat16m1_t v6, size_t vl);
void vssseg8e16_v_f16m1 (float16_t *base, ptrdiff_t bstride,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2,
    vfloat16m1_t v3, vfloat16m1_t v4, vfloat16m1_t v5,
    vfloat16m1_t v6, vfloat16m1_t v7, size_t vl);
void vssseg2e16_v_f16m2 (float16_t *base, ptrdiff_t bstride,
    vfloat16m2_t v0, vfloat16m2_t v1, size_t vl);
void vssseg3e16_v_f16m2 (float16_t *base, ptrdiff_t bstride,
    vfloat16m2_t v0, vfloat16m2_t v1, vfloat16m2_t v2, size_t
    vl);
void vssseg4e16_v_f16m2 (float16_t *base, ptrdiff_t bstride,
    vfloat16m2_t v0, vfloat16m2_t v1, vfloat16m2_t v2,
    vfloat16m2_t v3, size_t vl);
void vssseg2e16_v_f16m4 (float16_t *base, ptrdiff_t bstride,
    vfloat16m4_t v0, vfloat16m4_t v1, size_t vl);
void vssseg2e32_v_f32m1 (float32_t *base, ptrdiff_t bstride,
    vfloat32m1_t v0, vfloat32m1_t v1, size_t vl);
void vssseg3e32_v_f32m1 (float32_t *base, ptrdiff_t bstride,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2, size_t
    vl);

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void vssseg4e32_v_f32m1 (float32_t *base, ptrdiff_t bstride,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2,
    vfloat32m1_t v3, size_t vl);
void vssseg5e32_v_f32m1 (float32_t *base, ptrdiff_t bstride,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2,
    vfloat32m1_t v3, vfloat32m1_t v4, size_t vl);
void vssseg6e32_v_f32m1 (float32_t *base, ptrdiff_t bstride,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2,
    vfloat32m1_t v3, vfloat32m1_t v4, vfloat32m1_t v5, size_t
    vl);
void vssseg7e32_v_f32m1 (float32_t *base, ptrdiff_t bstride,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2,
    vfloat32m1_t v3, vfloat32m1_t v4, vfloat32m1_t v5,
    vfloat32m1_t v6, size_t vl);
void vssseg8e32_v_f32m1 (float32_t *base, ptrdiff_t bstride,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2,
    vfloat32m1_t v3, vfloat32m1_t v4, vfloat32m1_t v5,
    vfloat32m1_t v6, vfloat32m1_t v7, size_t vl);
void vssseg2e32_v_f32m2 (float32_t *base, ptrdiff_t bstride,
    vfloat32m2_t v0, vfloat32m2_t v1, size_t vl);
void vssseg3e32_v_f32m2 (float32_t *base, ptrdiff_t bstride,
    vfloat32m2_t v0, vfloat32m2_t v1, vfloat32m2_t v2, size_t
    vl);
void vssseg4e32_v_f32m2 (float32_t *base, ptrdiff_t bstride,
    vfloat32m2_t v0, vfloat32m2_t v1, vfloat32m2_t v2,
    vfloat32m2_t v3, size_t vl);
void vssseg2e32_v_f32m4 (float32_t *base, ptrdiff_t bstride,
    vfloat32m4_t v0, vfloat32m4_t v1, size_t vl);
void vssseg2e64_v_f64m1 (float64_t *base, ptrdiff_t bstride,
    vfloat64m1_t v0, vfloat64m1_t v1, size_t vl);
void vssseg3e64_v_f64m1 (float64_t *base, ptrdiff_t bstride,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2, size_t
    vl);
void vssseg4e64_v_f64m1 (float64_t *base, ptrdiff_t bstride,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2,
    vfloat64m1_t v3, size_t vl);
void vssseg5e64_v_f64m1 (float64_t *base, ptrdiff_t bstride,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2,
    vfloat64m1_t v3, vfloat64m1_t v4, size_t vl);
void vssseg6e64_v_f64m1 (float64_t *base, ptrdiff_t bstride,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2,
    vfloat64m1_t v3, vfloat64m1_t v4, vfloat64m1_t v5, size_t
    vl);
void vssseg7e64_v_f64m1 (float64_t *base, ptrdiff_t bstride,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2,
    vfloat64m1_t v3, vfloat64m1_t v4, vfloat64m1_t v5,

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    vfloat64m1_t v6, size_t vl);
void vssseg8e64_v_f64m1 (float64_t *base, ptrdiff_t bstride,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2,
    vfloat64m1_t v3, vfloat64m1_t v4, vfloat64m1_t v5,
    vfloat64m1_t v6, vfloat64m1_t v7, size_t vl);
void vssseg2e64_v_f64m2 (float64_t *base, ptrdiff_t bstride,
    vfloat64m2_t v0, vfloat64m2_t v1, size_t vl);
void vssseg3e64_v_f64m2 (float64_t *base, ptrdiff_t bstride,
    vfloat64m2_t v0, vfloat64m2_t v1, vfloat64m2_t v2, size_t
    vl);
void vssseg4e64_v_f64m2 (float64_t *base, ptrdiff_t bstride,
    vfloat64m2_t v0, vfloat64m2_t v1, vfloat64m2_t v2,
    vfloat64m2_t v3, size_t vl);
void vssseg2e64_v_f64m4 (float64_t *base, ptrdiff_t bstride,
    vfloat64m4_t v0, vfloat64m4_t v1, size_t vl);
// masked functions
void vssseg2e8_v_i8m1_m (vbool8_t mask, int8_t *base, ptrdiff_t
    bstride, vint8m1_t v0, vint8m1_t v1, size_t vl);
void vssseg3e8_v_i8m1_m (vbool8_t mask, int8_t *base, ptrdiff_t
    bstride, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, size_t
    vl);
void vssseg4e8_v_i8m1_m (vbool8_t mask, int8_t *base, ptrdiff_t
    bstride, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t
    v3, size_t vl);
void vssseg5e8_v_i8m1_m (vbool8_t mask, int8_t *base, ptrdiff_t
    bstride, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t
    v3, vint8m1_t v4, size_t vl);
void vssseg6e8_v_i8m1_m (vbool8_t mask, int8_t *base, ptrdiff_t
    bstride, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t
    v3, vint8m1_t v4, vint8m1_t v5, size_t vl);
void vssseg7e8_v_i8m1_m (vbool8_t mask, int8_t *base, ptrdiff_t
    bstride, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t
    v3, vint8m1_t v4, vint8m1_t v5, vint8m1_t v6, size_t vl);
void vssseg8e8_v_i8m1_m (vbool8_t mask, int8_t *base, ptrdiff_t
    bstride, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t
    v3, vint8m1_t v4, vint8m1_t v5, vint8m1_t v6, vint8m1_t v7,
    size_t vl);
void vssseg2e8_v_i8m2_m (vbool4_t mask, int8_t *base, ptrdiff_t
    bstride, vint8m2_t v0, vint8m2_t v1, size_t vl);
void vssseg3e8_v_i8m2_m (vbool4_t mask, int8_t *base, ptrdiff_t
    bstride, vint8m2_t v0, vint8m2_t v1, vint8m2_t v2, size_t
    vl);
void vssseg4e8_v_i8m2_m (vbool4_t mask, int8_t *base, ptrdiff_t
    bstride, vint8m2_t v0, vint8m2_t v1, vint8m2_t v2, vint8m2_t
    v3, size_t vl);

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void vssseg2e8_v_i8m4_m (vbool2_t mask, int8_t *base, ptrdiff_t
    bstride, vint8m4_t v0, vint8m4_t v1, size_t vl);
void vssseg2e16_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t bstride, vint16m1_t v0, vint16m1_t v1, size_t vl);
void vssseg3e16_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t bstride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, size_t vl);
void vssseg4e16_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t bstride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, size_t vl);
void vssseg5e16_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t bstride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, size_t vl);
void vssseg6e16_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t bstride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, size_t vl);
void vssseg7e16_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t bstride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, vint16m1_t
    v6, size_t vl);
void vssseg8e16_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t bstride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, vint16m1_t
    v6, vint16m1_t v7, size_t vl);
void vssseg2e16_v_i16m2_m (vbool8_t mask, int16_t *base,
    ptrdiff_t bstride, vint16m2_t v0, vint16m2_t v1, size_t vl);
void vssseg3e16_v_i16m2_m (vbool8_t mask, int16_t *base,
    ptrdiff_t bstride, vint16m2_t v0, vint16m2_t v1, vint16m2_t
    v2, size_t vl);
void vssseg4e16_v_i16m2_m (vbool8_t mask, int16_t *base,
    ptrdiff_t bstride, vint16m2_t v0, vint16m2_t v1, vint16m2_t
    v2, vint16m2_t v3, size_t vl);
void vssseg2e16_v_i16m4_m (vbool4_t mask, int16_t *base,
    ptrdiff_t bstride, vint16m4_t v0, vint16m4_t v1, size_t vl);
void vssseg2e32_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t bstride, vint32m1_t v0, vint32m1_t v1, size_t vl);
void vssseg3e32_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t bstride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, size_t vl);
void vssseg4e32_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t bstride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, size_t vl);
void vssseg5e32_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t bstride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, size_t vl);

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void vssseg6e32_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t bstride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, size_t vl);
void vssseg7e32_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t bstride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, vint32m1_t
    v6, size_t vl);
void vssseg8e32_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t bstride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, vint32m1_t
    v6, vint32m1_t v7, size_t vl);
void vssseg2e32_v_i32m2_m (vbool16_t mask, int32_t *base,
    ptrdiff_t bstride, vint32m2_t v0, vint32m2_t v1, size_t vl);
void vssseg3e32_v_i32m2_m (vbool16_t mask, int32_t *base,
    ptrdiff_t bstride, vint32m2_t v0, vint32m2_t v1, vint32m2_t
    v2, size_t vl);
void vssseg4e32_v_i32m2_m (vbool16_t mask, int32_t *base,
    ptrdiff_t bstride, vint32m2_t v0, vint32m2_t v1, vint32m2_t
    v2, vint32m2_t v3, size_t vl);
void vssseg2e32_v_i32m4_m (vbool8_t mask, int32_t *base,
    ptrdiff_t bstride, vint32m4_t v0, vint32m4_t v1, size_t vl);
void vssseg2e64_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t bstride, vint64m1_t v0, vint64m1_t v1, size_t vl);
void vssseg3e64_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t bstride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, size_t vl);
void vssseg4e64_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t bstride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, size_t vl);
void vssseg5e64_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t bstride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, size_t vl);
void vssseg6e64_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t bstride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, size_t vl);
void vssseg7e64_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t bstride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, vint64m1_t
    v6, size_t vl);
void vssseg8e64_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t bstride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, vint64m1_t
    v6, vint64m1_t v7, size_t vl);
void vssseg2e64_v_i64m2_m (vbool32_t mask, int64_t *base,
    ptrdiff_t bstride, vint64m2_t v0, vint64m2_t v1, size_t vl);

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void vssseg3e64_v_i64m2_m (vbool32_t mask, int64_t *base,
    ptrdiff_t bstride, vint64m2_t v0, vint64m2_t v1, vint64m2_t
    v2, size_t vl);
void vssseg4e64_v_i64m2_m (vbool32_t mask, int64_t *base,
    ptrdiff_t bstride, vint64m2_t v0, vint64m2_t v1, vint64m2_t
    v2, vint64m2_t v3, size_t vl);
void vssseg2e64_v_i64m4_m (vbool16_t mask, int64_t *base,
    ptrdiff_t bstride, vint64m4_t v0, vint64m4_t v1, size_t vl);
void vssseg2e8_v_u8m1_m (vbool8_t mask, uint8_t *base, ptrdiff_t
    bstride, vuint8m1_t v0, vuint8m1_t v1, size_t vl);
void vssseg3e8_v_u8m1_m (vbool8_t mask, uint8_t *base, ptrdiff_t
    bstride, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, size_t
    vl);
void vssseg4e8_v_u8m1_m (vbool8_t mask, uint8_t *base, ptrdiff_t
    bstride, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2,
    vuint8m1_t v3, size_t vl);
void vssseg5e8_v_u8m1_m (vbool8_t mask, uint8_t *base, ptrdiff_t
    bstride, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2,
    vuint8m1_t v3, vuint8m1_t v4, size_t vl);
void vssseg6e8_v_u8m1_m (vbool8_t mask, uint8_t *base, ptrdiff_t
    bstride, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2,
    vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t v5, size_t vl);
void vssseg7e8_v_u8m1_m (vbool8_t mask, uint8_t *base, ptrdiff_t
    bstride, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2,
    vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t v5, vuint8m1_t v6,
    size_t vl);
void vssseg8e8_v_u8m1_m (vbool8_t mask, uint8_t *base, ptrdiff_t
    bstride, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2,
    vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t v5, vuint8m1_t v6,
    vuint8m1_t v7, size_t vl);
void vssseg2e8_v_u8m2_m (vbool4_t mask, uint8_t *base, ptrdiff_t
    bstride, vuint8m2_t v0, vuint8m2_t v1, size_t vl);
void vssseg3e8_v_u8m2_m (vbool4_t mask, uint8_t *base, ptrdiff_t
    bstride, vuint8m2_t v0, vuint8m2_t v1, vuint8m2_t v2, size_t
    vl);
void vssseg4e8_v_u8m2_m (vbool4_t mask, uint8_t *base, ptrdiff_t
    bstride, vuint8m2_t v0, vuint8m2_t v1, vuint8m2_t v2,
    vuint8m2_t v3, size_t vl);
void vssseg2e8_v_u8m4_m (vbool2_t mask, uint8_t *base, ptrdiff_t
    bstride, vuint8m4_t v0, vuint8m4_t v1, size_t vl);
void vssseg2e16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t bstride, vuint16m1_t v0, vuint16m1_t v1, size_t
    vl);
void vssseg3e16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t bstride, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, size_t vl);

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void vssseg4e16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t bstride, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, size_t vl);
void vssseg5e16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t bstride, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, size_t vl);
void vssseg6e16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t bstride, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
    v5, size_t vl);
void vssseg7e16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t bstride, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
    v5, vuint16m1_t v6, size_t vl);
void vssseg8e16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t bstride, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
    v5, vuint16m1_t v6, vuint16m1_t v7, size_t vl);
void vssseg2e16_v_u16m2_m (vbool8_t mask, uint16_t *base,
    ptrdiff_t bstride, vuint16m2_t v0, vuint16m2_t v1, size_t
    vl);
void vssseg3e16_v_u16m2_m (vbool8_t mask, uint16_t *base,
    ptrdiff_t bstride, vuint16m2_t v0, vuint16m2_t v1,
    vuint16m2_t v2, size_t vl);
void vssseg4e16_v_u16m2_m (vbool8_t mask, uint16_t *base,
    ptrdiff_t bstride, vuint16m2_t v0, vuint16m2_t v1,
    vuint16m2_t v2, vuint16m2_t v3, size_t vl);
void vssseg2e16_v_u16m4_m (vbool4_t mask, uint16_t *base,
    ptrdiff_t bstride, vuint16m4_t v0, vuint16m4_t v1, size_t
    vl);
void vssseg2e32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t bstride, vuint32m1_t v0, vuint32m1_t v1, size_t
    vl);
void vssseg3e32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t bstride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, size_t vl);
void vssseg4e32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t bstride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, size_t vl);
void vssseg5e32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t bstride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, size_t vl);
void vssseg6e32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t bstride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, size_t vl);

```

```

void vssseg7e32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t bstride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, vuint32m1_t v6, size_t vl);
void vssseg8e32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t bstride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, vuint32m1_t v6, vuint32m1_t v7, size_t vl);
void vssseg2e32_v_u32m2_m (vbool16_t mask, uint32_t *base,
    ptrdiff_t bstride, vuint32m2_t v0, vuint32m2_t v1, size_t
    vl);
void vssseg3e32_v_u32m2_m (vbool16_t mask, uint32_t *base,
    ptrdiff_t bstride, vuint32m2_t v0, vuint32m2_t v1,
    vuint32m2_t v2, size_t vl);
void vssseg4e32_v_u32m2_m (vbool16_t mask, uint32_t *base,
    ptrdiff_t bstride, vuint32m2_t v0, vuint32m2_t v1,
    vuint32m2_t v2, vuint32m2_t v3, size_t vl);
void vssseg2e32_v_u32m4_m (vbool8_t mask, uint32_t *base,
    ptrdiff_t bstride, vuint32m4_t v0, vuint32m4_t v1, size_t
    vl);
void vssseg2e64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t bstride, vuint64m1_t v0, vuint64m1_t v1, size_t
    vl);
void vssseg3e64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t bstride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, size_t vl);
void vssseg4e64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t bstride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, size_t vl);
void vssseg5e64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t bstride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, size_t vl);
void vssseg6e64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t bstride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, size_t vl);
void vssseg7e64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t bstride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, vuint64m1_t v6, size_t vl);
void vssseg8e64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t bstride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, vuint64m1_t v6, vuint64m1_t v7, size_t vl);
void vssseg2e64_v_u64m2_m (vbool32_t mask, uint64_t *base,
    ptrdiff_t bstride, vuint64m2_t v0, vuint64m2_t v1, size_t

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    vl);
void vssseg3e64_v_u64m2_m (vbool32_t mask, uint64_t *base,
    ptrdiff_t bstride, vuint64m2_t v0, vuint64m2_t v1,
    vuint64m2_t v2, size_t vl);
void vssseg4e64_v_u64m2_m (vbool32_t mask, uint64_t *base,
    ptrdiff_t bstride, vuint64m2_t v0, vuint64m2_t v1,
    vuint64m2_t v2, vuint64m2_t v3, size_t vl);
void vssseg2e64_v_u64m4_m (vbool16_t mask, uint64_t *base,
    ptrdiff_t bstride, vuint64m4_t v0, vuint64m4_t v1, size_t
    vl);
void vssseg2e16_v_f16m1_m (vbool16_t mask, float16_t *base,
    ptrdiff_t bstride, vfloat16m1_t v0, vfloat16m1_t v1, size_t
    vl);
void vssseg3e16_v_f16m1_m (vbool16_t mask, float16_t *base,
    ptrdiff_t bstride, vfloat16m1_t v0, vfloat16m1_t v1,
    vfloat16m1_t v2, size_t vl);
void vssseg4e16_v_f16m1_m (vbool16_t mask, float16_t *base,
    ptrdiff_t bstride, vfloat16m1_t v0, vfloat16m1_t v1,
    vfloat16m1_t v2, vfloat16m1_t v3, size_t vl);
void vssseg5e16_v_f16m1_m (vbool16_t mask, float16_t *base,
    ptrdiff_t bstride, vfloat16m1_t v0, vfloat16m1_t v1,
    vfloat16m1_t v2, vfloat16m1_t v3, vfloat16m1_t v4, size_t
    vl);
void vssseg6e16_v_f16m1_m (vbool16_t mask, float16_t *base,
    ptrdiff_t bstride, vfloat16m1_t v0, vfloat16m1_t v1,
    vfloat16m1_t v2, vfloat16m1_t v3, vfloat16m1_t v4,
    vfloat16m1_t v5, size_t vl);
void vssseg7e16_v_f16m1_m (vbool16_t mask, float16_t *base,
    ptrdiff_t bstride, vfloat16m1_t v0, vfloat16m1_t v1,
    vfloat16m1_t v2, vfloat16m1_t v3, vfloat16m1_t v4,
    vfloat16m1_t v5, vfloat16m1_t v6, size_t vl);
void vssseg8e16_v_f16m1_m (vbool16_t mask, float16_t *base,
    ptrdiff_t bstride, vfloat16m1_t v0, vfloat16m1_t v1,
    vfloat16m1_t v2, vfloat16m1_t v3, vfloat16m1_t v4,
    vfloat16m1_t v5, vfloat16m1_t v6, vfloat16m1_t v7, size_t
    vl);
void vssseg2e16_v_f16m2_m (vbool8_t mask, float16_t *base,
    ptrdiff_t bstride, vfloat16m2_t v0, vfloat16m2_t v1, size_t
    vl);
void vssseg3e16_v_f16m2_m (vbool8_t mask, float16_t *base,
    ptrdiff_t bstride, vfloat16m2_t v0, vfloat16m2_t v1,
    vfloat16m2_t v2, size_t vl);
void vssseg4e16_v_f16m2_m (vbool8_t mask, float16_t *base,
    ptrdiff_t bstride, vfloat16m2_t v0, vfloat16m2_t v1,
    vfloat16m2_t v2, vfloat16m2_t v3, size_t vl);

```

```

void vssseg2e16_v_f16m4_m (vbool4_t mask, float16_t *base,
    ptrdiff_t bstride, vfloat16m4_t v0, vfloat16m4_t v1, size_t
    vl);
void vssseg2e32_v_f32m1_m (vbool32_t mask, float32_t *base,
    ptrdiff_t bstride, vfloat32m1_t v0, vfloat32m1_t v1, size_t
    vl);
void vssseg3e32_v_f32m1_m (vbool32_t mask, float32_t *base,
    ptrdiff_t bstride, vfloat32m1_t v0, vfloat32m1_t v1,
    vfloat32m1_t v2, size_t vl);
void vssseg4e32_v_f32m1_m (vbool32_t mask, float32_t *base,
    ptrdiff_t bstride, vfloat32m1_t v0, vfloat32m1_t v1,
    vfloat32m1_t v2, vfloat32m1_t v3, size_t vl);
void vssseg5e32_v_f32m1_m (vbool32_t mask, float32_t *base,
    ptrdiff_t bstride, vfloat32m1_t v0, vfloat32m1_t v1,
    vfloat32m1_t v2, vfloat32m1_t v3, vfloat32m1_t v4, size_t
    vl);
void vssseg6e32_v_f32m1_m (vbool32_t mask, float32_t *base,
    ptrdiff_t bstride, vfloat32m1_t v0, vfloat32m1_t v1,
    vfloat32m1_t v2, vfloat32m1_t v3, vfloat32m1_t v4,
    vfloat32m1_t v5, size_t vl);
void vssseg7e32_v_f32m1_m (vbool32_t mask, float32_t *base,
    ptrdiff_t bstride, vfloat32m1_t v0, vfloat32m1_t v1,
    vfloat32m1_t v2, vfloat32m1_t v3, vfloat32m1_t v4,
    vfloat32m1_t v5, vfloat32m1_t v6, size_t vl);
void vssseg8e32_v_f32m1_m (vbool32_t mask, float32_t *base,
    ptrdiff_t bstride, vfloat32m1_t v0, vfloat32m1_t v1,
    vfloat32m1_t v2, vfloat32m1_t v3, vfloat32m1_t v4,
    vfloat32m1_t v5, vfloat32m1_t v6, vfloat32m1_t v7, size_t
    vl);
void vssseg2e32_v_f32m2_m (vbool16_t mask, float32_t *base,
    ptrdiff_t bstride, vfloat32m2_t v0, vfloat32m2_t v1, size_t
    vl);
void vssseg3e32_v_f32m2_m (vbool16_t mask, float32_t *base,
    ptrdiff_t bstride, vfloat32m2_t v0, vfloat32m2_t v1,
    vfloat32m2_t v2, size_t vl);
void vssseg4e32_v_f32m2_m (vbool16_t mask, float32_t *base,
    ptrdiff_t bstride, vfloat32m2_t v0, vfloat32m2_t v1,
    vfloat32m2_t v2, vfloat32m2_t v3, size_t vl);
void vssseg2e32_v_f32m4_m (vbool8_t mask, float32_t *base,
    ptrdiff_t bstride, vfloat32m4_t v0, vfloat32m4_t v1, size_t
    vl);
void vssseg2e64_v_f64m1_m (vbool64_t mask, float64_t *base,
    ptrdiff_t bstride, vfloat64m1_t v0, vfloat64m1_t v1, size_t
    vl);
void vssseg3e64_v_f64m1_m (vbool64_t mask, float64_t *base,
    ptrdiff_t bstride, vfloat64m1_t v0, vfloat64m1_t v1,

```

```

    vfloat64m1_t v2, size_t vl);
void vssseg4e64_v_f64m1_m (vbool64_t mask, float64_t *base,
    ptrdiff_t bstride, vfloat64m1_t v0, vfloat64m1_t v1,
    vfloat64m1_t v2, vfloat64m1_t v3, size_t vl);
void vssseg5e64_v_f64m1_m (vbool64_t mask, float64_t *base,
    ptrdiff_t bstride, vfloat64m1_t v0, vfloat64m1_t v1,
    vfloat64m1_t v2, vfloat64m1_t v3, vfloat64m1_t v4, size_t
    vl);
void vssseg6e64_v_f64m1_m (vbool64_t mask, float64_t *base,
    ptrdiff_t bstride, vfloat64m1_t v0, vfloat64m1_t v1,
    vfloat64m1_t v2, vfloat64m1_t v3, vfloat64m1_t v4,
    vfloat64m1_t v5, size_t vl);
void vssseg7e64_v_f64m1_m (vbool64_t mask, float64_t *base,
    ptrdiff_t bstride, vfloat64m1_t v0, vfloat64m1_t v1,
    vfloat64m1_t v2, vfloat64m1_t v3, vfloat64m1_t v4,
    vfloat64m1_t v5, vfloat64m1_t v6, size_t vl);
void vssseg8e64_v_f64m1_m (vbool64_t mask, float64_t *base,
    ptrdiff_t bstride, vfloat64m1_t v0, vfloat64m1_t v1,
    vfloat64m1_t v2, vfloat64m1_t v3, vfloat64m1_t v4,
    vfloat64m1_t v5, vfloat64m1_t v6, vfloat64m1_t v7, size_t
    vl);
void vssseg2e64_v_f64m2_m (vbool32_t mask, float64_t *base,
    ptrdiff_t bstride, vfloat64m2_t v0, vfloat64m2_t v1, size_t
    vl);
void vssseg3e64_v_f64m2_m (vbool32_t mask, float64_t *base,
    ptrdiff_t bstride, vfloat64m2_t v0, vfloat64m2_t v1,
    vfloat64m2_t v2, size_t vl);
void vssseg4e64_v_f64m2_m (vbool32_t mask, float64_t *base,
    ptrdiff_t bstride, vfloat64m2_t v0, vfloat64m2_t v1,
    vfloat64m2_t v2, vfloat64m2_t v3, size_t vl);
void vssseg2e64_v_f64m4_m (vbool16_t mask, float64_t *base,
    ptrdiff_t bstride, vfloat64m4_t v0, vfloat64m4_t v1, size_t
    vl);

```

## Vector Indexed Segment Load Functions:

### Prototypes:

```

void vloxseg2ei8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, const
    int8_t *base, vuint8m1_t bindex, size_t vl);
void vloxseg3ei8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, const int8_t *base, vuint8m1_t bindex, size_t vl);
void vloxseg4ei8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, const int8_t *base, vuint8m1_t bindex,
    size_t vl);

```

```

void vloxseg5ei8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
*v2, vint8m1_t *v3, vint8m1_t *v4, const int8_t *base,
vuint8m1_t bindex, size_t vl);
void vloxseg6ei8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
*v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, const
int8_t *base, vuint8m1_t bindex, size_t vl);
void vloxseg7ei8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
*v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
*v6, const int8_t *base, vuint8m1_t bindex, size_t vl);
void vloxseg8ei8_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
*v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
*v6, vint8m1_t *v7, const int8_t *base, vuint8m1_t bindex,
size_t vl);
void vloxseg2ei8_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, const
int8_t *base, vuint8m2_t bindex, size_t vl);
void vloxseg3ei8_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
*v2, const int8_t *base, vuint8m2_t bindex, size_t vl);
void vloxseg4ei8_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
*v2, vint8m2_t *v3, const int8_t *base, vuint8m2_t bindex,
size_t vl);
void vloxseg2ei8_v_i8m4 (vint8m4_t *v0, vint8m4_t *v1, const
int8_t *base, vuint8m4_t bindex, size_t vl);
void vloxseg2ei16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, const
int16_t *base, vuint16m1_t bindex, size_t vl);
void vloxseg3ei16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, const int16_t *base, vuint16m1_t bindex,
size_t vl);
void vloxseg4ei16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, const int16_t *base,
vuint16m1_t bindex, size_t vl);
void vloxseg5ei16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, const
int16_t *base, vuint16m1_t bindex, size_t vl);
void vloxseg6ei16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, const int16_t *base, vuint16m1_t bindex, size_t vl);
void vloxseg7ei16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vint16m1_t *v6, const int16_t *base, vuint16m1_t
bindex, size_t vl);
void vloxseg8ei16_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vint16m1_t *v6, vint16m1_t *v7, const int16_t *base,
vuint16m1_t bindex, size_t vl);
void vloxseg2ei16_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1, const
int16_t *base, vuint16m2_t bindex, size_t vl);

```



```

void vloxseg3ei16_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, const int16_t *base, vuint16m2_t bindex,
    size_t vl);
void vloxseg4ei16_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vint16m2_t *v3, const int16_t *base,
    vuint16m2_t bindex, size_t vl);
void vloxseg2ei16_v_i16m4 (vint16m4_t *v0, vint16m4_t *v1, const
    int16_t *base, vuint16m4_t bindex, size_t vl);
void vloxseg2ei32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, const
    int32_t *base, vuint32m1_t bindex, size_t vl);
void vloxseg3ei32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, const int32_t *base, vuint32m1_t bindex,
    size_t vl);
void vloxseg4ei32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, const int32_t *base,
    vuint32m1_t bindex, size_t vl);
void vloxseg5ei32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, const
    int32_t *base, vuint32m1_t bindex, size_t vl);
void vloxseg6ei32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, const int32_t *base, vuint32m1_t bindex, size_t vl);
void vloxseg7ei32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, const int32_t *base, vuint32m1_t
    bindex, size_t vl);
void vloxseg8ei32_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, const int32_t *base,
    vuint32m1_t bindex, size_t vl);
void vloxseg2ei32_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, const
    int32_t *base, vuint32m2_t bindex, size_t vl);
void vloxseg3ei32_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, const int32_t *base, vuint32m2_t bindex,
    size_t vl);
void vloxseg4ei32_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, const int32_t *base,
    vuint32m2_t bindex, size_t vl);
void vloxseg2ei32_v_i32m4 (vint32m4_t *v0, vint32m4_t *v1, const
    int32_t *base, vuint32m4_t bindex, size_t vl);
void vloxseg2ei64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, const
    int64_t *base, vuint64m1_t bindex, size_t vl);
void vloxseg3ei64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, const int64_t *base, vuint64m1_t bindex,
    size_t vl);

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void vloxseg4ei64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, const int64_t *base,
    vuint64m1_t bindex, size_t vl);
void vloxseg5ei64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, const
    int64_t *base, vuint64m1_t bindex, size_t vl);
void vloxseg6ei64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, const int64_t *base, vuint64m1_t bindex, size_t vl);
void vloxseg7ei64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, const int64_t *base, vuint64m1_t
    bindex, size_t vl);
void vloxseg8ei64_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vint64m1_t *v7, const int64_t *base,
    vuint64m1_t bindex, size_t vl);
void vloxseg2ei64_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, const
    int64_t *base, vuint64m2_t bindex, size_t vl);
void vloxseg3ei64_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, const int64_t *base, vuint64m2_t bindex,
    size_t vl);
void vloxseg4ei64_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, const int64_t *base,
    vuint64m2_t bindex, size_t vl);
void vloxseg2ei64_v_i64m4 (vint64m4_t *v0, vint64m4_t *v1, const
    int64_t *base, vuint64m4_t bindex, size_t vl);
void vloxseg2ei8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, const
    uint8_t *base, vuint8m1_t bindex, size_t vl);
void vloxseg3ei8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, const uint8_t *base, vuint8m1_t bindex,
    size_t vl);
void vloxseg4ei8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, const uint8_t *base,
    vuint8m1_t bindex, size_t vl);
void vloxseg5ei8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, const
    uint8_t *base, vuint8m1_t bindex, size_t vl);
void vloxseg6ei8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, const uint8_t *base, vuint8m1_t bindex, size_t vl);
void vloxseg7ei8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, const uint8_t *base, vuint8m1_t bindex,
    size_t vl);

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void vloxseg8ei8_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, vuint8m1_t *v7, const uint8_t *base,
    vuint8m1_t bindex, size_t vl);
void vloxseg2ei8_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1, const
    uint8_t *base, vuint8m2_t bindex, size_t vl);
void vloxseg3ei8_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, const uint8_t *base, vuint8m2_t bindex,
    size_t vl);
void vloxseg4ei8_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, vuint8m2_t *v3, const uint8_t *base,
    vuint8m2_t bindex, size_t vl);
void vloxseg2ei8_v_u8m4 (vuint8m4_t *v0, vuint8m4_t *v1, const
    uint8_t *base, vuint8m4_t bindex, size_t vl);
void vloxseg2ei16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    const uint16_t *base, vuint16m1_t bindex, size_t vl);
void vloxseg3ei16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, const uint16_t *base, vuint16m1_t bindex,
    size_t vl);
void vloxseg4ei16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, const uint16_t *base,
    vuint16m1_t bindex, size_t vl);
void vloxseg5ei16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, const
    uint16_t *base, vuint16m1_t bindex, size_t vl);
void vloxseg6ei16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, const uint16_t *base, vuint16m1_t bindex,
    size_t vl);
void vloxseg7ei16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, const uint16_t *base,
    vuint16m1_t bindex, size_t vl);
void vloxseg8ei16_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, const
    uint16_t *base, vuint16m1_t bindex, size_t vl);
void vloxseg2ei16_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    const uint16_t *base, vuint16m2_t bindex, size_t vl);
void vloxseg3ei16_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, const uint16_t *base, vuint16m2_t bindex,
    size_t vl);
void vloxseg4ei16_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vuint16m2_t *v3, const uint16_t *base,
    vuint16m2_t bindex, size_t vl);

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void vloxseg2ei16_v_u16m4 (vuint16m4_t *v0, vuint16m4_t *v1,
    const uint16_t *base, vuint16m4_t bindex, size_t vl);
void vloxseg2ei32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    const uint32_t *base, vuint32m1_t bindex, size_t vl);
void vloxseg3ei32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, const uint32_t *base, vuint32m1_t bindex,
    size_t vl);
void vloxseg4ei32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, const uint32_t *base,
    vuint32m1_t bindex, size_t vl);
void vloxseg5ei32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, const
    uint32_t *base, vuint32m1_t bindex, size_t vl);
void vloxseg6ei32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, const uint32_t *base, vuint32m1_t bindex,
    size_t vl);
void vloxseg7ei32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, const uint32_t *base,
    vuint32m1_t bindex, size_t vl);
void vloxseg8ei32_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, const
    uint32_t *base, vuint32m1_t bindex, size_t vl);
void vloxseg2ei32_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    const uint32_t *base, vuint32m2_t bindex, size_t vl);
void vloxseg3ei32_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, const uint32_t *base, vuint32m2_t bindex,
    size_t vl);
void vloxseg4ei32_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, const uint32_t *base,
    vuint32m2_t bindex, size_t vl);
void vloxseg2ei32_v_u32m4 (vuint32m4_t *v0, vuint32m4_t *v1,
    const uint32_t *base, vuint32m4_t bindex, size_t vl);
void vloxseg2ei64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    const uint64_t *base, vuint64m1_t bindex, size_t vl);
void vloxseg3ei64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, const uint64_t *base, vuint64m1_t bindex,
    size_t vl);
void vloxseg4ei64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, const uint64_t *base,
    vuint64m1_t bindex, size_t vl);
void vloxseg5ei64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, const
    uint64_t *base, vuint64m1_t bindex, size_t vl);

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void vloxseg6ei64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, const uint64_t *base, vuint64m1_t bindex,
    size_t vl);
void vloxseg7ei64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, const uint64_t *base,
    vuint64m1_t bindex, size_t vl);
void vloxseg8ei64_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, const
    uint64_t *base, vuint64m1_t bindex, size_t vl);
void vloxseg2ei64_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    const uint64_t *base, vuint64m2_t bindex, size_t vl);
void vloxseg3ei64_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, const uint64_t *base, vuint64m2_t bindex,
    size_t vl);
void vloxseg4ei64_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, const uint64_t *base,
    vuint64m2_t bindex, size_t vl);
void vloxseg2ei64_v_u64m4 (vuint64m4_t *v0, vuint64m4_t *v1,
    const uint64_t *base, vuint64m4_t bindex, size_t vl);
void vloxseg2ei16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    const float16_t *base, vuint16m1_t bindex, size_t vl);
void vloxseg3ei16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, const float16_t *base, vuint16m1_t bindex,
    size_t vl);
void vloxseg4ei16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, const float16_t *base,
    vuint16m1_t bindex, size_t vl);
void vloxseg5ei16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4, const
    float16_t *base, vuint16m1_t bindex, size_t vl);
void vloxseg6ei16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, const float16_t *base, vuint16m1_t bindex,
    size_t vl);
void vloxseg7ei16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, vfloat16m1_t *v6, const float16_t *base,
    vuint16m1_t bindex, size_t vl);
void vloxseg8ei16_v_f16m1 (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, vfloat16m1_t *v6, vfloat16m1_t *v7, const
    float16_t *base, vuint16m1_t bindex, size_t vl);

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void vloxseg2ei16_v_f16m2 (vfloat16m2_t *v0, vfloat16m2_t *v1,
    const float16_t *base, uint16m2_t bindex, size_t vl);
void vloxseg3ei16_v_f16m2 (vfloat16m2_t *v0, vfloat16m2_t *v1,
    vfloat16m2_t *v2, const float16_t *base, uint16m2_t bindex,
    size_t vl);
void vloxseg4ei16_v_f16m2 (vfloat16m2_t *v0, vfloat16m2_t *v1,
    vfloat16m2_t *v2, vfloat16m2_t *v3, const float16_t *base,
    uint16m2_t bindex, size_t vl);
void vloxseg2ei16_v_f16m4 (vfloat16m4_t *v0, vfloat16m4_t *v1,
    const float16_t *base, uint16m4_t bindex, size_t vl);
void vloxseg2ei32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    const float32_t *base, uint32m1_t bindex, size_t vl);
void vloxseg3ei32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, const float32_t *base, uint32m1_t bindex,
    size_t vl);
void vloxseg4ei32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, const float32_t *base,
    uint32m1_t bindex, size_t vl);
void vloxseg5ei32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4, const
    float32_t *base, uint32m1_t bindex, size_t vl);
void vloxseg6ei32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, const float32_t *base, uint32m1_t bindex,
    size_t vl);
void vloxseg7ei32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, vfloat32m1_t *v6, const float32_t *base,
    uint32m1_t bindex, size_t vl);
void vloxseg8ei32_v_f32m1 (vfloat32m1_t *v0, vfloat32m1_t *v1,
    vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
    vfloat32m1_t *v5, vfloat32m1_t *v6, vfloat32m1_t *v7, const
    float32_t *base, uint32m1_t bindex, size_t vl);
void vloxseg2ei32_v_f32m2 (vfloat32m2_t *v0, vfloat32m2_t *v1,
    const float32_t *base, uint32m2_t bindex, size_t vl);
void vloxseg3ei32_v_f32m2 (vfloat32m2_t *v0, vfloat32m2_t *v1,
    vfloat32m2_t *v2, const float32_t *base, uint32m2_t bindex,
    size_t vl);
void vloxseg4ei32_v_f32m2 (vfloat32m2_t *v0, vfloat32m2_t *v1,
    vfloat32m2_t *v2, vfloat32m2_t *v3, const float32_t *base,
    uint32m2_t bindex, size_t vl);
void vloxseg2ei32_v_f32m4 (vfloat32m4_t *v0, vfloat32m4_t *v1,
    const float32_t *base, uint32m4_t bindex, size_t vl);
void vloxseg2ei64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    const float64_t *base, uint64m1_t bindex, size_t vl);

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void vloxseg3ei64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, const float64_t *base, uint64m1_t bindex,
    size_t vl);
void vloxseg4ei64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, const float64_t *base,
    uint64m1_t bindex, size_t vl);
void vloxseg5ei64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4, const
    float64_t *base, uint64m1_t bindex, size_t vl);
void vloxseg6ei64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, const float64_t *base, uint64m1_t bindex,
    size_t vl);
void vloxseg7ei64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, vfloat64m1_t *v6, const float64_t *base,
    uint64m1_t bindex, size_t vl);
void vloxseg8ei64_v_f64m1 (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, vfloat64m1_t *v6, vfloat64m1_t *v7, const
    float64_t *base, uint64m1_t bindex, size_t vl);
void vloxseg2ei64_v_f64m2 (vfloat64m2_t *v0, vfloat64m2_t *v1,
    const float64_t *base, uint64m2_t bindex, size_t vl);
void vloxseg3ei64_v_f64m2 (vfloat64m2_t *v0, vfloat64m2_t *v1,
    vfloat64m2_t *v2, const float64_t *base, uint64m2_t bindex,
    size_t vl);
void vloxseg4ei64_v_f64m2 (vfloat64m2_t *v0, vfloat64m2_t *v1,
    vfloat64m2_t *v2, vfloat64m2_t *v3, const float64_t *base,
    uint64m2_t bindex, size_t vl);
void vloxseg2ei64_v_f64m4 (vfloat64m4_t *v0, vfloat64m4_t *v1,
    const float64_t *base, uint64m4_t bindex, size_t vl);
// masked functions
void vloxseg2ei8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1,
    vbool8_t mask, vint8m1_t maskedoff0, vint8m1_t maskedoff1,
    const int8_t *base, uint8m1_t bindex, size_t vl);
void vloxseg3ei8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1,
    vint8m1_t *v2, vbool8_t mask, vint8m1_t maskedoff0,
    vint8m1_t maskedoff1, vint8m1_t maskedoff2, const int8_t
    *base, uint8m1_t bindex, size_t vl);
void vloxseg4ei8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1,
    vint8m1_t *v2, vint8m1_t *v3, vbool8_t mask, vint8m1_t
    maskedoff0, vint8m1_t maskedoff1, vint8m1_t maskedoff2,
    vint8m1_t maskedoff3, const int8_t *base, uint8m1_t bindex,
    size_t vl);
void vloxseg5ei8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1,
    vint8m1_t *v2, vint8m1_t *v3, vint8m1_t *v4, vbool8_t mask,

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vint8m1_t maskedoff0, vint8m1_t maskedoff1, vint8m1_t
maskedoff2, vint8m1_t maskedoff3, vint8m1_t maskedoff4,
    const int8_t *base, vuint8m1_t bindex, size_t vl);
void vloxseg6ei8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1,
vint8m1_t *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5,
vbool8_t mask, vint8m1_t maskedoff0, vint8m1_t maskedoff1,
vint8m1_t maskedoff2, vint8m1_t maskedoff3, vint8m1_t
maskedoff4, vint8m1_t maskedoff5, const int8_t *base,
vuint8m1_t bindex, size_t vl);
void vloxseg7ei8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1,
vint8m1_t *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5,
vint8m1_t *v6, vbool8_t mask, vint8m1_t maskedoff0,
vint8m1_t maskedoff1, vint8m1_t maskedoff2, vint8m1_t
maskedoff3, vint8m1_t maskedoff4, vint8m1_t maskedoff5,
vint8m1_t maskedoff6, const int8_t *base, vuint8m1_t bindex,
size_t vl);
void vloxseg8ei8_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1,
vint8m1_t *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5,
vint8m1_t *v6, vint8m1_t *v7, vbool8_t mask, vint8m1_t
maskedoff0, vint8m1_t maskedoff1, vint8m1_t maskedoff2,
vint8m1_t maskedoff3, vint8m1_t maskedoff4, vint8m1_t
maskedoff5, vint8m1_t maskedoff6, vint8m1_t maskedoff7,
    const int8_t *base, vuint8m1_t bindex, size_t vl);
void vloxseg2ei8_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1,
vbool4_t mask, vint8m2_t maskedoff0, vint8m2_t maskedoff1,
    const int8_t *base, vuint8m2_t bindex, size_t vl);
void vloxseg3ei8_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1,
vint8m2_t *v2, vbool4_t mask, vint8m2_t maskedoff0,
vint8m2_t maskedoff1, vint8m2_t maskedoff2, const int8_t
*base, vuint8m2_t bindex, size_t vl);
void vloxseg4ei8_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1,
vint8m2_t *v2, vint8m2_t *v3, vbool4_t mask, vint8m2_t
maskedoff0, vint8m2_t maskedoff1, vint8m2_t maskedoff2,
vint8m2_t maskedoff3, const int8_t *base, vuint8m2_t bindex,
size_t vl);
void vloxseg2ei8_v_i8m4_m (vint8m4_t *v0, vint8m4_t *v1,
vbool2_t mask, vint8m4_t maskedoff0, vint8m4_t maskedoff1,
    const int8_t *base, vuint8m4_t bindex, size_t vl);
void vloxseg2ei16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
maskedoff1, const int16_t *base, vuint16m1_t bindex, size_t
vl);
void vloxseg3ei16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vbool16_t mask, vint16m1_t maskedoff0,
vint16m1_t maskedoff1, vint16m1_t maskedoff2, const int16_t
*base, vuint16m1_t bindex, size_t vl);

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void vloxseg4ei16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vbool16_t mask, vint16m1_t
maskedoff0, vint16m1_t maskedoff1, vint16m1_t maskedoff2,
vint16m1_t maskedoff3, const int16_t *base, vuint16m1_t
bindex, size_t vl);
void vloxseg5ei16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vbool16_t
mask, vint16m1_t maskedoff0, vint16m1_t maskedoff1,
vint16m1_t maskedoff2, vint16m1_t maskedoff3, vint16m1_t
maskedoff4, const int16_t *base, vuint16m1_t bindex, size_t
vl);
void vloxseg6ei16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
maskedoff1, vint16m1_t maskedoff2, vint16m1_t maskedoff3,
vint16m1_t maskedoff4, vint16m1_t maskedoff5, const int16_t
*base, vuint16m1_t bindex, size_t vl);
void vloxseg7ei16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vint16m1_t *v6, vbool16_t mask, vint16m1_t maskedoff0,
vint16m1_t maskedoff1, vint16m1_t maskedoff2, vint16m1_t
maskedoff3, vint16m1_t maskedoff4, vint16m1_t maskedoff5,
vint16m1_t maskedoff6, const int16_t *base, vuint16m1_t
bindex, size_t vl);
void vloxseg8ei16_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vint16m1_t *v6, vint16m1_t *v7, vbool16_t mask,
vint16m1_t maskedoff0, vint16m1_t maskedoff1, vint16m1_t
maskedoff2, vint16m1_t maskedoff3, vint16m1_t maskedoff4,
vint16m1_t maskedoff5, vint16m1_t maskedoff6, vint16m1_t
maskedoff7, const int16_t *base, vuint16m1_t bindex, size_t
vl);
void vloxseg2ei16_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
vbool8_t mask, vint16m2_t maskedoff0, vint16m2_t maskedoff1,
const int16_t *base, vuint16m2_t bindex, size_t vl);
void vloxseg3ei16_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
vint16m2_t *v2, vbool8_t mask, vint16m2_t maskedoff0,
vint16m2_t maskedoff1, vint16m2_t maskedoff2, const int16_t
*base, vuint16m2_t bindex, size_t vl);
void vloxseg4ei16_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
vint16m2_t *v2, vint16m2_t *v3, vbool8_t mask, vint16m2_t
maskedoff0, vint16m2_t maskedoff1, vint16m2_t maskedoff2,
vint16m2_t maskedoff3, const int16_t *base, vuint16m2_t
bindex, size_t vl);
void vloxseg2ei16_v_i16m4_m (vint16m4_t *v0, vint16m4_t *v1,
vbool4_t mask, vint16m4_t maskedoff0, vint16m4_t maskedoff1,

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    const int16_t *base, vuint16m4_t bindex, size_t vl);
void vloxseg2ei32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, const int32_t *base, vuint32m1_t bindex, size_t
    vl);
void vloxseg3ei32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, const int32_t
    *base, vuint32m1_t bindex, size_t vl);
void vloxseg4ei32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vbool32_t mask, vint32m1_t
    maskedoff0, vint32m1_t maskedoff1, vint32m1_t maskedoff2,
    vint32m1_t maskedoff3, const int32_t *base, vuint32m1_t
    bindex, size_t vl);
void vloxseg5ei32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vbool32_t
    mask, vint32m1_t maskedoff0, vint32m1_t maskedoff1,
    vint32m1_t maskedoff2, vint32m1_t maskedoff3, vint32m1_t
    maskedoff4, const int32_t *base, vuint32m1_t bindex, size_t
    vl);
void vloxseg6ei32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, vint32m1_t maskedoff2, vint32m1_t maskedoff3,
    vint32m1_t maskedoff4, vint32m1_t maskedoff5, const int32_t
    *base, vuint32m1_t bindex, size_t vl);
void vloxseg7ei32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, vint32m1_t
    maskedoff3, vint32m1_t maskedoff4, vint32m1_t maskedoff5,
    vint32m1_t maskedoff6, const int32_t *base, vuint32m1_t
    bindex, size_t vl);
void vloxseg8ei32_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, vbool32_t mask,
    vint32m1_t maskedoff0, vint32m1_t maskedoff1, vint32m1_t
    maskedoff2, vint32m1_t maskedoff3, vint32m1_t maskedoff4,
    vint32m1_t maskedoff5, vint32m1_t maskedoff6, vint32m1_t
    maskedoff7, const int32_t *base, vuint32m1_t bindex, size_t
    vl);
void vloxseg2ei32_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vbool16_t mask, vint32m2_t maskedoff0, vint32m2_t
    maskedoff1, const int32_t *base, vuint32m2_t bindex, size_t
    vl);

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void vloxseg3ei32_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vbool16_t mask, vint32m2_t maskedoff0,
    vint32m2_t maskedoff1, vint32m2_t maskedoff2, const int32_t
    *base, vuint32m2_t bindex, size_t vl);
void vloxseg4ei32_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, vbool16_t mask, vint32m2_t
    maskedoff0, vint32m2_t maskedoff1, vint32m2_t maskedoff2,
    vint32m2_t maskedoff3, const int32_t *base, vuint32m2_t
    bindex, size_t vl);
void vloxseg2ei32_v_i32m4_m (vint32m4_t *v0, vint32m4_t *v1,
    vbool8_t mask, vint32m4_t maskedoff0, vint32m4_t maskedoff1,
    const int32_t *base, vuint32m4_t bindex, size_t vl);
void vloxseg2ei64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, const int64_t *base, vuint64m1_t bindex, size_t
    vl);
void vloxseg3ei64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, const int64_t
    *base, vuint64m1_t bindex, size_t vl);
void vloxseg4ei64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vbool64_t mask, vint64m1_t
    maskedoff0, vint64m1_t maskedoff1, vint64m1_t maskedoff2,
    vint64m1_t maskedoff3, const int64_t *base, vuint64m1_t
    bindex, size_t vl);
void vloxseg5ei64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vbool64_t
    mask, vint64m1_t maskedoff0, vint64m1_t maskedoff1,
    vint64m1_t maskedoff2, vint64m1_t maskedoff3, vint64m1_t
    maskedoff4, const int64_t *base, vuint64m1_t bindex, size_t
    vl);
void vloxseg6ei64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, vint64m1_t maskedoff2, vint64m1_t maskedoff3,
    vint64m1_t maskedoff4, vint64m1_t maskedoff5, const int64_t
    *base, vuint64m1_t bindex, size_t vl);
void vloxseg7ei64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, vint64m1_t
    maskedoff3, vint64m1_t maskedoff4, vint64m1_t maskedoff5,
    vint64m1_t maskedoff6, const int64_t *base, vuint64m1_t
    bindex, size_t vl);
void vloxseg8ei64_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t

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*v5, vint64m1_t *v6, vint64m1_t *v7, vbool64_t mask,
vint64m1_t maskedoff0, vint64m1_t maskedoff1, vint64m1_t
maskedoff2, vint64m1_t maskedoff3, vint64m1_t maskedoff4,
vint64m1_t maskedoff5, vint64m1_t maskedoff6, vint64m1_t
maskedoff7, const int64_t *base, vuint64m1_t bindex, size_t
vl);
void vloxseg2ei64_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
vbool32_t mask, vint64m2_t maskedoff0, vint64m2_t
maskedoff1, const int64_t *base, vuint64m2_t bindex, size_t
vl);
void vloxseg3ei64_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
vint64m2_t *v2, vbool32_t mask, vint64m2_t maskedoff0,
vint64m2_t maskedoff1, vint64m2_t maskedoff2, const int64_t
*base, vuint64m2_t bindex, size_t vl);
void vloxseg4ei64_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
vint64m2_t *v2, vint64m2_t *v3, vbool32_t mask, vint64m2_t
maskedoff0, vint64m2_t maskedoff1, vint64m2_t maskedoff2,
vint64m2_t maskedoff3, const int64_t *base, vuint64m2_t
bindex, size_t vl);
void vloxseg2ei64_v_i64m4_m (vint64m4_t *v0, vint64m4_t *v1,
vbool16_t mask, vint64m4_t maskedoff0, vint64m4_t
maskedoff1, const int64_t *base, vuint64m4_t bindex, size_t
vl);
void vloxseg2ei8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
vbool8_t mask, vuint8m1_t maskedoff0, vuint8m1_t maskedoff1,
const uint8_t *base, vuint8m1_t bindex, size_t vl);
void vloxseg3ei8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
vuint8m1_t *v2, vbool8_t mask, vuint8m1_t maskedoff0,
vuint8m1_t maskedoff1, vuint8m1_t maskedoff2, const uint8_t
*base, vuint8m1_t bindex, size_t vl);
void vloxseg4ei8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
vuint8m1_t *v2, vuint8m1_t *v3, vbool8_t mask, vuint8m1_t
maskedoff0, vuint8m1_t maskedoff1, vuint8m1_t maskedoff2,
vuint8m1_t maskedoff3, const uint8_t *base, vuint8m1_t
bindex, size_t vl);
void vloxseg5ei8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vbool8_t
mask, vuint8m1_t maskedoff0, vuint8m1_t maskedoff1,
vuint8m1_t maskedoff2, vuint8m1_t maskedoff3, vuint8m1_t
maskedoff4, const uint8_t *base, vuint8m1_t bindex, size_t
vl);
void vloxseg6ei8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
*v5, vbool8_t mask, vuint8m1_t maskedoff0, vuint8m1_t
maskedoff1, vuint8m1_t maskedoff2, vuint8m1_t maskedoff3,
vuint8m1_t maskedoff4, vuint8m1_t maskedoff5, const uint8_t

```

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    *base, vuint8m1_t bindex, size_t vl);
void vloxseg7ei8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
*v5, vuint8m1_t *v6, vbool8_t mask, vuint8m1_t maskedoff0,
vuint8m1_t maskedoff1, vuint8m1_t maskedoff2, vuint8m1_t
maskedoff3, vuint8m1_t maskedoff4, vuint8m1_t maskedoff5,
vuint8m1_t maskedoff6, const uint8_t *base, vuint8m1_t
bindex, size_t vl);
void vloxseg8ei8_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
*v5, vuint8m1_t *v6, vuint8m1_t *v7, vbool8_t mask,
vuint8m1_t maskedoff0, vuint8m1_t maskedoff1, vuint8m1_t
maskedoff2, vuint8m1_t maskedoff3, vuint8m1_t maskedoff4,
vuint8m1_t maskedoff5, vuint8m1_t maskedoff6, vuint8m1_t
maskedoff7, const uint8_t *base, vuint8m1_t bindex, size_t
vl);
void vloxseg2ei8_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
vbool4_t mask, vuint8m2_t maskedoff0, vuint8m2_t maskedoff1,
const uint8_t *base, vuint8m2_t bindex, size_t vl);
void vloxseg3ei8_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
vuint8m2_t *v2, vbool4_t mask, vuint8m2_t maskedoff0,
vuint8m2_t maskedoff1, vuint8m2_t maskedoff2, const uint8_t
*base, vuint8m2_t bindex, size_t vl);
void vloxseg4ei8_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
vuint8m2_t *v2, vuint8m2_t *v3, vbool4_t mask, vuint8m2_t
maskedoff0, vuint8m2_t maskedoff1, vuint8m2_t maskedoff2,
vuint8m2_t maskedoff3, const uint8_t *base, vuint8m2_t
bindex, size_t vl);
void vloxseg2ei8_v_u8m4_m (vuint8m4_t *v0, vuint8m4_t *v1,
vbool2_t mask, vuint8m4_t maskedoff0, vuint8m4_t maskedoff1,
const uint8_t *base, vuint8m4_t bindex, size_t vl);
void vloxseg2ei16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vbool16_t mask, vuint16m1_t maskedoff0, vuint16m1_t
maskedoff1, const uint16_t *base, vuint16m1_t bindex, size_t
vl);
void vloxseg3ei16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vbool16_t mask, vuint16m1_t maskedoff0,
vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, const
uint16_t *base, vuint16m1_t bindex, size_t vl);
void vloxseg4ei16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vbool16_t mask,
vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
maskedoff2, vuint16m1_t maskedoff3, const uint16_t *base,
vuint16m1_t bindex, size_t vl);
void vloxseg5ei16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, vbool16_t

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mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
maskedoff4, const uint16_t *base, vuint16m1_t bindex, size_t
vl);
void vloxseg6ei16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vbool16_t mask, vuint16m1_t maskedoff0,
vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, vuint16m1_t
maskedoff3, vuint16m1_t maskedoff4, vuint16m1_t maskedoff5,
const uint16_t *base, vuint16m1_t bindex, size_t vl);
void vloxseg7ei16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vuint16m1_t *v6, vbool16_t mask,
vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t maskedoff4,
vuint16m1_t maskedoff5, vuint16m1_t maskedoff6, const
uint16_t *base, vuint16m1_t bindex, size_t vl);
void vloxseg8ei16_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, vbool16_t
mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
maskedoff4, vuint16m1_t maskedoff5, vuint16m1_t maskedoff6,
vuint16m1_t maskedoff7, const uint16_t *base, vuint16m1_t
bindex, size_t vl);
void vloxseg2ei16_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
vbool8_t mask, vuint16m2_t maskedoff0, vuint16m2_t
maskedoff1, const uint16_t *base, vuint16m2_t bindex, size_t
vl);
void vloxseg3ei16_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
vuint16m2_t *v2, vbool8_t mask, vuint16m2_t maskedoff0,
vuint16m2_t maskedoff1, vuint16m2_t maskedoff2, const
uint16_t *base, vuint16m2_t bindex, size_t vl);
void vloxseg4ei16_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
vuint16m2_t *v2, vuint16m2_t *v3, vbool8_t mask, vuint16m2_t
maskedoff0, vuint16m2_t maskedoff1, vuint16m2_t maskedoff2,
vuint16m2_t maskedoff3, const uint16_t *base, vuint16m2_t
bindex, size_t vl);
void vloxseg2ei16_v_u16m4_m (vuint16m4_t *v0, vuint16m4_t *v1,
vbool4_t mask, vuint16m4_t maskedoff0, vuint16m4_t
maskedoff1, const uint16_t *base, vuint16m4_t bindex, size_t
vl);
void vloxseg2ei32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vbool32_t mask, vuint32m1_t maskedoff0, vuint32m1_t
maskedoff1, const uint32_t *base, vuint32m1_t bindex, size_t
vl);

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void vloxseg3ei32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, const
    uint32_t *base, vuint32m1_t bindex, size_t vl);
void vloxseg4ei32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, const uint32_t *base,
    vuint32m1_t bindex, size_t vl);
void vloxseg5ei32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, const uint32_t *base, vuint32m1_t bindex, size_t
    vl);
void vloxseg6ei32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, vuint32m1_t
    maskedoff3, vuint32m1_t maskedoff4, vuint32m1_t maskedoff5,
    const uint32_t *base, vuint32m1_t bindex, size_t vl);
void vloxseg7ei32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t maskedoff4,
    vuint32m1_t maskedoff5, vuint32m1_t maskedoff6, const
    uint32_t *base, vuint32m1_t bindex, size_t vl);
void vloxseg8ei32_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, vuint32m1_t maskedoff5, vuint32m1_t maskedoff6,
    vuint32m1_t maskedoff7, const uint32_t *base, vuint32m1_t
    bindex, size_t vl);
void vloxseg2ei32_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vbool16_t mask, vuint32m2_t maskedoff0, vuint32m2_t
    maskedoff1, const uint32_t *base, vuint32m2_t bindex, size_t
    vl);
void vloxseg3ei32_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vbool16_t mask, vuint32m2_t maskedoff0,
    vuint32m2_t maskedoff1, vuint32m2_t maskedoff2, const
    uint32_t *base, vuint32m2_t bindex, size_t vl);
void vloxseg4ei32_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, vbool16_t mask,

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    vuint32m2_t maskedoff0, vuint32m2_t maskedoff1, vuint32m2_t
    maskedoff2, vuint32m2_t maskedoff3, const uint32_t *base,
    vuint32m2_t bindex, size_t vl);
void vloxseg2ei32_v_u32m4_m (vuint32m4_t *v0, vuint32m4_t *v1,
    vbool8_t mask, vuint32m4_t maskedoff0, vuint32m4_t
    maskedoff1, const uint32_t *base, vuint32m4_t bindex, size_t
    vl);
void vloxseg2ei64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vbool64_t mask, vuint64m1_t maskedoff0, vuint64m1_t
    maskedoff1, const uint64_t *base, vuint64m1_t bindex, size_t
    vl);
void vloxseg3ei64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, const
    uint64_t *base, vuint64m1_t bindex, size_t vl);
void vloxseg4ei64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, const uint64_t *base,
    vuint64m1_t bindex, size_t vl);
void vloxseg5ei64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, const uint64_t *base, vuint64m1_t bindex, size_t
    vl);
void vloxseg6ei64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, vuint64m1_t
    maskedoff3, vuint64m1_t maskedoff4, vuint64m1_t maskedoff5,
    const uint64_t *base, vuint64m1_t bindex, size_t vl);
void vloxseg7ei64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t maskedoff4,
    vuint64m1_t maskedoff5, vuint64m1_t maskedoff6, const
    uint64_t *base, vuint64m1_t bindex, size_t vl);
void vloxseg8ei64_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, vuint64m1_t maskedoff5, vuint64m1_t maskedoff6,
    vuint64m1_t maskedoff7, const uint64_t *base, vuint64m1_t

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    bindex, size_t vl);
void vloxseg2ei64_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vbool32_t mask, vuint64m2_t maskedoff0, vuint64m2_t
    maskedoff1, const uint64_t *base, vuint64m2_t bindex, size_t
    vl);
void vloxseg3ei64_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vbool32_t mask, vuint64m2_t maskedoff0,
    vuint64m2_t maskedoff1, vuint64m2_t maskedoff2, const
    uint64_t *base, vuint64m2_t bindex, size_t vl);
void vloxseg4ei64_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, vbool32_t mask,
    vuint64m2_t maskedoff0, vuint64m2_t maskedoff1, vuint64m2_t
    maskedoff2, vuint64m2_t maskedoff3, const uint64_t *base,
    vuint64m2_t bindex, size_t vl);
void vloxseg2ei64_v_u64m4_m (vuint64m4_t *v0, vuint64m4_t *v1,
    vbool16_t mask, vuint64m4_t maskedoff0, vuint64m4_t
    maskedoff1, const uint64_t *base, vuint64m4_t bindex, size_t
    vl);
void vloxseg2ei16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vbool16_t mask, vfloat16m1_t maskedoff0, vfloat16m1_t
    maskedoff1, const float16_t *base, vuint16m1_t bindex,
    size_t vl);
void vloxseg3ei16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vbool16_t mask, vfloat16m1_t maskedoff0,
    vfloat16m1_t maskedoff1, vfloat16m1_t maskedoff2, const
    float16_t *base, vuint16m1_t bindex, size_t vl);
void vloxseg4ei16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vbool16_t mask,
    vfloat16m1_t maskedoff0, vfloat16m1_t maskedoff1,
    vfloat16m1_t maskedoff2, vfloat16m1_t maskedoff3, const
    float16_t *base, vuint16m1_t bindex, size_t vl);
void vloxseg5ei16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vbool16_t mask, vfloat16m1_t maskedoff0, vfloat16m1_t
    maskedoff1, vfloat16m1_t maskedoff2, vfloat16m1_t
    maskedoff3, vfloat16m1_t maskedoff4, const float16_t *base,
    vuint16m1_t bindex, size_t vl);
void vloxseg6ei16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
    vfloat16m1_t *v5, vbool16_t mask, vfloat16m1_t maskedoff0,
    vfloat16m1_t maskedoff1, vfloat16m1_t maskedoff2,
    vfloat16m1_t maskedoff3, vfloat16m1_t maskedoff4,
    vfloat16m1_t maskedoff5, const float16_t *base, vuint16m1_t
    bindex, size_t vl);
void vloxseg7ei16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
    vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,

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vfloat16m1_t *v5, vfloat16m1_t *v6, vbool16_t mask,
vfloat16m1_t maskedoff0, vfloat16m1_t maskedoff1,
vfloat16m1_t maskedoff2, vfloat16m1_t maskedoff3,
vfloat16m1_t maskedoff4, vfloat16m1_t maskedoff5,
vfloat16m1_t maskedoff6, const float16_t *base, uint16m1_t
bindex, size_t vl);
void vloxseg8ei16_v_f16m1_m (vfloat16m1_t *v0, vfloat16m1_t *v1,
vfloat16m1_t *v2, vfloat16m1_t *v3, vfloat16m1_t *v4,
vfloat16m1_t *v5, vfloat16m1_t *v6, vfloat16m1_t *v7,
vbool16_t mask, vfloat16m1_t maskedoff0, vfloat16m1_t
maskedoff1, vfloat16m1_t maskedoff2, vfloat16m1_t
maskedoff3, vfloat16m1_t maskedoff4, vfloat16m1_t
maskedoff5, vfloat16m1_t maskedoff6, vfloat16m1_t
maskedoff7, const float16_t *base, uint16m1_t bindex,
size_t vl);
void vloxseg2ei16_v_f16m2_m (vfloat16m2_t *v0, vfloat16m2_t *v1,
vbool8_t mask, vfloat16m2_t maskedoff0, vfloat16m2_t
maskedoff1, const float16_t *base, uint16m2_t bindex,
size_t vl);
void vloxseg3ei16_v_f16m2_m (vfloat16m2_t *v0, vfloat16m2_t *v1,
vfloat16m2_t *v2, vbool8_t mask, vfloat16m2_t maskedoff0,
vfloat16m2_t maskedoff1, vfloat16m2_t maskedoff2, const
float16_t *base, uint16m2_t bindex, size_t vl);
void vloxseg4ei16_v_f16m2_m (vfloat16m2_t *v0, vfloat16m2_t *v1,
vfloat16m2_t *v2, vfloat16m2_t *v3, vbool8_t mask,
vfloat16m2_t maskedoff0, vfloat16m2_t maskedoff1,
vfloat16m2_t maskedoff2, vfloat16m2_t maskedoff3, const
float16_t *base, uint16m2_t bindex, size_t vl);
void vloxseg2ei16_v_f16m4_m (vfloat16m4_t *v0, vfloat16m4_t *v1,
vbool4_t mask, vfloat16m4_t maskedoff0, vfloat16m4_t
maskedoff1, const float16_t *base, uint16m4_t bindex,
size_t vl);
void vloxseg2ei32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
vbool32_t mask, vfloat32m1_t maskedoff0, vfloat32m1_t
maskedoff1, const float32_t *base, uint32m1_t bindex,
size_t vl);
void vloxseg3ei32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
vfloat32m1_t *v2, vbool32_t mask, vfloat32m1_t maskedoff0,
vfloat32m1_t maskedoff1, vfloat32m1_t maskedoff2, const
float32_t *base, uint32m1_t bindex, size_t vl);
void vloxseg4ei32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
vfloat32m1_t *v2, vfloat32m1_t *v3, vbool32_t mask,
vfloat32m1_t maskedoff0, vfloat32m1_t maskedoff1,
vfloat32m1_t maskedoff2, vfloat32m1_t maskedoff3, const
float32_t *base, uint32m1_t bindex, size_t vl);

```

```

void vloxseg5ei32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
vbool32_t mask, vfloat32m1_t maskedoff0, vfloat32m1_t
maskedoff1, vfloat32m1_t maskedoff2, vfloat32m1_t
maskedoff3, vfloat32m1_t maskedoff4, const float32_t *base,
vuint32m1_t bindex, size_t vl);
void vloxseg6ei32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
vfloat32m1_t *v5, vbool32_t mask, vfloat32m1_t maskedoff0,
vfloat32m1_t maskedoff1, vfloat32m1_t maskedoff2,
vfloat32m1_t maskedoff3, vfloat32m1_t maskedoff4,
vfloat32m1_t maskedoff5, const float32_t *base, vuint32m1_t
bindex, size_t vl);
void vloxseg7ei32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
vfloat32m1_t *v5, vfloat32m1_t *v6, vbool32_t mask,
vfloat32m1_t maskedoff0, vfloat32m1_t maskedoff1,
vfloat32m1_t maskedoff2, vfloat32m1_t maskedoff3,
vfloat32m1_t maskedoff4, vfloat32m1_t maskedoff5,
vfloat32m1_t maskedoff6, const float32_t *base, vuint32m1_t
bindex, size_t vl);
void vloxseg8ei32_v_f32m1_m (vfloat32m1_t *v0, vfloat32m1_t *v1,
vfloat32m1_t *v2, vfloat32m1_t *v3, vfloat32m1_t *v4,
vfloat32m1_t *v5, vfloat32m1_t *v6, vfloat32m1_t *v7,
vbool32_t mask, vfloat32m1_t maskedoff0, vfloat32m1_t
maskedoff1, vfloat32m1_t maskedoff2, vfloat32m1_t
maskedoff3, vfloat32m1_t maskedoff4, vfloat32m1_t
maskedoff5, vfloat32m1_t maskedoff6, vfloat32m1_t
maskedoff7, const float32_t *base, vuint32m1_t bindex,
size_t vl);
void vloxseg2ei32_v_f32m2_m (vfloat32m2_t *v0, vfloat32m2_t *v1,
vbool16_t mask, vfloat32m2_t maskedoff0, vfloat32m2_t
maskedoff1, const float32_t *base, vuint32m2_t bindex,
size_t vl);
void vloxseg3ei32_v_f32m2_m (vfloat32m2_t *v0, vfloat32m2_t *v1,
vfloat32m2_t *v2, vbool16_t mask, vfloat32m2_t maskedoff0,
vfloat32m2_t maskedoff1, vfloat32m2_t maskedoff2, const
float32_t *base, vuint32m2_t bindex, size_t vl);
void vloxseg4ei32_v_f32m2_m (vfloat32m2_t *v0, vfloat32m2_t *v1,
vfloat32m2_t *v2, vfloat32m2_t *v3, vbool16_t mask,
vfloat32m2_t maskedoff0, vfloat32m2_t maskedoff1,
vfloat32m2_t maskedoff2, vfloat32m2_t maskedoff3, const
float32_t *base, vuint32m2_t bindex, size_t vl);
void vloxseg2ei32_v_f32m4_m (vfloat32m4_t *v0, vfloat32m4_t *v1,
vbool8_t mask, vfloat32m4_t maskedoff0, vfloat32m4_t
maskedoff1, const float32_t *base, vuint32m4_t bindex,

```

```

    size_t vl);
void vloxseg2ei64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vbool64_t mask, vfloat64m1_t maskedoff0, vfloat64m1_t
    maskedoff1, const float64_t *base, uint64m1_t bindex,
    size_t vl);
void vloxseg3ei64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vbool64_t mask, vfloat64m1_t maskedoff0,
    vfloat64m1_t maskedoff1, vfloat64m1_t maskedoff2, const
    float64_t *base, uint64m1_t bindex, size_t vl);
void vloxseg4ei64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vbool64_t mask,
    vfloat64m1_t maskedoff0, vfloat64m1_t maskedoff1,
    vfloat64m1_t maskedoff2, vfloat64m1_t maskedoff3, const
    float64_t *base, uint64m1_t bindex, size_t vl);
void vloxseg5ei64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vbool64_t mask, vfloat64m1_t maskedoff0, vfloat64m1_t
    maskedoff1, vfloat64m1_t maskedoff2, vfloat64m1_t
    maskedoff3, vfloat64m1_t maskedoff4, const float64_t *base,
    uint64m1_t bindex, size_t vl);
void vloxseg6ei64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, vbool64_t mask, vfloat64m1_t maskedoff0,
    vfloat64m1_t maskedoff1, vfloat64m1_t maskedoff2,
    vfloat64m1_t maskedoff3, vfloat64m1_t maskedoff4,
    vfloat64m1_t maskedoff5, const float64_t *base, uint64m1_t
    bindex, size_t vl);
void vloxseg7ei64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, vfloat64m1_t *v6, vbool64_t mask,
    vfloat64m1_t maskedoff0, vfloat64m1_t maskedoff1,
    vfloat64m1_t maskedoff2, vfloat64m1_t maskedoff3,
    vfloat64m1_t maskedoff4, vfloat64m1_t maskedoff5,
    vfloat64m1_t maskedoff6, const float64_t *base, uint64m1_t
    bindex, size_t vl);
void vloxseg8ei64_v_f64m1_m (vfloat64m1_t *v0, vfloat64m1_t *v1,
    vfloat64m1_t *v2, vfloat64m1_t *v3, vfloat64m1_t *v4,
    vfloat64m1_t *v5, vfloat64m1_t *v6, vfloat64m1_t *v7,
    vbool64_t mask, vfloat64m1_t maskedoff0, vfloat64m1_t
    maskedoff1, vfloat64m1_t maskedoff2, vfloat64m1_t
    maskedoff3, vfloat64m1_t maskedoff4, vfloat64m1_t
    maskedoff5, vfloat64m1_t maskedoff6, vfloat64m1_t
    maskedoff7, const float64_t *base, uint64m1_t bindex,
    size_t vl);
void vloxseg2ei64_v_f64m2_m (vfloat64m2_t *v0, vfloat64m2_t *v1,
    vbool32_t mask, vfloat64m2_t maskedoff0, vfloat64m2_t

```

```

maskedoff1, const float64_t *base, uint64m2_t bindex,
size_t vl);
void vloxseg3ei64_v_f64m2_m (vfloat64m2_t *v0, vfloat64m2_t *v1,
vfloat64m2_t *v2, vbool32_t mask, vfloat64m2_t maskedoff0,
vfloat64m2_t maskedoff1, vfloat64m2_t maskedoff2, const
float64_t *base, uint64m2_t bindex, size_t vl);
void vloxseg4ei64_v_f64m2_m (vfloat64m2_t *v0, vfloat64m2_t *v1,
vfloat64m2_t *v2, vfloat64m2_t *v3, vbool32_t mask,
vfloat64m2_t maskedoff0, vfloat64m2_t maskedoff1,
vfloat64m2_t maskedoff2, vfloat64m2_t maskedoff3, const
float64_t *base, uint64m2_t bindex, size_t vl);
void vloxseg2ei64_v_f64m4_m (vfloat64m4_t *v0, vfloat64m4_t *v1,
vbool16_t mask, vfloat64m4_t maskedoff0, vfloat64m4_t
maskedoff1, const float64_t *base, uint64m4_t bindex,
size_t vl);

```

## Vector Indexed Segment Store Functions:

### Prototypes:

```

void vsoxseg2ei8_v_i8m1 (int8_t *base, uint8m1_t bindex,
vint8m1_t v0, vint8m1_t v1, size_t vl);
void vsoxseg3ei8_v_i8m1 (int8_t *base, uint8m1_t bindex,
vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, size_t vl);
void vsoxseg4ei8_v_i8m1 (int8_t *base, uint8m1_t bindex,
vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3,
size_t vl);
void vsoxseg5ei8_v_i8m1 (int8_t *base, uint8m1_t bindex,
vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3,
vint8m1_t v4, size_t vl);
void vsoxseg6ei8_v_i8m1 (int8_t *base, uint8m1_t bindex,
vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3,
vint8m1_t v4, vint8m1_t v5, size_t vl);
void vsoxseg7ei8_v_i8m1 (int8_t *base, uint8m1_t bindex,
vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3,
vint8m1_t v4, vint8m1_t v5, vint8m1_t v6, size_t vl);
void vsoxseg8ei8_v_i8m1 (int8_t *base, uint8m1_t bindex,
vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3,
vint8m1_t v4, vint8m1_t v5, vint8m1_t v6, vint8m1_t v7,
size_t vl);
void vsoxseg2ei8_v_i8m2 (int8_t *base, uint8m2_t bindex,
vint8m2_t v0, vint8m2_t v1, size_t vl);
void vsoxseg3ei8_v_i8m2 (int8_t *base, uint8m2_t bindex,
vint8m2_t v0, vint8m2_t v1, vint8m2_t v2, size_t vl);
void vsoxseg4ei8_v_i8m2 (int8_t *base, uint8m2_t bindex,
vint8m2_t v0, vint8m2_t v1, vint8m2_t v2, vint8m2_t v3,

```

```

    size_t vl);
void vsxseg2ei8_v_i8m4 (int8_t *base, uint8m4_t bindex,
    vint8m4_t v0, vint8m4_t v1, size_t vl);
void vsxseg2ei16_v_i16m1 (int16_t *base, uint16m1_t bindex,
    vint16m1_t v0, vint16m1_t v1, size_t vl);
void vsxseg3ei16_v_i16m1 (int16_t *base, uint16m1_t bindex,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, size_t vl);
void vsxseg4ei16_v_i16m1 (int16_t *base, uint16m1_t bindex,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    size_t vl);
void vsxseg5ei16_v_i16m1 (int16_t *base, uint16m1_t bindex,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, size_t vl);
void vsxseg6ei16_v_i16m1 (int16_t *base, uint16m1_t bindex,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, size_t vl);
void vsxseg7ei16_v_i16m1 (int16_t *base, uint16m1_t bindex,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, size_t vl);
void vsxseg8ei16_v_i16m1 (int16_t *base, uint16m1_t bindex,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, vint16m1_t v7,
    size_t vl);
void vsxseg2ei16_v_i16m2 (int16_t *base, uint16m2_t bindex,
    vint16m2_t v0, vint16m2_t v1, size_t vl);
void vsxseg3ei16_v_i16m2 (int16_t *base, uint16m2_t bindex,
    vint16m2_t v0, vint16m2_t v1, vint16m2_t v2, size_t vl);
void vsxseg4ei16_v_i16m2 (int16_t *base, uint16m2_t bindex,
    vint16m2_t v0, vint16m2_t v1, vint16m2_t v2, vint16m2_t v3,
    size_t vl);
void vsxseg2ei16_v_i16m4 (int16_t *base, uint16m4_t bindex,
    vint16m4_t v0, vint16m4_t v1, size_t vl);
void vsxseg2ei32_v_i32m1 (int32_t *base, uint32m1_t bindex,
    vint32m1_t v0, vint32m1_t v1, size_t vl);
void vsxseg3ei32_v_i32m1 (int32_t *base, uint32m1_t bindex,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, size_t vl);
void vsxseg4ei32_v_i32m1 (int32_t *base, uint32m1_t bindex,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    size_t vl);
void vsxseg5ei32_v_i32m1 (int32_t *base, uint32m1_t bindex,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, size_t vl);
void vsxseg6ei32_v_i32m1 (int32_t *base, uint32m1_t bindex,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, size_t vl);

```

```

void vsoxseg7ei32_v_i32m1 (int32_t *base, uint32m1_t bindex,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, size_t vl);
void vsoxseg8ei32_v_i32m1 (int32_t *base, uint32m1_t bindex,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, vint32m1_t v7,
    size_t vl);
void vsoxseg2ei32_v_i32m2 (int32_t *base, uint32m2_t bindex,
    vint32m2_t v0, vint32m2_t v1, size_t vl);
void vsoxseg3ei32_v_i32m2 (int32_t *base, uint32m2_t bindex,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, size_t vl);
void vsoxseg4ei32_v_i32m2 (int32_t *base, uint32m2_t bindex,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, vint32m2_t v3,
    size_t vl);
void vsoxseg2ei32_v_i32m4 (int32_t *base, uint32m4_t bindex,
    vint32m4_t v0, vint32m4_t v1, size_t vl);
void vsoxseg2ei64_v_i64m1 (int64_t *base, uint64m1_t bindex,
    vint64m1_t v0, vint64m1_t v1, size_t vl);
void vsoxseg3ei64_v_i64m1 (int64_t *base, uint64m1_t bindex,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, size_t vl);
void vsoxseg4ei64_v_i64m1 (int64_t *base, uint64m1_t bindex,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    size_t vl);
void vsoxseg5ei64_v_i64m1 (int64_t *base, uint64m1_t bindex,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, size_t vl);
void vsoxseg6ei64_v_i64m1 (int64_t *base, uint64m1_t bindex,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, size_t vl);
void vsoxseg7ei64_v_i64m1 (int64_t *base, uint64m1_t bindex,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, size_t vl);
void vsoxseg8ei64_v_i64m1 (int64_t *base, uint64m1_t bindex,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, vint64m1_t v7,
    size_t vl);
void vsoxseg2ei64_v_i64m2 (int64_t *base, uint64m2_t bindex,
    vint64m2_t v0, vint64m2_t v1, size_t vl);
void vsoxseg3ei64_v_i64m2 (int64_t *base, uint64m2_t bindex,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, size_t vl);
void vsoxseg4ei64_v_i64m2 (int64_t *base, uint64m2_t bindex,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, vint64m2_t v3,
    size_t vl);
void vsoxseg2ei64_v_i64m4 (int64_t *base, uint64m4_t bindex,
    vint64m4_t v0, vint64m4_t v1, size_t vl);

```

```

void vsoxseg2ei8_v_u8m1 (uint8_t *base, vuint8m1_t bindex,
    vuint8m1_t v0, vuint8m1_t v1, size_t vl);
void vsoxseg3ei8_v_u8m1 (uint8_t *base, vuint8m1_t bindex,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, size_t vl);
void vsoxseg4ei8_v_u8m1 (uint8_t *base, vuint8m1_t bindex,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    size_t vl);
void vsoxseg5ei8_v_u8m1 (uint8_t *base, vuint8m1_t bindex,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    vuint8m1_t v4, size_t vl);
void vsoxseg6ei8_v_u8m1 (uint8_t *base, vuint8m1_t bindex,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    vuint8m1_t v4, vuint8m1_t v5, size_t vl);
void vsoxseg7ei8_v_u8m1 (uint8_t *base, vuint8m1_t bindex,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    vuint8m1_t v4, vuint8m1_t v5, vuint8m1_t v6, size_t vl);
void vsoxseg8ei8_v_u8m1 (uint8_t *base, vuint8m1_t bindex,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    vuint8m1_t v4, vuint8m1_t v5, vuint8m1_t v6, vuint8m1_t v7,
    size_t vl);
void vsoxseg2ei8_v_u8m2 (uint8_t *base, vuint8m2_t bindex,
    vuint8m2_t v0, vuint8m2_t v1, size_t vl);
void vsoxseg3ei8_v_u8m2 (uint8_t *base, vuint8m2_t bindex,
    vuint8m2_t v0, vuint8m2_t v1, vuint8m2_t v2, size_t vl);
void vsoxseg4ei8_v_u8m2 (uint8_t *base, vuint8m2_t bindex,
    vuint8m2_t v0, vuint8m2_t v1, vuint8m2_t v2, vuint8m2_t v3,
    size_t vl);
void vsoxseg2ei8_v_u8m4 (uint8_t *base, vuint8m4_t bindex,
    vuint8m4_t v0, vuint8m4_t v1, size_t vl);
void vsoxseg2ei16_v_u16m1 (uint16_t *base, vuint16m1_t bindex,
    vuint16m1_t v0, vuint16m1_t v1, size_t vl);
void vsoxseg3ei16_v_u16m1 (uint16_t *base, vuint16m1_t bindex,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, size_t vl);
void vsoxseg4ei16_v_u16m1 (uint16_t *base, vuint16m1_t bindex,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, size_t vl);
void vsoxseg5ei16_v_u16m1 (uint16_t *base, vuint16m1_t bindex,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, size_t vl);
void vsoxseg6ei16_v_u16m1 (uint16_t *base, vuint16m1_t bindex,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, size_t vl);
void vsoxseg7ei16_v_u16m1 (uint16_t *base, vuint16m1_t bindex,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6, size_t
    vl);

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void vsoxseg8ei16_v_u16m1 (uint16_t *base, vuint16m1_t bindex,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6,
    vuint16m1_t v7, size_t vl);
void vsoxseg2ei16_v_u16m2 (uint16_t *base, vuint16m2_t bindex,
    vuint16m2_t v0, vuint16m2_t v1, size_t vl);
void vsoxseg3ei16_v_u16m2 (uint16_t *base, vuint16m2_t bindex,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, size_t vl);
void vsoxseg4ei16_v_u16m2 (uint16_t *base, vuint16m2_t bindex,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, vuint16m2_t
    v3, size_t vl);
void vsoxseg2ei16_v_u16m4 (uint16_t *base, vuint16m4_t bindex,
    vuint16m4_t v0, vuint16m4_t v1, size_t vl);
void vsoxseg2ei32_v_u32m1 (uint32_t *base, vuint32m1_t bindex,
    vuint32m1_t v0, vuint32m1_t v1, size_t vl);
void vsoxseg3ei32_v_u32m1 (uint32_t *base, vuint32m1_t bindex,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, size_t vl);
void vsoxseg4ei32_v_u32m1 (uint32_t *base, vuint32m1_t bindex,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, size_t vl);
void vsoxseg5ei32_v_u32m1 (uint32_t *base, vuint32m1_t bindex,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, size_t vl);
void vsoxseg6ei32_v_u32m1 (uint32_t *base, vuint32m1_t bindex,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, size_t vl);
void vsoxseg7ei32_v_u32m1 (uint32_t *base, vuint32m1_t bindex,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6, size_t
    vl);
void vsoxseg8ei32_v_u32m1 (uint32_t *base, vuint32m1_t bindex,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6,
    vuint32m1_t v7, size_t vl);
void vsoxseg2ei32_v_u32m2 (uint32_t *base, vuint32m2_t bindex,
    vuint32m2_t v0, vuint32m2_t v1, size_t vl);
void vsoxseg3ei32_v_u32m2 (uint32_t *base, vuint32m2_t bindex,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, size_t vl);
void vsoxseg4ei32_v_u32m2 (uint32_t *base, vuint32m2_t bindex,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, vuint32m2_t
    v3, size_t vl);
void vsoxseg2ei32_v_u32m4 (uint32_t *base, vuint32m4_t bindex,
    vuint32m4_t v0, vuint32m4_t v1, size_t vl);
void vsoxseg2ei64_v_u64m1 (uint64_t *base, vuint64m1_t bindex,
    vuint64m1_t v0, vuint64m1_t v1, size_t vl);

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void vsoxseg3ei64_v_u64m1 (uint64_t *base, vuint64m1_t bindex,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, size_t vl);
void vsoxseg4ei64_v_u64m1 (uint64_t *base, vuint64m1_t bindex,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, size_t vl);
void vsoxseg5ei64_v_u64m1 (uint64_t *base, vuint64m1_t bindex,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, size_t vl);
void vsoxseg6ei64_v_u64m1 (uint64_t *base, vuint64m1_t bindex,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, size_t vl);
void vsoxseg7ei64_v_u64m1 (uint64_t *base, vuint64m1_t bindex,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6, size_t
    vl);
void vsoxseg8ei64_v_u64m1 (uint64_t *base, vuint64m1_t bindex,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6,
    vuint64m1_t v7, size_t vl);
void vsoxseg2ei64_v_u64m2 (uint64_t *base, vuint64m2_t bindex,
    vuint64m2_t v0, vuint64m2_t v1, size_t vl);
void vsoxseg3ei64_v_u64m2 (uint64_t *base, vuint64m2_t bindex,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, size_t vl);
void vsoxseg4ei64_v_u64m2 (uint64_t *base, vuint64m2_t bindex,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, vuint64m2_t
    v3, size_t vl);
void vsoxseg2ei64_v_u64m4 (uint64_t *base, vuint64m4_t bindex,
    vuint64m4_t v0, vuint64m4_t v1, size_t vl);
void vsoxseg2ei16_v_f16m1 (float16_t *base, vuint16m1_t bindex,
    vfloat16m1_t v0, vfloat16m1_t v1, size_t vl);
void vsoxseg3ei16_v_f16m1 (float16_t *base, vuint16m1_t bindex,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2, size_t
    vl);
void vsoxseg4ei16_v_f16m1 (float16_t *base, vuint16m1_t bindex,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2,
    vfloat16m1_t v3, size_t vl);
void vsoxseg5ei16_v_f16m1 (float16_t *base, vuint16m1_t bindex,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2,
    vfloat16m1_t v3, vfloat16m1_t v4, size_t vl);
void vsoxseg6ei16_v_f16m1 (float16_t *base, vuint16m1_t bindex,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2,
    vfloat16m1_t v3, vfloat16m1_t v4, vfloat16m1_t v5, size_t
    vl);
void vsoxseg7ei16_v_f16m1 (float16_t *base, vuint16m1_t bindex,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2,
    vfloat16m1_t v3, vfloat16m1_t v4, vfloat16m1_t v5,

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    vfloat16m1_t v6, size_t vl);
void vsortex8ei16_v_f16m1 (float16_t *base, uint16m1_t bindex,
    vfloat16m1_t v0, vfloat16m1_t v1, vfloat16m1_t v2,
    vfloat16m1_t v3, vfloat16m1_t v4, vfloat16m1_t v5,
    vfloat16m1_t v6, vfloat16m1_t v7, size_t vl);
void vsortex2ei16_v_f16m2 (float16_t *base, uint16m2_t bindex,
    vfloat16m2_t v0, vfloat16m2_t v1, size_t vl);
void vsortex3ei16_v_f16m2 (float16_t *base, uint16m2_t bindex,
    vfloat16m2_t v0, vfloat16m2_t v1, vfloat16m2_t v2, size_t
    vl);
void vsortex4ei16_v_f16m2 (float16_t *base, uint16m2_t bindex,
    vfloat16m2_t v0, vfloat16m2_t v1, vfloat16m2_t v2,
    vfloat16m2_t v3, size_t vl);
void vsortex2ei16_v_f16m4 (float16_t *base, uint16m4_t bindex,
    vfloat16m4_t v0, vfloat16m4_t v1, size_t vl);
void vsortex2ei32_v_f32m1 (float32_t *base, uint32m1_t bindex,
    vfloat32m1_t v0, vfloat32m1_t v1, size_t vl);
void vsortex3ei32_v_f32m1 (float32_t *base, uint32m1_t bindex,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2, size_t
    vl);
void vsortex4ei32_v_f32m1 (float32_t *base, uint32m1_t bindex,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2,
    vfloat32m1_t v3, size_t vl);
void vsortex5ei32_v_f32m1 (float32_t *base, uint32m1_t bindex,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2,
    vfloat32m1_t v3, vfloat32m1_t v4, size_t vl);
void vsortex6ei32_v_f32m1 (float32_t *base, uint32m1_t bindex,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2,
    vfloat32m1_t v3, vfloat32m1_t v4, vfloat32m1_t v5, size_t
    vl);
void vsortex7ei32_v_f32m1 (float32_t *base, uint32m1_t bindex,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2,
    vfloat32m1_t v3, vfloat32m1_t v4, vfloat32m1_t v5,
    vfloat32m1_t v6, size_t vl);
void vsortex8ei32_v_f32m1 (float32_t *base, uint32m1_t bindex,
    vfloat32m1_t v0, vfloat32m1_t v1, vfloat32m1_t v2,
    vfloat32m1_t v3, vfloat32m1_t v4, vfloat32m1_t v5,
    vfloat32m1_t v6, vfloat32m1_t v7, size_t vl);
void vsortex2ei32_v_f32m2 (float32_t *base, uint32m2_t bindex,
    vfloat32m2_t v0, vfloat32m2_t v1, size_t vl);
void vsortex3ei32_v_f32m2 (float32_t *base, uint32m2_t bindex,
    vfloat32m2_t v0, vfloat32m2_t v1, vfloat32m2_t v2, size_t
    vl);
void vsortex4ei32_v_f32m2 (float32_t *base, uint32m2_t bindex,
    vfloat32m2_t v0, vfloat32m2_t v1, vfloat32m2_t v2,
    vfloat32m2_t v3, size_t vl);

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void vsoxseg2ei32_v_f32m4 (float32_t *base, uint32m4_t bindex,
    vfloat32m4_t v0, vfloat32m4_t v1, size_t vl);
void vsoxseg2ei64_v_f64m1 (float64_t *base, uint64m1_t bindex,
    vfloat64m1_t v0, vfloat64m1_t v1, size_t vl);
void vsoxseg3ei64_v_f64m1 (float64_t *base, uint64m1_t bindex,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2, size_t
    vl);
void vsoxseg4ei64_v_f64m1 (float64_t *base, uint64m1_t bindex,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2,
    vfloat64m1_t v3, size_t vl);
void vsoxseg5ei64_v_f64m1 (float64_t *base, uint64m1_t bindex,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2,
    vfloat64m1_t v3, vfloat64m1_t v4, size_t vl);
void vsoxseg6ei64_v_f64m1 (float64_t *base, uint64m1_t bindex,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2,
    vfloat64m1_t v3, vfloat64m1_t v4, vfloat64m1_t v5, size_t
    vl);
void vsoxseg7ei64_v_f64m1 (float64_t *base, uint64m1_t bindex,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2,
    vfloat64m1_t v3, vfloat64m1_t v4, vfloat64m1_t v5,
    vfloat64m1_t v6, size_t vl);
void vsoxseg8ei64_v_f64m1 (float64_t *base, uint64m1_t bindex,
    vfloat64m1_t v0, vfloat64m1_t v1, vfloat64m1_t v2,
    vfloat64m1_t v3, vfloat64m1_t v4, vfloat64m1_t v5,
    vfloat64m1_t v6, vfloat64m1_t v7, size_t vl);
void vsoxseg2ei64_v_f64m2 (float64_t *base, uint64m2_t bindex,
    vfloat64m2_t v0, vfloat64m2_t v1, size_t vl);
void vsoxseg3ei64_v_f64m2 (float64_t *base, uint64m2_t bindex,
    vfloat64m2_t v0, vfloat64m2_t v1, vfloat64m2_t v2, size_t
    vl);
void vsoxseg4ei64_v_f64m2 (float64_t *base, uint64m2_t bindex,
    vfloat64m2_t v0, vfloat64m2_t v1, vfloat64m2_t v2,
    vfloat64m2_t v3, size_t vl);
void vsoxseg2ei64_v_f64m4 (float64_t *base, uint64m4_t bindex,
    vfloat64m4_t v0, vfloat64m4_t v1, size_t vl);
// masked functions
void vsoxseg2ei8_v_i8m1_m (vbool8_t mask, int8_t *base,
    uint8m1_t bindex, vint8m1_t v0, vint8m1_t v1, size_t vl);
void vsoxseg3ei8_v_i8m1_m (vbool8_t mask, int8_t *base,
    uint8m1_t bindex, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2,
    size_t vl);
void vsoxseg4ei8_v_i8m1_m (vbool8_t mask, int8_t *base,
    uint8m1_t bindex, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2,
    vint8m1_t v3, size_t vl);
void vsoxseg5ei8_v_i8m1_m (vbool8_t mask, int8_t *base,
    uint8m1_t bindex, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2,

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    vint8m1_t v3, vint8m1_t v4, size_t vl);
void vsoxseg6ei8_v_i8m1_m (vbool8_t mask, int8_t *base,
    vuint8m1_t bindex, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2,
    vint8m1_t v3, vint8m1_t v4, vint8m1_t v5, size_t vl);
void vsoxseg7ei8_v_i8m1_m (vbool8_t mask, int8_t *base,
    vuint8m1_t bindex, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2,
    vint8m1_t v3, vint8m1_t v4, vint8m1_t v5, vint8m1_t v6,
    size_t vl);
void vsoxseg8ei8_v_i8m1_m (vbool8_t mask, int8_t *base,
    vuint8m1_t bindex, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2,
    vint8m1_t v3, vint8m1_t v4, vint8m1_t v5, vint8m1_t v6,
    vint8m1_t v7, size_t vl);
void vsoxseg2ei8_v_i8m2_m (vbool4_t mask, int8_t *base,
    vuint8m2_t bindex, vint8m2_t v0, vint8m2_t v1, size_t vl);
void vsoxseg3ei8_v_i8m2_m (vbool4_t mask, int8_t *base,
    vuint8m2_t bindex, vint8m2_t v0, vint8m2_t v1, vint8m2_t v2,
    size_t vl);
void vsoxseg4ei8_v_i8m2_m (vbool4_t mask, int8_t *base,
    vuint8m2_t bindex, vint8m2_t v0, vint8m2_t v1, vint8m2_t v2,
    vint8m2_t v3, size_t vl);
void vsoxseg2ei8_v_i8m4_m (vbool2_t mask, int8_t *base,
    vuint8m4_t bindex, vint8m4_t v0, vint8m4_t v1, size_t vl);
void vsoxseg2ei16_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t bindex, vint16m1_t v0, vint16m1_t v1, size_t vl);
void vsoxseg3ei16_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t bindex, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, size_t vl);
void vsoxseg4ei16_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t bindex, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, size_t vl);
void vsoxseg5ei16_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t bindex, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, size_t vl);
void vsoxseg6ei16_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t bindex, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, size_t vl);
void vsoxseg7ei16_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t bindex, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, vint16m1_t
    v6, size_t vl);
void vsoxseg8ei16_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t bindex, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, vint16m1_t
    v6, vint16m1_t v7, size_t vl);
void vsoxseg2ei16_v_i16m2_m (vbool8_t mask, int16_t *base,
    vuint16m2_t bindex, vint16m2_t v0, vint16m2_t v1, size_t vl);

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void vsortex3ei16_v_i16m2_m (vbool8_t mask, int16_t *base,
    vuint16m2_t bindex, vint16m2_t v0, vint16m2_t v1, vint16m2_t
    v2, size_t vl);
void vsortex4ei16_v_i16m2_m (vbool8_t mask, int16_t *base,
    vuint16m2_t bindex, vint16m2_t v0, vint16m2_t v1, vint16m2_t
    v2, vint16m2_t v3, size_t vl);
void vsortex2ei16_v_i16m4_m (vbool4_t mask, int16_t *base,
    vuint16m4_t bindex, vint16m4_t v0, vint16m4_t v1, size_t vl);
void vsortex2ei32_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t bindex, vint32m1_t v0, vint32m1_t v1, size_t vl);
void vsortex3ei32_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t bindex, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, size_t vl);
void vsortex4ei32_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t bindex, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, size_t vl);
void vsortex5ei32_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t bindex, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, size_t vl);
void vsortex6ei32_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t bindex, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, size_t vl);
void vsortex7ei32_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t bindex, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, vint32m1_t
    v6, size_t vl);
void vsortex8ei32_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t bindex, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, vint32m1_t
    v6, vint32m1_t v7, size_t vl);
void vsortex2ei32_v_i32m2_m (vbool16_t mask, int32_t *base,
    vuint32m2_t bindex, vint32m2_t v0, vint32m2_t v1, size_t vl);
void vsortex3ei32_v_i32m2_m (vbool16_t mask, int32_t *base,
    vuint32m2_t bindex, vint32m2_t v0, vint32m2_t v1, vint32m2_t
    v2, size_t vl);
void vsortex4ei32_v_i32m2_m (vbool16_t mask, int32_t *base,
    vuint32m2_t bindex, vint32m2_t v0, vint32m2_t v1, vint32m2_t
    v2, vint32m2_t v3, size_t vl);
void vsortex2ei32_v_i32m4_m (vbool8_t mask, int32_t *base,
    vuint32m4_t bindex, vint32m4_t v0, vint32m4_t v1, size_t vl);
void vsortex2ei64_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t bindex, vint64m1_t v0, vint64m1_t v1, size_t vl);
void vsortex3ei64_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t bindex, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, size_t vl);

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void vsoxseg4ei64_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t bindex, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, size_t vl);
void vsoxseg5ei64_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t bindex, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, size_t vl);
void vsoxseg6ei64_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t bindex, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, size_t vl);
void vsoxseg7ei64_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t bindex, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, vint64m1_t
    v6, size_t vl);
void vsoxseg8ei64_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t bindex, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, vint64m1_t
    v6, vint64m1_t v7, size_t vl);
void vsoxseg2ei64_v_i64m2_m (vbool32_t mask, int64_t *base,
    vuint64m2_t bindex, vint64m2_t v0, vint64m2_t v1, size_t vl);
void vsoxseg3ei64_v_i64m2_m (vbool32_t mask, int64_t *base,
    vuint64m2_t bindex, vint64m2_t v0, vint64m2_t v1, vint64m2_t
    v2, size_t vl);
void vsoxseg4ei64_v_i64m2_m (vbool32_t mask, int64_t *base,
    vuint64m2_t bindex, vint64m2_t v0, vint64m2_t v1, vint64m2_t
    v2, vint64m2_t v3, size_t vl);
void vsoxseg2ei64_v_i64m4_m (vbool16_t mask, int64_t *base,
    vuint64m4_t bindex, vint64m4_t v0, vint64m4_t v1, size_t vl);
void vsoxseg2ei8_v_u8m1_m (vbool8_t mask, uint8_t *base,
    vuint8m1_t bindex, vuint8m1_t v0, vuint8m1_t v1, size_t vl);
void vsoxseg3ei8_v_u8m1_m (vbool8_t mask, uint8_t *base,
    vuint8m1_t bindex, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t
    v2, size_t vl);
void vsoxseg4ei8_v_u8m1_m (vbool8_t mask, uint8_t *base,
    vuint8m1_t bindex, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t
    v2, vuint8m1_t v3, size_t vl);
void vsoxseg5ei8_v_u8m1_m (vbool8_t mask, uint8_t *base,
    vuint8m1_t bindex, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t
    v2, vuint8m1_t v3, vuint8m1_t v4, size_t vl);
void vsoxseg6ei8_v_u8m1_m (vbool8_t mask, uint8_t *base,
    vuint8m1_t bindex, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t
    v2, vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t v5, size_t vl);
void vsoxseg7ei8_v_u8m1_m (vbool8_t mask, uint8_t *base,
    vuint8m1_t bindex, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t
    v2, vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t v5, vuint8m1_t
    v6, size_t vl);

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void vsoxseg8ei8_v_u8m1_m (vbool8_t mask, uint8_t *base,
    vuint8m1_t bindex, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t
    v2, vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t v5, vuint8m1_t
    v6, vuint8m1_t v7, size_t vl);
void vsoxseg2ei8_v_u8m2_m (vbool4_t mask, uint8_t *base,
    vuint8m2_t bindex, vuint8m2_t v0, vuint8m2_t v1, size_t vl);
void vsoxseg3ei8_v_u8m2_m (vbool4_t mask, uint8_t *base,
    vuint8m2_t bindex, vuint8m2_t v0, vuint8m2_t v1, vuint8m2_t
    v2, size_t vl);
void vsoxseg4ei8_v_u8m2_m (vbool4_t mask, uint8_t *base,
    vuint8m2_t bindex, vuint8m2_t v0, vuint8m2_t v1, vuint8m2_t
    v2, vuint8m2_t v3, size_t vl);
void vsoxseg2ei8_v_u8m4_m (vbool2_t mask, uint8_t *base,
    vuint8m4_t bindex, vuint8m4_t v0, vuint8m4_t v1, size_t vl);
void vsoxseg2ei16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t bindex, vuint16m1_t v0, vuint16m1_t v1, size_t
    vl);
void vsoxseg3ei16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t bindex, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, size_t vl);
void vsoxseg4ei16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t bindex, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, size_t vl);
void vsoxseg5ei16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t bindex, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, size_t vl);
void vsoxseg6ei16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t bindex, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
    v5, size_t vl);
void vsoxseg7ei16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t bindex, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
    v5, vuint16m1_t v6, size_t vl);
void vsoxseg8ei16_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t bindex, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
    v5, vuint16m1_t v6, vuint16m1_t v7, size_t vl);
void vsoxseg2ei16_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t bindex, vuint16m2_t v0, vuint16m2_t v1, size_t
    vl);
void vsoxseg3ei16_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t bindex, vuint16m2_t v0, vuint16m2_t v1,
    vuint16m2_t v2, size_t vl);
void vsoxseg4ei16_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t bindex, vuint16m2_t v0, vuint16m2_t v1,

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    vuint16m2_t v2, vuint16m2_t v3, size_t vl);
void vsortex2ei16_v_u16m4_m (vbool4_t mask, uint16_t *base,
    vuint16m4_t bindex, vuint16m4_t v0, vuint16m4_t v1, size_t
    vl);
void vsortex2ei32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t bindex, vuint32m1_t v0, vuint32m1_t v1, size_t
    vl);
void vsortex3ei32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t bindex, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, size_t vl);
void vsortex4ei32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t bindex, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, size_t vl);
void vsortex5ei32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t bindex, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, size_t vl);
void vsortex6ei32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t bindex, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, size_t vl);
void vsortex7ei32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t bindex, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, vuint32m1_t v6, size_t vl);
void vsortex8ei32_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t bindex, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, vuint32m1_t v6, vuint32m1_t v7, size_t vl);
void vsortex2ei32_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t bindex, vuint32m2_t v0, vuint32m2_t v1, size_t
    vl);
void vsortex3ei32_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t bindex, vuint32m2_t v0, vuint32m2_t v1,
    vuint32m2_t v2, size_t vl);
void vsortex4ei32_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t bindex, vuint32m2_t v0, vuint32m2_t v1,
    vuint32m2_t v2, vuint32m2_t v3, size_t vl);
void vsortex2ei32_v_u32m4_m (vbool8_t mask, uint32_t *base,
    vuint32m4_t bindex, vuint32m4_t v0, vuint32m4_t v1, size_t
    vl);
void vsortex2ei64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t bindex, vuint64m1_t v0, vuint64m1_t v1, size_t
    vl);
void vsortex3ei64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t bindex, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, size_t vl);

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void vsoxseg4ei64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t bindex, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, size_t vl);
void vsoxseg5ei64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t bindex, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, size_t vl);
void vsoxseg6ei64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t bindex, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, size_t vl);
void vsoxseg7ei64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t bindex, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, vuint64m1_t v6, size_t vl);
void vsoxseg8ei64_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t bindex, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, vuint64m1_t v6, vuint64m1_t v7, size_t vl);
void vsoxseg2ei64_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t bindex, vuint64m2_t v0, vuint64m2_t v1, size_t
    vl);
void vsoxseg3ei64_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t bindex, vuint64m2_t v0, vuint64m2_t v1,
    vuint64m2_t v2, size_t vl);
void vsoxseg4ei64_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t bindex, vuint64m2_t v0, vuint64m2_t v1,
    vuint64m2_t v2, vuint64m2_t v3, size_t vl);
void vsoxseg2ei64_v_u64m4_m (vbool16_t mask, uint64_t *base,
    vuint64m4_t bindex, vuint64m4_t v0, vuint64m4_t v1, size_t
    vl);
void vsoxseg2ei16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vuint16m1_t bindex, vfloat16m1_t v0, vfloat16m1_t v1, size_t
    vl);
void vsoxseg3ei16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vuint16m1_t bindex, vfloat16m1_t v0, vfloat16m1_t v1,
    vfloat16m1_t v2, size_t vl);
void vsoxseg4ei16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vuint16m1_t bindex, vfloat16m1_t v0, vfloat16m1_t v1,
    vfloat16m1_t v2, vfloat16m1_t v3, size_t vl);
void vsoxseg5ei16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vuint16m1_t bindex, vfloat16m1_t v0, vfloat16m1_t v1,
    vfloat16m1_t v2, vfloat16m1_t v3, vfloat16m1_t v4, size_t
    vl);
void vsoxseg6ei16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vuint16m1_t bindex, vfloat16m1_t v0, vfloat16m1_t v1,
    vfloat16m1_t v2, vfloat16m1_t v3, vfloat16m1_t v4,

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    vfloat16m1_t v5, size_t vl);
void vsxseg7ei16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vuint16m1_t bindex, vfloat16m1_t v0, vfloat16m1_t v1,
    vfloat16m1_t v2, vfloat16m1_t v3, vfloat16m1_t v4,
    vfloat16m1_t v5, vfloat16m1_t v6, size_t vl);
void vsxseg8ei16_v_f16m1_m (vbool16_t mask, float16_t *base,
    vuint16m1_t bindex, vfloat16m1_t v0, vfloat16m1_t v1,
    vfloat16m1_t v2, vfloat16m1_t v3, vfloat16m1_t v4,
    vfloat16m1_t v5, vfloat16m1_t v6, vfloat16m1_t v7, size_t
    vl);
void vsxseg2ei16_v_f16m2_m (vbool8_t mask, float16_t *base,
    vuint16m2_t bindex, vfloat16m2_t v0, vfloat16m2_t v1, size_t
    vl);
void vsxseg3ei16_v_f16m2_m (vbool8_t mask, float16_t *base,
    vuint16m2_t bindex, vfloat16m2_t v0, vfloat16m2_t v1,
    vfloat16m2_t v2, size_t vl);
void vsxseg4ei16_v_f16m2_m (vbool8_t mask, float16_t *base,
    vuint16m2_t bindex, vfloat16m2_t v0, vfloat16m2_t v1,
    vfloat16m2_t v2, vfloat16m2_t v3, size_t vl);
void vsxseg2ei16_v_f16m4_m (vbool4_t mask, float16_t *base,
    vuint16m4_t bindex, vfloat16m4_t v0, vfloat16m4_t v1, size_t
    vl);
void vsxseg2ei32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vuint32m1_t bindex, vfloat32m1_t v0, vfloat32m1_t v1, size_t
    vl);
void vsxseg3ei32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vuint32m1_t bindex, vfloat32m1_t v0, vfloat32m1_t v1,
    vfloat32m1_t v2, size_t vl);
void vsxseg4ei32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vuint32m1_t bindex, vfloat32m1_t v0, vfloat32m1_t v1,
    vfloat32m1_t v2, vfloat32m1_t v3, size_t vl);
void vsxseg5ei32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vuint32m1_t bindex, vfloat32m1_t v0, vfloat32m1_t v1,
    vfloat32m1_t v2, vfloat32m1_t v3, vfloat32m1_t v4, size_t
    vl);
void vsxseg6ei32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vuint32m1_t bindex, vfloat32m1_t v0, vfloat32m1_t v1,
    vfloat32m1_t v2, vfloat32m1_t v3, vfloat32m1_t v4,
    vfloat32m1_t v5, size_t vl);
void vsxseg7ei32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vuint32m1_t bindex, vfloat32m1_t v0, vfloat32m1_t v1,
    vfloat32m1_t v2, vfloat32m1_t v3, vfloat32m1_t v4,
    vfloat32m1_t v5, vfloat32m1_t v6, size_t vl);
void vsxseg8ei32_v_f32m1_m (vbool32_t mask, float32_t *base,
    vuint32m1_t bindex, vfloat32m1_t v0, vfloat32m1_t v1,
    vfloat32m1_t v2, vfloat32m1_t v3, vfloat32m1_t v4,

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    vfloat32m1_t v5, vfloat32m1_t v6, vfloat32m1_t v7, size_t
    vl);
void vsortex2ei32_v_f32m2_m (vbool16_t mask, float32_t *base,
    vuint32m2_t bindex, vfloat32m2_t v0, vfloat32m2_t v1, size_t
    vl);
void vsortex3ei32_v_f32m2_m (vbool16_t mask, float32_t *base,
    vuint32m2_t bindex, vfloat32m2_t v0, vfloat32m2_t v1,
    vfloat32m2_t v2, size_t vl);
void vsortex4ei32_v_f32m2_m (vbool16_t mask, float32_t *base,
    vuint32m2_t bindex, vfloat32m2_t v0, vfloat32m2_t v1,
    vfloat32m2_t v2, vfloat32m2_t v3, size_t vl);
void vsortex2ei32_v_f32m4_m (vbool8_t mask, float32_t *base,
    vuint32m4_t bindex, vfloat32m4_t v0, vfloat32m4_t v1, size_t
    vl);
void vsortex2ei64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vuint64m1_t bindex, vfloat64m1_t v0, vfloat64m1_t v1, size_t
    vl);
void vsortex3ei64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vuint64m1_t bindex, vfloat64m1_t v0, vfloat64m1_t v1,
    vfloat64m1_t v2, size_t vl);
void vsortex4ei64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vuint64m1_t bindex, vfloat64m1_t v0, vfloat64m1_t v1,
    vfloat64m1_t v2, vfloat64m1_t v3, size_t vl);
void vsortex5ei64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vuint64m1_t bindex, vfloat64m1_t v0, vfloat64m1_t v1,
    vfloat64m1_t v2, vfloat64m1_t v3, vfloat64m1_t v4, size_t
    vl);
void vsortex6ei64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vuint64m1_t bindex, vfloat64m1_t v0, vfloat64m1_t v1,
    vfloat64m1_t v2, vfloat64m1_t v3, vfloat64m1_t v4,
    vfloat64m1_t v5, size_t vl);
void vsortex7ei64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vuint64m1_t bindex, vfloat64m1_t v0, vfloat64m1_t v1,
    vfloat64m1_t v2, vfloat64m1_t v3, vfloat64m1_t v4,
    vfloat64m1_t v5, vfloat64m1_t v6, size_t vl);
void vsortex8ei64_v_f64m1_m (vbool64_t mask, float64_t *base,
    vuint64m1_t bindex, vfloat64m1_t v0, vfloat64m1_t v1,
    vfloat64m1_t v2, vfloat64m1_t v3, vfloat64m1_t v4,
    vfloat64m1_t v5, vfloat64m1_t v6, vfloat64m1_t v7, size_t
    vl);
void vsortex2ei64_v_f64m2_m (vbool32_t mask, float64_t *base,
    vuint64m2_t bindex, vfloat64m2_t v0, vfloat64m2_t v1, size_t
    vl);
void vsortex3ei64_v_f64m2_m (vbool32_t mask, float64_t *base,
    vuint64m2_t bindex, vfloat64m2_t v0, vfloat64m2_t v1,
    vfloat64m2_t v2, size_t vl);

```

```

void vsoxseg4ei64_v_f64m2_m (vbool32_t mask, float64_t *base,
    vuint64m2_t bindex, vfloat64m2_t v0, vfloat64m2_t v1,
    vfloat64m2_t v2, vfloat64m2_t v3, size_t vl);
void vsoxseg2ei64_v_f64m4_m (vbool16_t mask, float64_t *base,
    vuint64m4_t bindex, vfloat64m4_t v0, vfloat64m4_t v1, size_t
    vl);

```

## Vector Indexed Segment 16b Loads and Stores:

### Prototypes:

```

void vlxseg2h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, const
    int16_t *base, vuint16m1_t index, size_t vl);
void vlxseg2h_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1, const
    int16_t *base, vuint16m2_t index, size_t vl);
void vlxseg2h_v_i16m4 (vint16m4_t *v0, vint16m4_t *v1, const
    int16_t *base, vuint16m4_t index, size_t vl);
void vlxseg2h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, const
    int32_t *base, vuint32m1_t index, size_t vl);
void vlxseg2h_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, const
    int32_t *base, vuint32m2_t index, size_t vl);
void vlxseg2h_v_i32m4 (vint32m4_t *v0, vint32m4_t *v1, const
    int32_t *base, vuint32m4_t index, size_t vl);
void vlxseg2h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, const
    int64_t *base, vuint64m1_t index, size_t vl);
void vlxseg2h_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, const
    int64_t *base, vuint64m2_t index, size_t vl);
void vlxseg2h_v_i64m4 (vint64m4_t *v0, vint64m4_t *v1, const
    int64_t *base, vuint64m4_t index, size_t vl);
void vlxseg2hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1, const
    uint16_t *base, vuint16m1_t index, size_t vl);
void vlxseg2hu_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1, const
    uint16_t *base, vuint16m2_t index, size_t vl);
void vlxseg2hu_v_u16m4 (vuint16m4_t *v0, vuint16m4_t *v1, const
    uint16_t *base, vuint16m4_t index, size_t vl);
void vlxseg2hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1, const
    uint32_t *base, vuint32m1_t index, size_t vl);
void vlxseg2hu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1, const
    uint32_t *base, vuint32m2_t index, size_t vl);
void vlxseg2hu_v_u32m4 (vuint32m4_t *v0, vuint32m4_t *v1, const
    uint32_t *base, vuint32m4_t index, size_t vl);
void vlxseg2hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1, const
    uint64_t *base, vuint64m1_t index, size_t vl);
void vlxseg2hu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1, const
    uint64_t *base, vuint64m2_t index, size_t vl);

```

```

void vlxseg2hu_v_u64m4 (vuint64m4_t *v0, vuint64m4_t *v1, const
    uint64_t *base, vuint64m4_t index, size_t vl);
void vlxseg3h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, const int16_t *base, vuint16m1_t index,
    size_t vl);
void vlxseg3h_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, const int16_t *base, vuint16m2_t index,
    size_t vl);
void vlxseg3h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, const int32_t *base, vuint32m1_t index,
    size_t vl);
void vlxseg3h_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, const int32_t *base, vuint32m2_t index,
    size_t vl);
void vlxseg3h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, const int64_t *base, vuint64m1_t index,
    size_t vl);
void vlxseg3h_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, const int64_t *base, vuint64m2_t index,
    size_t vl);
void vlxseg3hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, const uint16_t *base, vuint16m1_t index,
    size_t vl);
void vlxseg3hu_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, const uint16_t *base, vuint16m2_t index,
    size_t vl);
void vlxseg3hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, const uint32_t *base, vuint32m1_t index,
    size_t vl);
void vlxseg3hu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, const uint32_t *base, vuint32m2_t index,
    size_t vl);
void vlxseg3hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, const uint64_t *base, vuint64m1_t index,
    size_t vl);
void vlxseg3hu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, const uint64_t *base, vuint64m2_t index,
    size_t vl);
void vlxseg4h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, const int16_t *base,
    vuint16m1_t index, size_t vl);
void vlxseg4h_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vint16m2_t *v3, const int16_t *base,
    vuint16m2_t index, size_t vl);
void vlxseg4h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, const int32_t *base,

```

```

    vuint32m1_t index, size_t vl);
void vlxseg4h_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, const int32_t *base,
    vuint32m2_t index, size_t vl);
void vlxseg4h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, const int64_t *base,
    vuint64m1_t index, size_t vl);
void vlxseg4h_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, const int64_t *base,
    vuint64m2_t index, size_t vl);
void vlxseg4hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, const uint16_t *base,
    vuint16m1_t index, size_t vl);
void vlxseg4hu_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vuint16m2_t *v3, const uint16_t *base,
    vuint16m2_t index, size_t vl);
void vlxseg4hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, const uint32_t *base,
    vuint32m1_t index, size_t vl);
void vlxseg4hu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, const uint32_t *base,
    vuint32m2_t index, size_t vl);
void vlxseg4hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, const uint64_t *base,
    vuint64m1_t index, size_t vl);
void vlxseg4hu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, const uint64_t *base,
    vuint64m2_t index, size_t vl);
void vlxseg5h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, const
    int16_t *base, vuint16m1_t index, size_t vl);
void vlxseg5h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, const
    int32_t *base, vuint32m1_t index, size_t vl);
void vlxseg5h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, const
    int64_t *base, vuint64m1_t index, size_t vl);
void vlxseg5hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, const
    uint16_t *base, vuint16m1_t index, size_t vl);
void vlxseg5hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, const
    uint32_t *base, vuint32m1_t index, size_t vl);
void vlxseg5hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, const
    uint64_t *base, vuint64m1_t index, size_t vl);

```

```

void vlxseg6h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, const int16_t *base, vuint16m1_t index, size_t vl);
void vlxseg6h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, const int32_t *base, vuint32m1_t index, size_t vl);
void vlxseg6h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, const int64_t *base, vuint64m1_t index, size_t vl);
void vlxseg6hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, const uint16_t *base, vuint16m1_t index,
    size_t vl);
void vlxseg6hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, const uint32_t *base, vuint32m1_t index,
    size_t vl);
void vlxseg6hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, const uint64_t *base, vuint64m1_t index,
    size_t vl);
void vlxseg7h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, const int16_t *base, vuint16m1_t index,
    size_t vl);
void vlxseg7h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, const int32_t *base, vuint32m1_t index,
    size_t vl);
void vlxseg7h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, const int64_t *base, vuint64m1_t index,
    size_t vl);
void vlxseg7hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, const uint16_t *base,
    vuint16m1_t index, size_t vl);
void vlxseg7hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, const uint32_t *base,
    vuint32m1_t index, size_t vl);
void vlxseg7hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, const uint64_t *base,
    vuint64m1_t index, size_t vl);

```



```

void vlxseg8h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vint16m1_t *v6, vint16m1_t *v7, const int16_t *base,
vuint16m1_t index, size_t vl);
void vlxseg8h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
*v5, vint32m1_t *v6, vint32m1_t *v7, const int32_t *base,
vuint32m1_t index, size_t vl);
void vlxseg8h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
*v5, vint64m1_t *v6, vint64m1_t *v7, const int64_t *base,
vuint64m1_t index, size_t vl);
void vlxseg8hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, const
uint16_t *base, vuint16m1_t index, size_t vl);
void vlxseg8hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, const
uint32_t *base, vuint32m1_t index, size_t vl);
void vlxseg8hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, const
uint64_t *base, vuint64m1_t index, size_t vl);
void vsxseg2h_v_i16m1 (int16_t *base, vuint16m1_t index,
vint16m1_t v0, vint16m1_t v1, size_t vl);
void vsxseg2h_v_i16m2 (int16_t *base, vuint16m2_t index,
vint16m2_t v0, vint16m2_t v1, size_t vl);
void vsxseg2h_v_i16m4 (int16_t *base, vuint16m4_t index,
vint16m4_t v0, vint16m4_t v1, size_t vl);
void vsxseg2h_v_i32m1 (int32_t *base, vuint32m1_t index,
vint32m1_t v0, vint32m1_t v1, size_t vl);
void vsxseg2h_v_i32m2 (int32_t *base, vuint32m2_t index,
vint32m2_t v0, vint32m2_t v1, size_t vl);
void vsxseg2h_v_i32m4 (int32_t *base, vuint32m4_t index,
vint32m4_t v0, vint32m4_t v1, size_t vl);
void vsxseg2h_v_i64m1 (int64_t *base, vuint64m1_t index,
vint64m1_t v0, vint64m1_t v1, size_t vl);
void vsxseg2h_v_i64m2 (int64_t *base, vuint64m2_t index,
vint64m2_t v0, vint64m2_t v1, size_t vl);
void vsxseg2h_v_i64m4 (int64_t *base, vuint64m4_t index,
vint64m4_t v0, vint64m4_t v1, size_t vl);
void vsxseg2h_v_u16m1 (uint16_t *base, vuint16m1_t index,
vuint16m1_t v0, vuint16m1_t v1, size_t vl);
void vsxseg2h_v_u16m2 (uint16_t *base, vuint16m2_t index,
vuint16m2_t v0, vuint16m2_t v1, size_t vl);

```

```

void vsxseg2h_v_u16m4 (uint16_t *base, vuint16m4_t index,
    vuint16m4_t v0, vuint16m4_t v1, size_t vl);
void vsxseg2h_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, size_t vl);
void vsxseg2h_v_u32m2 (uint32_t *base, vuint32m2_t index,
    vuint32m2_t v0, vuint32m2_t v1, size_t vl);
void vsxseg2h_v_u32m4 (uint32_t *base, vuint32m4_t index,
    vuint32m4_t v0, vuint32m4_t v1, size_t vl);
void vsxseg2h_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, size_t vl);
void vsxseg2h_v_u64m2 (uint64_t *base, vuint64m2_t index,
    vuint64m2_t v0, vuint64m2_t v1, size_t vl);
void vsxseg2h_v_u64m4 (uint64_t *base, vuint64m4_t index,
    vuint64m4_t v0, vuint64m4_t v1, size_t vl);
void vsxseg3h_v_i16m1 (int16_t *base, vuint16m1_t index,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, size_t vl);
void vsxseg3h_v_i16m2 (int16_t *base, vuint16m2_t index,
    vint16m2_t v0, vint16m2_t v1, vint16m2_t v2, size_t vl);
void vsxseg3h_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, size_t vl);
void vsxseg3h_v_i32m2 (int32_t *base, vuint32m2_t index,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, size_t vl);
void vsxseg3h_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, size_t vl);
void vsxseg3h_v_i64m2 (int64_t *base, vuint64m2_t index,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, size_t vl);
void vsxseg3h_v_u16m1 (uint16_t *base, vuint16m1_t index,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, size_t vl);
void vsxseg3h_v_u16m2 (uint16_t *base, vuint16m2_t index,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, size_t vl);
void vsxseg3h_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, size_t vl);
void vsxseg3h_v_u32m2 (uint32_t *base, vuint32m2_t index,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, size_t vl);
void vsxseg3h_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, size_t vl);
void vsxseg3h_v_u64m2 (uint64_t *base, vuint64m2_t index,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, size_t vl);
void vsxseg4h_v_i16m1 (int16_t *base, vuint16m1_t index,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    size_t vl);
void vsxseg4h_v_i16m2 (int16_t *base, vuint16m2_t index,
    vint16m2_t v0, vint16m2_t v1, vint16m2_t v2, vint16m2_t v3,
    size_t vl);
void vsxseg4h_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,

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    size_t vl);
void vsxseg4h_v_i32m2 (int32_t *base, vuint32m2_t index,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, vint32m2_t v3,
    size_t vl);
void vsxseg4h_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    size_t vl);
void vsxseg4h_v_i64m2 (int64_t *base, vuint64m2_t index,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, vint64m2_t v3,
    size_t vl);
void vsxseg4h_v_u16m1 (uint16_t *base, vuint16m1_t index,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, size_t vl);
void vsxseg4h_v_u16m2 (uint16_t *base, vuint16m2_t index,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, vuint16m2_t
    v3, size_t vl);
void vsxseg4h_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, size_t vl);
void vsxseg4h_v_u32m2 (uint32_t *base, vuint32m2_t index,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, vuint32m2_t
    v3, size_t vl);
void vsxseg4h_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, size_t vl);
void vsxseg4h_v_u64m2 (uint64_t *base, vuint64m2_t index,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, vuint64m2_t
    v3, size_t vl);
void vsxseg5h_v_i16m1 (int16_t *base, vuint16m1_t index,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, size_t vl);
void vsxseg5h_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, size_t vl);
void vsxseg5h_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, size_t vl);
void vsxseg5h_v_u16m1 (uint16_t *base, vuint16m1_t index,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, size_t vl);
void vsxseg5h_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, size_t vl);
void vsxseg5h_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, size_t vl);

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void vsxseg6h_v_i16m1 (int16_t *base, vuint16m1_t index,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, size_t vl);
void vsxseg6h_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, size_t vl);
void vsxseg6h_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, size_t vl);
void vsxseg6h_v_u16m1 (uint16_t *base, vuint16m1_t index,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, size_t vl);
void vsxseg6h_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, size_t vl);
void vsxseg6h_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, size_t vl);
void vsxseg7h_v_i16m1 (int16_t *base, vuint16m1_t index,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, size_t vl);
void vsxseg7h_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, size_t vl);
void vsxseg7h_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, size_t vl);
void vsxseg7h_v_u16m1 (uint16_t *base, vuint16m1_t index,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6, size_t
    vl);
void vsxseg7h_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6, size_t
    vl);
void vsxseg7h_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6, size_t
    vl);
void vsxseg8h_v_i16m1 (int16_t *base, vuint16m1_t index,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, vint16m1_t v7,
    size_t vl);
void vsxseg8h_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, vint32m1_t v7,

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    size_t vl);
void vsxseg8h_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, vint64m1_t v7,
    size_t vl);
void vsxseg8h_v_u16m1 (uint16_t *base, vuint16m1_t index,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6,
    vuint16m1_t v7, size_t vl);
void vsxseg8h_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6,
    vuint32m1_t v7, size_t vl);
void vsxseg8h_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6,
    vuint64m1_t v7, size_t vl);
// masked functions
void vlxseg2h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
    maskedoff1, const int16_t *base, vuint16m1_t index, size_t
    vl);
void vlxseg2h_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
    vbool8_t mask, vint16m2_t maskedoff0, vint16m2_t maskedoff1,
    const int16_t *base, vuint16m2_t index, size_t vl);
void vlxseg2h_v_i16m4_m (vint16m4_t *v0, vint16m4_t *v1,
    vbool4_t mask, vint16m4_t maskedoff0, vint16m4_t maskedoff1,
    const int16_t *base, vuint16m4_t index, size_t vl);
void vlxseg2h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, const int32_t *base, vuint32m1_t index, size_t
    vl);
void vlxseg2h_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vbool16_t mask, vint32m2_t maskedoff0, vint32m2_t
    maskedoff1, const int32_t *base, vuint32m2_t index, size_t
    vl);
void vlxseg2h_v_i32m4_m (vint32m4_t *v0, vint32m4_t *v1,
    vbool8_t mask, vint32m4_t maskedoff0, vint32m4_t maskedoff1,
    const int32_t *base, vuint32m4_t index, size_t vl);
void vlxseg2h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, const int64_t *base, vuint64m1_t index, size_t
    vl);
void vlxseg2h_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vbool32_t mask, vint64m2_t maskedoff0, vint64m2_t
    maskedoff1, const int64_t *base, vuint64m2_t index, size_t

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    vl);
void vlxseg2h_v_i64m4_m (vint64m4_t *v0, vint64m4_t *v1,
    vbool16_t mask, vint64m4_t maskedoff0, vint64m4_t
    maskedoff1, const int64_t *base, vuint64m4_t index, size_t
    vl);
void vlxseg2hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vbool16_t mask, vuint16m1_t maskedoff0, vuint16m1_t
    maskedoff1, const uint16_t *base, vuint16m1_t index, size_t
    vl);
void vlxseg2hu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vbool8_t mask, vuint16m2_t maskedoff0, vuint16m2_t
    maskedoff1, const uint16_t *base, vuint16m2_t index, size_t
    vl);
void vlxseg2hu_v_u16m4_m (vuint16m4_t *v0, vuint16m4_t *v1,
    vbool4_t mask, vuint16m4_t maskedoff0, vuint16m4_t
    maskedoff1, const uint16_t *base, vuint16m4_t index, size_t
    vl);
void vlxseg2hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vbool32_t mask, vuint32m1_t maskedoff0, vuint32m1_t
    maskedoff1, const uint32_t *base, vuint32m1_t index, size_t
    vl);
void vlxseg2hu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vbool16_t mask, vuint32m2_t maskedoff0, vuint32m2_t
    maskedoff1, const uint32_t *base, vuint32m2_t index, size_t
    vl);
void vlxseg2hu_v_u32m4_m (vuint32m4_t *v0, vuint32m4_t *v1,
    vbool8_t mask, vuint32m4_t maskedoff0, vuint32m4_t
    maskedoff1, const uint32_t *base, vuint32m4_t index, size_t
    vl);
void vlxseg2hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vbool64_t mask, vuint64m1_t maskedoff0, vuint64m1_t
    maskedoff1, const uint64_t *base, vuint64m1_t index, size_t
    vl);
void vlxseg2hu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vbool32_t mask, vuint64m2_t maskedoff0, vuint64m2_t
    maskedoff1, const uint64_t *base, vuint64m2_t index, size_t
    vl);
void vlxseg2hu_v_u64m4_m (vuint64m4_t *v0, vuint64m4_t *v1,
    vbool16_t mask, vuint64m4_t maskedoff0, vuint64m4_t
    maskedoff1, const uint64_t *base, vuint64m4_t index, size_t
    vl);
void vlxseg3h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vbool16_t mask, vint16m1_t maskedoff0,
    vint16m1_t maskedoff1, vint16m1_t maskedoff2, const int16_t
    *base, vuint16m1_t index, size_t vl);

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void vlxseg3h_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vbool8_t mask, vint16m2_t maskedoff0,
    vint16m2_t maskedoff1, vint16m2_t maskedoff2, const int16_t
    *base, vuint16m2_t index, size_t vl);
void vlxseg3h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, const int32_t
    *base, vuint32m1_t index, size_t vl);
void vlxseg3h_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vbool16_t mask, vint32m2_t maskedoff0,
    vint32m2_t maskedoff1, vint32m2_t maskedoff2, const int32_t
    *base, vuint32m2_t index, size_t vl);
void vlxseg3h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, const int64_t
    *base, vuint64m1_t index, size_t vl);
void vlxseg3h_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vbool32_t mask, vint64m2_t maskedoff0,
    vint64m2_t maskedoff1, vint64m2_t maskedoff2, const int64_t
    *base, vuint64m2_t index, size_t vl);
void vlxseg3hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vbool16_t mask, vuint16m1_t maskedoff0,
    vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, const
    uint16_t *base, vuint16m1_t index, size_t vl);
void vlxseg3hu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vbool8_t mask, vuint16m2_t maskedoff0,
    vuint16m2_t maskedoff1, vuint16m2_t maskedoff2, const
    uint16_t *base, vuint16m2_t index, size_t vl);
void vlxseg3hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, const
    uint32_t *base, vuint32m1_t index, size_t vl);
void vlxseg3hu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vbool16_t mask, vuint32m2_t maskedoff0,
    vuint32m2_t maskedoff1, vuint32m2_t maskedoff2, const
    uint32_t *base, vuint32m2_t index, size_t vl);
void vlxseg3hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, const
    uint64_t *base, vuint64m1_t index, size_t vl);
void vlxseg3hu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vbool32_t mask, vuint64m2_t maskedoff0,
    vuint64m2_t maskedoff1, vuint64m2_t maskedoff2, const
    uint64_t *base, vuint64m2_t index, size_t vl);
void vlxseg4h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vbool16_t mask, vint16m1_t

```

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maskedoff0, vint16m1_t maskedoff1, vint16m1_t maskedoff2,
vint16m1_t maskedoff3, const int16_t *base, vuint16m1_t
index, size_t vl);
void vlxseg4h_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
vint16m2_t *v2, vint16m2_t *v3, vbool8_t mask, vint16m2_t
maskedoff0, vint16m2_t maskedoff1, vint16m2_t maskedoff2,
vint16m2_t maskedoff3, const int16_t *base, vuint16m2_t
index, size_t vl);
void vlxseg4h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vbool32_t mask, vint32m1_t
maskedoff0, vint32m1_t maskedoff1, vint32m1_t maskedoff2,
vint32m1_t maskedoff3, const int32_t *base, vuint32m1_t
index, size_t vl);
void vlxseg4h_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
vint32m2_t *v2, vint32m2_t *v3, vbool16_t mask, vint32m2_t
maskedoff0, vint32m2_t maskedoff1, vint32m2_t maskedoff2,
vint32m2_t maskedoff3, const int32_t *base, vuint32m2_t
index, size_t vl);
void vlxseg4h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vbool64_t mask, vint64m1_t
maskedoff0, vint64m1_t maskedoff1, vint64m1_t maskedoff2,
vint64m1_t maskedoff3, const int64_t *base, vuint64m1_t
index, size_t vl);
void vlxseg4h_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
vint64m2_t *v2, vint64m2_t *v3, vbool32_t mask, vint64m2_t
maskedoff0, vint64m2_t maskedoff1, vint64m2_t maskedoff2,
vint64m2_t maskedoff3, const int64_t *base, vuint64m2_t
index, size_t vl);
void vlxseg4hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vbool16_t mask,
vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
maskedoff2, vuint16m1_t maskedoff3, const uint16_t *base,
vuint16m1_t index, size_t vl);
void vlxseg4hu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
vuint16m2_t *v2, vuint16m2_t *v3, vbool8_t mask, vuint16m2_t
maskedoff0, vuint16m2_t maskedoff1, vuint16m2_t maskedoff2,
vuint16m2_t maskedoff3, const uint16_t *base, vuint16m2_t
index, size_t vl);
void vlxseg4hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vbool32_t mask,
vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
maskedoff2, vuint32m1_t maskedoff3, const uint32_t *base,
vuint32m1_t index, size_t vl);
void vlxseg4hu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
vuint32m2_t *v2, vuint32m2_t *v3, vbool16_t mask,
vuint32m2_t maskedoff0, vuint32m2_t maskedoff1, vuint32m2_t

```



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maskedoff2, vuint32m2_t maskedoff3, const uint32_t *base,
vuint32m2_t index, size_t vl);
void vlxseg4hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vbool64_t mask,
vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
maskedoff2, vuint64m1_t maskedoff3, const uint64_t *base,
vuint64m1_t index, size_t vl);
void vlxseg4hu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
vuint64m2_t *v2, vuint64m2_t *v3, vbool32_t mask,
vuint64m2_t maskedoff0, vuint64m2_t maskedoff1, vuint64m2_t
maskedoff2, vuint64m2_t maskedoff3, const uint64_t *base,
vuint64m2_t index, size_t vl);
void vlxseg5h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vbool16_t
mask, vint16m1_t maskedoff0, vint16m1_t maskedoff1,
vint16m1_t maskedoff2, vint16m1_t maskedoff3, vint16m1_t
maskedoff4, const int16_t *base, vuint16m1_t index, size_t
vl);
void vlxseg5h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vbool32_t
mask, vint32m1_t maskedoff0, vint32m1_t maskedoff1,
vint32m1_t maskedoff2, vint32m1_t maskedoff3, vint32m1_t
maskedoff4, const int32_t *base, vuint32m1_t index, size_t
vl);
void vlxseg5h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vbool64_t
mask, vint64m1_t maskedoff0, vint64m1_t maskedoff1,
vint64m1_t maskedoff2, vint64m1_t maskedoff3, vint64m1_t
maskedoff4, const int64_t *base, vuint64m1_t index, size_t
vl);
void vlxseg5hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, vbool16_t
mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
maskedoff4, const uint16_t *base, vuint16m1_t index, size_t
vl);
void vlxseg5hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, vbool32_t
mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
maskedoff4, const uint32_t *base, vuint32m1_t index, size_t
vl);
void vlxseg5hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, vbool64_t
mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t

```

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maskedoff4, const uint64_t *base, vuint64m1_t index, size_t
vl);
void vlxseg6h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
maskedoff1, vint16m1_t maskedoff2, vint16m1_t maskedoff3,
vint16m1_t maskedoff4, vint16m1_t maskedoff5, const int16_t
*base, vuint16m1_t index, size_t vl);
void vlxseg6h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
*v5, vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
maskedoff1, vint32m1_t maskedoff2, vint32m1_t maskedoff3,
vint32m1_t maskedoff4, vint32m1_t maskedoff5, const int32_t
*base, vuint32m1_t index, size_t vl);
void vlxseg6h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
*v5, vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
maskedoff1, vint64m1_t maskedoff2, vint64m1_t maskedoff3,
vint64m1_t maskedoff4, vint64m1_t maskedoff5, const int64_t
*base, vuint64m1_t index, size_t vl);
void vlxseg6hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vbool16_t mask, vuint16m1_t maskedoff0,
vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, vuint16m1_t
maskedoff3, vuint16m1_t maskedoff4, vuint16m1_t maskedoff5,
const uint16_t *base, vuint16m1_t index, size_t vl);
void vlxseg6hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vbool32_t mask, vuint32m1_t maskedoff0,
vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, vuint32m1_t
maskedoff3, vuint32m1_t maskedoff4, vuint32m1_t maskedoff5,
const uint32_t *base, vuint32m1_t index, size_t vl);
void vlxseg6hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
vuint64m1_t *v5, vbool64_t mask, vuint64m1_t maskedoff0,
vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, vuint64m1_t
maskedoff3, vuint64m1_t maskedoff4, vuint64m1_t maskedoff5,
const uint64_t *base, vuint64m1_t index, size_t vl);
void vlxseg7h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vint16m1_t *v6, vbool16_t mask, vint16m1_t maskedoff0,
vint16m1_t maskedoff1, vint16m1_t maskedoff2, vint16m1_t
maskedoff3, vint16m1_t maskedoff4, vint16m1_t maskedoff5,
vint16m1_t maskedoff6, const int16_t *base, vuint16m1_t
index, size_t vl);

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void vlxseg7h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, vint32m1_t
    maskedoff3, vint32m1_t maskedoff4, vint32m1_t maskedoff5,
    vint32m1_t maskedoff6, const int32_t *base, vuint32m1_t
    index, size_t vl);
void vlxseg7h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, vint64m1_t
    maskedoff3, vint64m1_t maskedoff4, vint64m1_t maskedoff5,
    vint64m1_t maskedoff6, const int64_t *base, vuint64m1_t
    index, size_t vl);
void vlxseg7hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, vbool16_t mask,
    vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
    maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t maskedoff4,
    vuint16m1_t maskedoff5, vuint16m1_t maskedoff6, const
    uint16_t *base, vuint16m1_t index, size_t vl);
void vlxseg7hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t maskedoff4,
    vuint32m1_t maskedoff5, vuint32m1_t maskedoff6, const
    uint32_t *base, vuint32m1_t index, size_t vl);
void vlxseg7hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t maskedoff4,
    vuint64m1_t maskedoff5, vuint64m1_t maskedoff6, const
    uint64_t *base, vuint64m1_t index, size_t vl);
void vlxseg8h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, vint16m1_t *v7, vbool16_t mask,
    vint16m1_t maskedoff0, vint16m1_t maskedoff1, vint16m1_t
    maskedoff2, vint16m1_t maskedoff3, vint16m1_t maskedoff4,
    vint16m1_t maskedoff5, vint16m1_t maskedoff6, vint16m1_t
    maskedoff7, const int16_t *base, vuint16m1_t index, size_t
    vl);
void vlxseg8h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, vbool32_t mask,

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vint32m1_t maskedoff0, vint32m1_t maskedoff1, vint32m1_t
maskedoff2, vint32m1_t maskedoff3, vint32m1_t maskedoff4,
vint32m1_t maskedoff5, vint32m1_t maskedoff6, vint32m1_t
maskedoff7, const int32_t *base, vuint32m1_t index, size_t
vl);
void vlxseg8h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
*v5, vint64m1_t *v6, vint64m1_t *v7, vbool64_t mask,
vint64m1_t maskedoff0, vint64m1_t maskedoff1, vint64m1_t
maskedoff2, vint64m1_t maskedoff3, vint64m1_t maskedoff4,
vint64m1_t maskedoff5, vint64m1_t maskedoff6, vint64m1_t
maskedoff7, const int64_t *base, vuint64m1_t index, size_t
vl);
void vlxseg8hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, vbool16_t
mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
maskedoff4, vuint16m1_t maskedoff5, vuint16m1_t maskedoff6,
vuint16m1_t maskedoff7, const uint16_t *base, vuint16m1_t
index, size_t vl);
void vlxseg8hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, vbool32_t
mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
maskedoff4, vuint32m1_t maskedoff5, vuint32m1_t maskedoff6,
vuint32m1_t maskedoff7, const uint32_t *base, vuint32m1_t
index, size_t vl);
void vlxseg8hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, vbool64_t
mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
maskedoff4, vuint64m1_t maskedoff5, vuint64m1_t maskedoff6,
vuint64m1_t maskedoff7, const uint64_t *base, vuint64m1_t
index, size_t vl);
void vsxseg2h_v_i16m1_m (vbool16_t mask, int16_t *base,
vuint16m1_t index, vint16m1_t v0, vint16m1_t v1, size_t vl);
void vsxseg2h_v_i16m2_m (vbool8_t mask, int16_t *base,
vuint16m2_t index, vint16m2_t v0, vint16m2_t v1, size_t vl);
void vsxseg2h_v_i16m4_m (vbool4_t mask, int16_t *base,
vuint16m4_t index, vint16m4_t v0, vint16m4_t v1, size_t vl);
void vsxseg2h_v_i32m1_m (vbool32_t mask, int32_t *base,
vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, size_t vl);

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void vsxseg2h_v_i32m2_m (vbool16_t mask, int32_t *base,
    vuint32m2_t index, vint32m2_t v0, vint32m2_t v1, size_t vl);
void vsxseg2h_v_i32m4_m (vbool8_t mask, int32_t *base,
    vuint32m4_t index, vint32m4_t v0, vint32m4_t v1, size_t vl);
void vsxseg2h_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, size_t vl);
void vsxseg2h_v_i64m2_m (vbool32_t mask, int64_t *base,
    vuint64m2_t index, vint64m2_t v0, vint64m2_t v1, size_t vl);
void vsxseg2h_v_i64m4_m (vbool16_t mask, int64_t *base,
    vuint64m4_t index, vint64m4_t v0, vint64m4_t v1, size_t vl);
void vsxseg2h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t index, vuint16m1_t v0, vuint16m1_t v1, size_t
    vl);
void vsxseg2h_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t index, vuint16m2_t v0, vuint16m2_t v1, size_t
    vl);
void vsxseg2h_v_u16m4_m (vbool4_t mask, uint16_t *base,
    vuint16m4_t index, vuint16m4_t v0, vuint16m4_t v1, size_t
    vl);
void vsxseg2h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1, size_t
    vl);
void vsxseg2h_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t index, vuint32m2_t v0, vuint32m2_t v1, size_t
    vl);
void vsxseg2h_v_u32m4_m (vbool8_t mask, uint32_t *base,
    vuint32m4_t index, vuint32m4_t v0, vuint32m4_t v1, size_t
    vl);
void vsxseg2h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1, size_t
    vl);
void vsxseg2h_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t index, vuint64m2_t v0, vuint64m2_t v1, size_t
    vl);
void vsxseg2h_v_u64m4_m (vbool16_t mask, uint64_t *base,
    vuint64m4_t index, vuint64m4_t v0, vuint64m4_t v1, size_t
    vl);
void vsxseg3h_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t index, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, size_t vl);
void vsxseg3h_v_i16m2_m (vbool8_t mask, int16_t *base,
    vuint16m2_t index, vint16m2_t v0, vint16m2_t v1, vint16m2_t
    v2, size_t vl);
void vsxseg3h_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, size_t vl);

```

```

void vsxseg3h_v_i32m2_m (vbool16_t mask, int32_t *base,
    vuint32m2_t index, vint32m2_t v0, vint32m2_t v1, vint32m2_t
    v2, size_t vl);
void vsxseg3h_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, size_t vl);
void vsxseg3h_v_i64m2_m (vbool32_t mask, int64_t *base,
    vuint64m2_t index, vint64m2_t v0, vint64m2_t v1, vint64m2_t
    v2, size_t vl);
void vsxseg3h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t index, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, size_t vl);
void vsxseg3h_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t index, vuint16m2_t v0, vuint16m2_t v1,
    vuint16m2_t v2, size_t vl);
void vsxseg3h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, size_t vl);
void vsxseg3h_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t index, vuint32m2_t v0, vuint32m2_t v1,
    vuint32m2_t v2, size_t vl);
void vsxseg3h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, size_t vl);
void vsxseg3h_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t index, vuint64m2_t v0, vuint64m2_t v1,
    vuint64m2_t v2, size_t vl);
void vsxseg4h_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t index, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, size_t vl);
void vsxseg4h_v_i16m2_m (vbool8_t mask, int16_t *base,
    vuint16m2_t index, vint16m2_t v0, vint16m2_t v1, vint16m2_t
    v2, vint16m2_t v3, size_t vl);
void vsxseg4h_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, size_t vl);
void vsxseg4h_v_i32m2_m (vbool16_t mask, int32_t *base,
    vuint32m2_t index, vint32m2_t v0, vint32m2_t v1, vint32m2_t
    v2, vint32m2_t v3, size_t vl);
void vsxseg4h_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, size_t vl);
void vsxseg4h_v_i64m2_m (vbool32_t mask, int64_t *base,
    vuint64m2_t index, vint64m2_t v0, vint64m2_t v1, vint64m2_t
    v2, vint64m2_t v3, size_t vl);

```

```

void vsxseg4h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t index, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, size_t vl);
void vsxseg4h_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t index, vuint16m2_t v0, vuint16m2_t v1,
    vuint16m2_t v2, vuint16m2_t v3, size_t vl);
void vsxseg4h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, size_t vl);
void vsxseg4h_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t index, vuint32m2_t v0, vuint32m2_t v1,
    vuint32m2_t v2, vuint32m2_t v3, size_t vl);
void vsxseg4h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, size_t vl);
void vsxseg4h_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t index, vuint64m2_t v0, vuint64m2_t v1,
    vuint64m2_t v2, vuint64m2_t v3, size_t vl);
void vsxseg5h_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t index, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, size_t vl);
void vsxseg5h_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, size_t vl);
void vsxseg5h_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, size_t vl);
void vsxseg5h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t index, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, size_t vl);
void vsxseg5h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, size_t vl);
void vsxseg5h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, size_t vl);
void vsxseg6h_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t index, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, size_t vl);
void vsxseg6h_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, size_t vl);
void vsxseg6h_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, size_t vl);

```

```

void vsxseg6h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t index, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
    v5, size_t vl);
void vsxseg6h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, size_t vl);
void vsxseg6h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, size_t vl);
void vsxseg7h_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t index, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, vint16m1_t
    v6, size_t vl);
void vsxseg7h_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, vint32m1_t
    v6, size_t vl);
void vsxseg7h_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, vint64m1_t
    v6, size_t vl);
void vsxseg7h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t index, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
    v5, vuint16m1_t v6, size_t vl);
void vsxseg7h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, vuint32m1_t v6, size_t vl);
void vsxseg7h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, vuint64m1_t v6, size_t vl);
void vsxseg8h_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t index, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, vint16m1_t
    v6, vint16m1_t v7, size_t vl);
void vsxseg8h_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, vint32m1_t
    v6, vint32m1_t v7, size_t vl);
void vsxseg8h_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t

```



```

v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, vint64m1_t
v6, vint64m1_t v7, size_t vl);
void vsxseg8h_v_u16m1_m (vbool16_t mask, uint16_t *base,
vuint16m1_t index, vuint16m1_t v0, vuint16m1_t v1,
vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
v5, vuint16m1_t v6, vuint16m1_t v7, size_t vl);
void vsxseg8h_v_u32m1_m (vbool32_t mask, uint32_t *base,
vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
v5, vuint32m1_t v6, vuint32m1_t v7, size_t vl);
void vsxseg8h_v_u64m1_m (vbool64_t mask, uint64_t *base,
vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
v5, vuint64m1_t v6, vuint64m1_t v7, size_t vl);

```

## Vector Indexed Segment 32b Loads and Stores:

### Prototypes:

```

void vlxseg2w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, const
int32_t *base, vuint32m1_t index, size_t vl);
void vlxseg2w_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, const
int32_t *base, vuint32m2_t index, size_t vl);
void vlxseg2w_v_i32m4 (vint32m4_t *v0, vint32m4_t *v1, const
int32_t *base, vuint32m4_t index, size_t vl);
void vlxseg2w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, const
int64_t *base, vuint64m1_t index, size_t vl);
void vlxseg2w_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, const
int64_t *base, vuint64m2_t index, size_t vl);
void vlxseg2w_v_i64m4 (vint64m4_t *v0, vint64m4_t *v1, const
int64_t *base, vuint64m4_t index, size_t vl);
void vlxseg2wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1, const
uint32_t *base, vuint32m1_t index, size_t vl);
void vlxseg2wu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1, const
uint32_t *base, vuint32m2_t index, size_t vl);
void vlxseg2wu_v_u32m4 (vuint32m4_t *v0, vuint32m4_t *v1, const
uint32_t *base, vuint32m4_t index, size_t vl);
void vlxseg2wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1, const
uint64_t *base, vuint64m1_t index, size_t vl);
void vlxseg2wu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1, const
uint64_t *base, vuint64m2_t index, size_t vl);
void vlxseg2wu_v_u64m4 (vuint64m4_t *v0, vuint64m4_t *v1, const
uint64_t *base, vuint64m4_t index, size_t vl);
void vlxseg3w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, const int32_t *base, vuint32m1_t index,
size_t vl);

```

```

void vlxseg3w_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, const int32_t *base, vuint32m2_t index,
    size_t vl);
void vlxseg3w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, const int64_t *base, vuint64m1_t index,
    size_t vl);
void vlxseg3w_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, const int64_t *base, vuint64m2_t index,
    size_t vl);
void vlxseg3wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, const uint32_t *base, vuint32m1_t index,
    size_t vl);
void vlxseg3wu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, const uint32_t *base, vuint32m2_t index,
    size_t vl);
void vlxseg3wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, const uint64_t *base, vuint64m1_t index,
    size_t vl);
void vlxseg3wu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, const uint64_t *base, vuint64m2_t index,
    size_t vl);
void vlxseg4w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, const int32_t *base,
    vuint32m1_t index, size_t vl);
void vlxseg4w_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, const int32_t *base,
    vuint32m2_t index, size_t vl);
void vlxseg4w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, const int64_t *base,
    vuint64m1_t index, size_t vl);
void vlxseg4w_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, const int64_t *base,
    vuint64m2_t index, size_t vl);
void vlxseg4wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, const uint32_t *base,
    vuint32m1_t index, size_t vl);
void vlxseg4wu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, const uint32_t *base,
    vuint32m2_t index, size_t vl);
void vlxseg4wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, const uint64_t *base,
    vuint64m1_t index, size_t vl);
void vlxseg4wu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, const uint64_t *base,
    vuint64m2_t index, size_t vl);

```

```

void vlxseg5w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, const
    int32_t *base, vuint32m1_t index, size_t vl);
void vlxseg5w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, const
    int64_t *base, vuint64m1_t index, size_t vl);
void vlxseg5wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, const
    uint32_t *base, vuint32m1_t index, size_t vl);
void vlxseg5wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, const
    uint64_t *base, vuint64m1_t index, size_t vl);
void vlxseg6w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, const int32_t *base, vuint32m1_t index, size_t vl);
void vlxseg6w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, const int64_t *base, vuint64m1_t index, size_t vl);
void vlxseg6wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, const uint32_t *base, vuint32m1_t index,
    size_t vl);
void vlxseg6wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, const uint64_t *base, vuint64m1_t index,
    size_t vl);
void vlxseg7w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, const int32_t *base, vuint32m1_t index,
    size_t vl);
void vlxseg7w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, const int64_t *base, vuint64m1_t index,
    size_t vl);
void vlxseg7wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, const uint32_t *base,
    vuint32m1_t index, size_t vl);
void vlxseg7wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, const uint64_t *base,
    vuint64m1_t index, size_t vl);
void vlxseg8w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, const int32_t *base,
    vuint32m1_t index, size_t vl);

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void vlxseg8w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vint64m1_t *v7, const int64_t *base,
    vuint64m1_t index, size_t vl);
void vlxseg8wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, const
    uint32_t *base, vuint32m1_t index, size_t vl);
void vlxseg8wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, const
    uint64_t *base, vuint64m1_t index, size_t vl);
void vsxseg2w_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, size_t vl);
void vsxseg2w_v_i32m2 (int32_t *base, vuint32m2_t index,
    vint32m2_t v0, vint32m2_t v1, size_t vl);
void vsxseg2w_v_i32m4 (int32_t *base, vuint32m4_t index,
    vint32m4_t v0, vint32m4_t v1, size_t vl);
void vsxseg2w_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, size_t vl);
void vsxseg2w_v_i64m2 (int64_t *base, vuint64m2_t index,
    vint64m2_t v0, vint64m2_t v1, size_t vl);
void vsxseg2w_v_i64m4 (int64_t *base, vuint64m4_t index,
    vint64m4_t v0, vint64m4_t v1, size_t vl);
void vsxseg2w_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, size_t vl);
void vsxseg2w_v_u32m2 (uint32_t *base, vuint32m2_t index,
    vuint32m2_t v0, vuint32m2_t v1, size_t vl);
void vsxseg2w_v_u32m4 (uint32_t *base, vuint32m4_t index,
    vuint32m4_t v0, vuint32m4_t v1, size_t vl);
void vsxseg2w_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, size_t vl);
void vsxseg2w_v_u64m2 (uint64_t *base, vuint64m2_t index,
    vuint64m2_t v0, vuint64m2_t v1, size_t vl);
void vsxseg2w_v_u64m4 (uint64_t *base, vuint64m4_t index,
    vuint64m4_t v0, vuint64m4_t v1, size_t vl);
void vsxseg3w_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, size_t vl);
void vsxseg3w_v_i32m2 (int32_t *base, vuint32m2_t index,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, size_t vl);
void vsxseg3w_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, size_t vl);
void vsxseg3w_v_i64m2 (int64_t *base, vuint64m2_t index,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, size_t vl);
void vsxseg3w_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, size_t vl);

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void vsxseg3w_v_u32m2 (uint32_t *base, vuint32m2_t index,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, size_t vl);
void vsxseg3w_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, size_t vl);
void vsxseg3w_v_u64m2 (uint64_t *base, vuint64m2_t index,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, size_t vl);
void vsxseg4w_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    size_t vl);
void vsxseg4w_v_i32m2 (int32_t *base, vuint32m2_t index,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, vint32m2_t v3,
    size_t vl);
void vsxseg4w_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    size_t vl);
void vsxseg4w_v_i64m2 (int64_t *base, vuint64m2_t index,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, vint64m2_t v3,
    size_t vl);
void vsxseg4w_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, size_t vl);
void vsxseg4w_v_u32m2 (uint32_t *base, vuint32m2_t index,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, vuint32m2_t
    v3, size_t vl);
void vsxseg4w_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, size_t vl);
void vsxseg4w_v_u64m2 (uint64_t *base, vuint64m2_t index,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, vuint64m2_t
    v3, size_t vl);
void vsxseg5w_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, size_t vl);
void vsxseg5w_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, size_t vl);
void vsxseg5w_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, size_t vl);
void vsxseg5w_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, size_t vl);
void vsxseg6w_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, size_t vl);

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void vsxseg6w_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, size_t vl);
void vsxseg6w_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, size_t vl);
void vsxseg6w_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, size_t vl);
void vsxseg7w_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, size_t vl);
void vsxseg7w_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, size_t vl);
void vsxseg7w_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6, size_t
    vl);
void vsxseg7w_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6, size_t
    vl);
void vsxseg8w_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, vint32m1_t v7,
    size_t vl);
void vsxseg8w_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, vint64m1_t v7,
    size_t vl);
void vsxseg8w_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6,
    vuint32m1_t v7, size_t vl);
void vsxseg8w_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6,
    vuint64m1_t v7, size_t vl);
// masked functions
void vlxseg2w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, const int32_t *base, vuint32m1_t index, size_t
    vl);
void vlxseg2w_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vbool16_t mask, vint32m2_t maskedoff0, vint32m2_t

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maskedoff1, const int32_t *base, vuint32m2_t index, size_t
vl);
void vlxseg2w_v_i32m4_m (vint32m4_t *v0, vint32m4_t *v1,
vbool8_t mask, vint32m4_t maskedoff0, vint32m4_t maskedoff1,
const int32_t *base, vuint32m4_t index, size_t vl);
void vlxseg2w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
maskedoff1, const int64_t *base, vuint64m1_t index, size_t
vl);
void vlxseg2w_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
vbool32_t mask, vint64m2_t maskedoff0, vint64m2_t
maskedoff1, const int64_t *base, vuint64m2_t index, size_t
vl);
void vlxseg2w_v_i64m4_m (vint64m4_t *v0, vint64m4_t *v1,
vbool16_t mask, vint64m4_t maskedoff0, vint64m4_t
maskedoff1, const int64_t *base, vuint64m4_t index, size_t
vl);
void vlxseg2wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vbool32_t mask, vuint32m1_t maskedoff0, vuint32m1_t
maskedoff1, const uint32_t *base, vuint32m1_t index, size_t
vl);
void vlxseg2wu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
vbool16_t mask, vuint32m2_t maskedoff0, vuint32m2_t
maskedoff1, const uint32_t *base, vuint32m2_t index, size_t
vl);
void vlxseg2wu_v_u32m4_m (vuint32m4_t *v0, vuint32m4_t *v1,
vbool8_t mask, vuint32m4_t maskedoff0, vuint32m4_t
maskedoff1, const uint32_t *base, vuint32m4_t index, size_t
vl);
void vlxseg2wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
vbool64_t mask, vuint64m1_t maskedoff0, vuint64m1_t
maskedoff1, const uint64_t *base, vuint64m1_t index, size_t
vl);
void vlxseg2wu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
vbool32_t mask, vuint64m2_t maskedoff0, vuint64m2_t
maskedoff1, const uint64_t *base, vuint64m2_t index, size_t
vl);
void vlxseg2wu_v_u64m4_m (vuint64m4_t *v0, vuint64m4_t *v1,
vbool16_t mask, vuint64m4_t maskedoff0, vuint64m4_t
maskedoff1, const uint64_t *base, vuint64m4_t index, size_t
vl);
void vlxseg3w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vbool32_t mask, vint32m1_t maskedoff0,
vint32m1_t maskedoff1, vint32m1_t maskedoff2, const int32_t
*base, vuint32m1_t index, size_t vl);

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void vlxseg3w_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vbool16_t mask, vint32m2_t maskedoff0,
    vint32m2_t maskedoff1, vint32m2_t maskedoff2, const int32_t
    *base, vuint32m2_t index, size_t vl);
void vlxseg3w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, const int64_t
    *base, vuint64m1_t index, size_t vl);
void vlxseg3w_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vbool32_t mask, vint64m2_t maskedoff0,
    vint64m2_t maskedoff1, vint64m2_t maskedoff2, const int64_t
    *base, vuint64m2_t index, size_t vl);
void vlxseg3wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, const
    uint32_t *base, vuint32m1_t index, size_t vl);
void vlxseg3wu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vbool16_t mask, vuint32m2_t maskedoff0,
    vuint32m2_t maskedoff1, vuint32m2_t maskedoff2, const
    uint32_t *base, vuint32m2_t index, size_t vl);
void vlxseg3wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, const
    uint64_t *base, vuint64m1_t index, size_t vl);
void vlxseg3wu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vbool32_t mask, vuint64m2_t maskedoff0,
    vuint64m2_t maskedoff1, vuint64m2_t maskedoff2, const
    uint64_t *base, vuint64m2_t index, size_t vl);
void vlxseg4w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vbool32_t mask, vint32m1_t
    maskedoff0, vint32m1_t maskedoff1, vint32m1_t maskedoff2,
    vint32m1_t maskedoff3, const int32_t *base, vuint32m1_t
    index, size_t vl);
void vlxseg4w_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, vbool16_t mask, vint32m2_t
    maskedoff0, vint32m2_t maskedoff1, vint32m2_t maskedoff2,
    vint32m2_t maskedoff3, const int32_t *base, vuint32m2_t
    index, size_t vl);
void vlxseg4w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vbool64_t mask, vint64m1_t
    maskedoff0, vint64m1_t maskedoff1, vint64m1_t maskedoff2,
    vint64m1_t maskedoff3, const int64_t *base, vuint64m1_t
    index, size_t vl);
void vlxseg4w_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, vbool32_t mask, vint64m2_t
    maskedoff0, vint64m2_t maskedoff1, vint64m2_t maskedoff2,

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    vint64m2_t maskedoff3, const int64_t *base, vuint64m2_t
    index, size_t vl);
void vlxseg4wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, const uint32_t *base,
    vuint32m1_t index, size_t vl);
void vlxseg4wu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, vbool16_t mask,
    vuint32m2_t maskedoff0, vuint32m2_t maskedoff1, vuint32m2_t
    maskedoff2, vuint32m2_t maskedoff3, const uint32_t *base,
    vuint32m2_t index, size_t vl);
void vlxseg4wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, const uint64_t *base,
    vuint64m1_t index, size_t vl);
void vlxseg4wu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, vbool32_t mask,
    vuint64m2_t maskedoff0, vuint64m2_t maskedoff1, vuint64m2_t
    maskedoff2, vuint64m2_t maskedoff3, const uint64_t *base,
    vuint64m2_t index, size_t vl);
void vlxseg5w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vbool32_t
    mask, vint32m1_t maskedoff0, vint32m1_t maskedoff1,
    vint32m1_t maskedoff2, vint32m1_t maskedoff3, vint32m1_t
    maskedoff4, const int32_t *base, vuint32m1_t index, size_t
    vl);
void vlxseg5w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vbool64_t
    mask, vint64m1_t maskedoff0, vint64m1_t maskedoff1,
    vint64m1_t maskedoff2, vint64m1_t maskedoff3, vint64m1_t
    maskedoff4, const int64_t *base, vuint64m1_t index, size_t
    vl);
void vlxseg5wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, const uint32_t *base, vuint32m1_t index, size_t
    vl);
void vlxseg5wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, const uint64_t *base, vuint64m1_t index, size_t
    vl);

```

```

void vlxseg6w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, vint32m1_t maskedoff2, vint32m1_t maskedoff3,
    vint32m1_t maskedoff4, vint32m1_t maskedoff5, const int32_t
    *base, vuint32m1_t index, size_t vl);
void vlxseg6w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, vint64m1_t maskedoff2, vint64m1_t maskedoff3,
    vint64m1_t maskedoff4, vint64m1_t maskedoff5, const int64_t
    *base, vuint64m1_t index, size_t vl);
void vlxseg6wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, vuint32m1_t
    maskedoff3, vuint32m1_t maskedoff4, vuint32m1_t maskedoff5,
    const uint32_t *base, vuint32m1_t index, size_t vl);
void vlxseg6wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, vuint64m1_t
    maskedoff3, vuint64m1_t maskedoff4, vuint64m1_t maskedoff5,
    const uint64_t *base, vuint64m1_t index, size_t vl);
void vlxseg7w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, vint32m1_t
    maskedoff3, vint32m1_t maskedoff4, vint32m1_t maskedoff5,
    vint32m1_t maskedoff6, const int32_t *base, vuint32m1_t
    index, size_t vl);
void vlxseg7w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, vint64m1_t
    maskedoff3, vint64m1_t maskedoff4, vint64m1_t maskedoff5,
    vint64m1_t maskedoff6, const int64_t *base, vuint64m1_t
    index, size_t vl);
void vlxseg7wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t maskedoff4,
    vuint32m1_t maskedoff5, vuint32m1_t maskedoff6, const
    uint32_t *base, vuint32m1_t index, size_t vl);

```

```

void vlxseg7wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t maskedoff4,
    vuint64m1_t maskedoff5, vuint64m1_t maskedoff6, const
    uint64_t *base, vuint64m1_t index, size_t vl);
void vlxseg8w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, vbool32_t mask,
    vint32m1_t maskedoff0, vint32m1_t maskedoff1, vint32m1_t
    maskedoff2, vint32m1_t maskedoff3, vint32m1_t maskedoff4,
    vint32m1_t maskedoff5, vint32m1_t maskedoff6, vint32m1_t
    maskedoff7, const int32_t *base, vuint32m1_t index, size_t
    vl);
void vlxseg8w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vint64m1_t *v7, vbool64_t mask,
    vint64m1_t maskedoff0, vint64m1_t maskedoff1, vint64m1_t
    maskedoff2, vint64m1_t maskedoff3, vint64m1_t maskedoff4,
    vint64m1_t maskedoff5, vint64m1_t maskedoff6, vint64m1_t
    maskedoff7, const int64_t *base, vuint64m1_t index, size_t
    vl);
void vlxseg8wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, vuint32m1_t maskedoff5, vuint32m1_t maskedoff6,
    vuint32m1_t maskedoff7, const uint32_t *base, vuint32m1_t
    index, size_t vl);
void vlxseg8wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, vuint64m1_t maskedoff5, vuint64m1_t maskedoff6,
    vuint64m1_t maskedoff7, const uint64_t *base, vuint64m1_t
    index, size_t vl);
void vsxseg2w_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, size_t vl);
void vsxseg2w_v_i32m2_m (vbool16_t mask, int32_t *base,
    vuint32m2_t index, vint32m2_t v0, vint32m2_t v1, size_t vl);
void vsxseg2w_v_i32m4_m (vbool8_t mask, int32_t *base,
    vuint32m4_t index, vint32m4_t v0, vint32m4_t v1, size_t vl);

```

```

void vsxseg2w_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, size_t vl);
void vsxseg2w_v_i64m2_m (vbool32_t mask, int64_t *base,
    vuint64m2_t index, vint64m2_t v0, vint64m2_t v1, size_t vl);
void vsxseg2w_v_i64m4_m (vbool16_t mask, int64_t *base,
    vuint64m4_t index, vint64m4_t v0, vint64m4_t v1, size_t vl);
void vsxseg2w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1, size_t
    vl);
void vsxseg2w_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t index, vuint32m2_t v0, vuint32m2_t v1, size_t
    vl);
void vsxseg2w_v_u32m4_m (vbool8_t mask, uint32_t *base,
    vuint32m4_t index, vuint32m4_t v0, vuint32m4_t v1, size_t
    vl);
void vsxseg2w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1, size_t
    vl);
void vsxseg2w_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t index, vuint64m2_t v0, vuint64m2_t v1, size_t
    vl);
void vsxseg2w_v_u64m4_m (vbool16_t mask, uint64_t *base,
    vuint64m4_t index, vuint64m4_t v0, vuint64m4_t v1, size_t
    vl);
void vsxseg3w_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, size_t vl);
void vsxseg3w_v_i32m2_m (vbool16_t mask, int32_t *base,
    vuint32m2_t index, vint32m2_t v0, vint32m2_t v1, vint32m2_t
    v2, size_t vl);
void vsxseg3w_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, size_t vl);
void vsxseg3w_v_i64m2_m (vbool32_t mask, int64_t *base,
    vuint64m2_t index, vint64m2_t v0, vint64m2_t v1, vint64m2_t
    v2, size_t vl);
void vsxseg3w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, size_t vl);
void vsxseg3w_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t index, vuint32m2_t v0, vuint32m2_t v1,
    vuint32m2_t v2, size_t vl);
void vsxseg3w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, size_t vl);

```

```

void vsxseg3w_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t index, vuint64m2_t v0, vuint64m2_t v1,
    vuint64m2_t v2, size_t vl);
void vsxseg4w_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, size_t vl);
void vsxseg4w_v_i32m2_m (vbool16_t mask, int32_t *base,
    vuint32m2_t index, vint32m2_t v0, vint32m2_t v1, vint32m2_t
    v2, vint32m2_t v3, size_t vl);
void vsxseg4w_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, size_t vl);
void vsxseg4w_v_i64m2_m (vbool32_t mask, int64_t *base,
    vuint64m2_t index, vint64m2_t v0, vint64m2_t v1, vint64m2_t
    v2, vint64m2_t v3, size_t vl);
void vsxseg4w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, size_t vl);
void vsxseg4w_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t index, vuint32m2_t v0, vuint32m2_t v1,
    vuint32m2_t v2, vuint32m2_t v3, size_t vl);
void vsxseg4w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, size_t vl);
void vsxseg4w_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t index, vuint64m2_t v0, vuint64m2_t v1,
    vuint64m2_t v2, vuint64m2_t v3, size_t vl);
void vsxseg5w_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, size_t vl);
void vsxseg5w_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, size_t vl);
void vsxseg5w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, size_t vl);
void vsxseg5w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, size_t vl);
void vsxseg6w_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, size_t vl);
void vsxseg6w_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, size_t vl);

```

```

void vsxseg6w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, size_t vl);
void vsxseg6w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, size_t vl);
void vsxseg7w_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, vint32m1_t
    v6, size_t vl);
void vsxseg7w_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, vint64m1_t
    v6, size_t vl);
void vsxseg7w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, vuint32m1_t v6, size_t vl);
void vsxseg7w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, vuint64m1_t v6, size_t vl);
void vsxseg8w_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, vint32m1_t
    v6, vint32m1_t v7, size_t vl);
void vsxseg8w_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, vint64m1_t
    v6, vint64m1_t v7, size_t vl);
void vsxseg8w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, vuint32m1_t v6, vuint32m1_t v7, size_t vl);
void vsxseg8w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, vuint64m1_t v6, vuint64m1_t v7, size_t vl);

```

## Vector Indexed Segment 8b Loads and Stores:

### Prototypes:

```

void vlxseg2b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, const
    int16_t *base, vuint16m1_t index, size_t vl);
void vlxseg2b_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1, const
    int16_t *base, vuint16m2_t index, size_t vl);
void vlxseg2b_v_i16m4 (vint16m4_t *v0, vint16m4_t *v1, const
    int16_t *base, vuint16m4_t index, size_t vl);
void vlxseg2b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, const
    int32_t *base, vuint32m1_t index, size_t vl);
void vlxseg2b_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, const
    int32_t *base, vuint32m2_t index, size_t vl);
void vlxseg2b_v_i32m4 (vint32m4_t *v0, vint32m4_t *v1, const
    int32_t *base, vuint32m4_t index, size_t vl);
void vlxseg2b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, const
    int64_t *base, vuint64m1_t index, size_t vl);
void vlxseg2b_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, const
    int64_t *base, vuint64m2_t index, size_t vl);
void vlxseg2b_v_i64m4 (vint64m4_t *v0, vint64m4_t *v1, const
    int64_t *base, vuint64m4_t index, size_t vl);
void vlxseg2b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, const int8_t
    *base, vuint8m1_t index, size_t vl);
void vlxseg2b_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, const int8_t
    *base, vuint8m2_t index, size_t vl);
void vlxseg2b_v_i8m4 (vint8m4_t *v0, vint8m4_t *v1, const int8_t
    *base, vuint8m4_t index, size_t vl);
void vlxseg2bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1, const
    uint16_t *base, vuint16m1_t index, size_t vl);
void vlxseg2bu_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1, const
    uint16_t *base, vuint16m2_t index, size_t vl);
void vlxseg2bu_v_u16m4 (vuint16m4_t *v0, vuint16m4_t *v1, const
    uint16_t *base, vuint16m4_t index, size_t vl);
void vlxseg2bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1, const
    uint32_t *base, vuint32m1_t index, size_t vl);
void vlxseg2bu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1, const
    uint32_t *base, vuint32m2_t index, size_t vl);
void vlxseg2bu_v_u32m4 (vuint32m4_t *v0, vuint32m4_t *v1, const
    uint32_t *base, vuint32m4_t index, size_t vl);
void vlxseg2bu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1, const
    uint64_t *base, vuint64m1_t index, size_t vl);
void vlxseg2bu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1, const
    uint64_t *base, vuint64m2_t index, size_t vl);
void vlxseg2bu_v_u64m4 (vuint64m4_t *v0, vuint64m4_t *v1, const
    uint64_t *base, vuint64m4_t index, size_t vl);
void vlxseg2bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, const
    uint8_t *base, vuint8m1_t index, size_t vl);
void vlxseg2bu_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1, const
    uint8_t *base, vuint8m2_t index, size_t vl);

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void vlxseg2bu_v_u8m4 (vuint8m4_t *v0, vuint8m4_t *v1, const
    uint8_t *base, vuint8m4_t index, size_t vl);
void vlxseg3b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, const int16_t *base, vuint16m1_t index,
    size_t vl);
void vlxseg3b_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, const int16_t *base, vuint16m2_t index,
    size_t vl);
void vlxseg3b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, const int32_t *base, vuint32m1_t index,
    size_t vl);
void vlxseg3b_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, const int32_t *base, vuint32m2_t index,
    size_t vl);
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    vint64m1_t *v2, const int64_t *base, vuint64m1_t index,
    size_t vl);
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    vint64m2_t *v2, const int64_t *base, vuint64m2_t index,
    size_t vl);
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    *v2, const int8_t *base, vuint8m1_t index, size_t vl);
void vlxseg3b_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, const int8_t *base, vuint8m2_t index, size_t vl);
void vlxseg3bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, const uint16_t *base, vuint16m1_t index,
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    vuint16m2_t *v2, const uint16_t *base, vuint16m2_t index,
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    vuint32m1_t *v2, const uint32_t *base, vuint32m1_t index,
    size_t vl);
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    vuint32m2_t *v2, const uint32_t *base, vuint32m2_t index,
    size_t vl);
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    vuint64m1_t *v2, const uint64_t *base, vuint64m1_t index,
    size_t vl);
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    vuint64m2_t *v2, const uint64_t *base, vuint64m2_t index,
    size_t vl);
void vlxseg3bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, const uint8_t *base, vuint8m1_t index,
    size_t vl);

```



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void vlxseg3bu_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, const uint8_t *base, vuint8m2_t index,
    size_t vl);
void vlxseg4b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, const int16_t *base,
    vuint16m1_t index, size_t vl);
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    vint16m2_t *v2, vint16m2_t *v3, const int16_t *base,
    vuint16m2_t index, size_t vl);
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    vint32m1_t *v2, vint32m1_t *v3, const int32_t *base,
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    vuint64m1_t index, size_t vl);
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    *v2, vint8m1_t *v3, const int8_t *base, vuint8m1_t index,
    size_t vl);
void vlxseg4b_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, vint8m2_t *v3, const int8_t *base, vuint8m2_t index,
    size_t vl);
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    vuint16m1_t *v2, vuint16m1_t *v3, const uint16_t *base,
    vuint16m1_t index, size_t vl);
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    vuint16m2_t *v2, vuint16m2_t *v3, const uint16_t *base,
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    vuint32m1_t *v2, vuint32m1_t *v3, const uint32_t *base,
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    vuint32m2_t index, size_t vl);
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    vuint64m1_t *v2, vuint64m1_t *v3, const uint64_t *base,
    vuint64m1_t index, size_t vl);
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    vuint64m2_t *v2, vuint64m2_t *v3, const uint64_t *base,
    vuint64m2_t index, size_t vl);

```

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void vlxseg4bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, const uint8_t *base,
    vuint8m1_t index, size_t vl);
void vlxseg4bu_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, vuint8m2_t *v3, const uint8_t *base,
    vuint8m2_t index, size_t vl);
void vlxseg5b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, const
    int16_t *base, vuint16m1_t index, size_t vl);
void vlxseg5b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, const
    int32_t *base, vuint32m1_t index, size_t vl);
void vlxseg5b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, const
    int64_t *base, vuint64m1_t index, size_t vl);
void vlxseg5b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, const int8_t *base,
    vuint8m1_t index, size_t vl);
void vlxseg5bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, const
    uint16_t *base, vuint16m1_t index, size_t vl);
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    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, const
    uint32_t *base, vuint32m1_t index, size_t vl);
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    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, const
    uint64_t *base, vuint64m1_t index, size_t vl);
void vlxseg5bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, const
    uint8_t *base, vuint8m1_t index, size_t vl);
void vlxseg6b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, const int16_t *base, vuint16m1_t index, size_t vl);
void vlxseg6b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, const int32_t *base, vuint32m1_t index, size_t vl);
void vlxseg6b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, const int64_t *base, vuint64m1_t index, size_t vl);
void vlxseg6b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, const
    int8_t *base, vuint8m1_t index, size_t vl);
void vlxseg6bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, const uint16_t *base, vuint16m1_t index,
    size_t vl);

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void vlxseg6bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, const uint32_t *base, vuint32m1_t index,
    size_t vl);
void vlxseg6bu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, const uint64_t *base, vuint64m1_t index,
    size_t vl);
void vlxseg6bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, const uint8_t *base, vuint8m1_t index, size_t vl);
void vlxseg7b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, const int16_t *base, vuint16m1_t index,
    size_t vl);
void vlxseg7b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, const int32_t *base, vuint32m1_t index,
    size_t vl);
void vlxseg7b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, const int64_t *base, vuint64m1_t index,
    size_t vl);
void vlxseg7b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
    *v6, const int8_t *base, vuint8m1_t index, size_t vl);
void vlxseg7bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, const uint16_t *base,
    vuint16m1_t index, size_t vl);
void vlxseg7bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, const uint32_t *base,
    vuint32m1_t index, size_t vl);
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    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, const uint64_t *base,
    vuint64m1_t index, size_t vl);
void vlxseg7bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, const uint8_t *base, vuint8m1_t index,
    size_t vl);
void vlxseg8b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, vint16m1_t *v7, const int16_t *base,
    vuint16m1_t index, size_t vl);

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void vlxseg8b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, const int32_t *base,
    vuint32m1_t index, size_t vl);
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    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vint64m1_t *v7, const int64_t *base,
    vuint64m1_t index, size_t vl);
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    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
    *v6, vint8m1_t *v7, const int8_t *base, vuint8m1_t index,
    size_t vl);
void vlxseg8bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, const
    uint16_t *base, vuint16m1_t index, size_t vl);
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    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, const
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    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, const
    uint64_t *base, vuint64m1_t index, size_t vl);
void vlxseg8bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, vuint8m1_t *v7, const uint8_t *base,
    vuint8m1_t index, size_t vl);
void vsxseg2b_v_i16m1 (int16_t *base, vuint16m1_t index,
    vint16m1_t v0, vint16m1_t v1, size_t vl);
void vsxseg2b_v_i16m2 (int16_t *base, vuint16m2_t index,
    vint16m2_t v0, vint16m2_t v1, size_t vl);
void vsxseg2b_v_i16m4 (int16_t *base, vuint16m4_t index,
    vint16m4_t v0, vint16m4_t v1, size_t vl);
void vsxseg2b_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, size_t vl);
void vsxseg2b_v_i32m2 (int32_t *base, vuint32m2_t index,
    vint32m2_t v0, vint32m2_t v1, size_t vl);
void vsxseg2b_v_i32m4 (int32_t *base, vuint32m4_t index,
    vint32m4_t v0, vint32m4_t v1, size_t vl);
void vsxseg2b_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, size_t vl);
void vsxseg2b_v_i64m2 (int64_t *base, vuint64m2_t index,
    vint64m2_t v0, vint64m2_t v1, size_t vl);
void vsxseg2b_v_i64m4 (int64_t *base, vuint64m4_t index,
    vint64m4_t v0, vint64m4_t v1, size_t vl);

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void vsxseg2b_v_i8m1 (int8_t *base, vuint8m1_t index, vint8m1_t
    v0, vint8m1_t v1, size_t vl);
void vsxseg2b_v_i8m2 (int8_t *base, vuint8m2_t index, vint8m2_t
    v0, vint8m2_t v1, size_t vl);
void vsxseg2b_v_i8m4 (int8_t *base, vuint8m4_t index, vint8m4_t
    v0, vint8m4_t v1, size_t vl);
void vsxseg2b_v_u16m1 (uint16_t *base, vuint16m1_t index,
    vuint16m1_t v0, vuint16m1_t v1, size_t vl);
void vsxseg2b_v_u16m2 (uint16_t *base, vuint16m2_t index,
    vuint16m2_t v0, vuint16m2_t v1, size_t vl);
void vsxseg2b_v_u16m4 (uint16_t *base, vuint16m4_t index,
    vuint16m4_t v0, vuint16m4_t v1, size_t vl);
void vsxseg2b_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, size_t vl);
void vsxseg2b_v_u32m2 (uint32_t *base, vuint32m2_t index,
    vuint32m2_t v0, vuint32m2_t v1, size_t vl);
void vsxseg2b_v_u32m4 (uint32_t *base, vuint32m4_t index,
    vuint32m4_t v0, vuint32m4_t v1, size_t vl);
void vsxseg2b_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, size_t vl);
void vsxseg2b_v_u64m2 (uint64_t *base, vuint64m2_t index,
    vuint64m2_t v0, vuint64m2_t v1, size_t vl);
void vsxseg2b_v_u64m4 (uint64_t *base, vuint64m4_t index,
    vuint64m4_t v0, vuint64m4_t v1, size_t vl);
void vsxseg2b_v_u8m1 (uint8_t *base, vuint8m1_t index,
    vuint8m1_t v0, vuint8m1_t v1, size_t vl);
void vsxseg2b_v_u8m2 (uint8_t *base, vuint8m2_t index,
    vuint8m2_t v0, vuint8m2_t v1, size_t vl);
void vsxseg2b_v_u8m4 (uint8_t *base, vuint8m4_t index,
    vuint8m4_t v0, vuint8m4_t v1, size_t vl);
void vsxseg3b_v_i16m1 (int16_t *base, vuint16m1_t index,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, size_t vl);
void vsxseg3b_v_i16m2 (int16_t *base, vuint16m2_t index,
    vint16m2_t v0, vint16m2_t v1, vint16m2_t v2, size_t vl);
void vsxseg3b_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, size_t vl);
void vsxseg3b_v_i32m2 (int32_t *base, vuint32m2_t index,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, size_t vl);
void vsxseg3b_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, size_t vl);
void vsxseg3b_v_i64m2 (int64_t *base, vuint64m2_t index,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, size_t vl);
void vsxseg3b_v_i8m1 (int8_t *base, vuint8m1_t index, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, size_t vl);
void vsxseg3b_v_i8m2 (int8_t *base, vuint8m2_t index, vint8m2_t
    v0, vint8m2_t v1, vint8m2_t v2, size_t vl);

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void vsxseg3b_v_u16m1 (uint16_t *base, vuint16m1_t index,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, size_t vl);
void vsxseg3b_v_u16m2 (uint16_t *base, vuint16m2_t index,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, size_t vl);
void vsxseg3b_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, size_t vl);
void vsxseg3b_v_u32m2 (uint32_t *base, vuint32m2_t index,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, size_t vl);
void vsxseg3b_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, size_t vl);
void vsxseg3b_v_u64m2 (uint64_t *base, vuint64m2_t index,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, size_t vl);
void vsxseg3b_v_u8m1 (uint8_t *base, vuint8m1_t index,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, size_t vl);
void vsxseg3b_v_u8m2 (uint8_t *base, vuint8m2_t index,
    vuint8m2_t v0, vuint8m2_t v1, vuint8m2_t v2, size_t vl);
void vsxseg4b_v_i16m1 (int16_t *base, vuint16m1_t index,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    size_t vl);
void vsxseg4b_v_i16m2 (int16_t *base, vuint16m2_t index,
    vint16m2_t v0, vint16m2_t v1, vint16m2_t v2, vint16m2_t v3,
    size_t vl);
void vsxseg4b_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    size_t vl);
void vsxseg4b_v_i32m2 (int32_t *base, vuint32m2_t index,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, vint32m2_t v3,
    size_t vl);
void vsxseg4b_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    size_t vl);
void vsxseg4b_v_i64m2 (int64_t *base, vuint64m2_t index,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, vint64m2_t v3,
    size_t vl);
void vsxseg4b_v_i8m1 (int8_t *base, vuint8m1_t index, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, size_t vl);
void vsxseg4b_v_i8m2 (int8_t *base, vuint8m2_t index, vint8m2_t
    v0, vint8m2_t v1, vint8m2_t v2, vint8m2_t v3, size_t vl);
void vsxseg4b_v_u16m1 (uint16_t *base, vuint16m1_t index,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, size_t vl);
void vsxseg4b_v_u16m2 (uint16_t *base, vuint16m2_t index,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, vuint16m2_t
    v3, size_t vl);
void vsxseg4b_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t

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    v3, size_t vl);
void vsxseg4b_v_u32m2 (uint32_t *base, vuint32m2_t index,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, vuint32m2_t
    v3, size_t vl);
void vsxseg4b_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, size_t vl);
void vsxseg4b_v_u64m2 (uint64_t *base, vuint64m2_t index,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, vuint64m2_t
    v3, size_t vl);
void vsxseg4b_v_u8m1 (uint8_t *base, vuint8m1_t index,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    size_t vl);
void vsxseg4b_v_u8m2 (uint8_t *base, vuint8m2_t index,
    vuint8m2_t v0, vuint8m2_t v1, vuint8m2_t v2, vuint8m2_t v3,
    size_t vl);
void vsxseg5b_v_i16m1 (int16_t *base, vuint16m1_t index,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, size_t vl);
void vsxseg5b_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, size_t vl);
void vsxseg5b_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, size_t vl);
void vsxseg5b_v_i8m1 (int8_t *base, vuint8m1_t index, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, vint8m1_t v4,
    size_t vl);
void vsxseg5b_v_u16m1 (uint16_t *base, vuint16m1_t index,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, size_t vl);
void vsxseg5b_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, size_t vl);
void vsxseg5b_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, size_t vl);
void vsxseg5b_v_u8m1 (uint8_t *base, vuint8m1_t index,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    vuint8m1_t v4, size_t vl);
void vsxseg6b_v_i16m1 (int16_t *base, vuint16m1_t index,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, size_t vl);
void vsxseg6b_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, size_t vl);

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void vsxseg6b_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, size_t vl);
void vsxseg6b_v_i8m1 (int8_t *base, vuint8m1_t index, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, vint8m1_t v4,
    vint8m1_t v5, size_t vl);
void vsxseg6b_v_u16m1 (uint16_t *base, vuint16m1_t index,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, size_t vl);
void vsxseg6b_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, size_t vl);
void vsxseg6b_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, size_t vl);
void vsxseg6b_v_u8m1 (uint8_t *base, vuint8m1_t index,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    vuint8m1_t v4, vuint8m1_t v5, size_t vl);
void vsxseg7b_v_i16m1 (int16_t *base, vuint16m1_t index,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, size_t vl);
void vsxseg7b_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, size_t vl);
void vsxseg7b_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, size_t vl);
void vsxseg7b_v_i8m1 (int8_t *base, vuint8m1_t index, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, vint8m1_t v4,
    vint8m1_t v5, vint8m1_t v6, size_t vl);
void vsxseg7b_v_u16m1 (uint16_t *base, vuint16m1_t index,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6, size_t
    vl);
void vsxseg7b_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6, size_t
    vl);
void vsxseg7b_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6, size_t
    vl);
void vsxseg7b_v_u8m1 (uint8_t *base, vuint8m1_t index,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    vuint8m1_t v4, vuint8m1_t v5, vuint8m1_t v6, size_t vl);

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void vsxseg8b_v_i16m1 (int16_t *base, vuint16m1_t index,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, vint16m1_t v7,
    size_t vl);
void vsxseg8b_v_i32m1 (int32_t *base, vuint32m1_t index,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, vint32m1_t v7,
    size_t vl);
void vsxseg8b_v_i64m1 (int64_t *base, vuint64m1_t index,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, vint64m1_t v7,
    size_t vl);
void vsxseg8b_v_i8m1 (int8_t *base, vuint8m1_t index, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, vint8m1_t v4,
    vint8m1_t v5, vint8m1_t v6, vint8m1_t v7, size_t vl);
void vsxseg8b_v_u16m1 (uint16_t *base, vuint16m1_t index,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6,
    vuint16m1_t v7, size_t vl);
void vsxseg8b_v_u32m1 (uint32_t *base, vuint32m1_t index,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6,
    vuint32m1_t v7, size_t vl);
void vsxseg8b_v_u64m1 (uint64_t *base, vuint64m1_t index,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6,
    vuint64m1_t v7, size_t vl);
void vsxseg8b_v_u8m1 (uint8_t *base, vuint8m1_t index,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    vuint8m1_t v4, vuint8m1_t v5, vuint8m1_t v6, vuint8m1_t v7,
    size_t vl);
// masked functions
void vlxseg2b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
    maskedoff1, const int16_t *base, vuint16m1_t index, size_t
    vl);
void vlxseg2b_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
    vbool8_t mask, vint16m2_t maskedoff0, vint16m2_t maskedoff1,
    const int16_t *base, vuint16m2_t index, size_t vl);
void vlxseg2b_v_i16m4_m (vint16m4_t *v0, vint16m4_t *v1,
    vbool4_t mask, vint16m4_t maskedoff0, vint16m4_t maskedoff1,
    const int16_t *base, vuint16m4_t index, size_t vl);
void vlxseg2b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, const int32_t *base, vuint32m1_t index, size_t
    vl);

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void vlxseg2b_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vbool16_t mask, vint32m2_t maskedoff0, vint32m2_t
    maskedoff1, const int32_t *base, vuint32m2_t index, size_t
    vl);
void vlxseg2b_v_i32m4_m (vint32m4_t *v0, vint32m4_t *v1,
    vbool8_t mask, vint32m4_t maskedoff0, vint32m4_t maskedoff1,
    const int32_t *base, vuint32m4_t index, size_t vl);
void vlxseg2b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, const int64_t *base, vuint64m1_t index, size_t
    vl);
void vlxseg2b_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vbool32_t mask, vint64m2_t maskedoff0, vint64m2_t
    maskedoff1, const int64_t *base, vuint64m2_t index, size_t
    vl);
void vlxseg2b_v_i64m4_m (vint64m4_t *v0, vint64m4_t *v1,
    vbool16_t mask, vint64m4_t maskedoff0, vint64m4_t
    maskedoff1, const int64_t *base, vuint64m4_t index, size_t
    vl);
void vlxseg2b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vbool8_t
    mask, vint8m1_t maskedoff0, vint8m1_t maskedoff1, const
    int8_t *base, vuint8m1_t index, size_t vl);
void vlxseg2b_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1, vbool4_t
    mask, vint8m2_t maskedoff0, vint8m2_t maskedoff1, const
    int8_t *base, vuint8m2_t index, size_t vl);
void vlxseg2b_v_i8m4_m (vint8m4_t *v0, vint8m4_t *v1, vbool2_t
    mask, vint8m4_t maskedoff0, vint8m4_t maskedoff1, const
    int8_t *base, vuint8m4_t index, size_t vl);
void vlxseg2bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vbool16_t mask, vuint16m1_t maskedoff0, vuint16m1_t
    maskedoff1, const uint16_t *base, vuint16m1_t index, size_t
    vl);
void vlxseg2bu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vbool8_t mask, vuint16m2_t maskedoff0, vuint16m2_t
    maskedoff1, const uint16_t *base, vuint16m2_t index, size_t
    vl);
void vlxseg2bu_v_u16m4_m (vuint16m4_t *v0, vuint16m4_t *v1,
    vbool4_t mask, vuint16m4_t maskedoff0, vuint16m4_t
    maskedoff1, const uint16_t *base, vuint16m4_t index, size_t
    vl);
void vlxseg2bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vbool32_t mask, vuint32m1_t maskedoff0, vuint32m1_t
    maskedoff1, const uint32_t *base, vuint32m1_t index, size_t
    vl);
void vlxseg2bu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vbool16_t mask, vuint32m2_t maskedoff0, vuint32m2_t

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maskedoff1, const uint32_t *base, vuint32m2_t index, size_t
vl);
void vlxseg2bu_v_u32m4_m (vuint32m4_t *v0, vuint32m4_t *v1,
vbool8_t mask, vuint32m4_t maskedoff0, vuint32m4_t
maskedoff1, const uint32_t *base, vuint32m4_t index, size_t
vl);
void vlxseg2bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
vbool64_t mask, vuint64m1_t maskedoff0, vuint64m1_t
maskedoff1, const uint64_t *base, vuint64m1_t index, size_t
vl);
void vlxseg2bu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
vbool32_t mask, vuint64m2_t maskedoff0, vuint64m2_t
maskedoff1, const uint64_t *base, vuint64m2_t index, size_t
vl);
void vlxseg2bu_v_u64m4_m (vuint64m4_t *v0, vuint64m4_t *v1,
vbool16_t mask, vuint64m4_t maskedoff0, vuint64m4_t
maskedoff1, const uint64_t *base, vuint64m4_t index, size_t
vl);
void vlxseg2bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
vbool8_t mask, vuint8m1_t maskedoff0, vuint8m1_t maskedoff1,
const uint8_t *base, vuint8m1_t index, size_t vl);
void vlxseg2bu_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
vbool4_t mask, vuint8m2_t maskedoff0, vuint8m2_t maskedoff1,
const uint8_t *base, vuint8m2_t index, size_t vl);
void vlxseg2bu_v_u8m4_m (vuint8m4_t *v0, vuint8m4_t *v1,
vbool2_t mask, vuint8m4_t maskedoff0, vuint8m4_t maskedoff1,
const uint8_t *base, vuint8m4_t index, size_t vl);
void vlxseg3b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vbool16_t mask, vint16m1_t maskedoff0,
vint16m1_t maskedoff1, vint16m1_t maskedoff2, const int16_t
*base, vuint16m1_t index, size_t vl);
void vlxseg3b_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
vint16m2_t *v2, vbool8_t mask, vint16m2_t maskedoff0,
vint16m2_t maskedoff1, vint16m2_t maskedoff2, const int16_t
*base, vuint16m2_t index, size_t vl);
void vlxseg3b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vbool32_t mask, vint32m1_t maskedoff0,
vint32m1_t maskedoff1, vint32m1_t maskedoff2, const int32_t
*base, vuint32m1_t index, size_t vl);
void vlxseg3b_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
vint32m2_t *v2, vbool16_t mask, vint32m2_t maskedoff0,
vint32m2_t maskedoff1, vint32m2_t maskedoff2, const int32_t
*base, vuint32m2_t index, size_t vl);
void vlxseg3b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vbool64_t mask, vint64m1_t maskedoff0,
vint64m1_t maskedoff1, vint64m1_t maskedoff2, const int64_t

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    *base, vuint64m1_t index, size_t vl);
void vlxseg3b_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vbool32_t mask, vint64m2_t maskedoff0,
    vint64m2_t maskedoff1, vint64m2_t maskedoff2, const int64_t
    *base, vuint64m2_t index, size_t vl);
void vlxseg3b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vbool8_t mask, vint8m1_t maskedoff0, vint8m1_t
    maskedoff1, vint8m1_t maskedoff2, const int8_t *base,
    vuint8m1_t index, size_t vl);
void vlxseg3b_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, vbool4_t mask, vint8m2_t maskedoff0, vint8m2_t
    maskedoff1, vint8m2_t maskedoff2, const int8_t *base,
    vuint8m2_t index, size_t vl);
void vlxseg3bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vbool16_t mask, vuint16m1_t maskedoff0,
    vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, const
    uint16_t *base, vuint16m1_t index, size_t vl);
void vlxseg3bu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vbool8_t mask, vuint16m2_t maskedoff0,
    vuint16m2_t maskedoff1, vuint16m2_t maskedoff2, const
    uint16_t *base, vuint16m2_t index, size_t vl);
void vlxseg3bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, const
    uint32_t *base, vuint32m1_t index, size_t vl);
void vlxseg3bu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vbool16_t mask, vuint32m2_t maskedoff0,
    vuint32m2_t maskedoff1, vuint32m2_t maskedoff2, const
    uint32_t *base, vuint32m2_t index, size_t vl);
void vlxseg3bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, const
    uint64_t *base, vuint64m1_t index, size_t vl);
void vlxseg3bu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vbool32_t mask, vuint64m2_t maskedoff0,
    vuint64m2_t maskedoff1, vuint64m2_t maskedoff2, const
    uint64_t *base, vuint64m2_t index, size_t vl);
void vlxseg3bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vbool8_t mask, vuint8m1_t maskedoff0,
    vuint8m1_t maskedoff1, vuint8m1_t maskedoff2, const uint8_t
    *base, vuint8m1_t index, size_t vl);
void vlxseg3bu_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, vbool4_t mask, vuint8m2_t maskedoff0,
    vuint8m2_t maskedoff1, vuint8m2_t maskedoff2, const uint8_t
    *base, vuint8m2_t index, size_t vl);

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void vlxseg4b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vbool16_t mask, vint16m1_t
    maskedoff0, vint16m1_t maskedoff1, vint16m1_t maskedoff2,
    vint16m1_t maskedoff3, const int16_t *base, vuint16m1_t
    index, size_t vl);
void vlxseg4b_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vint16m2_t *v3, vbool8_t mask, vint16m2_t
    maskedoff0, vint16m2_t maskedoff1, vint16m2_t maskedoff2,
    vint16m2_t maskedoff3, const int16_t *base, vuint16m2_t
    index, size_t vl);
void vlxseg4b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vbool32_t mask, vint32m1_t
    maskedoff0, vint32m1_t maskedoff1, vint32m1_t maskedoff2,
    vint32m1_t maskedoff3, const int32_t *base, vuint32m1_t
    index, size_t vl);
void vlxseg4b_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, vbool16_t mask, vint32m2_t
    maskedoff0, vint32m2_t maskedoff1, vint32m2_t maskedoff2,
    vint32m2_t maskedoff3, const int32_t *base, vuint32m2_t
    index, size_t vl);
void vlxseg4b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vbool64_t mask, vint64m1_t
    maskedoff0, vint64m1_t maskedoff1, vint64m1_t maskedoff2,
    vint64m1_t maskedoff3, const int64_t *base, vuint64m1_t
    index, size_t vl);
void vlxseg4b_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, vbool32_t mask, vint64m2_t
    maskedoff0, vint64m2_t maskedoff1, vint64m2_t maskedoff2,
    vint64m2_t maskedoff3, const int64_t *base, vuint64m2_t
    index, size_t vl);
void vlxseg4b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vbool8_t mask, vint8m1_t maskedoff0,
    vint8m1_t maskedoff1, vint8m1_t maskedoff2, vint8m1_t
    maskedoff3, const int8_t *base, vuint8m1_t index, size_t vl);
void vlxseg4b_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, vint8m2_t *v3, vbool4_t mask, vint8m2_t maskedoff0,
    vint8m2_t maskedoff1, vint8m2_t maskedoff2, vint8m2_t
    maskedoff3, const int8_t *base, vuint8m2_t index, size_t vl);
void vlxseg4bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vbool16_t mask,
    vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
    maskedoff2, vuint16m1_t maskedoff3, const uint16_t *base,
    vuint16m1_t index, size_t vl);
void vlxseg4bu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vuint16m2_t *v3, vbool8_t mask, vuint16m2_t
    maskedoff0, vuint16m2_t maskedoff1, vuint16m2_t maskedoff2,

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    vuint16m2_t maskedoff3, const uint16_t *base, vuint16m2_t
    index, size_t vl);
void vlxseg4bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, const uint32_t *base,
    vuint32m1_t index, size_t vl);
void vlxseg4bu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, vbool16_t mask,
    vuint32m2_t maskedoff0, vuint32m2_t maskedoff1, vuint32m2_t
    maskedoff2, vuint32m2_t maskedoff3, const uint32_t *base,
    vuint32m2_t index, size_t vl);
void vlxseg4bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, const uint64_t *base,
    vuint64m1_t index, size_t vl);
void vlxseg4bu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, vbool32_t mask,
    vuint64m2_t maskedoff0, vuint64m2_t maskedoff1, vuint64m2_t
    maskedoff2, vuint64m2_t maskedoff3, const uint64_t *base,
    vuint64m2_t index, size_t vl);
void vlxseg4bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vbool8_t mask, vuint8m1_t
    maskedoff0, vuint8m1_t maskedoff1, vuint8m1_t maskedoff2,
    vuint8m1_t maskedoff3, const uint8_t *base, vuint8m1_t
    index, size_t vl);
void vlxseg4bu_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, vuint8m2_t *v3, vbool4_t mask, vuint8m2_t
    maskedoff0, vuint8m2_t maskedoff1, vuint8m2_t maskedoff2,
    vuint8m2_t maskedoff3, const uint8_t *base, vuint8m2_t
    index, size_t vl);
void vlxseg5b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vbool16_t
    mask, vint16m1_t maskedoff0, vint16m1_t maskedoff1,
    vint16m1_t maskedoff2, vint16m1_t maskedoff3, vint16m1_t
    maskedoff4, const int16_t *base, vuint16m1_t index, size_t
    vl);
void vlxseg5b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vbool32_t
    mask, vint32m1_t maskedoff0, vint32m1_t maskedoff1,
    vint32m1_t maskedoff2, vint32m1_t maskedoff3, vint32m1_t
    maskedoff4, const int32_t *base, vuint32m1_t index, size_t
    vl);
void vlxseg5b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vbool64_t

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    mask, vint64m1_t maskedoff0, vint64m1_t maskedoff1,
    vint64m1_t maskedoff2, vint64m1_t maskedoff3, vint64m1_t
    maskedoff4, const int64_t *base, vuint64m1_t index, size_t
    vl);
void vlxseg5b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vbool8_t mask, vint8m1_t
    maskedoff0, vint8m1_t maskedoff1, vint8m1_t maskedoff2,
    vint8m1_t maskedoff3, vint8m1_t maskedoff4, const int8_t
    *base, vuint8m1_t index, size_t vl);
void vlxseg5bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, vbool16_t
    mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
    vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
    maskedoff4, const uint16_t *base, vuint16m1_t index, size_t
    vl);
void vlxseg5bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, const uint32_t *base, vuint32m1_t index, size_t
    vl);
void vlxseg5bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, const uint64_t *base, vuint64m1_t index, size_t
    vl);
void vlxseg5bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vbool8_t
    mask, vuint8m1_t maskedoff0, vuint8m1_t maskedoff1,
    vuint8m1_t maskedoff2, vuint8m1_t maskedoff3, vuint8m1_t
    maskedoff4, const uint8_t *base, vuint8m1_t index, size_t
    vl);
void vlxseg6b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
    maskedoff1, vint16m1_t maskedoff2, vint16m1_t maskedoff3,
    vint16m1_t maskedoff4, vint16m1_t maskedoff5, const int16_t
    *base, vuint16m1_t index, size_t vl);
void vlxseg6b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, vint32m1_t maskedoff2, vint32m1_t maskedoff3,
    vint32m1_t maskedoff4, vint32m1_t maskedoff5, const int32_t
    *base, vuint32m1_t index, size_t vl);

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void vlxseg6b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, vint64m1_t maskedoff2, vint64m1_t maskedoff3,
    vint64m1_t maskedoff4, vint64m1_t maskedoff5, const int64_t
    *base, vuint64m1_t index, size_t vl);
void vlxseg6b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vbool8_t
    mask, vint8m1_t maskedoff0, vint8m1_t maskedoff1, vint8m1_t
    maskedoff2, vint8m1_t maskedoff3, vint8m1_t maskedoff4,
    vint8m1_t maskedoff5, const int8_t *base, vuint8m1_t index,
    size_t vl);
void vlxseg6bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vbool16_t mask, vuint16m1_t maskedoff0,
    vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, vuint16m1_t
    maskedoff3, vuint16m1_t maskedoff4, vuint16m1_t maskedoff5,
    const uint16_t *base, vuint16m1_t index, size_t vl);
void vlxseg6bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, vuint32m1_t
    maskedoff3, vuint32m1_t maskedoff4, vuint32m1_t maskedoff5,
    const uint32_t *base, vuint32m1_t index, size_t vl);
void vlxseg6bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, vuint64m1_t
    maskedoff3, vuint64m1_t maskedoff4, vuint64m1_t maskedoff5,
    const uint64_t *base, vuint64m1_t index, size_t vl);
void vlxseg6bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vbool8_t mask, vuint8m1_t maskedoff0, vuint8m1_t
    maskedoff1, vuint8m1_t maskedoff2, vuint8m1_t maskedoff3,
    vuint8m1_t maskedoff4, vuint8m1_t maskedoff5, const uint8_t
    *base, vuint8m1_t index, size_t vl);
void vlxseg7b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, vbool16_t mask, vint16m1_t maskedoff0,
    vint16m1_t maskedoff1, vint16m1_t maskedoff2, vint16m1_t
    maskedoff3, vint16m1_t maskedoff4, vint16m1_t maskedoff5,
    vint16m1_t maskedoff6, const int16_t *base, vuint16m1_t
    index, size_t vl);
void vlxseg7b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vbool32_t mask, vint32m1_t maskedoff0,

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vint32m1_t maskedoff1, vint32m1_t maskedoff2, vint32m1_t
maskedoff3, vint32m1_t maskedoff4, vint32m1_t maskedoff5,
vint32m1_t maskedoff6, const int32_t *base, vuint32m1_t
index, size_t vl);
void vlxseg7b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
*v5, vint64m1_t *v6, vbool64_t mask, vint64m1_t maskedoff0,
vint64m1_t maskedoff1, vint64m1_t maskedoff2, vint64m1_t
maskedoff3, vint64m1_t maskedoff4, vint64m1_t maskedoff5,
vint64m1_t maskedoff6, const int64_t *base, vuint64m1_t
index, size_t vl);
void vlxseg7b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
*v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
*v6, vbool8_t mask, vint8m1_t maskedoff0, vint8m1_t
maskedoff1, vint8m1_t maskedoff2, vint8m1_t maskedoff3,
vint8m1_t maskedoff4, vint8m1_t maskedoff5, vint8m1_t
maskedoff6, const int8_t *base, vuint8m1_t index, size_t vl);
void vlxseg7bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vuint16m1_t *v6, vbool16_t mask,
vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t maskedoff4,
vuint16m1_t maskedoff5, vuint16m1_t maskedoff6, const
uint16_t *base, vuint16m1_t index, size_t vl);
void vlxseg7bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vuint32m1_t *v6, vbool32_t mask,
vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t maskedoff4,
vuint32m1_t maskedoff5, vuint32m1_t maskedoff6, const
uint32_t *base, vuint32m1_t index, size_t vl);
void vlxseg7bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
vuint64m1_t *v5, vuint64m1_t *v6, vbool64_t mask,
vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t maskedoff4,
vuint64m1_t maskedoff5, vuint64m1_t maskedoff6, const
uint64_t *base, vuint64m1_t index, size_t vl);
void vlxseg7bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
*v5, vuint8m1_t *v6, vbool8_t mask, vuint8m1_t maskedoff0,
vuint8m1_t maskedoff1, vuint8m1_t maskedoff2, vuint8m1_t
maskedoff3, vuint8m1_t maskedoff4, vuint8m1_t maskedoff5,
vuint8m1_t maskedoff6, const uint8_t *base, vuint8m1_t
index, size_t vl);

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void vlxseg8b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vint16m1_t *v6, vint16m1_t *v7, vbool16_t mask,
vint16m1_t maskedoff0, vint16m1_t maskedoff1, vint16m1_t
maskedoff2, vint16m1_t maskedoff3, vint16m1_t maskedoff4,
vint16m1_t maskedoff5, vint16m1_t maskedoff6, vint16m1_t
maskedoff7, const int16_t *base, vuint16m1_t index, size_t
vl);
void vlxseg8b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
*v5, vint32m1_t *v6, vint32m1_t *v7, vbool32_t mask,
vint32m1_t maskedoff0, vint32m1_t maskedoff1, vint32m1_t
maskedoff2, vint32m1_t maskedoff3, vint32m1_t maskedoff4,
vint32m1_t maskedoff5, vint32m1_t maskedoff6, vint32m1_t
maskedoff7, const int32_t *base, vuint32m1_t index, size_t
vl);
void vlxseg8b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
*v5, vint64m1_t *v6, vint64m1_t *v7, vbool64_t mask,
vint64m1_t maskedoff0, vint64m1_t maskedoff1, vint64m1_t
maskedoff2, vint64m1_t maskedoff3, vint64m1_t maskedoff4,
vint64m1_t maskedoff5, vint64m1_t maskedoff6, vint64m1_t
maskedoff7, const int64_t *base, vuint64m1_t index, size_t
vl);
void vlxseg8b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
*v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
*v6, vint8m1_t *v7, vbool8_t mask, vint8m1_t maskedoff0,
vint8m1_t maskedoff1, vint8m1_t maskedoff2, vint8m1_t
maskedoff3, vint8m1_t maskedoff4, vint8m1_t maskedoff5,
vint8m1_t maskedoff6, vint8m1_t maskedoff7, const int8_t
*base, vuint8m1_t index, size_t vl);
void vlxseg8bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, vbool16_t
mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
maskedoff4, vuint16m1_t maskedoff5, vuint16m1_t maskedoff6,
vuint16m1_t maskedoff7, const uint16_t *base, vuint16m1_t
index, size_t vl);
void vlxseg8bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, vbool32_t
mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
maskedoff4, vuint32m1_t maskedoff5, vuint32m1_t maskedoff6,
vuint32m1_t maskedoff7, const uint32_t *base, vuint32m1_t

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    index, size_t vl);
void vlxseg8bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, vuint64m1_t maskedoff5, vuint64m1_t maskedoff6,
    vuint64m1_t maskedoff7, const uint64_t *base, vuint64m1_t
    index, size_t vl);
void vlxseg8bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, vuint8m1_t *v7, vbool8_t mask,
    vuint8m1_t maskedoff0, vuint8m1_t maskedoff1, vuint8m1_t
    maskedoff2, vuint8m1_t maskedoff3, vuint8m1_t maskedoff4,
    vuint8m1_t maskedoff5, vuint8m1_t maskedoff6, vuint8m1_t
    maskedoff7, const uint8_t *base, vuint8m1_t index, size_t
    vl);
void vsxseg2b_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t index, vint16m1_t v0, vint16m1_t v1, size_t vl);
void vsxseg2b_v_i16m2_m (vbool8_t mask, int16_t *base,
    vuint16m2_t index, vint16m2_t v0, vint16m2_t v1, size_t vl);
void vsxseg2b_v_i16m4_m (vbool4_t mask, int16_t *base,
    vuint16m4_t index, vint16m4_t v0, vint16m4_t v1, size_t vl);
void vsxseg2b_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, size_t vl);
void vsxseg2b_v_i32m2_m (vbool16_t mask, int32_t *base,
    vuint32m2_t index, vint32m2_t v0, vint32m2_t v1, size_t vl);
void vsxseg2b_v_i32m4_m (vbool8_t mask, int32_t *base,
    vuint32m4_t index, vint32m4_t v0, vint32m4_t v1, size_t vl);
void vsxseg2b_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, size_t vl);
void vsxseg2b_v_i64m2_m (vbool32_t mask, int64_t *base,
    vuint64m2_t index, vint64m2_t v0, vint64m2_t v1, size_t vl);
void vsxseg2b_v_i64m4_m (vbool16_t mask, int64_t *base,
    vuint64m4_t index, vint64m4_t v0, vint64m4_t v1, size_t vl);
void vsxseg2b_v_i8m1_m (vbool8_t mask, int8_t *base, vuint8m1_t
    index, vint8m1_t v0, vint8m1_t v1, size_t vl);
void vsxseg2b_v_i8m2_m (vbool4_t mask, int8_t *base, vuint8m2_t
    index, vint8m2_t v0, vint8m2_t v1, size_t vl);
void vsxseg2b_v_i8m4_m (vbool2_t mask, int8_t *base, vuint8m4_t
    index, vint8m4_t v0, vint8m4_t v1, size_t vl);
void vsxseg2b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t index, vuint16m1_t v0, vuint16m1_t v1, size_t
    vl);
void vsxseg2b_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t index, vuint16m2_t v0, vuint16m2_t v1, size_t

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    vl);
void vsxseg2b_v_u16m4_m (vbool4_t mask, uint16_t *base,
    vuint16m4_t index, vuint16m4_t v0, vuint16m4_t v1, size_t
    vl);
void vsxseg2b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1, size_t
    vl);
void vsxseg2b_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t index, vuint32m2_t v0, vuint32m2_t v1, size_t
    vl);
void vsxseg2b_v_u32m4_m (vbool8_t mask, uint32_t *base,
    vuint32m4_t index, vuint32m4_t v0, vuint32m4_t v1, size_t
    vl);
void vsxseg2b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1, size_t
    vl);
void vsxseg2b_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t index, vuint64m2_t v0, vuint64m2_t v1, size_t
    vl);
void vsxseg2b_v_u64m4_m (vbool16_t mask, uint64_t *base,
    vuint64m4_t index, vuint64m4_t v0, vuint64m4_t v1, size_t
    vl);
void vsxseg2b_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    index, vuint8m1_t v0, vuint8m1_t v1, size_t vl);
void vsxseg2b_v_u8m2_m (vbool4_t mask, uint8_t *base, vuint8m2_t
    index, vuint8m2_t v0, vuint8m2_t v1, size_t vl);
void vsxseg2b_v_u8m4_m (vbool2_t mask, uint8_t *base, vuint8m4_t
    index, vuint8m4_t v0, vuint8m4_t v1, size_t vl);
void vsxseg3b_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t index, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, size_t vl);
void vsxseg3b_v_i16m2_m (vbool8_t mask, int16_t *base,
    vuint16m2_t index, vint16m2_t v0, vint16m2_t v1, vint16m2_t
    v2, size_t vl);
void vsxseg3b_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, size_t vl);
void vsxseg3b_v_i32m2_m (vbool16_t mask, int32_t *base,
    vuint32m2_t index, vint32m2_t v0, vint32m2_t v1, vint32m2_t
    v2, size_t vl);
void vsxseg3b_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, size_t vl);
void vsxseg3b_v_i64m2_m (vbool32_t mask, int64_t *base,
    vuint64m2_t index, vint64m2_t v0, vint64m2_t v1, vint64m2_t
    v2, size_t vl);

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void vsxseg3b_v_i8m1_m (vbool8_t mask, int8_t *base, vuint8m1_t
    index, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, size_t vl);
void vsxseg3b_v_i8m2_m (vbool4_t mask, int8_t *base, vuint8m2_t
    index, vint8m2_t v0, vint8m2_t v1, vint8m2_t v2, size_t vl);
void vsxseg3b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t index, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, size_t vl);
void vsxseg3b_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t index, vuint16m2_t v0, vuint16m2_t v1,
    vuint16m2_t v2, size_t vl);
void vsxseg3b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, size_t vl);
void vsxseg3b_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t index, vuint32m2_t v0, vuint32m2_t v1,
    vuint32m2_t v2, size_t vl);
void vsxseg3b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, size_t vl);
void vsxseg3b_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t index, vuint64m2_t v0, vuint64m2_t v1,
    vuint64m2_t v2, size_t vl);
void vsxseg3b_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    index, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, size_t
    vl);
void vsxseg3b_v_u8m2_m (vbool4_t mask, uint8_t *base, vuint8m2_t
    index, vuint8m2_t v0, vuint8m2_t v1, vuint8m2_t v2, size_t
    vl);
void vsxseg4b_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t index, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, size_t vl);
void vsxseg4b_v_i16m2_m (vbool8_t mask, int16_t *base,
    vuint16m2_t index, vint16m2_t v0, vint16m2_t v1, vint16m2_t
    v2, vint16m2_t v3, size_t vl);
void vsxseg4b_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, size_t vl);
void vsxseg4b_v_i32m2_m (vbool16_t mask, int32_t *base,
    vuint32m2_t index, vint32m2_t v0, vint32m2_t v1, vint32m2_t
    v2, vint32m2_t v3, size_t vl);
void vsxseg4b_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, size_t vl);
void vsxseg4b_v_i64m2_m (vbool32_t mask, int64_t *base,
    vuint64m2_t index, vint64m2_t v0, vint64m2_t v1, vint64m2_t
    v2, vint64m2_t v3, size_t vl);

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void vsxseg4b_v_i8m1_m (vbool8_t mask, int8_t *base, vuint8m1_t
    index, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t
    v3, size_t vl);
void vsxseg4b_v_i8m2_m (vbool4_t mask, int8_t *base, vuint8m2_t
    index, vint8m2_t v0, vint8m2_t v1, vint8m2_t v2, vint8m2_t
    v3, size_t vl);
void vsxseg4b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t index, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, size_t vl);
void vsxseg4b_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t index, vuint16m2_t v0, vuint16m2_t v1,
    vuint16m2_t v2, vuint16m2_t v3, size_t vl);
void vsxseg4b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, size_t vl);
void vsxseg4b_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t index, vuint32m2_t v0, vuint32m2_t v1,
    vuint32m2_t v2, vuint32m2_t v3, size_t vl);
void vsxseg4b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, size_t vl);
void vsxseg4b_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t index, vuint64m2_t v0, vuint64m2_t v1,
    vuint64m2_t v2, vuint64m2_t v3, size_t vl);
void vsxseg4b_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    index, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2,
    vuint8m1_t v3, size_t vl);
void vsxseg4b_v_u8m2_m (vbool4_t mask, uint8_t *base, vuint8m2_t
    index, vuint8m2_t v0, vuint8m2_t v1, vuint8m2_t v2,
    vuint8m2_t v3, size_t vl);
void vsxseg5b_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t index, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, size_t vl);
void vsxseg5b_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, size_t vl);
void vsxseg5b_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, size_t vl);
void vsxseg5b_v_i8m1_m (vbool8_t mask, int8_t *base, vuint8m1_t
    index, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t
    v3, vint8m1_t v4, size_t vl);
void vsxseg5b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t index, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, size_t vl);

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void vsxseg5b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, size_t vl);
void vsxseg5b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, size_t vl);
void vsxseg5b_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    index, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2,
    vuint8m1_t v3, vuint8m1_t v4, size_t vl);
void vsxseg6b_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t index, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, size_t vl);
void vsxseg6b_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, size_t vl);
void vsxseg6b_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, size_t vl);
void vsxseg6b_v_i8m1_m (vbool8_t mask, int8_t *base, vuint8m1_t
    index, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t
    v3, vint8m1_t v4, vint8m1_t v5, size_t vl);
void vsxseg6b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t index, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
    v5, size_t vl);
void vsxseg6b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, size_t vl);
void vsxseg6b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, size_t vl);
void vsxseg6b_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    index, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2,
    vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t v5, size_t vl);
void vsxseg7b_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t index, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, vint16m1_t
    v6, size_t vl);
void vsxseg7b_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, vint32m1_t
    v6, size_t vl);
void vsxseg7b_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t

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    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, vint64m1_t
    v6, size_t vl);
void vsxseg7b_v_i8m1_m (vbool8_t mask, int8_t *base, vuint8m1_t
    index, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t
    v3, vint8m1_t v4, vint8m1_t v5, vint8m1_t v6, size_t vl);
void vsxseg7b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t index, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
    v5, vuint16m1_t v6, size_t vl);
void vsxseg7b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, vuint32m1_t v6, size_t vl);
void vsxseg7b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, vuint64m1_t v6, size_t vl);
void vsxseg7b_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    index, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2,
    vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t v5, vuint8m1_t v6,
    size_t vl);
void vsxseg8b_v_i16m1_m (vbool16_t mask, int16_t *base,
    vuint16m1_t index, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, vint16m1_t
    v6, vint16m1_t v7, size_t vl);
void vsxseg8b_v_i32m1_m (vbool32_t mask, int32_t *base,
    vuint32m1_t index, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, vint32m1_t
    v6, vint32m1_t v7, size_t vl);
void vsxseg8b_v_i64m1_m (vbool64_t mask, int64_t *base,
    vuint64m1_t index, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, vint64m1_t
    v6, vint64m1_t v7, size_t vl);
void vsxseg8b_v_i8m1_m (vbool8_t mask, int8_t *base, vuint8m1_t
    index, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t
    v3, vint8m1_t v4, vint8m1_t v5, vint8m1_t v6, vint8m1_t v7,
    size_t vl);
void vsxseg8b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t index, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
    v5, vuint16m1_t v6, vuint16m1_t v7, size_t vl);
void vsxseg8b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t index, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, vuint32m1_t v6, vuint32m1_t v7, size_t vl);

```



```

void vsxseg8b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t index, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, vuint64m1_t v6, vuint64m1_t v7, size_t vl);
void vsxseg8b_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    index, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2,
    vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t v5, vuint8m1_t v6,
    vuint8m1_t v7, size_t vl);

```

## Vector Strided Segment 16b Loads and Stores:

### Prototypes:

```

void vlsseg2h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, const
    int16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2h_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1, const
    int16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2h_v_i16m4 (vint16m4_t *v0, vint16m4_t *v1, const
    int16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, const
    int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2h_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, const
    int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2h_v_i32m4 (vint32m4_t *v0, vint32m4_t *v1, const
    int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, const
    int64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2h_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, const
    int64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2h_v_i64m4 (vint64m4_t *v0, vint64m4_t *v1, const
    int64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1, const
    uint16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2hu_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1, const
    uint16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2hu_v_u16m4 (vuint16m4_t *v0, vuint16m4_t *v1, const
    uint16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2hu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2hu_v_u32m4 (vuint32m4_t *v0, vuint32m4_t *v1, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1, const
    uint64_t *base, ptrdiff_t stride, size_t vl);

```

```

void vlsseg2hu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2hu_v_u64m4 (vuint64m4_t *v0, vuint64m4_t *v1, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, const int16_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3h_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, const int16_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, const int32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3h_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, const int32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, const int64_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3h_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, const int64_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, const uint16_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3hu_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, const uint16_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, const uint32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3hu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, const uint32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, const uint64_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3hu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, const uint64_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg4h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, const int16_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4h_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vint16m2_t *v3, const int16_t *base,
    ptrdiff_t stride, size_t vl);

```

```

void vlsseg4h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, const int32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4h_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, const int32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, const int64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4h_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, const int64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, const uint16_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4hu_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vuint16m2_t *v3, const uint16_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, const uint32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4hu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, const uint32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, const uint64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4hu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, const uint64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg5h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, const
    int16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg5h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, const
    int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg5h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, const
    int64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg5hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, const
    uint16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg5hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, const
    uint32_t *base, ptrdiff_t stride, size_t vl);

```

```

void vlsseg5hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, const int16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, const int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, const int64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, const uint16_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg6hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, const uint32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg6hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, const uint64_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg7h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, const int16_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg7h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, const int32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg7h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, const int64_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg7hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, const uint16_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg7hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, const uint32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg7hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,

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    vuint64m1_t *v5, vuint64m1_t *v6, const uint64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg8h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, vint16m1_t *v7, const int16_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg8h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, const int32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg8h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vint64m1_t *v7, const int64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg8hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, const
    uint16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg8hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg8hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vssseg2h_v_i16m1 (int16_t *base, ptrdiff_t stride,
    vint16m1_t v0, vint16m1_t v1, size_t vl);
void vssseg2h_v_i16m2 (int16_t *base, ptrdiff_t stride,
    vint16m2_t v0, vint16m2_t v1, size_t vl);
void vssseg2h_v_i16m4 (int16_t *base, ptrdiff_t stride,
    vint16m4_t v0, vint16m4_t v1, size_t vl);
void vssseg2h_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, size_t vl);
void vssseg2h_v_i32m2 (int32_t *base, ptrdiff_t stride,
    vint32m2_t v0, vint32m2_t v1, size_t vl);
void vssseg2h_v_i32m4 (int32_t *base, ptrdiff_t stride,
    vint32m4_t v0, vint32m4_t v1, size_t vl);
void vssseg2h_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, size_t vl);
void vssseg2h_v_i64m2 (int64_t *base, ptrdiff_t stride,
    vint64m2_t v0, vint64m2_t v1, size_t vl);
void vssseg2h_v_i64m4 (int64_t *base, ptrdiff_t stride,
    vint64m4_t v0, vint64m4_t v1, size_t vl);
void vssseg2h_v_u16m1 (uint16_t *base, ptrdiff_t stride,
    vuint16m1_t v0, vuint16m1_t v1, size_t vl);

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void vssseg2h_v_u16m2 (uint16_t *base, ptrdiff_t stride,
    vuint16m2_t v0, vuint16m2_t v1, size_t vl);
void vssseg2h_v_u16m4 (uint16_t *base, ptrdiff_t stride,
    vuint16m4_t v0, vuint16m4_t v1, size_t vl);
void vssseg2h_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, size_t vl);
void vssseg2h_v_u32m2 (uint32_t *base, ptrdiff_t stride,
    vuint32m2_t v0, vuint32m2_t v1, size_t vl);
void vssseg2h_v_u32m4 (uint32_t *base, ptrdiff_t stride,
    vuint32m4_t v0, vuint32m4_t v1, size_t vl);
void vssseg2h_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, size_t vl);
void vssseg2h_v_u64m2 (uint64_t *base, ptrdiff_t stride,
    vuint64m2_t v0, vuint64m2_t v1, size_t vl);
void vssseg2h_v_u64m4 (uint64_t *base, ptrdiff_t stride,
    vuint64m4_t v0, vuint64m4_t v1, size_t vl);
void vssseg3h_v_i16m1 (int16_t *base, ptrdiff_t stride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, size_t vl);
void vssseg3h_v_i16m2 (int16_t *base, ptrdiff_t stride,
    vint16m2_t v0, vint16m2_t v1, vint16m2_t v2, size_t vl);
void vssseg3h_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, size_t vl);
void vssseg3h_v_i32m2 (int32_t *base, ptrdiff_t stride,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, size_t vl);
void vssseg3h_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, size_t vl);
void vssseg3h_v_i64m2 (int64_t *base, ptrdiff_t stride,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, size_t vl);
void vssseg3h_v_u16m1 (uint16_t *base, ptrdiff_t stride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, size_t vl);
void vssseg3h_v_u16m2 (uint16_t *base, ptrdiff_t stride,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, size_t vl);
void vssseg3h_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, size_t vl);
void vssseg3h_v_u32m2 (uint32_t *base, ptrdiff_t stride,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, size_t vl);
void vssseg3h_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, size_t vl);
void vssseg3h_v_u64m2 (uint64_t *base, ptrdiff_t stride,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, size_t vl);
void vssseg4h_v_i16m1 (int16_t *base, ptrdiff_t stride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    size_t vl);
void vssseg4h_v_i16m2 (int16_t *base, ptrdiff_t stride,
    vint16m2_t v0, vint16m2_t v1, vint16m2_t v2, vint16m2_t v3,
    size_t vl);

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void vssseg4h_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    size_t vl);
void vssseg4h_v_i32m2 (int32_t *base, ptrdiff_t stride,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, vint32m2_t v3,
    size_t vl);
void vssseg4h_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    size_t vl);
void vssseg4h_v_i64m2 (int64_t *base, ptrdiff_t stride,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, vint64m2_t v3,
    size_t vl);
void vssseg4h_v_u16m1 (uint16_t *base, ptrdiff_t stride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, size_t vl);
void vssseg4h_v_u16m2 (uint16_t *base, ptrdiff_t stride,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, vuint16m2_t
    v3, size_t vl);
void vssseg4h_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, size_t vl);
void vssseg4h_v_u32m2 (uint32_t *base, ptrdiff_t stride,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, vuint32m2_t
    v3, size_t vl);
void vssseg4h_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, size_t vl);
void vssseg4h_v_u64m2 (uint64_t *base, ptrdiff_t stride,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, vuint64m2_t
    v3, size_t vl);
void vssseg5h_v_i16m1 (int16_t *base, ptrdiff_t stride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, size_t vl);
void vssseg5h_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, size_t vl);
void vssseg5h_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, size_t vl);
void vssseg5h_v_u16m1 (uint16_t *base, ptrdiff_t stride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, size_t vl);
void vssseg5h_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, size_t vl);

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void vssseg5h_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, size_t vl);
void vssseg6h_v_i16m1 (int16_t *base, ptrdiff_t stride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, size_t vl);
void vssseg6h_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, size_t vl);
void vssseg6h_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, size_t vl);
void vssseg6h_v_u16m1 (uint16_t *base, ptrdiff_t stride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, size_t vl);
void vssseg6h_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, size_t vl);
void vssseg6h_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, size_t vl);
void vssseg7h_v_i16m1 (int16_t *base, ptrdiff_t stride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, size_t vl);
void vssseg7h_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, size_t vl);
void vssseg7h_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, size_t vl);
void vssseg7h_v_u16m1 (uint16_t *base, ptrdiff_t stride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6, size_t
    vl);
void vssseg7h_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6, size_t
    vl);
void vssseg7h_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6, size_t
    vl);
void vssseg8h_v_i16m1 (int16_t *base, ptrdiff_t stride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, vint16m1_t v7,
    size_t vl);

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void vssseg8h_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, vint32m1_t v7,
    size_t vl);
void vssseg8h_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, vint64m1_t v7,
    size_t vl);
void vssseg8h_v_u16m1 (uint16_t *base, ptrdiff_t stride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6,
    vuint16m1_t v7, size_t vl);
void vssseg8h_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6,
    vuint32m1_t v7, size_t vl);
void vssseg8h_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6,
    vuint64m1_t v7, size_t vl);
// masked functions
void vlsseg2h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
    maskedoff1, const int16_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2h_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
    vbool8_t mask, vint16m2_t maskedoff0, vint16m2_t maskedoff1,
    const int16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2h_v_i16m4_m (vint16m4_t *v0, vint16m4_t *v1,
    vbool4_t mask, vint16m4_t maskedoff0, vint16m4_t maskedoff1,
    const int16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, const int32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2h_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vbool16_t mask, vint32m2_t maskedoff0, vint32m2_t
    maskedoff1, const int32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2h_v_i32m4_m (vint32m4_t *v0, vint32m4_t *v1,
    vbool8_t mask, vint32m4_t maskedoff0, vint32m4_t maskedoff1,
    const int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, const int64_t *base, ptrdiff_t stride, size_t
    vl);

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void vlsseg2h_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vbool32_t mask, vint64m2_t maskedoff0, vint64m2_t
    maskedoff1, const int64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2h_v_i64m4_m (vint64m4_t *v0, vint64m4_t *v1,
    vbool16_t mask, vint64m4_t maskedoff0, vint64m4_t
    maskedoff1, const int64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vbool16_t mask, vuint16m1_t maskedoff0, vuint16m1_t
    maskedoff1, const uint16_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2hu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vbool8_t mask, vuint16m2_t maskedoff0, vuint16m2_t
    maskedoff1, const uint16_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2hu_v_u16m4_m (vuint16m4_t *v0, vuint16m4_t *v1,
    vbool4_t mask, vuint16m4_t maskedoff0, vuint16m4_t
    maskedoff1, const uint16_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vbool32_t mask, vuint32m1_t maskedoff0, vuint32m1_t
    maskedoff1, const uint32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2hu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vbool16_t mask, vuint32m2_t maskedoff0, vuint32m2_t
    maskedoff1, const uint32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2hu_v_u32m4_m (vuint32m4_t *v0, vuint32m4_t *v1,
    vbool8_t mask, vuint32m4_t maskedoff0, vuint32m4_t
    maskedoff1, const uint32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vbool64_t mask, vuint64m1_t maskedoff0, vuint64m1_t
    maskedoff1, const uint64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2hu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vbool32_t mask, vuint64m2_t maskedoff0, vuint64m2_t
    maskedoff1, const uint64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2hu_v_u64m4_m (vuint64m4_t *v0, vuint64m4_t *v1,
    vbool16_t mask, vuint64m4_t maskedoff0, vuint64m4_t
    maskedoff1, const uint64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg3h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vbool16_t mask, vint16m1_t maskedoff0,

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    vint16m1_t maskedoff1, vint16m1_t maskedoff2, const int16_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3h_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vbool8_t mask, vint16m2_t maskedoff0,
    vint16m2_t maskedoff1, vint16m2_t maskedoff2, const int16_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, const int32_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3h_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vbool16_t mask, vint32m2_t maskedoff0,
    vint32m2_t maskedoff1, vint32m2_t maskedoff2, const int32_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, const int64_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3h_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vbool32_t mask, vint64m2_t maskedoff0,
    vint64m2_t maskedoff1, vint64m2_t maskedoff2, const int64_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vbool16_t mask, vuint16m1_t maskedoff0,
    vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, const
    uint16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3hu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vbool8_t mask, vuint16m2_t maskedoff0,
    vuint16m2_t maskedoff1, vuint16m2_t maskedoff2, const
    uint16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3hu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vbool16_t mask, vuint32m2_t maskedoff0,
    vuint32m2_t maskedoff1, vuint32m2_t maskedoff2, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3hu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vbool32_t mask, vuint64m2_t maskedoff0,
    vuint64m2_t maskedoff1, vuint64m2_t maskedoff2, const
    uint64_t *base, ptrdiff_t stride, size_t vl);

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void vlsseg4h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vbool16_t mask, vint16m1_t
    maskedoff0, vint16m1_t maskedoff1, vint16m1_t maskedoff2,
    vint16m1_t maskedoff3, const int16_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4h_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vint16m2_t *v3, vbool8_t mask, vint16m2_t
    maskedoff0, vint16m2_t maskedoff1, vint16m2_t maskedoff2,
    vint16m2_t maskedoff3, const int16_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vbool32_t mask, vint32m1_t
    maskedoff0, vint32m1_t maskedoff1, vint32m1_t maskedoff2,
    vint32m1_t maskedoff3, const int32_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4h_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, vbool16_t mask, vint32m2_t
    maskedoff0, vint32m2_t maskedoff1, vint32m2_t maskedoff2,
    vint32m2_t maskedoff3, const int32_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vbool64_t mask, vint64m1_t
    maskedoff0, vint64m1_t maskedoff1, vint64m1_t maskedoff2,
    vint64m1_t maskedoff3, const int64_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4h_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, vbool32_t mask, vint64m2_t
    maskedoff0, vint64m2_t maskedoff1, vint64m2_t maskedoff2,
    vint64m2_t maskedoff3, const int64_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vbool16_t mask,
    vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
    maskedoff2, vuint16m1_t maskedoff3, const uint16_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4hu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vuint16m2_t *v3, vbool8_t mask, vuint16m2_t
    maskedoff0, vuint16m2_t maskedoff1, vuint16m2_t maskedoff2,
    vuint16m2_t maskedoff3, const uint16_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, const uint32_t *base,
    ptrdiff_t stride, size_t vl);

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void vlsseg4hu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, vbool16_t mask,
    vuint32m2_t maskedoff0, vuint32m2_t maskedoff1, vuint32m2_t
    maskedoff2, vuint32m2_t maskedoff3, const uint32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, const uint64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4hu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, vbool32_t mask,
    vuint64m2_t maskedoff0, vuint64m2_t maskedoff1, vuint64m2_t
    maskedoff2, vuint64m2_t maskedoff3, const uint64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg5h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vbool16_t
    mask, vint16m1_t maskedoff0, vint16m1_t maskedoff1,
    vint16m1_t maskedoff2, vint16m1_t maskedoff3, vint16m1_t
    maskedoff4, const int16_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg5h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vbool32_t
    mask, vint32m1_t maskedoff0, vint32m1_t maskedoff1,
    vint32m1_t maskedoff2, vint32m1_t maskedoff3, vint32m1_t
    maskedoff4, const int32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg5h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vbool64_t
    mask, vint64m1_t maskedoff0, vint64m1_t maskedoff1,
    vint64m1_t maskedoff2, vint64m1_t maskedoff3, vint64m1_t
    maskedoff4, const int64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg5hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, vbool16_t
    mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
    vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
    maskedoff4, const uint16_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg5hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, const uint32_t *base, ptrdiff_t stride, size_t
    vl);

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void vlsseg5hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, const uint64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg6h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
    maskedoff1, vint16m1_t maskedoff2, vint16m1_t maskedoff3,
    vint16m1_t maskedoff4, vint16m1_t maskedoff5, const int16_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg6h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, vint32m1_t maskedoff2, vint32m1_t maskedoff3,
    vint32m1_t maskedoff4, vint32m1_t maskedoff5, const int32_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg6h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, vint64m1_t maskedoff2, vint64m1_t maskedoff3,
    vint64m1_t maskedoff4, vint64m1_t maskedoff5, const int64_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg6hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vbool16_t mask, vuint16m1_t maskedoff0,
    vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, vuint16m1_t
    maskedoff3, vuint16m1_t maskedoff4, vuint16m1_t maskedoff5,
    const uint16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, vuint32m1_t
    maskedoff3, vuint32m1_t maskedoff4, vuint32m1_t maskedoff5,
    const uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, vuint64m1_t
    maskedoff3, vuint64m1_t maskedoff4, vuint64m1_t maskedoff5,
    const uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg7h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, vbool16_t mask, vint16m1_t maskedoff0,
    vint16m1_t maskedoff1, vint16m1_t maskedoff2, vint16m1_t

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maskedoff3, vint16m1_t maskedoff4, vint16m1_t maskedoff5,
vint16m1_t maskedoff6, const int16_t *base, ptrdiff_t
stride, size_t vl);
void vlsseg7h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
*v5, vint32m1_t *v6, vbool32_t mask, vint32m1_t maskedoff0,
vint32m1_t maskedoff1, vint32m1_t maskedoff2, vint32m1_t
maskedoff3, vint32m1_t maskedoff4, vint32m1_t maskedoff5,
vint32m1_t maskedoff6, const int32_t *base, ptrdiff_t
stride, size_t vl);
void vlsseg7h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
*v5, vint64m1_t *v6, vbool64_t mask, vint64m1_t maskedoff0,
vint64m1_t maskedoff1, vint64m1_t maskedoff2, vint64m1_t
maskedoff3, vint64m1_t maskedoff4, vint64m1_t maskedoff5,
vint64m1_t maskedoff6, const int64_t *base, ptrdiff_t
stride, size_t vl);
void vlsseg7hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vuint16m1_t *v6, vbool16_t mask,
vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t maskedoff4,
vuint16m1_t maskedoff5, vuint16m1_t maskedoff6, const
uint16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg7hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vuint32m1_t *v6, vbool32_t mask,
vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t maskedoff4,
vuint32m1_t maskedoff5, vuint32m1_t maskedoff6, const
uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg7hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
vuint64m1_t *v5, vuint64m1_t *v6, vbool64_t mask,
vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t maskedoff4,
vuint64m1_t maskedoff5, vuint64m1_t maskedoff6, const
uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg8h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vint16m1_t *v6, vint16m1_t *v7, vbool16_t mask,
vint16m1_t maskedoff0, vint16m1_t maskedoff1, vint16m1_t
maskedoff2, vint16m1_t maskedoff3, vint16m1_t maskedoff4,
vint16m1_t maskedoff5, vint16m1_t maskedoff6, vint16m1_t
maskedoff7, const int16_t *base, ptrdiff_t stride, size_t
vl);

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void vlsseg8h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, vbool32_t mask,
    vint32m1_t maskedoff0, vint32m1_t maskedoff1, vint32m1_t
    maskedoff2, vint32m1_t maskedoff3, vint32m1_t maskedoff4,
    vint32m1_t maskedoff5, vint32m1_t maskedoff6, vint32m1_t
    maskedoff7, const int32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg8h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vint64m1_t *v7, vbool64_t mask,
    vint64m1_t maskedoff0, vint64m1_t maskedoff1, vint64m1_t
    maskedoff2, vint64m1_t maskedoff3, vint64m1_t maskedoff4,
    vint64m1_t maskedoff5, vint64m1_t maskedoff6, vint64m1_t
    maskedoff7, const int64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg8hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, vbool16_t
    mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
    vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
    maskedoff4, vuint16m1_t maskedoff5, vuint16m1_t maskedoff6,
    vuint16m1_t maskedoff7, const uint16_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg8hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, vuint32m1_t maskedoff5, vuint32m1_t maskedoff6,
    vuint32m1_t maskedoff7, const uint32_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg8hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, vuint64m1_t maskedoff5, vuint64m1_t maskedoff6,
    vuint64m1_t maskedoff7, const uint64_t *base, ptrdiff_t
    stride, size_t vl);
void vssseg2h_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t stride, vint16m1_t v0, vint16m1_t v1, size_t vl);
void vssseg2h_v_i16m2_m (vbool8_t mask, int16_t *base, ptrdiff_t
    stride, vint16m2_t v0, vint16m2_t v1, size_t vl);
void vssseg2h_v_i16m4_m (vbool4_t mask, int16_t *base, ptrdiff_t
    stride, vint16m4_t v0, vint16m4_t v1, size_t vl);

```



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void vssseg2h_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, size_t vl);
void vssseg2h_v_i32m2_m (vbool16_t mask, int32_t *base,
    ptrdiff_t stride, vint32m2_t v0, vint32m2_t v1, size_t vl);
void vssseg2h_v_i32m4_m (vbool8_t mask, int32_t *base, ptrdiff_t
    stride, vint32m4_t v0, vint32m4_t v1, size_t vl);
void vssseg2h_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, size_t vl);
void vssseg2h_v_i64m2_m (vbool32_t mask, int64_t *base,
    ptrdiff_t stride, vint64m2_t v0, vint64m2_t v1, size_t vl);
void vssseg2h_v_i64m4_m (vbool16_t mask, int64_t *base,
    ptrdiff_t stride, vint64m4_t v0, vint64m4_t v1, size_t vl);
void vssseg2h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m1_t v0, vuint16m1_t v1, size_t vl);
void vssseg2h_v_u16m2_m (vbool8_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m2_t v0, vuint16m2_t v1, size_t vl);
void vssseg2h_v_u16m4_m (vbool4_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m4_t v0, vuint16m4_t v1, size_t vl);
void vssseg2h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1, size_t vl);
void vssseg2h_v_u32m2_m (vbool16_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m2_t v0, vuint32m2_t v1, size_t vl);
void vssseg2h_v_u32m4_m (vbool8_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m4_t v0, vuint32m4_t v1, size_t vl);
void vssseg2h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1, size_t vl);
void vssseg2h_v_u64m2_m (vbool32_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m2_t v0, vuint64m2_t v1, size_t vl);
void vssseg2h_v_u64m4_m (vbool16_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m4_t v0, vuint64m4_t v1, size_t vl);
void vssseg3h_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t stride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, size_t vl);
void vssseg3h_v_i16m2_m (vbool8_t mask, int16_t *base, ptrdiff_t
    stride, vint16m2_t v0, vint16m2_t v1, vint16m2_t v2, size_t
    vl);
void vssseg3h_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, size_t vl);
void vssseg3h_v_i32m2_m (vbool16_t mask, int32_t *base,
    ptrdiff_t stride, vint32m2_t v0, vint32m2_t v1, vint32m2_t
    v2, size_t vl);
void vssseg3h_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, size_t vl);

```

```

void vssseg3h_v_i64m2_m (vbool32_t mask, int64_t *base,
    ptrdiff_t stride, vint64m2_t v0, vint64m2_t v1, vint64m2_t
    v2, size_t vl);
void vssseg3h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, size_t vl);
void vssseg3h_v_u16m2_m (vbool8_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m2_t v0, vuint16m2_t v1,
    vuint16m2_t v2, size_t vl);
void vssseg3h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, size_t vl);
void vssseg3h_v_u32m2_m (vbool16_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m2_t v0, vuint32m2_t v1,
    vuint32m2_t v2, size_t vl);
void vssseg3h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, size_t vl);
void vssseg3h_v_u64m2_m (vbool32_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m2_t v0, vuint64m2_t v1,
    vuint64m2_t v2, size_t vl);
void vssseg4h_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t stride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, size_t vl);
void vssseg4h_v_i16m2_m (vbool8_t mask, int16_t *base, ptrdiff_t
    stride, vint16m2_t v0, vint16m2_t v1, vint16m2_t v2,
    vint16m2_t v3, size_t vl);
void vssseg4h_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, size_t vl);
void vssseg4h_v_i32m2_m (vbool16_t mask, int32_t *base,
    ptrdiff_t stride, vint32m2_t v0, vint32m2_t v1, vint32m2_t
    v2, vint32m2_t v3, size_t vl);
void vssseg4h_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, size_t vl);
void vssseg4h_v_i64m2_m (vbool32_t mask, int64_t *base,
    ptrdiff_t stride, vint64m2_t v0, vint64m2_t v1, vint64m2_t
    v2, vint64m2_t v3, size_t vl);
void vssseg4h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, size_t vl);
void vssseg4h_v_u16m2_m (vbool8_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m2_t v0, vuint16m2_t v1,
    vuint16m2_t v2, vuint16m2_t v3, size_t vl);

```

```

void vssseg4h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, size_t vl);
void vssseg4h_v_u32m2_m (vbool16_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m2_t v0, vuint32m2_t v1,
    vuint32m2_t v2, vuint32m2_t v3, size_t vl);
void vssseg4h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, size_t vl);
void vssseg4h_v_u64m2_m (vbool32_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m2_t v0, vuint64m2_t v1,
    vuint64m2_t v2, vuint64m2_t v3, size_t vl);
void vssseg5h_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t stride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, size_t vl);
void vssseg5h_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, size_t vl);
void vssseg5h_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, size_t vl);
void vssseg5h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, size_t vl);
void vssseg5h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, size_t vl);
void vssseg5h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, size_t vl);
void vssseg6h_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t stride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, size_t vl);
void vssseg6h_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, size_t vl);
void vssseg6h_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, size_t vl);
void vssseg6h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
    v5, size_t vl);
void vssseg6h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t

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v5, size_t vl);
void vssseg6h_v_u64m1_m (vbool64_t mask, uint64_t *base,
ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
v5, size_t vl);
void vssseg7h_v_i16m1_m (vbool16_t mask, int16_t *base,
ptrdiff_t stride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, vint16m1_t
v6, size_t vl);
void vssseg7h_v_i32m1_m (vbool32_t mask, int32_t *base,
ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, vint32m1_t
v6, size_t vl);
void vssseg7h_v_i64m1_m (vbool64_t mask, int64_t *base,
ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, vint64m1_t
v6, size_t vl);
void vssseg7h_v_u16m1_m (vbool16_t mask, uint16_t *base,
ptrdiff_t stride, vuint16m1_t v0, vuint16m1_t v1,
vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
v5, vuint16m1_t v6, size_t vl);
void vssseg7h_v_u32m1_m (vbool32_t mask, uint32_t *base,
ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
v5, vuint32m1_t v6, size_t vl);
void vssseg7h_v_u64m1_m (vbool64_t mask, uint64_t *base,
ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
v5, vuint64m1_t v6, size_t vl);
void vssseg8h_v_i16m1_m (vbool16_t mask, int16_t *base,
ptrdiff_t stride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, vint16m1_t
v6, vint16m1_t v7, size_t vl);
void vssseg8h_v_i32m1_m (vbool32_t mask, int32_t *base,
ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, vint32m1_t
v6, vint32m1_t v7, size_t vl);
void vssseg8h_v_i64m1_m (vbool64_t mask, int64_t *base,
ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, vint64m1_t
v6, vint64m1_t v7, size_t vl);
void vssseg8h_v_u16m1_m (vbool16_t mask, uint16_t *base,
ptrdiff_t stride, vuint16m1_t v0, vuint16m1_t v1,
vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
v5, vuint16m1_t v6, vuint16m1_t v7, size_t vl);

```

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void vssseg8h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, vuint32m1_t v6, vuint32m1_t v7, size_t vl);
void vssseg8h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, vuint64m1_t v6, vuint64m1_t v7, size_t vl);

```

### Vector Strided Segment 32b Loads and Stores:

#### Prototypes:

```

void vlsseg2w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, const
    int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2w_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, const
    int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2w_v_i32m4 (vint32m4_t *v0, vint32m4_t *v1, const
    int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, const
    int64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2w_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, const
    int64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2w_v_i64m4 (vint64m4_t *v0, vint64m4_t *v1, const
    int64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2wu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2wu_v_u32m4 (vuint32m4_t *v0, vuint32m4_t *v1, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2wu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2wu_v_u64m4 (vuint64m4_t *v0, vuint64m4_t *v1, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, const int32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3w_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, const int32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, const int64_t *base, ptrdiff_t stride,
    size_t vl);

```

```

void vlsseg3w_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, const int64_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, const uint32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3wu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, const uint32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, const uint64_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3wu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, const uint64_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg4w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, const int32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4w_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, const int32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, const int64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4w_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, const int64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, const uint32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4wu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, const uint32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, const uint64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4wu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, const uint64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg5w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, const
    int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg5w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, const
    int64_t *base, ptrdiff_t stride, size_t vl);

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void vlsseg5wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg5wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, const int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, const int64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, const uint32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg6wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, const uint64_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg7w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, const int32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg7w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, const int64_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg7wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, const uint32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg7wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, const uint64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg8w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, const int32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg8w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vint64m1_t *v7, const int64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg8wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,

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    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg8wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vssseg2w_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, size_t vl);
void vssseg2w_v_i32m2 (int32_t *base, ptrdiff_t stride,
    vint32m2_t v0, vint32m2_t v1, size_t vl);
void vssseg2w_v_i32m4 (int32_t *base, ptrdiff_t stride,
    vint32m4_t v0, vint32m4_t v1, size_t vl);
void vssseg2w_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, size_t vl);
void vssseg2w_v_i64m2 (int64_t *base, ptrdiff_t stride,
    vint64m2_t v0, vint64m2_t v1, size_t vl);
void vssseg2w_v_i64m4 (int64_t *base, ptrdiff_t stride,
    vint64m4_t v0, vint64m4_t v1, size_t vl);
void vssseg2w_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, size_t vl);
void vssseg2w_v_u32m2 (uint32_t *base, ptrdiff_t stride,
    vuint32m2_t v0, vuint32m2_t v1, size_t vl);
void vssseg2w_v_u32m4 (uint32_t *base, ptrdiff_t stride,
    vuint32m4_t v0, vuint32m4_t v1, size_t vl);
void vssseg2w_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, size_t vl);
void vssseg2w_v_u64m2 (uint64_t *base, ptrdiff_t stride,
    vuint64m2_t v0, vuint64m2_t v1, size_t vl);
void vssseg2w_v_u64m4 (uint64_t *base, ptrdiff_t stride,
    vuint64m4_t v0, vuint64m4_t v1, size_t vl);
void vssseg3w_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, size_t vl);
void vssseg3w_v_i32m2 (int32_t *base, ptrdiff_t stride,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, size_t vl);
void vssseg3w_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, size_t vl);
void vssseg3w_v_i64m2 (int64_t *base, ptrdiff_t stride,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, size_t vl);
void vssseg3w_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, size_t vl);
void vssseg3w_v_u32m2 (uint32_t *base, ptrdiff_t stride,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, size_t vl);
void vssseg3w_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, size_t vl);
void vssseg3w_v_u64m2 (uint64_t *base, ptrdiff_t stride,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, size_t vl);

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void vssseg4w_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    size_t vl);
void vssseg4w_v_i32m2 (int32_t *base, ptrdiff_t stride,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, vint32m2_t v3,
    size_t vl);
void vssseg4w_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    size_t vl);
void vssseg4w_v_i64m2 (int64_t *base, ptrdiff_t stride,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, vint64m2_t v3,
    size_t vl);
void vssseg4w_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, size_t vl);
void vssseg4w_v_u32m2 (uint32_t *base, ptrdiff_t stride,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, vuint32m2_t
    v3, size_t vl);
void vssseg4w_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, size_t vl);
void vssseg4w_v_u64m2 (uint64_t *base, ptrdiff_t stride,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, vuint64m2_t
    v3, size_t vl);
void vssseg5w_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, size_t vl);
void vssseg5w_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, size_t vl);
void vssseg5w_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, size_t vl);
void vssseg5w_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, size_t vl);
void vssseg6w_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, size_t vl);
void vssseg6w_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, size_t vl);
void vssseg6w_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, size_t vl);

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void vssseg6w_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, size_t vl);
void vssseg7w_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, size_t vl);
void vssseg7w_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, size_t vl);
void vssseg7w_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6, size_t
    vl);
void vssseg7w_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6, size_t
    vl);
void vssseg8w_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, vint32m1_t v7,
    size_t vl);
void vssseg8w_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, vint64m1_t v7,
    size_t vl);
void vssseg8w_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6,
    vuint32m1_t v7, size_t vl);
void vssseg8w_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6,
    vuint64m1_t v7, size_t vl);
// masked functions
void vlsseg2w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, const int32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2w_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vbool16_t mask, vint32m2_t maskedoff0, vint32m2_t
    maskedoff1, const int32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2w_v_i32m4_m (vint32m4_t *v0, vint32m4_t *v1,
    vbool8_t mask, vint32m4_t maskedoff0, vint32m4_t maskedoff1,
    const int32_t *base, ptrdiff_t stride, size_t vl);

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void vlsseg2w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, const int64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2w_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vbool32_t mask, vint64m2_t maskedoff0, vint64m2_t
    maskedoff1, const int64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2w_v_i64m4_m (vint64m4_t *v0, vint64m4_t *v1,
    vbool16_t mask, vint64m4_t maskedoff0, vint64m4_t
    maskedoff1, const int64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vbool32_t mask, vuint32m1_t maskedoff0, vuint32m1_t
    maskedoff1, const uint32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2wu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vbool16_t mask, vuint32m2_t maskedoff0, vuint32m2_t
    maskedoff1, const uint32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2wu_v_u32m4_m (vuint32m4_t *v0, vuint32m4_t *v1,
    vbool8_t mask, vuint32m4_t maskedoff0, vuint32m4_t
    maskedoff1, const uint32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vbool64_t mask, vuint64m1_t maskedoff0, vuint64m1_t
    maskedoff1, const uint64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2wu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vbool32_t mask, vuint64m2_t maskedoff0, vuint64m2_t
    maskedoff1, const uint64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2wu_v_u64m4_m (vuint64m4_t *v0, vuint64m4_t *v1,
    vbool16_t mask, vuint64m4_t maskedoff0, vuint64m4_t
    maskedoff1, const uint64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg3w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, const int32_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3w_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vbool16_t mask, vint32m2_t maskedoff0,
    vint32m2_t maskedoff1, vint32m2_t maskedoff2, const int32_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vbool64_t mask, vint64m1_t maskedoff0,

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    vint64m1_t maskedoff1, vint64m1_t maskedoff2, const int64_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3w_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vbool32_t mask, vint64m2_t maskedoff0,
    vint64m2_t maskedoff1, vint64m2_t maskedoff2, const int64_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3wu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vbool16_t mask, vuint32m2_t maskedoff0,
    vuint32m2_t maskedoff1, vuint32m2_t maskedoff2, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3wu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vbool32_t mask, vuint64m2_t maskedoff0,
    vuint64m2_t maskedoff1, vuint64m2_t maskedoff2, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg4w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vbool32_t mask, vint32m1_t
    maskedoff0, vint32m1_t maskedoff1, vint32m1_t maskedoff2,
    vint32m1_t maskedoff3, const int32_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4w_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, vbool16_t mask, vint32m2_t
    maskedoff0, vint32m2_t maskedoff1, vint32m2_t maskedoff2,
    vint32m2_t maskedoff3, const int32_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vbool64_t mask, vint64m1_t
    maskedoff0, vint64m1_t maskedoff1, vint64m1_t maskedoff2,
    vint64m1_t maskedoff3, const int64_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4w_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, vbool32_t mask, vint64m2_t
    maskedoff0, vint64m2_t maskedoff1, vint64m2_t maskedoff2,
    vint64m2_t maskedoff3, const int64_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, const uint32_t *base,

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    ptrdiff_t stride, size_t vl);
void vlsseg4wu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, vbool16_t mask,
    vuint32m2_t maskedoff0, vuint32m2_t maskedoff1, vuint32m2_t
    maskedoff2, vuint32m2_t maskedoff3, const uint32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, const uint64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4wu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, vbool32_t mask,
    vuint64m2_t maskedoff0, vuint64m2_t maskedoff1, vuint64m2_t
    maskedoff2, vuint64m2_t maskedoff3, const uint64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg5w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vbool32_t
    mask, vint32m1_t maskedoff0, vint32m1_t maskedoff1,
    vint32m1_t maskedoff2, vint32m1_t maskedoff3, vint32m1_t
    maskedoff4, const int32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg5w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vbool64_t
    mask, vint64m1_t maskedoff0, vint64m1_t maskedoff1,
    vint64m1_t maskedoff2, vint64m1_t maskedoff3, vint64m1_t
    maskedoff4, const int64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg5wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, const uint32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg5wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, const uint64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg6w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, vint32m1_t maskedoff2, vint32m1_t maskedoff3,
    vint32m1_t maskedoff4, vint32m1_t maskedoff5, const int32_t
    *base, ptrdiff_t stride, size_t vl);

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void vlsseg6w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, vint64m1_t maskedoff2, vint64m1_t maskedoff3,
    vint64m1_t maskedoff4, vint64m1_t maskedoff5, const int64_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg6wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, vuint32m1_t
    maskedoff3, vuint32m1_t maskedoff4, vuint32m1_t maskedoff5,
    const uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, vuint64m1_t
    maskedoff3, vuint64m1_t maskedoff4, vuint64m1_t maskedoff5,
    const uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg7w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, vint32m1_t
    maskedoff3, vint32m1_t maskedoff4, vint32m1_t maskedoff5,
    vint32m1_t maskedoff6, const int32_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg7w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, vint64m1_t
    maskedoff3, vint64m1_t maskedoff4, vint64m1_t maskedoff5,
    vint64m1_t maskedoff6, const int64_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg7wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t maskedoff4,
    vuint32m1_t maskedoff5, vuint32m1_t maskedoff6, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg7wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t maskedoff4,
    vuint64m1_t maskedoff5, vuint64m1_t maskedoff6, const
    uint64_t *base, ptrdiff_t stride, size_t vl);

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void vlsseg8w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, vbool32_t mask,
    vint32m1_t maskedoff0, vint32m1_t maskedoff1, vint32m1_t
    maskedoff2, vint32m1_t maskedoff3, vint32m1_t maskedoff4,
    vint32m1_t maskedoff5, vint32m1_t maskedoff6, vint32m1_t
    maskedoff7, const int32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg8w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vint64m1_t *v7, vbool64_t mask,
    vint64m1_t maskedoff0, vint64m1_t maskedoff1, vint64m1_t
    maskedoff2, vint64m1_t maskedoff3, vint64m1_t maskedoff4,
    vint64m1_t maskedoff5, vint64m1_t maskedoff6, vint64m1_t
    maskedoff7, const int64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg8wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, vuint32m1_t maskedoff5, vuint32m1_t maskedoff6,
    vuint32m1_t maskedoff7, const uint32_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg8wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, vuint64m1_t maskedoff5, vuint64m1_t maskedoff6,
    vuint64m1_t maskedoff7, const uint64_t *base, ptrdiff_t
    stride, size_t vl);
void vssseg2w_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, size_t vl);
void vssseg2w_v_i32m2_m (vbool16_t mask, int32_t *base,
    ptrdiff_t stride, vint32m2_t v0, vint32m2_t v1, size_t vl);
void vssseg2w_v_i32m4_m (vbool8_t mask, int32_t *base, ptrdiff_t
    stride, vint32m4_t v0, vint32m4_t v1, size_t vl);
void vssseg2w_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, size_t vl);
void vssseg2w_v_i64m2_m (vbool32_t mask, int64_t *base,
    ptrdiff_t stride, vint64m2_t v0, vint64m2_t v1, size_t vl);
void vssseg2w_v_i64m4_m (vbool16_t mask, int64_t *base,
    ptrdiff_t stride, vint64m4_t v0, vint64m4_t v1, size_t vl);
void vssseg2w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1, size_t vl);

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void vssseg2w_v_u32m2_m (vbool16_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m2_t v0, vuint32m2_t v1, size_t vl);
void vssseg2w_v_u32m4_m (vbool8_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m4_t v0, vuint32m4_t v1, size_t vl);
void vssseg2w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1, size_t vl);
void vssseg2w_v_u64m2_m (vbool32_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m2_t v0, vuint64m2_t v1, size_t vl);
void vssseg2w_v_u64m4_m (vbool16_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m4_t v0, vuint64m4_t v1, size_t vl);
void vssseg3w_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, size_t vl);
void vssseg3w_v_i32m2_m (vbool16_t mask, int32_t *base,
    ptrdiff_t stride, vint32m2_t v0, vint32m2_t v1, vint32m2_t
    v2, size_t vl);
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    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, size_t vl);
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    ptrdiff_t stride, vint64m2_t v0, vint64m2_t v1, vint64m2_t
    v2, size_t vl);
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    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, size_t vl);
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    ptrdiff_t stride, vuint32m2_t v0, vuint32m2_t v1,
    vuint32m2_t v2, size_t vl);
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    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, size_t vl);
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    ptrdiff_t stride, vuint64m2_t v0, vuint64m2_t v1,
    vuint64m2_t v2, size_t vl);
void vssseg4w_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, size_t vl);
void vssseg4w_v_i32m2_m (vbool16_t mask, int32_t *base,
    ptrdiff_t stride, vint32m2_t v0, vint32m2_t v1, vint32m2_t
    v2, vint32m2_t v3, size_t vl);
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    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, size_t vl);
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    ptrdiff_t stride, vint64m2_t v0, vint64m2_t v1, vint64m2_t
    v2, vint64m2_t v3, size_t vl);

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void vssseg4w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, size_t vl);
void vssseg4w_v_u32m2_m (vbool16_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m2_t v0, vuint32m2_t v1,
    vuint32m2_t v2, vuint32m2_t v3, size_t vl);
void vssseg4w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, size_t vl);
void vssseg4w_v_u64m2_m (vbool32_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m2_t v0, vuint64m2_t v1,
    vuint64m2_t v2, vuint64m2_t v3, size_t vl);
void vssseg5w_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, size_t vl);
void vssseg5w_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, size_t vl);
void vssseg5w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, size_t vl);
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    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, size_t vl);
void vssseg6w_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, size_t vl);
void vssseg6w_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, size_t vl);
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    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, size_t vl);
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    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, size_t vl);
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    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, vint32m1_t
    v6, size_t vl);
void vssseg7w_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, vint64m1_t
    v6, size_t vl);

```

```

void vssseg7w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, vuint32m1_t v6, size_t vl);
void vssseg7w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, vuint64m1_t v6, size_t vl);
void vssseg8w_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, vint32m1_t
    v6, vint32m1_t v7, size_t vl);
void vssseg8w_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, vint64m1_t
    v6, vint64m1_t v7, size_t vl);
void vssseg8w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, vuint32m1_t v6, vuint32m1_t v7, size_t vl);
void vssseg8w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, vuint64m1_t v6, vuint64m1_t v7, size_t vl);

```

## Vector Strided Segment 8b Loads and Stores:

### Prototypes:

```

void vlsseg2b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, const
    int16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2b_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1, const
    int16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2b_v_i16m4 (vint16m4_t *v0, vint16m4_t *v1, const
    int16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, const
    int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2b_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, const
    int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2b_v_i32m4 (vint32m4_t *v0, vint32m4_t *v1, const
    int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, const
    int64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2b_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, const
    int64_t *base, ptrdiff_t stride, size_t vl);

```

```

void vlsseg2b_v_i64m4 (vint64m4_t *v0, vint64m4_t *v1, const
    int64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, const int8_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg2b_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, const int8_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg2b_v_i8m4 (vint8m4_t *v0, vint8m4_t *v1, const int8_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg2bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1, const
    uint16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2bu_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1, const
    uint16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2bu_v_u16m4 (vuint16m4_t *v0, vuint16m4_t *v1, const
    uint16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2bu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2bu_v_u32m4 (vuint32m4_t *v0, vuint32m4_t *v1, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2bu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2bu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2bu_v_u64m4 (vuint64m4_t *v0, vuint64m4_t *v1, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, const
    uint8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2bu_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1, const
    uint8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2bu_v_u8m4 (vuint8m4_t *v0, vuint8m4_t *v1, const
    uint8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, const int16_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3b_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, const int16_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, const int32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3b_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, const int32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, const int64_t *base, ptrdiff_t stride,

```

```

    size_t vl);
void vlsseg3b_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, const int64_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, const int8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3b_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, const int8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, const uint16_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3bu_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, const uint16_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, const uint32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3bu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, const uint32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3bu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, const uint64_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3bu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, const uint64_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, const uint8_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg3bu_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, const uint8_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg4b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, const int16_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4b_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vint16m2_t *v3, const int16_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, const int32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4b_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, const int32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, const int64_t *base,

```

```

    ptrdiff_t stride, size_t vl);
void vlsseg4b_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, const int64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, const int8_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg4b_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, vint8m2_t *v3, const int8_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg4bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, const uint16_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4bu_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vuint16m2_t *v3, const uint16_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, const uint32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4bu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, const uint32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4bu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, const uint64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4bu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, const uint64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, const uint8_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4bu_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, vuint8m2_t *v3, const uint8_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg5b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, const
    int16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg5b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, const
    int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg5b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, const
    int64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg5b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, const int8_t *base,
    ptrdiff_t stride, size_t vl);

```

```

void vlsseg5bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, const
    uint16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg5bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg5bu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg5bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, const
    uint8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, const int16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, const int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, const int64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, const
    int8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, const uint16_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg6bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, const uint32_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg6bu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, const uint64_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg6bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, const uint8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg7b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, const int16_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg7b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, const int32_t *base, ptrdiff_t stride,

```

```

    size_t vl);
void vlsseg7b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, const int64_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg7b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
    *v6, const int8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg7bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, const uint16_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg7bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, const uint32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg7bu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, const uint64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg7bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, const uint8_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg8b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, vint16m1_t *v7, const int16_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg8b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, const int32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg8b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vint64m1_t *v7, const int64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg8b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
    *v6, vint8m1_t *v7, const int8_t *base, ptrdiff_t stride,
    size_t vl);
void vlsseg8bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, const
    uint16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg8bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,

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    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg8bu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg8bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, vuint8m1_t *v7, const uint8_t *base,
    ptrdiff_t stride, size_t vl);
void vssseg2b_v_i16m1 (int16_t *base, ptrdiff_t stride,
    vint16m1_t v0, vint16m1_t v1, size_t vl);
void vssseg2b_v_i16m2 (int16_t *base, ptrdiff_t stride,
    vint16m2_t v0, vint16m2_t v1, size_t vl);
void vssseg2b_v_i16m4 (int16_t *base, ptrdiff_t stride,
    vint16m4_t v0, vint16m4_t v1, size_t vl);
void vssseg2b_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, size_t vl);
void vssseg2b_v_i32m2 (int32_t *base, ptrdiff_t stride,
    vint32m2_t v0, vint32m2_t v1, size_t vl);
void vssseg2b_v_i32m4 (int32_t *base, ptrdiff_t stride,
    vint32m4_t v0, vint32m4_t v1, size_t vl);
void vssseg2b_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, size_t vl);
void vssseg2b_v_i64m2 (int64_t *base, ptrdiff_t stride,
    vint64m2_t v0, vint64m2_t v1, size_t vl);
void vssseg2b_v_i64m4 (int64_t *base, ptrdiff_t stride,
    vint64m4_t v0, vint64m4_t v1, size_t vl);
void vssseg2b_v_i8m1 (int8_t *base, ptrdiff_t stride, vint8m1_t
    v0, vint8m1_t v1, size_t vl);
void vssseg2b_v_i8m2 (int8_t *base, ptrdiff_t stride, vint8m2_t
    v0, vint8m2_t v1, size_t vl);
void vssseg2b_v_i8m4 (int8_t *base, ptrdiff_t stride, vint8m4_t
    v0, vint8m4_t v1, size_t vl);
void vssseg2b_v_u16m1 (uint16_t *base, ptrdiff_t stride,
    vuint16m1_t v0, vuint16m1_t v1, size_t vl);
void vssseg2b_v_u16m2 (uint16_t *base, ptrdiff_t stride,
    vuint16m2_t v0, vuint16m2_t v1, size_t vl);
void vssseg2b_v_u16m4 (uint16_t *base, ptrdiff_t stride,
    vuint16m4_t v0, vuint16m4_t v1, size_t vl);
void vssseg2b_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, size_t vl);
void vssseg2b_v_u32m2 (uint32_t *base, ptrdiff_t stride,
    vuint32m2_t v0, vuint32m2_t v1, size_t vl);
void vssseg2b_v_u32m4 (uint32_t *base, ptrdiff_t stride,
    vuint32m4_t v0, vuint32m4_t v1, size_t vl);

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void vssseg2b_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, size_t vl);
void vssseg2b_v_u64m2 (uint64_t *base, ptrdiff_t stride,
    vuint64m2_t v0, vuint64m2_t v1, size_t vl);
void vssseg2b_v_u64m4 (uint64_t *base, ptrdiff_t stride,
    vuint64m4_t v0, vuint64m4_t v1, size_t vl);
void vssseg2b_v_u8m1 (uint8_t *base, ptrdiff_t stride,
    vuint8m1_t v0, vuint8m1_t v1, size_t vl);
void vssseg2b_v_u8m2 (uint8_t *base, ptrdiff_t stride,
    vuint8m2_t v0, vuint8m2_t v1, size_t vl);
void vssseg2b_v_u8m4 (uint8_t *base, ptrdiff_t stride,
    vuint8m4_t v0, vuint8m4_t v1, size_t vl);
void vssseg3b_v_i16m1 (int16_t *base, ptrdiff_t stride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, size_t vl);
void vssseg3b_v_i16m2 (int16_t *base, ptrdiff_t stride,
    vint16m2_t v0, vint16m2_t v1, vint16m2_t v2, size_t vl);
void vssseg3b_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, size_t vl);
void vssseg3b_v_i32m2 (int32_t *base, ptrdiff_t stride,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, size_t vl);
void vssseg3b_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, size_t vl);
void vssseg3b_v_i64m2 (int64_t *base, ptrdiff_t stride,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, size_t vl);
void vssseg3b_v_i8m1 (int8_t *base, ptrdiff_t stride, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, size_t vl);
void vssseg3b_v_i8m2 (int8_t *base, ptrdiff_t stride, vint8m2_t
    v0, vint8m2_t v1, vint8m2_t v2, size_t vl);
void vssseg3b_v_u16m1 (uint16_t *base, ptrdiff_t stride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, size_t vl);
void vssseg3b_v_u16m2 (uint16_t *base, ptrdiff_t stride,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, size_t vl);
void vssseg3b_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, size_t vl);
void vssseg3b_v_u32m2 (uint32_t *base, ptrdiff_t stride,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, size_t vl);
void vssseg3b_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, size_t vl);
void vssseg3b_v_u64m2 (uint64_t *base, ptrdiff_t stride,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, size_t vl);
void vssseg3b_v_u8m1 (uint8_t *base, ptrdiff_t stride,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, size_t vl);
void vssseg3b_v_u8m2 (uint8_t *base, ptrdiff_t stride,
    vuint8m2_t v0, vuint8m2_t v1, vuint8m2_t v2, size_t vl);
void vssseg4b_v_i16m1 (int16_t *base, ptrdiff_t stride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,

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    size_t vl);
void vssseg4b_v_i16m2 (int16_t *base, ptrdiff_t stride,
    vint16m2_t v0, vint16m2_t v1, vint16m2_t v2, vint16m2_t v3,
    size_t vl);
void vssseg4b_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    size_t vl);
void vssseg4b_v_i32m2 (int32_t *base, ptrdiff_t stride,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, vint32m2_t v3,
    size_t vl);
void vssseg4b_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    size_t vl);
void vssseg4b_v_i64m2 (int64_t *base, ptrdiff_t stride,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, vint64m2_t v3,
    size_t vl);
void vssseg4b_v_i8m1 (int8_t *base, ptrdiff_t stride, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, size_t vl);
void vssseg4b_v_i8m2 (int8_t *base, ptrdiff_t stride, vint8m2_t
    v0, vint8m2_t v1, vint8m2_t v2, vint8m2_t v3, size_t vl);
void vssseg4b_v_u16m1 (uint16_t *base, ptrdiff_t stride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, size_t vl);
void vssseg4b_v_u16m2 (uint16_t *base, ptrdiff_t stride,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, vuint16m2_t
    v3, size_t vl);
void vssseg4b_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, size_t vl);
void vssseg4b_v_u32m2 (uint32_t *base, ptrdiff_t stride,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, vuint32m2_t
    v3, size_t vl);
void vssseg4b_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, size_t vl);
void vssseg4b_v_u64m2 (uint64_t *base, ptrdiff_t stride,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, vuint64m2_t
    v3, size_t vl);
void vssseg4b_v_u8m1 (uint8_t *base, ptrdiff_t stride,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    size_t vl);
void vssseg4b_v_u8m2 (uint8_t *base, ptrdiff_t stride,
    vuint8m2_t v0, vuint8m2_t v1, vuint8m2_t v2, vuint8m2_t v3,
    size_t vl);
void vssseg5b_v_i16m1 (int16_t *base, ptrdiff_t stride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,

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    vint16m1_t v4, size_t vl);
void vssseg5b_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, size_t vl);
void vssseg5b_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, size_t vl);
void vssseg5b_v_i8m1 (int8_t *base, ptrdiff_t stride, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, vint8m1_t v4,
    size_t vl);
void vssseg5b_v_u16m1 (uint16_t *base, ptrdiff_t stride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, size_t vl);
void vssseg5b_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, size_t vl);
void vssseg5b_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, size_t vl);
void vssseg5b_v_u8m1 (uint8_t *base, ptrdiff_t stride,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    vuint8m1_t v4, size_t vl);
void vssseg6b_v_i16m1 (int16_t *base, ptrdiff_t stride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, size_t vl);
void vssseg6b_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, size_t vl);
void vssseg6b_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, size_t vl);
void vssseg6b_v_i8m1 (int8_t *base, ptrdiff_t stride, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, vint8m1_t v4,
    vint8m1_t v5, size_t vl);
void vssseg6b_v_u16m1 (uint16_t *base, ptrdiff_t stride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, size_t vl);
void vssseg6b_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, size_t vl);
void vssseg6b_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, size_t vl);
void vssseg6b_v_u8m1 (uint8_t *base, ptrdiff_t stride,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    vuint8m1_t v4, vuint8m1_t v5, size_t vl);

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void vssseg7b_v_i16m1 (int16_t *base, ptrdiff_t stride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, size_t vl);
void vssseg7b_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, size_t vl);
void vssseg7b_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, size_t vl);
void vssseg7b_v_i8m1 (int8_t *base, ptrdiff_t stride, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, vint8m1_t v4,
    vint8m1_t v5, vint8m1_t v6, size_t vl);
void vssseg7b_v_u16m1 (uint16_t *base, ptrdiff_t stride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6, size_t
    vl);
void vssseg7b_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6, size_t
    vl);
void vssseg7b_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6, size_t
    vl);
void vssseg7b_v_u8m1 (uint8_t *base, ptrdiff_t stride,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    vuint8m1_t v4, vuint8m1_t v5, vuint8m1_t v6, size_t vl);
void vssseg8b_v_i16m1 (int16_t *base, ptrdiff_t stride,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, vint16m1_t v7,
    size_t vl);
void vssseg8b_v_i32m1 (int32_t *base, ptrdiff_t stride,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, vint32m1_t v7,
    size_t vl);
void vssseg8b_v_i64m1 (int64_t *base, ptrdiff_t stride,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, vint64m1_t v7,
    size_t vl);
void vssseg8b_v_i8m1 (int8_t *base, ptrdiff_t stride, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, vint8m1_t v4,
    vint8m1_t v5, vint8m1_t v6, vint8m1_t v7, size_t vl);
void vssseg8b_v_u16m1 (uint16_t *base, ptrdiff_t stride,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6,
    vuint16m1_t v7, size_t vl);

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void vssseg8b_v_u32m1 (uint32_t *base, ptrdiff_t stride,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6,
    vuint32m1_t v7, size_t vl);
void vssseg8b_v_u64m1 (uint64_t *base, ptrdiff_t stride,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6,
    vuint64m1_t v7, size_t vl);
void vssseg8b_v_u8m1 (uint8_t *base, ptrdiff_t stride,
    vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3,
    vuint8m1_t v4, vuint8m1_t v5, vuint8m1_t v6, vuint8m1_t v7,
    size_t vl);
// masked functions
void vlsseg2b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
    maskedoff1, const int16_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2b_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
    vbool8_t mask, vint16m2_t maskedoff0, vint16m2_t maskedoff1,
    const int16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2b_v_i16m4_m (vint16m4_t *v0, vint16m4_t *v1,
    vbool4_t mask, vint16m4_t maskedoff0, vint16m4_t maskedoff1,
    const int16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, const int32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2b_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vbool16_t mask, vint32m2_t maskedoff0, vint32m2_t
    maskedoff1, const int32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2b_v_i32m4_m (vint32m4_t *v0, vint32m4_t *v1,
    vbool8_t mask, vint32m4_t maskedoff0, vint32m4_t maskedoff1,
    const int32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, const int64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2b_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vbool32_t mask, vint64m2_t maskedoff0, vint64m2_t
    maskedoff1, const int64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2b_v_i64m4_m (vint64m4_t *v0, vint64m4_t *v1,
    vbool16_t mask, vint64m4_t maskedoff0, vint64m4_t
    maskedoff1, const int64_t *base, ptrdiff_t stride, size_t
    vl);

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void vlsseg2b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vbool8_t
    mask, vint8m1_t maskedoff0, vint8m1_t maskedoff1, const
    int8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2b_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1, vbool4_t
    mask, vint8m2_t maskedoff0, vint8m2_t maskedoff1, const
    int8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2b_v_i8m4_m (vint8m4_t *v0, vint8m4_t *v1, vbool2_t
    mask, vint8m4_t maskedoff0, vint8m4_t maskedoff1, const
    int8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vbool16_t mask, vuint16m1_t maskedoff0, vuint16m1_t
    maskedoff1, const uint16_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2bu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vbool8_t mask, vuint16m2_t maskedoff0, vuint16m2_t
    maskedoff1, const uint16_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2bu_v_u16m4_m (vuint16m4_t *v0, vuint16m4_t *v1,
    vbool4_t mask, vuint16m4_t maskedoff0, vuint16m4_t
    maskedoff1, const uint16_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vbool32_t mask, vuint32m1_t maskedoff0, vuint32m1_t
    maskedoff1, const uint32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2bu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vbool16_t mask, vuint32m2_t maskedoff0, vuint32m2_t
    maskedoff1, const uint32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2bu_v_u32m4_m (vuint32m4_t *v0, vuint32m4_t *v1,
    vbool8_t mask, vuint32m4_t maskedoff0, vuint32m4_t
    maskedoff1, const uint32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vbool64_t mask, vuint64m1_t maskedoff0, vuint64m1_t
    maskedoff1, const uint64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2bu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vbool32_t mask, vuint64m2_t maskedoff0, vuint64m2_t
    maskedoff1, const uint64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg2bu_v_u64m4_m (vuint64m4_t *v0, vuint64m4_t *v1,
    vbool16_t mask, vuint64m4_t maskedoff0, vuint64m4_t
    maskedoff1, const uint64_t *base, ptrdiff_t stride, size_t
    vl);

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void vlsseg2bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vbool8_t mask, vuint8m1_t maskedoff0, vuint8m1_t maskedoff1,
    const uint8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2bu_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
    vbool4_t mask, vuint8m2_t maskedoff0, vuint8m2_t maskedoff1,
    const uint8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg2bu_v_u8m4_m (vuint8m4_t *v0, vuint8m4_t *v1,
    vbool2_t mask, vuint8m4_t maskedoff0, vuint8m4_t maskedoff1,
    const uint8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vbool16_t mask, vint16m1_t maskedoff0,
    vint16m1_t maskedoff1, vint16m1_t maskedoff2, const int16_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3b_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vbool8_t mask, vint16m2_t maskedoff0,
    vint16m2_t maskedoff1, vint16m2_t maskedoff2, const int16_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, const int32_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3b_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vbool16_t mask, vint32m2_t maskedoff0,
    vint32m2_t maskedoff1, vint32m2_t maskedoff2, const int32_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, const int64_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3b_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vbool32_t mask, vint64m2_t maskedoff0,
    vint64m2_t maskedoff1, vint64m2_t maskedoff2, const int64_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vbool8_t mask, vint8m1_t maskedoff0, vint8m1_t
    maskedoff1, vint8m1_t maskedoff2, const int8_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg3b_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, vbool4_t mask, vint8m2_t maskedoff0, vint8m2_t
    maskedoff1, vint8m2_t maskedoff2, const int8_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg3bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vbool16_t mask, vuint16m1_t maskedoff0,
    vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, const
    uint16_t *base, ptrdiff_t stride, size_t vl);

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void vlsseg3bu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vbool8_t mask, vuint16m2_t maskedoff0,
    vuint16m2_t maskedoff1, vuint16m2_t maskedoff2, const
    uint16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3bu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vbool16_t mask, vuint32m2_t maskedoff0,
    vuint32m2_t maskedoff1, vuint32m2_t maskedoff2, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3bu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vbool32_t mask, vuint64m2_t maskedoff0,
    vuint64m2_t maskedoff1, vuint64m2_t maskedoff2, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg3bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vbool8_t mask, vuint8m1_t maskedoff0,
    vuint8m1_t maskedoff1, vuint8m1_t maskedoff2, const uint8_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg3bu_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, vbool4_t mask, vuint8m2_t maskedoff0,
    vuint8m2_t maskedoff1, vuint8m2_t maskedoff2, const uint8_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg4b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vbool16_t mask, vint16m1_t
    maskedoff0, vint16m1_t maskedoff1, vint16m1_t maskedoff2,
    vint16m1_t maskedoff3, const int16_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4b_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vint16m2_t *v3, vbool8_t mask, vint16m2_t
    maskedoff0, vint16m2_t maskedoff1, vint16m2_t maskedoff2,
    vint16m2_t maskedoff3, const int16_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vbool32_t mask, vint32m1_t
    maskedoff0, vint32m1_t maskedoff1, vint32m1_t maskedoff2,
    vint32m1_t maskedoff3, const int32_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4b_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, vbool16_t mask, vint32m2_t
    maskedoff0, vint32m2_t maskedoff1, vint32m2_t maskedoff2,

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    vint32m2_t maskedoff3, const int32_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vbool64_t mask, vint64m1_t
    maskedoff0, vint64m1_t maskedoff1, vint64m1_t maskedoff2,
    vint64m1_t maskedoff3, const int64_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4b_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, vbool32_t mask, vint64m2_t
    maskedoff0, vint64m2_t maskedoff1, vint64m2_t maskedoff2,
    vint64m2_t maskedoff3, const int64_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vbool8_t mask, vint8m1_t maskedoff0,
    vint8m1_t maskedoff1, vint8m1_t maskedoff2, vint8m1_t
    maskedoff3, const int8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg4b_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, vint8m2_t *v3, vbool4_t mask, vint8m2_t maskedoff0,
    vint8m2_t maskedoff1, vint8m2_t maskedoff2, vint8m2_t
    maskedoff3, const int8_t *base, ptrdiff_t stride, size_t vl);
void vlsseg4bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vbool16_t mask,
    vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
    maskedoff2, vuint16m1_t maskedoff3, const uint16_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4bu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vuint16m2_t *v3, vbool8_t mask, vuint16m2_t
    maskedoff0, vuint16m2_t maskedoff1, vuint16m2_t maskedoff2,
    vuint16m2_t maskedoff3, const uint16_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, const uint32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4bu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, vbool16_t mask,
    vuint32m2_t maskedoff0, vuint32m2_t maskedoff1, vuint32m2_t
    maskedoff2, vuint32m2_t maskedoff3, const uint32_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, const uint64_t *base,
    ptrdiff_t stride, size_t vl);

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void vlsseg4bu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, vbool32_t mask,
    vuint64m2_t maskedoff0, vuint64m2_t maskedoff1, vuint64m2_t
    maskedoff2, vuint64m2_t maskedoff3, const uint64_t *base,
    ptrdiff_t stride, size_t vl);
void vlsseg4bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vbool8_t mask, vuint8m1_t
    maskedoff0, vuint8m1_t maskedoff1, vuint8m1_t maskedoff2,
    vuint8m1_t maskedoff3, const uint8_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg4bu_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, vuint8m2_t *v3, vbool4_t mask, vuint8m2_t
    maskedoff0, vuint8m2_t maskedoff1, vuint8m2_t maskedoff2,
    vuint8m2_t maskedoff3, const uint8_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg5b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vbool16_t
    mask, vint16m1_t maskedoff0, vint16m1_t maskedoff1,
    vint16m1_t maskedoff2, vint16m1_t maskedoff3, vint16m1_t
    maskedoff4, const int16_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg5b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vbool32_t
    mask, vint32m1_t maskedoff0, vint32m1_t maskedoff1,
    vint32m1_t maskedoff2, vint32m1_t maskedoff3, vint32m1_t
    maskedoff4, const int32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg5b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vbool64_t
    mask, vint64m1_t maskedoff0, vint64m1_t maskedoff1,
    vint64m1_t maskedoff2, vint64m1_t maskedoff3, vint64m1_t
    maskedoff4, const int64_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg5b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vbool8_t mask, vint8m1_t
    maskedoff0, vint8m1_t maskedoff1, vint8m1_t maskedoff2,
    vint8m1_t maskedoff3, vint8m1_t maskedoff4, const int8_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg5bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, vbool16_t
    mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
    vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
    maskedoff4, const uint16_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg5bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, vbool32_t

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mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
maskedoff4, const uint32_t *base, ptrdiff_t stride, size_t
vl);
void vlsseg5bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, vbool64_t
mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
maskedoff4, const uint64_t *base, ptrdiff_t stride, size_t
vl);
void vlsseg5bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vbool8_t
mask, vuint8m1_t maskedoff0, vuint8m1_t maskedoff1,
vuint8m1_t maskedoff2, vuint8m1_t maskedoff3, vuint8m1_t
maskedoff4, const uint8_t *base, ptrdiff_t stride, size_t
vl);
void vlsseg6b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
maskedoff1, vint16m1_t maskedoff2, vint16m1_t maskedoff3,
vint16m1_t maskedoff4, vint16m1_t maskedoff5, const int16_t
*base, ptrdiff_t stride, size_t vl);
void vlsseg6b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
*v5, vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
maskedoff1, vint32m1_t maskedoff2, vint32m1_t maskedoff3,
vint32m1_t maskedoff4, vint32m1_t maskedoff5, const int32_t
*base, ptrdiff_t stride, size_t vl);
void vlsseg6b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
*v5, vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
maskedoff1, vint64m1_t maskedoff2, vint64m1_t maskedoff3,
vint64m1_t maskedoff4, vint64m1_t maskedoff5, const int64_t
*base, ptrdiff_t stride, size_t vl);
void vlsseg6b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
*v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vbool8_t
mask, vint8m1_t maskedoff0, vint8m1_t maskedoff1, vint8m1_t
maskedoff2, vint8m1_t maskedoff3, vint8m1_t maskedoff4,
vint8m1_t maskedoff5, const int8_t *base, ptrdiff_t stride,
size_t vl);
void vlsseg6bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vbool16_t mask, vuint16m1_t maskedoff0,
vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, vuint16m1_t
maskedoff3, vuint16m1_t maskedoff4, vuint16m1_t maskedoff5,
const uint16_t *base, ptrdiff_t stride, size_t vl);

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void vlsseg6bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, vuint32m1_t
    maskedoff3, vuint32m1_t maskedoff4, vuint32m1_t maskedoff5,
    const uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, vuint64m1_t
    maskedoff3, vuint64m1_t maskedoff4, vuint64m1_t maskedoff5,
    const uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg6bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vbool8_t mask, vuint8m1_t maskedoff0, vuint8m1_t
    maskedoff1, vuint8m1_t maskedoff2, vuint8m1_t maskedoff3,
    vuint8m1_t maskedoff4, vuint8m1_t maskedoff5, const uint8_t
    *base, ptrdiff_t stride, size_t vl);
void vlsseg7b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, vbool16_t mask, vint16m1_t maskedoff0,
    vint16m1_t maskedoff1, vint16m1_t maskedoff2, vint16m1_t
    maskedoff3, vint16m1_t maskedoff4, vint16m1_t maskedoff5,
    vint16m1_t maskedoff6, const int16_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg7b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, vint32m1_t
    maskedoff3, vint32m1_t maskedoff4, vint32m1_t maskedoff5,
    vint32m1_t maskedoff6, const int32_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg7b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, vint64m1_t
    maskedoff3, vint64m1_t maskedoff4, vint64m1_t maskedoff5,
    vint64m1_t maskedoff6, const int64_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg7b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
    *v6, vbool8_t mask, vint8m1_t maskedoff0, vint8m1_t
    maskedoff1, vint8m1_t maskedoff2, vint8m1_t maskedoff3,
    vint8m1_t maskedoff4, vint8m1_t maskedoff5, vint8m1_t
    maskedoff6, const int8_t *base, ptrdiff_t stride, size_t vl);

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void vlsseg7bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, vbool16_t mask,
    vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
    maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t maskedoff4,
    vuint16m1_t maskedoff5, vuint16m1_t maskedoff6, const
    uint16_t *base, ptrdiff_t stride, size_t vl);
void vlsseg7bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t maskedoff4,
    vuint32m1_t maskedoff5, vuint32m1_t maskedoff6, const
    uint32_t *base, ptrdiff_t stride, size_t vl);
void vlsseg7bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t maskedoff4,
    vuint64m1_t maskedoff5, vuint64m1_t maskedoff6, const
    uint64_t *base, ptrdiff_t stride, size_t vl);
void vlsseg7bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, vbool8_t mask, vuint8m1_t maskedoff0,
    vuint8m1_t maskedoff1, vuint8m1_t maskedoff2, vuint8m1_t
    maskedoff3, vuint8m1_t maskedoff4, vuint8m1_t maskedoff5,
    vuint8m1_t maskedoff6, const uint8_t *base, ptrdiff_t
    stride, size_t vl);
void vlsseg8b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, vint16m1_t *v7, vbool16_t mask,
    vint16m1_t maskedoff0, vint16m1_t maskedoff1, vint16m1_t
    maskedoff2, vint16m1_t maskedoff3, vint16m1_t maskedoff4,
    vint16m1_t maskedoff5, vint16m1_t maskedoff6, vint16m1_t
    maskedoff7, const int16_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg8b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, vbool32_t mask,
    vint32m1_t maskedoff0, vint32m1_t maskedoff1, vint32m1_t
    maskedoff2, vint32m1_t maskedoff3, vint32m1_t maskedoff4,
    vint32m1_t maskedoff5, vint32m1_t maskedoff6, vint32m1_t
    maskedoff7, const int32_t *base, ptrdiff_t stride, size_t
    vl);
void vlsseg8b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t

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*v5, vint64m1_t *v6, vint64m1_t *v7, vbool64_t mask,
vint64m1_t maskedoff0, vint64m1_t maskedoff1, vint64m1_t
maskedoff2, vint64m1_t maskedoff3, vint64m1_t maskedoff4,
vint64m1_t maskedoff5, vint64m1_t maskedoff6, vint64m1_t
maskedoff7, const int64_t *base, ptrdiff_t stride, size_t
vl);
void vlsseg8b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
*v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
*v6, vint8m1_t *v7, vbool8_t mask, vint8m1_t maskedoff0,
vint8m1_t maskedoff1, vint8m1_t maskedoff2, vint8m1_t
maskedoff3, vint8m1_t maskedoff4, vint8m1_t maskedoff5,
vint8m1_t maskedoff6, vint8m1_t maskedoff7, const int8_t
*base, ptrdiff_t stride, size_t vl);
void vlsseg8bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, vbool16_t
mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
maskedoff4, vuint16m1_t maskedoff5, vuint16m1_t maskedoff6,
vuint16m1_t maskedoff7, const uint16_t *base, ptrdiff_t
stride, size_t vl);
void vlsseg8bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, vbool32_t
mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
maskedoff4, vuint32m1_t maskedoff5, vuint32m1_t maskedoff6,
vuint32m1_t maskedoff7, const uint32_t *base, ptrdiff_t
stride, size_t vl);
void vlsseg8bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, vbool64_t
mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
maskedoff4, vuint64m1_t maskedoff5, vuint64m1_t maskedoff6,
vuint64m1_t maskedoff7, const uint64_t *base, ptrdiff_t
stride, size_t vl);
void vlsseg8bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
*v5, vuint8m1_t *v6, vuint8m1_t *v7, vbool8_t mask,
vuint8m1_t maskedoff0, vuint8m1_t maskedoff1, vuint8m1_t
maskedoff2, vuint8m1_t maskedoff3, vuint8m1_t maskedoff4,
vuint8m1_t maskedoff5, vuint8m1_t maskedoff6, vuint8m1_t
maskedoff7, const uint8_t *base, ptrdiff_t stride, size_t
vl);

```

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void vssseg2b_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t stride, vint16m1_t v0, vint16m1_t v1, size_t vl);
void vssseg2b_v_i16m2_m (vbool8_t mask, int16_t *base, ptrdiff_t
    stride, vint16m2_t v0, vint16m2_t v1, size_t vl);
void vssseg2b_v_i16m4_m (vbool4_t mask, int16_t *base, ptrdiff_t
    stride, vint16m4_t v0, vint16m4_t v1, size_t vl);
void vssseg2b_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, size_t vl);
void vssseg2b_v_i32m2_m (vbool16_t mask, int32_t *base,
    ptrdiff_t stride, vint32m2_t v0, vint32m2_t v1, size_t vl);
void vssseg2b_v_i32m4_m (vbool8_t mask, int32_t *base, ptrdiff_t
    stride, vint32m4_t v0, vint32m4_t v1, size_t vl);
void vssseg2b_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, size_t vl);
void vssseg2b_v_i64m2_m (vbool32_t mask, int64_t *base,
    ptrdiff_t stride, vint64m2_t v0, vint64m2_t v1, size_t vl);
void vssseg2b_v_i64m4_m (vbool16_t mask, int64_t *base,
    ptrdiff_t stride, vint64m4_t v0, vint64m4_t v1, size_t vl);
void vssseg2b_v_i8m1_m (vbool8_t mask, int8_t *base, ptrdiff_t
    stride, vint8m1_t v0, vint8m1_t v1, size_t vl);
void vssseg2b_v_i8m2_m (vbool4_t mask, int8_t *base, ptrdiff_t
    stride, vint8m2_t v0, vint8m2_t v1, size_t vl);
void vssseg2b_v_i8m4_m (vbool2_t mask, int8_t *base, ptrdiff_t
    stride, vint8m4_t v0, vint8m4_t v1, size_t vl);
void vssseg2b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m1_t v0, vuint16m1_t v1, size_t vl);
void vssseg2b_v_u16m2_m (vbool8_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m2_t v0, vuint16m2_t v1, size_t vl);
void vssseg2b_v_u16m4_m (vbool4_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m4_t v0, vuint16m4_t v1, size_t vl);
void vssseg2b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1, size_t vl);
void vssseg2b_v_u32m2_m (vbool16_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m2_t v0, vuint32m2_t v1, size_t vl);
void vssseg2b_v_u32m4_m (vbool8_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m4_t v0, vuint32m4_t v1, size_t vl);
void vssseg2b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1, size_t vl);
void vssseg2b_v_u64m2_m (vbool32_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m2_t v0, vuint64m2_t v1, size_t vl);
void vssseg2b_v_u64m4_m (vbool16_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m4_t v0, vuint64m4_t v1, size_t vl);
void vssseg2b_v_u8m1_m (vbool8_t mask, uint8_t *base, ptrdiff_t
    stride, vuint8m1_t v0, vuint8m1_t v1, size_t vl);
void vssseg2b_v_u8m2_m (vbool4_t mask, uint8_t *base, ptrdiff_t
    stride, vuint8m2_t v0, vuint8m2_t v1, size_t vl);

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void vssseg2b_v_u8m4_m (vbool2_t mask, uint8_t *base, ptrdiff_t
    stride, vuint8m4_t v0, vuint8m4_t v1, size_t vl);
void vssseg3b_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t stride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, size_t vl);
void vssseg3b_v_i16m2_m (vbool8_t mask, int16_t *base, ptrdiff_t
    stride, vint16m2_t v0, vint16m2_t v1, vint16m2_t v2, size_t
    vl);
void vssseg3b_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, size_t vl);
void vssseg3b_v_i32m2_m (vbool16_t mask, int32_t *base,
    ptrdiff_t stride, vint32m2_t v0, vint32m2_t v1, vint32m2_t
    v2, size_t vl);
void vssseg3b_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, size_t vl);
void vssseg3b_v_i64m2_m (vbool32_t mask, int64_t *base,
    ptrdiff_t stride, vint64m2_t v0, vint64m2_t v1, vint64m2_t
    v2, size_t vl);
void vssseg3b_v_i8m1_m (vbool8_t mask, int8_t *base, ptrdiff_t
    stride, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, size_t vl);
void vssseg3b_v_i8m2_m (vbool4_t mask, int8_t *base, ptrdiff_t
    stride, vint8m2_t v0, vint8m2_t v1, vint8m2_t v2, size_t vl);
void vssseg3b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, size_t vl);
void vssseg3b_v_u16m2_m (vbool8_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m2_t v0, vuint16m2_t v1,
    vuint16m2_t v2, size_t vl);
void vssseg3b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, size_t vl);
void vssseg3b_v_u32m2_m (vbool16_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m2_t v0, vuint32m2_t v1,
    vuint32m2_t v2, size_t vl);
void vssseg3b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, size_t vl);
void vssseg3b_v_u64m2_m (vbool32_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m2_t v0, vuint64m2_t v1,
    vuint64m2_t v2, size_t vl);
void vssseg3b_v_u8m1_m (vbool8_t mask, uint8_t *base, ptrdiff_t
    stride, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2, size_t
    vl);

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void vssseg3b_v_u8m2_m (vbool4_t mask, uint8_t *base, ptrdiff_t
    stride, vuint8m2_t v0, vuint8m2_t v1, vuint8m2_t v2, size_t
    vl);
void vssseg4b_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t stride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, size_t vl);
void vssseg4b_v_i16m2_m (vbool8_t mask, int16_t *base, ptrdiff_t
    stride, vint16m2_t v0, vint16m2_t v1, vint16m2_t v2,
    vint16m2_t v3, size_t vl);
void vssseg4b_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, size_t vl);
void vssseg4b_v_i32m2_m (vbool16_t mask, int32_t *base,
    ptrdiff_t stride, vint32m2_t v0, vint32m2_t v1, vint32m2_t
    v2, vint32m2_t v3, size_t vl);
void vssseg4b_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, size_t vl);
void vssseg4b_v_i64m2_m (vbool32_t mask, int64_t *base,
    ptrdiff_t stride, vint64m2_t v0, vint64m2_t v1, vint64m2_t
    v2, vint64m2_t v3, size_t vl);
void vssseg4b_v_i8m1_m (vbool8_t mask, int8_t *base, ptrdiff_t
    stride, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t
    v3, size_t vl);
void vssseg4b_v_i8m2_m (vbool4_t mask, int8_t *base, ptrdiff_t
    stride, vint8m2_t v0, vint8m2_t v1, vint8m2_t v2, vint8m2_t
    v3, size_t vl);
void vssseg4b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, size_t vl);
void vssseg4b_v_u16m2_m (vbool8_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m2_t v0, vuint16m2_t v1,
    vuint16m2_t v2, vuint16m2_t v3, size_t vl);
void vssseg4b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, size_t vl);
void vssseg4b_v_u32m2_m (vbool16_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m2_t v0, vuint32m2_t v1,
    vuint32m2_t v2, vuint32m2_t v3, size_t vl);
void vssseg4b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, size_t vl);
void vssseg4b_v_u64m2_m (vbool32_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m2_t v0, vuint64m2_t v1,
    vuint64m2_t v2, vuint64m2_t v3, size_t vl);

```

```

void vssseg4b_v_u8m1_m (vbool8_t mask, uint8_t *base, ptrdiff_t
    stride, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2,
    vuint8m1_t v3, size_t vl);
void vssseg4b_v_u8m2_m (vbool4_t mask, uint8_t *base, ptrdiff_t
    stride, vuint8m2_t v0, vuint8m2_t v1, vuint8m2_t v2,
    vuint8m2_t v3, size_t vl);
void vssseg5b_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t stride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, size_t vl);
void vssseg5b_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, size_t vl);
void vssseg5b_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, size_t vl);
void vssseg5b_v_i8m1_m (vbool8_t mask, int8_t *base, ptrdiff_t
    stride, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t
    v3, vint8m1_t v4, size_t vl);
void vssseg5b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, size_t vl);
void vssseg5b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, size_t vl);
void vssseg5b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, size_t vl);
void vssseg5b_v_u8m1_m (vbool8_t mask, uint8_t *base, ptrdiff_t
    stride, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2,
    vuint8m1_t v3, vuint8m1_t v4, size_t vl);
void vssseg6b_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t stride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, size_t vl);
void vssseg6b_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, size_t vl);
void vssseg6b_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, size_t vl);
void vssseg6b_v_i8m1_m (vbool8_t mask, int8_t *base, ptrdiff_t
    stride, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t
    v3, vint8m1_t v4, vint8m1_t v5, size_t vl);
void vssseg6b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
    v5, size_t vl);

```

```

void vssseg6b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, size_t vl);
void vssseg6b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, size_t vl);
void vssseg6b_v_u8m1_m (vbool8_t mask, uint8_t *base, ptrdiff_t
    stride, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2,
    vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t v5, size_t vl);
void vssseg7b_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t stride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, vint16m1_t
    v6, size_t vl);
void vssseg7b_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, vint32m1_t
    v6, size_t vl);
void vssseg7b_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, vint64m1_t
    v6, size_t vl);
void vssseg7b_v_i8m1_m (vbool8_t mask, int8_t *base, ptrdiff_t
    stride, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t
    v3, vint8m1_t v4, vint8m1_t v5, vint8m1_t v6, size_t vl);
void vssseg7b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
    v5, vuint16m1_t v6, size_t vl);
void vssseg7b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, vuint32m1_t v6, size_t vl);
void vssseg7b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, vuint64m1_t v6, size_t vl);
void vssseg7b_v_u8m1_m (vbool8_t mask, uint8_t *base, ptrdiff_t
    stride, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2,
    vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t v5, vuint8m1_t v6,
    size_t vl);
void vssseg8b_v_i16m1_m (vbool16_t mask, int16_t *base,
    ptrdiff_t stride, vint16m1_t v0, vint16m1_t v1, vint16m1_t
    v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t v5, vint16m1_t
    v6, vint16m1_t v7, size_t vl);

```

```

void vssseg8b_v_i32m1_m (vbool32_t mask, int32_t *base,
    ptrdiff_t stride, vint32m1_t v0, vint32m1_t v1, vint32m1_t
    v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t v5, vint32m1_t
    v6, vint32m1_t v7, size_t vl);
void vssseg8b_v_i64m1_m (vbool64_t mask, int64_t *base,
    ptrdiff_t stride, vint64m1_t v0, vint64m1_t v1, vint64m1_t
    v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t v5, vint64m1_t
    v6, vint64m1_t v7, size_t vl);
void vssseg8b_v_i8m1_m (vbool8_t mask, int8_t *base, ptrdiff_t
    stride, vint8m1_t v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t
    v3, vint8m1_t v4, vint8m1_t v5, vint8m1_t v6, vint8m1_t v7,
    size_t vl);
void vssseg8b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    ptrdiff_t stride, vuint16m1_t v0, vuint16m1_t v1,
    vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t v4, vuint16m1_t
    v5, vuint16m1_t v6, vuint16m1_t v7, size_t vl);
void vssseg8b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    ptrdiff_t stride, vuint32m1_t v0, vuint32m1_t v1,
    vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t v4, vuint32m1_t
    v5, vuint32m1_t v6, vuint32m1_t v7, size_t vl);
void vssseg8b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    ptrdiff_t stride, vuint64m1_t v0, vuint64m1_t v1,
    vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t v4, vuint64m1_t
    v5, vuint64m1_t v6, vuint64m1_t v7, size_t vl);
void vssseg8b_v_u8m1_m (vbool8_t mask, uint8_t *base, ptrdiff_t
    stride, vuint8m1_t v0, vuint8m1_t v1, vuint8m1_t v2,
    vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t v5, vuint8m1_t v6,
    vuint8m1_t v7, size_t vl);

```

## Vector Unit-Stride Segment 16b Loads and Stores:

### Prototypes:

```

void vlseg2h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, const
    int16_t *base, size_t vl);
void vlseg2h_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1, const
    int16_t *base, size_t vl);
void vlseg2h_v_i16m4 (vint16m4_t *v0, vint16m4_t *v1, const
    int16_t *base, size_t vl);
void vlseg2h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, const
    int32_t *base, size_t vl);
void vlseg2h_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, const
    int32_t *base, size_t vl);
void vlseg2h_v_i32m4 (vint32m4_t *v0, vint32m4_t *v1, const
    int32_t *base, size_t vl);

```

```

void vlseg2h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, const
    int64_t *base, size_t vl);
void vlseg2h_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, const
    int64_t *base, size_t vl);
void vlseg2h_v_i64m4 (vint64m4_t *v0, vint64m4_t *v1, const
    int64_t *base, size_t vl);
void vlseg2hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1, const
    uint16_t *base, size_t vl);
void vlseg2hu_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1, const
    uint16_t *base, size_t vl);
void vlseg2hu_v_u16m4 (vuint16m4_t *v0, vuint16m4_t *v1, const
    uint16_t *base, size_t vl);
void vlseg2hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1, const
    uint32_t *base, size_t vl);
void vlseg2hu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1, const
    uint32_t *base, size_t vl);
void vlseg2hu_v_u32m4 (vuint32m4_t *v0, vuint32m4_t *v1, const
    uint32_t *base, size_t vl);
void vlseg2hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1, const
    uint64_t *base, size_t vl);
void vlseg2hu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1, const
    uint64_t *base, size_t vl);
void vlseg2hu_v_u64m4 (vuint64m4_t *v0, vuint64m4_t *v1, const
    uint64_t *base, size_t vl);
void vlseg3h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, vint16m1_t
    *v2, const int16_t *base, size_t vl);
void vlseg3h_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1, vint16m2_t
    *v2, const int16_t *base, size_t vl);
void vlseg3h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
    *v2, const int32_t *base, size_t vl);
void vlseg3h_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, vint32m2_t
    *v2, const int32_t *base, size_t vl);
void vlseg3h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
    *v2, const int64_t *base, size_t vl);
void vlseg3h_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, vint64m2_t
    *v2, const int64_t *base, size_t vl);
void vlseg3hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, const uint16_t *base, size_t vl);
void vlseg3hu_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, const uint16_t *base, size_t vl);
void vlseg3hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, const uint32_t *base, size_t vl);
void vlseg3hu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, const uint32_t *base, size_t vl);
void vlseg3hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, const uint64_t *base, size_t vl);

```

```

void vlseg3hu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, const uint64_t *base, size_t vl);
void vlseg4h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, vint16m1_t
    *v2, vint16m1_t *v3, const int16_t *base, size_t vl);
void vlseg4h_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1, vint16m2_t
    *v2, vint16m2_t *v3, const int16_t *base, size_t vl);
void vlseg4h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
    *v2, vint32m1_t *v3, const int32_t *base, size_t vl);
void vlseg4h_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, vint32m2_t
    *v2, vint32m2_t *v3, const int32_t *base, size_t vl);
void vlseg4h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
    *v2, vint64m1_t *v3, const int64_t *base, size_t vl);
void vlseg4h_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, vint64m2_t
    *v2, vint64m2_t *v3, const int64_t *base, size_t vl);
void vlseg4hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, const uint16_t *base,
    size_t vl);
void vlseg4hu_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vuint16m2_t *v3, const uint16_t *base,
    size_t vl);
void vlseg4hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, const uint32_t *base,
    size_t vl);
void vlseg4hu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, const uint32_t *base,
    size_t vl);
void vlseg4hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, const uint64_t *base,
    size_t vl);
void vlseg4hu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, const uint64_t *base,
    size_t vl);
void vlseg5h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, vint16m1_t
    *v2, vint16m1_t *v3, vint16m1_t *v4, const int16_t *base,
    size_t vl);
void vlseg5h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
    *v2, vint32m1_t *v3, vint32m1_t *v4, const int32_t *base,
    size_t vl);
void vlseg5h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
    *v2, vint64m1_t *v3, vint64m1_t *v4, const int64_t *base,
    size_t vl);
void vlseg5hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, const
    uint16_t *base, size_t vl);
void vlseg5hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, const

```

```

uint32_t *base, size_t vl);
void vlseg5hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, const
    uint64_t *base, size_t vl);
void vlseg6h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, vint16m1_t
    *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t *v5, const
    int16_t *base, size_t vl);
void vlseg6h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
    *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t *v5, const
    int32_t *base, size_t vl);
void vlseg6h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
    *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t *v5, const
    int64_t *base, size_t vl);
void vlseg6hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, const uint16_t *base, size_t vl);
void vlseg6hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, const uint32_t *base, size_t vl);
void vlseg6hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, const uint64_t *base, size_t vl);
void vlseg7h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, vint16m1_t
    *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t *v5,
    vint16m1_t *v6, const int16_t *base, size_t vl);
void vlseg7h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
    *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t *v5,
    vint32m1_t *v6, const int32_t *base, size_t vl);
void vlseg7h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
    *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t *v5,
    vint64m1_t *v6, const int64_t *base, size_t vl);
void vlseg7hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, const uint16_t *base,
    size_t vl);
void vlseg7hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, const uint32_t *base,
    size_t vl);
void vlseg7hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, const uint64_t *base,
    size_t vl);
void vlseg8h_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, vint16m1_t
    *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t *v5,
    vint16m1_t *v6, vint16m1_t *v7, const int16_t *base, size_t

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    vl);
void vlseg8h_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
*v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t *v5,
vint32m1_t *v6, vint32m1_t *v7, const int32_t *base, size_t
vl);
void vlseg8h_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
*v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t *v5,
vint64m1_t *v6, vint64m1_t *v7, const int64_t *base, size_t
vl);
void vlseg8hu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, const
uint16_t *base, size_t vl);
void vlseg8hu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, const
uint32_t *base, size_t vl);
void vlseg8hu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, const
uint64_t *base, size_t vl);
void vsseg2h_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
v1, size_t vl);
void vsseg2h_v_i16m2 (int16_t *base, vint16m2_t v0, vint16m2_t
v1, size_t vl);
void vsseg2h_v_i16m4 (int16_t *base, vint16m4_t v0, vint16m4_t
v1, size_t vl);
void vsseg2h_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
v1, size_t vl);
void vsseg2h_v_i32m2 (int32_t *base, vint32m2_t v0, vint32m2_t
v1, size_t vl);
void vsseg2h_v_i32m4 (int32_t *base, vint32m4_t v0, vint32m4_t
v1, size_t vl);
void vsseg2h_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
v1, size_t vl);
void vsseg2h_v_i64m2 (int64_t *base, vint64m2_t v0, vint64m2_t
v1, size_t vl);
void vsseg2h_v_i64m4 (int64_t *base, vint64m4_t v0, vint64m4_t
v1, size_t vl);
void vsseg2h_v_u16m1 (uint16_t *base, vuint16m1_t v0,
vuint16m1_t v1, size_t vl);
void vsseg2h_v_u16m2 (uint16_t *base, vuint16m2_t v0,
vuint16m2_t v1, size_t vl);
void vsseg2h_v_u16m4 (uint16_t *base, vuint16m4_t v0,
vuint16m4_t v1, size_t vl);

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void vsseg2h_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, size_t vl);
void vsseg2h_v_u32m2 (uint32_t *base, vuint32m2_t v0,
    vuint32m2_t v1, size_t vl);
void vsseg2h_v_u32m4 (uint32_t *base, vuint32m4_t v0,
    vuint32m4_t v1, size_t vl);
void vsseg2h_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, size_t vl);
void vsseg2h_v_u64m2 (uint64_t *base, vuint64m2_t v0,
    vuint64m2_t v1, size_t vl);
void vsseg2h_v_u64m4 (uint64_t *base, vuint64m4_t v0,
    vuint64m4_t v1, size_t vl);
void vsseg3h_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, size_t vl);
void vsseg3h_v_i16m2 (int16_t *base, vint16m2_t v0, vint16m2_t
    v1, vint16m2_t v2, size_t vl);
void vsseg3h_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, size_t vl);
void vsseg3h_v_i32m2 (int32_t *base, vint32m2_t v0, vint32m2_t
    v1, vint32m2_t v2, size_t vl);
void vsseg3h_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, size_t vl);
void vsseg3h_v_i64m2 (int64_t *base, vint64m2_t v0, vint64m2_t
    v1, vint64m2_t v2, size_t vl);
void vsseg3h_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, size_t vl);
void vsseg3h_v_u16m2 (uint16_t *base, vuint16m2_t v0,
    vuint16m2_t v1, vuint16m2_t v2, size_t vl);
void vsseg3h_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, size_t vl);
void vsseg3h_v_u32m2 (uint32_t *base, vuint32m2_t v0,
    vuint32m2_t v1, vuint32m2_t v2, size_t vl);
void vsseg3h_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, size_t vl);
void vsseg3h_v_u64m2 (uint64_t *base, vuint64m2_t v0,
    vuint64m2_t v1, vuint64m2_t v2, size_t vl);
void vsseg4h_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, vint16m1_t v3, size_t vl);
void vsseg4h_v_i16m2 (int16_t *base, vint16m2_t v0, vint16m2_t
    v1, vint16m2_t v2, vint16m2_t v3, size_t vl);
void vsseg4h_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, size_t vl);
void vsseg4h_v_i32m2 (int32_t *base, vint32m2_t v0, vint32m2_t
    v1, vint32m2_t v2, vint32m2_t v3, size_t vl);
void vsseg4h_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, size_t vl);

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void vsseg4h_v_i64m2 (int64_t *base, vint64m2_t v0, vint64m2_t
    v1, vint64m2_t v2, vint64m2_t v3, size_t vl);
void vsseg4h_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t v3, size_t vl);
void vsseg4h_v_u16m2 (uint16_t *base, vuint16m2_t v0,
    vuint16m2_t v1, vuint16m2_t v2, vuint16m2_t v3, size_t vl);
void vsseg4h_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, size_t vl);
void vsseg4h_v_u32m2 (uint32_t *base, vuint32m2_t v0,
    vuint32m2_t v1, vuint32m2_t v2, vuint32m2_t v3, size_t vl);
void vsseg4h_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, size_t vl);
void vsseg4h_v_u64m2 (uint64_t *base, vuint64m2_t v0,
    vuint64m2_t v1, vuint64m2_t v2, vuint64m2_t v3, size_t vl);
void vsseg5h_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, vint16m1_t v3, vint16m1_t v4, size_t vl);
void vsseg5h_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, vint32m1_t v4, size_t vl);
void vsseg5h_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, vint64m1_t v4, size_t vl);
void vsseg5h_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t
    v4, size_t vl);
void vsseg5h_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t
    v4, size_t vl);
void vsseg5h_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t
    v4, size_t vl);
void vsseg6h_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t
    v5, size_t vl);
void vsseg6h_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t
    v5, size_t vl);
void vsseg6h_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t
    v5, size_t vl);
void vsseg6h_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t
    v4, vuint16m1_t v5, size_t vl);
void vsseg6h_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t
    v4, vuint32m1_t v5, size_t vl);
void vsseg6h_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t

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    v4, vuint64m1_t v5, size_t vl);
void vsseg7h_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t
    v5, vint16m1_t v6, size_t vl);
void vsseg7h_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t
    v5, vint32m1_t v6, size_t vl);
void vsseg7h_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t
    v5, vint64m1_t v6, size_t vl);
void vsseg7h_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t
    v4, vuint16m1_t v5, vuint16m1_t v6, size_t vl);
void vsseg7h_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t
    v4, vuint32m1_t v5, vuint32m1_t v6, size_t vl);
void vsseg7h_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t
    v4, vuint64m1_t v5, vuint64m1_t v6, size_t vl);
void vsseg8h_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t
    v5, vint16m1_t v6, vint16m1_t v7, size_t vl);
void vsseg8h_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t
    v5, vint32m1_t v6, vint32m1_t v7, size_t vl);
void vsseg8h_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t
    v5, vint64m1_t v6, vint64m1_t v7, size_t vl);
void vsseg8h_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t
    v4, vuint16m1_t v5, vuint16m1_t v6, vuint16m1_t v7, size_t
    vl);
void vsseg8h_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t
    v4, vuint32m1_t v5, vuint32m1_t v6, vuint32m1_t v7, size_t
    vl);
void vsseg8h_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t
    v4, vuint64m1_t v5, vuint64m1_t v6, vuint64m1_t v7, size_t
    vl);
// masked functions
void vlseg2h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
    maskedoff1, const int16_t *base, size_t vl);
void vlseg2h_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1, vbool8_t
    mask, vint16m2_t maskedoff0, vint16m2_t maskedoff1, const

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    int16_t *base, size_t vl);
void vlseg2h_v_i16m4_m (vint16m4_t *v0, vint16m4_t *v1, vbool4_t
    mask, vint16m4_t maskedoff0, vint16m4_t maskedoff1, const
    int16_t *base, size_t vl);
void vlseg2h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, const int32_t *base, size_t vl);
void vlseg2h_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vbool16_t mask, vint32m2_t maskedoff0, vint32m2_t
    maskedoff1, const int32_t *base, size_t vl);
void vlseg2h_v_i32m4_m (vint32m4_t *v0, vint32m4_t *v1, vbool8_t
    mask, vint32m4_t maskedoff0, vint32m4_t maskedoff1, const
    int32_t *base, size_t vl);
void vlseg2h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, const int64_t *base, size_t vl);
void vlseg2h_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vbool32_t mask, vint64m2_t maskedoff0, vint64m2_t
    maskedoff1, const int64_t *base, size_t vl);
void vlseg2h_v_i64m4_m (vint64m4_t *v0, vint64m4_t *v1,
    vbool16_t mask, vint64m4_t maskedoff0, vint64m4_t
    maskedoff1, const int64_t *base, size_t vl);
void vlseg2hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vbool16_t mask, vuint16m1_t maskedoff0, vuint16m1_t
    maskedoff1, const uint16_t *base, size_t vl);
void vlseg2hu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vbool8_t mask, vuint16m2_t maskedoff0, vuint16m2_t
    maskedoff1, const uint16_t *base, size_t vl);
void vlseg2hu_v_u16m4_m (vuint16m4_t *v0, vuint16m4_t *v1,
    vbool4_t mask, vuint16m4_t maskedoff0, vuint16m4_t
    maskedoff1, const uint16_t *base, size_t vl);
void vlseg2hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vbool32_t mask, vuint32m1_t maskedoff0, vuint32m1_t
    maskedoff1, const uint32_t *base, size_t vl);
void vlseg2hu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vbool16_t mask, vuint32m2_t maskedoff0, vuint32m2_t
    maskedoff1, const uint32_t *base, size_t vl);
void vlseg2hu_v_u32m4_m (vuint32m4_t *v0, vuint32m4_t *v1,
    vbool8_t mask, vuint32m4_t maskedoff0, vuint32m4_t
    maskedoff1, const uint32_t *base, size_t vl);
void vlseg2hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vbool64_t mask, vuint64m1_t maskedoff0, vuint64m1_t
    maskedoff1, const uint64_t *base, size_t vl);
void vlseg2hu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vbool32_t mask, vuint64m2_t maskedoff0, vuint64m2_t
    maskedoff1, const uint64_t *base, size_t vl);

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void vlseg2hu_v_u64m4_m (vuint64m4_t *v0, vuint64m4_t *v1,
    vbool16_t mask, vuint64m4_t maskedoff0, vuint64m4_t
    maskedoff1, const uint64_t *base, size_t vl);
void vlseg3h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vbool16_t mask, vint16m1_t maskedoff0,
    vint16m1_t maskedoff1, vint16m1_t maskedoff2, const int16_t
    *base, size_t vl);
void vlseg3h_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vbool8_t mask, vint16m2_t maskedoff0,
    vint16m2_t maskedoff1, vint16m2_t maskedoff2, const int16_t
    *base, size_t vl);
void vlseg3h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, const int32_t
    *base, size_t vl);
void vlseg3h_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vbool16_t mask, vint32m2_t maskedoff0,
    vint32m2_t maskedoff1, vint32m2_t maskedoff2, const int32_t
    *base, size_t vl);
void vlseg3h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, const int64_t
    *base, size_t vl);
void vlseg3h_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vbool32_t mask, vint64m2_t maskedoff0,
    vint64m2_t maskedoff1, vint64m2_t maskedoff2, const int64_t
    *base, size_t vl);
void vlseg3hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vbool16_t mask, vuint16m1_t maskedoff0,
    vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, const
    uint16_t *base, size_t vl);
void vlseg3hu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vbool8_t mask, vuint16m2_t maskedoff0,
    vuint16m2_t maskedoff1, vuint16m2_t maskedoff2, const
    uint16_t *base, size_t vl);
void vlseg3hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, const
    uint32_t *base, size_t vl);
void vlseg3hu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vbool16_t mask, vuint32m2_t maskedoff0,
    vuint32m2_t maskedoff1, vuint32m2_t maskedoff2, const
    uint32_t *base, size_t vl);
void vlseg3hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, const

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uint64_t *base, size_t vl);
void vlseg3hu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
vuint64m2_t *v2, vbool32_t mask, vuint64m2_t maskedoff0,
vuint64m2_t maskedoff1, vuint64m2_t maskedoff2, const
uint64_t *base, size_t vl);
void vlseg4h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vbool16_t mask, vint16m1_t
maskedoff0, vint16m1_t maskedoff1, vint16m1_t maskedoff2,
vint16m1_t maskedoff3, const int16_t *base, size_t vl);
void vlseg4h_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
vint16m2_t *v2, vint16m2_t *v3, vbool8_t mask, vint16m2_t
maskedoff0, vint16m2_t maskedoff1, vint16m2_t maskedoff2,
vint16m2_t maskedoff3, const int16_t *base, size_t vl);
void vlseg4h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vbool32_t mask, vint32m1_t
maskedoff0, vint32m1_t maskedoff1, vint32m1_t maskedoff2,
vint32m1_t maskedoff3, const int32_t *base, size_t vl);
void vlseg4h_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
vint32m2_t *v2, vint32m2_t *v3, vbool16_t mask, vint32m2_t
maskedoff0, vint32m2_t maskedoff1, vint32m2_t maskedoff2,
vint32m2_t maskedoff3, const int32_t *base, size_t vl);
void vlseg4h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vbool64_t mask, vint64m1_t
maskedoff0, vint64m1_t maskedoff1, vint64m1_t maskedoff2,
vint64m1_t maskedoff3, const int64_t *base, size_t vl);
void vlseg4h_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
vint64m2_t *v2, vint64m2_t *v3, vbool32_t mask, vint64m2_t
maskedoff0, vint64m2_t maskedoff1, vint64m2_t maskedoff2,
vint64m2_t maskedoff3, const int64_t *base, size_t vl);
void vlseg4hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vbool16_t mask,
vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
maskedoff2, vuint16m1_t maskedoff3, const uint16_t *base,
size_t vl);
void vlseg4hu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
vuint16m2_t *v2, vuint16m2_t *v3, vbool8_t mask, vuint16m2_t
maskedoff0, vuint16m2_t maskedoff1, vuint16m2_t maskedoff2,
vuint16m2_t maskedoff3, const uint16_t *base, size_t vl);
void vlseg4hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vbool32_t mask,
vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
maskedoff2, vuint32m1_t maskedoff3, const uint32_t *base,
size_t vl);
void vlseg4hu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
vuint32m2_t *v2, vuint32m2_t *v3, vbool16_t mask,
vuint32m2_t maskedoff0, vuint32m2_t maskedoff1, vuint32m2_t

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        maskedoff2, vuint32m2_t maskedoff3, const uint32_t *base,
        size_t vl);
void vlseg4hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, const uint64_t *base,
    size_t vl);
void vlseg4hu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, vbool32_t mask,
    vuint64m2_t maskedoff0, vuint64m2_t maskedoff1, vuint64m2_t
    maskedoff2, vuint64m2_t maskedoff3, const uint64_t *base,
    size_t vl);
void vlseg5h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vbool16_t
    mask, vint16m1_t maskedoff0, vint16m1_t maskedoff1,
    vint16m1_t maskedoff2, vint16m1_t maskedoff3, vint16m1_t
    maskedoff4, const int16_t *base, size_t vl);
void vlseg5h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vbool32_t
    mask, vint32m1_t maskedoff0, vint32m1_t maskedoff1,
    vint32m1_t maskedoff2, vint32m1_t maskedoff3, vint32m1_t
    maskedoff4, const int32_t *base, size_t vl);
void vlseg5h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vbool64_t
    mask, vint64m1_t maskedoff0, vint64m1_t maskedoff1,
    vint64m1_t maskedoff2, vint64m1_t maskedoff3, vint64m1_t
    maskedoff4, const int64_t *base, size_t vl);
void vlseg5hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, vbool16_t
    mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
    vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
    maskedoff4, const uint16_t *base, size_t vl);
void vlseg5hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, const uint32_t *base, size_t vl);
void vlseg5hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, const uint64_t *base, size_t vl);
void vlseg6h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
    maskedoff1, vint16m1_t maskedoff2, vint16m1_t maskedoff3,

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    vint16m1_t maskedoff4, vint16m1_t maskedoff5, const int16_t
    *base, size_t vl);
void vlseg6h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, vint32m1_t maskedoff2, vint32m1_t maskedoff3,
    vint32m1_t maskedoff4, vint32m1_t maskedoff5, const int32_t
    *base, size_t vl);
void vlseg6h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, vint64m1_t maskedoff2, vint64m1_t maskedoff3,
    vint64m1_t maskedoff4, vint64m1_t maskedoff5, const int64_t
    *base, size_t vl);
void vlseg6hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vbool16_t mask, vuint16m1_t maskedoff0,
    vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, vuint16m1_t
    maskedoff3, vuint16m1_t maskedoff4, vuint16m1_t maskedoff5,
    const uint16_t *base, size_t vl);
void vlseg6hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, vuint32m1_t
    maskedoff3, vuint32m1_t maskedoff4, vuint32m1_t maskedoff5,
    const uint32_t *base, size_t vl);
void vlseg6hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, vuint64m1_t
    maskedoff3, vuint64m1_t maskedoff4, vuint64m1_t maskedoff5,
    const uint64_t *base, size_t vl);
void vlseg7h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, vbool16_t mask, vint16m1_t maskedoff0,
    vint16m1_t maskedoff1, vint16m1_t maskedoff2, vint16m1_t
    maskedoff3, vint16m1_t maskedoff4, vint16m1_t maskedoff5,
    vint16m1_t maskedoff6, const int16_t *base, size_t vl);
void vlseg7h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, vint32m1_t
    maskedoff3, vint32m1_t maskedoff4, vint32m1_t maskedoff5,
    vint32m1_t maskedoff6, const int32_t *base, size_t vl);
void vlseg7h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t

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*v5, vint64m1_t *v6, vbool64_t mask, vint64m1_t maskedoff0,
vint64m1_t maskedoff1, vint64m1_t maskedoff2, vint64m1_t
maskedoff3, vint64m1_t maskedoff4, vint64m1_t maskedoff5,
vint64m1_t maskedoff6, const int64_t *base, size_t vl);
void vlseg7hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vuint16m1_t *v6, vbool16_t mask,
vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t maskedoff4,
vuint16m1_t maskedoff5, vuint16m1_t maskedoff6, const
uint16_t *base, size_t vl);
void vlseg7hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vuint32m1_t *v6, vbool32_t mask,
vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t maskedoff4,
vuint32m1_t maskedoff5, vuint32m1_t maskedoff6, const
uint32_t *base, size_t vl);
void vlseg7hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
vuint64m1_t *v5, vuint64m1_t *v6, vbool64_t mask,
vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t maskedoff4,
vuint64m1_t maskedoff5, vuint64m1_t maskedoff6, const
uint64_t *base, size_t vl);
void vlseg8h_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vint16m1_t *v6, vint16m1_t *v7, vbool16_t mask,
vint16m1_t maskedoff0, vint16m1_t maskedoff1, vint16m1_t
maskedoff2, vint16m1_t maskedoff3, vint16m1_t maskedoff4,
vint16m1_t maskedoff5, vint16m1_t maskedoff6, vint16m1_t
maskedoff7, const int16_t *base, size_t vl);
void vlseg8h_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
*v5, vint32m1_t *v6, vint32m1_t *v7, vbool32_t mask,
vint32m1_t maskedoff0, vint32m1_t maskedoff1, vint32m1_t
maskedoff2, vint32m1_t maskedoff3, vint32m1_t maskedoff4,
vint32m1_t maskedoff5, vint32m1_t maskedoff6, vint32m1_t
maskedoff7, const int32_t *base, size_t vl);
void vlseg8h_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
*v5, vint64m1_t *v6, vint64m1_t *v7, vbool64_t mask,
vint64m1_t maskedoff0, vint64m1_t maskedoff1, vint64m1_t
maskedoff2, vint64m1_t maskedoff3, vint64m1_t maskedoff4,
vint64m1_t maskedoff5, vint64m1_t maskedoff6, vint64m1_t
maskedoff7, const int64_t *base, size_t vl);

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void vlseg8hu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, vbool16_t
    mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
    vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
    maskedoff4, vuint16m1_t maskedoff5, vuint16m1_t maskedoff6,
    vuint16m1_t maskedoff7, const uint16_t *base, size_t vl);
void vlseg8hu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, vuint32m1_t maskedoff5, vuint32m1_t maskedoff6,
    vuint32m1_t maskedoff7, const uint32_t *base, size_t vl);
void vlseg8hu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, vuint64m1_t maskedoff5, vuint64m1_t maskedoff6,
    vuint64m1_t maskedoff7, const uint64_t *base, size_t vl);
void vsseg2h_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, size_t vl);
void vsseg2h_v_i16m2_m (vbool8_t mask, int16_t *base, vint16m2_t
    v0, vint16m2_t v1, size_t vl);
void vsseg2h_v_i16m4_m (vbool4_t mask, int16_t *base, vint16m4_t
    v0, vint16m4_t v1, size_t vl);
void vsseg2h_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, size_t vl);
void vsseg2h_v_i32m2_m (vbool16_t mask, int32_t *base,
    vint32m2_t v0, vint32m2_t v1, size_t vl);
void vsseg2h_v_i32m4_m (vbool8_t mask, int32_t *base, vint32m4_t
    v0, vint32m4_t v1, size_t vl);
void vsseg2h_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, size_t vl);
void vsseg2h_v_i64m2_m (vbool32_t mask, int64_t *base,
    vint64m2_t v0, vint64m2_t v1, size_t vl);
void vsseg2h_v_i64m4_m (vbool16_t mask, int64_t *base,
    vint64m4_t v0, vint64m4_t v1, size_t vl);
void vsseg2h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, size_t vl);
void vsseg2h_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t v0, vuint16m2_t v1, size_t vl);
void vsseg2h_v_u16m4_m (vbool4_t mask, uint16_t *base,
    vuint16m4_t v0, vuint16m4_t v1, size_t vl);

```

```

void vsseg2h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, size_t vl);
void vsseg2h_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t v0, vuint32m2_t v1, size_t vl);
void vsseg2h_v_u32m4_m (vbool8_t mask, uint32_t *base,
    vuint32m4_t v0, vuint32m4_t v1, size_t vl);
void vsseg2h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, size_t vl);
void vsseg2h_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t v0, vuint64m2_t v1, size_t vl);
void vsseg2h_v_u64m4_m (vbool16_t mask, uint64_t *base,
    vuint64m4_t v0, vuint64m4_t v1, size_t vl);
void vsseg3h_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, size_t vl);
void vsseg3h_v_i16m2_m (vbool8_t mask, int16_t *base, vint16m2_t
    v0, vint16m2_t v1, vint16m2_t v2, size_t vl);
void vsseg3h_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, size_t vl);
void vsseg3h_v_i32m2_m (vbool16_t mask, int32_t *base,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, size_t vl);
void vsseg3h_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, size_t vl);
void vsseg3h_v_i64m2_m (vbool32_t mask, int64_t *base,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, size_t vl);
void vsseg3h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, size_t vl);
void vsseg3h_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, size_t vl);
void vsseg3h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, size_t vl);
void vsseg3h_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, size_t vl);
void vsseg3h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, size_t vl);
void vsseg3h_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, size_t vl);
void vsseg4h_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    size_t vl);
void vsseg4h_v_i16m2_m (vbool8_t mask, int16_t *base, vint16m2_t
    v0, vint16m2_t v1, vint16m2_t v2, vint16m2_t v3, size_t vl);
void vsseg4h_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    size_t vl);
void vsseg4h_v_i32m2_m (vbool16_t mask, int32_t *base,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, vint32m2_t v3,

```

```

    size_t vl);
void vsseg4h_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    size_t vl);
void vsseg4h_v_i64m2_m (vbool32_t mask, int64_t *base,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, vint64m2_t v3,
    size_t vl);
void vsseg4h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, size_t vl);
void vsseg4h_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, vuint16m2_t
    v3, size_t vl);
void vsseg4h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, size_t vl);
void vsseg4h_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, vuint32m2_t
    v3, size_t vl);
void vsseg4h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, size_t vl);
void vsseg4h_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, vuint64m2_t
    v3, size_t vl);
void vsseg5h_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, size_t vl);
void vsseg5h_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, size_t vl);
void vsseg5h_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, size_t vl);
void vsseg5h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, size_t vl);
void vsseg5h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, size_t vl);
void vsseg5h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, size_t vl);
void vsseg6h_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, size_t vl);

```

```

void vsseg6h_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, size_t vl);
void vsseg6h_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, size_t vl);
void vsseg6h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, size_t vl);
void vsseg6h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, size_t vl);
void vsseg6h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, size_t vl);
void vsseg7h_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, size_t vl);
void vsseg7h_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, size_t vl);
void vsseg7h_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, size_t vl);
void vsseg7h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6, size_t
    vl);
void vsseg7h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6, size_t
    vl);
void vsseg7h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6, size_t
    vl);
void vsseg8h_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, vint16m1_t v7,
    size_t vl);
void vsseg8h_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, vint32m1_t v7,
    size_t vl);
void vsseg8h_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,

```

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    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, vint64m1_t v7,
    size_t vl);
void vsseg8h_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6,
    vuint16m1_t v7, size_t vl);
void vsseg8h_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6,
    vuint32m1_t v7, size_t vl);
void vsseg8h_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6,
    vuint64m1_t v7, size_t vl);

```

## Vector Unit-Stride Segment 32b Loads and Stores:

### Prototypes:

```

void vlseg2w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, const
    int32_t *base, size_t vl);
void vlseg2w_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, const
    int32_t *base, size_t vl);
void vlseg2w_v_i32m4 (vint32m4_t *v0, vint32m4_t *v1, const
    int32_t *base, size_t vl);
void vlseg2w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, const
    int64_t *base, size_t vl);
void vlseg2w_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, const
    int64_t *base, size_t vl);
void vlseg2w_v_i64m4 (vint64m4_t *v0, vint64m4_t *v1, const
    int64_t *base, size_t vl);
void vlseg2wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1, const
    uint32_t *base, size_t vl);
void vlseg2wu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1, const
    uint32_t *base, size_t vl);
void vlseg2wu_v_u32m4 (vuint32m4_t *v0, vuint32m4_t *v1, const
    uint32_t *base, size_t vl);
void vlseg2wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1, const
    uint64_t *base, size_t vl);
void vlseg2wu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1, const
    uint64_t *base, size_t vl);
void vlseg2wu_v_u64m4 (vuint64m4_t *v0, vuint64m4_t *v1, const
    uint64_t *base, size_t vl);
void vlseg3w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
    *v2, const int32_t *base, size_t vl);

```

```

void vlseg3w_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, vint32m2_t
    *v2, const int32_t *base, size_t vl);
void vlseg3w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
    *v2, const int64_t *base, size_t vl);
void vlseg3w_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, vint64m2_t
    *v2, const int64_t *base, size_t vl);
void vlseg3wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, const uint32_t *base, size_t vl);
void vlseg3wu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, const uint32_t *base, size_t vl);
void vlseg3wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, const uint64_t *base, size_t vl);
void vlseg3wu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, const uint64_t *base, size_t vl);
void vlseg4w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
    *v2, vint32m1_t *v3, const int32_t *base, size_t vl);
void vlseg4w_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, vint32m2_t
    *v2, vint32m2_t *v3, const int32_t *base, size_t vl);
void vlseg4w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
    *v2, vint64m1_t *v3, const int64_t *base, size_t vl);
void vlseg4w_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, vint64m2_t
    *v2, vint64m2_t *v3, const int64_t *base, size_t vl);
void vlseg4wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, const uint32_t *base,
    size_t vl);
void vlseg4wu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, const uint32_t *base,
    size_t vl);
void vlseg4wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, const uint64_t *base,
    size_t vl);
void vlseg4wu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, const uint64_t *base,
    size_t vl);
void vlseg5w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
    *v2, vint32m1_t *v3, vint32m1_t *v4, const int32_t *base,
    size_t vl);
void vlseg5w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
    *v2, vint64m1_t *v3, vint64m1_t *v4, const int64_t *base,
    size_t vl);
void vlseg5wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, const
    uint32_t *base, size_t vl);
void vlseg5wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, const
    uint64_t *base, size_t vl);

```

```

void vlseg6w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
*v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t *v5, const
int32_t *base, size_t vl);
void vlseg6w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
*v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t *v5, const
int64_t *base, size_t vl);
void vlseg6wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, const uint32_t *base, size_t vl);
void vlseg6wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
vuint64m1_t *v5, const uint64_t *base, size_t vl);
void vlseg7w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
*v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t *v5,
vint32m1_t *v6, const int32_t *base, size_t vl);
void vlseg7w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
*v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t *v5,
vint64m1_t *v6, const int64_t *base, size_t vl);
void vlseg7wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vuint32m1_t *v6, const uint32_t *base,
size_t vl);
void vlseg7wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
vuint64m1_t *v5, vuint64m1_t *v6, const uint64_t *base,
size_t vl);
void vlseg8w_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
*v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t *v5,
vint32m1_t *v6, vint32m1_t *v7, const int32_t *base, size_t
vl);
void vlseg8w_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
*v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t *v5,
vint64m1_t *v6, vint64m1_t *v7, const int64_t *base, size_t
vl);
void vlseg8wu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, const
uint32_t *base, size_t vl);
void vlseg8wu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, const
uint64_t *base, size_t vl);
void vsseg2w_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
v1, size_t vl);
void vsseg2w_v_i32m2 (int32_t *base, vint32m2_t v0, vint32m2_t
v1, size_t vl);

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void vsseg2w_v_i32m4 (int32_t *base, vint32m4_t v0, vint32m4_t
    v1, size_t vl);
void vsseg2w_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, size_t vl);
void vsseg2w_v_i64m2 (int64_t *base, vint64m2_t v0, vint64m2_t
    v1, size_t vl);
void vsseg2w_v_i64m4 (int64_t *base, vint64m4_t v0, vint64m4_t
    v1, size_t vl);
void vsseg2w_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, size_t vl);
void vsseg2w_v_u32m2 (uint32_t *base, vuint32m2_t v0,
    vuint32m2_t v1, size_t vl);
void vsseg2w_v_u32m4 (uint32_t *base, vuint32m4_t v0,
    vuint32m4_t v1, size_t vl);
void vsseg2w_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, size_t vl);
void vsseg2w_v_u64m2 (uint64_t *base, vuint64m2_t v0,
    vuint64m2_t v1, size_t vl);
void vsseg2w_v_u64m4 (uint64_t *base, vuint64m4_t v0,
    vuint64m4_t v1, size_t vl);
void vsseg3w_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, size_t vl);
void vsseg3w_v_i32m2 (int32_t *base, vint32m2_t v0, vint32m2_t
    v1, vint32m2_t v2, size_t vl);
void vsseg3w_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, size_t vl);
void vsseg3w_v_i64m2 (int64_t *base, vint64m2_t v0, vint64m2_t
    v1, vint64m2_t v2, size_t vl);
void vsseg3w_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, size_t vl);
void vsseg3w_v_u32m2 (uint32_t *base, vuint32m2_t v0,
    vuint32m2_t v1, vuint32m2_t v2, size_t vl);
void vsseg3w_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, size_t vl);
void vsseg3w_v_u64m2 (uint64_t *base, vuint64m2_t v0,
    vuint64m2_t v1, vuint64m2_t v2, size_t vl);
void vsseg4w_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, size_t vl);
void vsseg4w_v_i32m2 (int32_t *base, vint32m2_t v0, vint32m2_t
    v1, vint32m2_t v2, vint32m2_t v3, size_t vl);
void vsseg4w_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, size_t vl);
void vsseg4w_v_i64m2 (int64_t *base, vint64m2_t v0, vint64m2_t
    v1, vint64m2_t v2, vint64m2_t v3, size_t vl);
void vsseg4w_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, size_t vl);

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void vsseg4w_v_u32m2 (uint32_t *base, vuint32m2_t v0,
    vuint32m2_t v1, vuint32m2_t v2, vuint32m2_t v3, size_t vl);
void vsseg4w_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, size_t vl);
void vsseg4w_v_u64m2 (uint64_t *base, vuint64m2_t v0,
    vuint64m2_t v1, vuint64m2_t v2, vuint64m2_t v3, size_t vl);
void vsseg5w_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, vint32m1_t v4, size_t vl);
void vsseg5w_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, vint64m1_t v4, size_t vl);
void vsseg5w_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t
    v4, size_t vl);
void vsseg5w_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t
    v4, size_t vl);
void vsseg6w_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t
    v5, size_t vl);
void vsseg6w_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t
    v5, size_t vl);
void vsseg6w_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t
    v4, vuint32m1_t v5, size_t vl);
void vsseg6w_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t
    v4, vuint64m1_t v5, size_t vl);
void vsseg7w_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t
    v5, vint32m1_t v6, size_t vl);
void vsseg7w_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t
    v5, vint64m1_t v6, size_t vl);
void vsseg7w_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t
    v4, vuint32m1_t v5, vuint32m1_t v6, size_t vl);
void vsseg7w_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t
    v4, vuint64m1_t v5, vuint64m1_t v6, size_t vl);
void vsseg8w_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t
    v5, vint32m1_t v6, vint32m1_t v7, size_t vl);
void vsseg8w_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t
    v5, vint64m1_t v6, vint64m1_t v7, size_t vl);

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void vsseg8w_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t
    v4, vuint32m1_t v5, vuint32m1_t v6, vuint32m1_t v7, size_t
    vl);
void vsseg8w_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t
    v4, vuint64m1_t v5, vuint64m1_t v6, vuint64m1_t v7, size_t
    vl);
// masked functions
void vlseg2w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, const int32_t *base, size_t vl);
void vlseg2w_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vbool16_t mask, vint32m2_t maskedoff0, vint32m2_t
    maskedoff1, const int32_t *base, size_t vl);
void vlseg2w_v_i32m4_m (vint32m4_t *v0, vint32m4_t *v1, vbool8_t
    mask, vint32m4_t maskedoff0, vint32m4_t maskedoff1, const
    int32_t *base, size_t vl);
void vlseg2w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, const int64_t *base, size_t vl);
void vlseg2w_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vbool32_t mask, vint64m2_t maskedoff0, vint64m2_t
    maskedoff1, const int64_t *base, size_t vl);
void vlseg2w_v_i64m4_m (vint64m4_t *v0, vint64m4_t *v1,
    vbool16_t mask, vint64m4_t maskedoff0, vint64m4_t
    maskedoff1, const int64_t *base, size_t vl);
void vlseg2wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vbool32_t mask, vuint32m1_t maskedoff0, vuint32m1_t
    maskedoff1, const uint32_t *base, size_t vl);
void vlseg2wu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vbool16_t mask, vuint32m2_t maskedoff0, vuint32m2_t
    maskedoff1, const uint32_t *base, size_t vl);
void vlseg2wu_v_u32m4_m (vuint32m4_t *v0, vuint32m4_t *v1,
    vbool8_t mask, vuint32m4_t maskedoff0, vuint32m4_t
    maskedoff1, const uint32_t *base, size_t vl);
void vlseg2wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vbool64_t mask, vuint64m1_t maskedoff0, vuint64m1_t
    maskedoff1, const uint64_t *base, size_t vl);
void vlseg2wu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vbool32_t mask, vuint64m2_t maskedoff0, vuint64m2_t
    maskedoff1, const uint64_t *base, size_t vl);
void vlseg2wu_v_u64m4_m (vuint64m4_t *v0, vuint64m4_t *v1,
    vbool16_t mask, vuint64m4_t maskedoff0, vuint64m4_t
    maskedoff1, const uint64_t *base, size_t vl);

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void vlseg3w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, const int32_t
    *base, size_t vl);
void vlseg3w_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vbool16_t mask, vint32m2_t maskedoff0,
    vint32m2_t maskedoff1, vint32m2_t maskedoff2, const int32_t
    *base, size_t vl);
void vlseg3w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, const int64_t
    *base, size_t vl);
void vlseg3w_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vbool32_t mask, vint64m2_t maskedoff0,
    vint64m2_t maskedoff1, vint64m2_t maskedoff2, const int64_t
    *base, size_t vl);
void vlseg3wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, const
    uint32_t *base, size_t vl);
void vlseg3wu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vbool16_t mask, vuint32m2_t maskedoff0,
    vuint32m2_t maskedoff1, vuint32m2_t maskedoff2, const
    uint32_t *base, size_t vl);
void vlseg3wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, const
    uint64_t *base, size_t vl);
void vlseg3wu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vbool32_t mask, vuint64m2_t maskedoff0,
    vuint64m2_t maskedoff1, vuint64m2_t maskedoff2, const
    uint64_t *base, size_t vl);
void vlseg4w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vbool32_t mask, vint32m1_t
    maskedoff0, vint32m1_t maskedoff1, vint32m1_t maskedoff2,
    vint32m1_t maskedoff3, const int32_t *base, size_t vl);
void vlseg4w_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, vbool16_t mask, vint32m2_t
    maskedoff0, vint32m2_t maskedoff1, vint32m2_t maskedoff2,
    vint32m2_t maskedoff3, const int32_t *base, size_t vl);
void vlseg4w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vbool64_t mask, vint64m1_t
    maskedoff0, vint64m1_t maskedoff1, vint64m1_t maskedoff2,
    vint64m1_t maskedoff3, const int64_t *base, size_t vl);
void vlseg4w_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, vbool32_t mask, vint64m2_t

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maskedoff0, vint64m2_t maskedoff1, vint64m2_t maskedoff2,
vint64m2_t maskedoff3, const int64_t *base, size_t vl);
void vlseg4wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vbool32_t mask,
vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
maskedoff2, vuint32m1_t maskedoff3, const uint32_t *base,
size_t vl);
void vlseg4wu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
vuint32m2_t *v2, vuint32m2_t *v3, vbool16_t mask,
vuint32m2_t maskedoff0, vuint32m2_t maskedoff1, vuint32m2_t
maskedoff2, vuint32m2_t maskedoff3, const uint32_t *base,
size_t vl);
void vlseg4wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vbool64_t mask,
vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
maskedoff2, vuint64m1_t maskedoff3, const uint64_t *base,
size_t vl);
void vlseg4wu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
vuint64m2_t *v2, vuint64m2_t *v3, vbool32_t mask,
vuint64m2_t maskedoff0, vuint64m2_t maskedoff1, vuint64m2_t
maskedoff2, vuint64m2_t maskedoff3, const uint64_t *base,
size_t vl);
void vlseg5w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vbool32_t
mask, vint32m1_t maskedoff0, vint32m1_t maskedoff1,
vint32m1_t maskedoff2, vint32m1_t maskedoff3, vint32m1_t
maskedoff4, const int32_t *base, size_t vl);
void vlseg5w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vbool64_t
mask, vint64m1_t maskedoff0, vint64m1_t maskedoff1,
vint64m1_t maskedoff2, vint64m1_t maskedoff3, vint64m1_t
maskedoff4, const int64_t *base, size_t vl);
void vlseg5wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, vbool32_t
mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
maskedoff4, const uint32_t *base, size_t vl);
void vlseg5wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, vbool64_t
mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
maskedoff4, const uint64_t *base, size_t vl);
void vlseg6w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
*v5, vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
maskedoff1, vint32m1_t maskedoff2, vint32m1_t maskedoff3,

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    vint32m1_t maskedoff4, vint32m1_t maskedoff5, const int32_t
    *base, size_t vl);
void vlseg6w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, vint64m1_t maskedoff2, vint64m1_t maskedoff3,
    vint64m1_t maskedoff4, vint64m1_t maskedoff5, const int64_t
    *base, size_t vl);
void vlseg6wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, vuint32m1_t
    maskedoff3, vuint32m1_t maskedoff4, vuint32m1_t maskedoff5,
    const uint32_t *base, size_t vl);
void vlseg6wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, vuint64m1_t
    maskedoff3, vuint64m1_t maskedoff4, vuint64m1_t maskedoff5,
    const uint64_t *base, size_t vl);
void vlseg7w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, vint32m1_t
    maskedoff3, vint32m1_t maskedoff4, vint32m1_t maskedoff5,
    vint32m1_t maskedoff6, const int32_t *base, size_t vl);
void vlseg7w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, vint64m1_t
    maskedoff3, vint64m1_t maskedoff4, vint64m1_t maskedoff5,
    vint64m1_t maskedoff6, const int64_t *base, size_t vl);
void vlseg7wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t maskedoff4,
    vuint32m1_t maskedoff5, vuint32m1_t maskedoff6, const
    uint32_t *base, size_t vl);
void vlseg7wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t maskedoff4,
    vuint64m1_t maskedoff5, vuint64m1_t maskedoff6, const
    uint64_t *base, size_t vl);

```

```

void vlseg8w_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vint32m1_t *v7, vbool32_t mask,
    vint32m1_t maskedoff0, vint32m1_t maskedoff1, vint32m1_t
    maskedoff2, vint32m1_t maskedoff3, vint32m1_t maskedoff4,
    vint32m1_t maskedoff5, vint32m1_t maskedoff6, vint32m1_t
    maskedoff7, const int32_t *base, size_t vl);
void vlseg8w_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vint64m1_t *v7, vbool64_t mask,
    vint64m1_t maskedoff0, vint64m1_t maskedoff1, vint64m1_t
    maskedoff2, vint64m1_t maskedoff3, vint64m1_t maskedoff4,
    vint64m1_t maskedoff5, vint64m1_t maskedoff6, vint64m1_t
    maskedoff7, const int64_t *base, size_t vl);
void vlseg8wu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, vuint32m1_t maskedoff5, vuint32m1_t maskedoff6,
    vuint32m1_t maskedoff7, const uint32_t *base, size_t vl);
void vlseg8wu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, vuint64m1_t maskedoff5, vuint64m1_t maskedoff6,
    vuint64m1_t maskedoff7, const uint64_t *base, size_t vl);
void vsseg2w_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, size_t vl);
void vsseg2w_v_i32m2_m (vbool16_t mask, int32_t *base,
    vint32m2_t v0, vint32m2_t v1, size_t vl);
void vsseg2w_v_i32m4_m (vbool8_t mask, int32_t *base, vint32m4_t
    v0, vint32m4_t v1, size_t vl);
void vsseg2w_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, size_t vl);
void vsseg2w_v_i64m2_m (vbool32_t mask, int64_t *base,
    vint64m2_t v0, vint64m2_t v1, size_t vl);
void vsseg2w_v_i64m4_m (vbool16_t mask, int64_t *base,
    vint64m4_t v0, vint64m4_t v1, size_t vl);
void vsseg2w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, size_t vl);
void vsseg2w_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t v0, vuint32m2_t v1, size_t vl);
void vsseg2w_v_u32m4_m (vbool8_t mask, uint32_t *base,
    vuint32m4_t v0, vuint32m4_t v1, size_t vl);

```

```

void vsseg2w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, size_t vl);
void vsseg2w_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t v0, vuint64m2_t v1, size_t vl);
void vsseg2w_v_u64m4_m (vbool16_t mask, uint64_t *base,
    vuint64m4_t v0, vuint64m4_t v1, size_t vl);
void vsseg3w_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, size_t vl);
void vsseg3w_v_i32m2_m (vbool16_t mask, int32_t *base,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, size_t vl);
void vsseg3w_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, size_t vl);
void vsseg3w_v_i64m2_m (vbool32_t mask, int64_t *base,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, size_t vl);
void vsseg3w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, size_t vl);
void vsseg3w_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, size_t vl);
void vsseg3w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, size_t vl);
void vsseg3w_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, size_t vl);
void vsseg4w_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    size_t vl);
void vsseg4w_v_i32m2_m (vbool16_t mask, int32_t *base,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, vint32m2_t v3,
    size_t vl);
void vsseg4w_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    size_t vl);
void vsseg4w_v_i64m2_m (vbool32_t mask, int64_t *base,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, vint64m2_t v3,
    size_t vl);
void vsseg4w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, size_t vl);
void vsseg4w_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, vuint32m2_t
    v3, size_t vl);
void vsseg4w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, size_t vl);
void vsseg4w_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, vuint64m2_t
    v3, size_t vl);

```



```

void vsseg5w_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, size_t vl);
void vsseg5w_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, size_t vl);
void vsseg5w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, size_t vl);
void vsseg5w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, size_t vl);
void vsseg6w_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, size_t vl);
void vsseg6w_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, size_t vl);
void vsseg6w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, size_t vl);
void vsseg6w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, size_t vl);
void vsseg7w_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, size_t vl);
void vsseg7w_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, size_t vl);
void vsseg7w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6, size_t
    vl);
void vsseg7w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6, size_t
    vl);
void vsseg8w_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, vint32m1_t v7,
    size_t vl);
void vsseg8w_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, vint64m1_t v7,
    size_t vl);

```

```

void vsseg8w_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6,
    vuint32m1_t v7, size_t vl);
void vsseg8w_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6,
    vuint64m1_t v7, size_t vl);

```

## Vector Unit-Stride Segment 8b Loads and Stores:

### Prototypes:

```

void vlseg2b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, const
    int16_t *base, size_t vl);
void vlseg2b_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1, const
    int16_t *base, size_t vl);
void vlseg2b_v_i16m4 (vint16m4_t *v0, vint16m4_t *v1, const
    int16_t *base, size_t vl);
void vlseg2b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, const
    int32_t *base, size_t vl);
void vlseg2b_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, const
    int32_t *base, size_t vl);
void vlseg2b_v_i32m4 (vint32m4_t *v0, vint32m4_t *v1, const
    int32_t *base, size_t vl);
void vlseg2b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, const
    int64_t *base, size_t vl);
void vlseg2b_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, const
    int64_t *base, size_t vl);
void vlseg2b_v_i64m4 (vint64m4_t *v0, vint64m4_t *v1, const
    int64_t *base, size_t vl);
void vlseg2b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, const int8_t
    *base, size_t vl);
void vlseg2b_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, const int8_t
    *base, size_t vl);
void vlseg2b_v_i8m4 (vint8m4_t *v0, vint8m4_t *v1, const int8_t
    *base, size_t vl);
void vlseg2bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1, const
    uint16_t *base, size_t vl);
void vlseg2bu_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1, const
    uint16_t *base, size_t vl);
void vlseg2bu_v_u16m4 (vuint16m4_t *v0, vuint16m4_t *v1, const
    uint16_t *base, size_t vl);
void vlseg2bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1, const
    uint32_t *base, size_t vl);

```

```

void vlseg2bu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1, const
    uint32_t *base, size_t vl);
void vlseg2bu_v_u32m4 (vuint32m4_t *v0, vuint32m4_t *v1, const
    uint32_t *base, size_t vl);
void vlseg2bu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1, const
    uint64_t *base, size_t vl);
void vlseg2bu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1, const
    uint64_t *base, size_t vl);
void vlseg2bu_v_u64m4 (vuint64m4_t *v0, vuint64m4_t *v1, const
    uint64_t *base, size_t vl);
void vlseg2bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, const
    uint8_t *base, size_t vl);
void vlseg2bu_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1, const
    uint8_t *base, size_t vl);
void vlseg2bu_v_u8m4 (vuint8m4_t *v0, vuint8m4_t *v1, const
    uint8_t *base, size_t vl);
void vlseg3b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, vint16m1_t
    *v2, const int16_t *base, size_t vl);
void vlseg3b_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1, vint16m2_t
    *v2, const int16_t *base, size_t vl);
void vlseg3b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
    *v2, const int32_t *base, size_t vl);
void vlseg3b_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, vint32m2_t
    *v2, const int32_t *base, size_t vl);
void vlseg3b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
    *v2, const int64_t *base, size_t vl);
void vlseg3b_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, vint64m2_t
    *v2, const int64_t *base, size_t vl);
void vlseg3b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, const int8_t *base, size_t vl);
void vlseg3b_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, const int8_t *base, size_t vl);
void vlseg3bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, const uint16_t *base, size_t vl);
void vlseg3bu_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, const uint16_t *base, size_t vl);
void vlseg3bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, const uint32_t *base, size_t vl);
void vlseg3bu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, const uint32_t *base, size_t vl);
void vlseg3bu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, const uint64_t *base, size_t vl);
void vlseg3bu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, const uint64_t *base, size_t vl);
void vlseg3bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, vuint8m1_t
    *v2, const uint8_t *base, size_t vl);

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void vlseg3bu_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1, vuint8m2_t
    *v2, const uint8_t *base, size_t vl);
void vlseg4b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, vint16m1_t
    *v2, vint16m1_t *v3, const int16_t *base, size_t vl);
void vlseg4b_v_i16m2 (vint16m2_t *v0, vint16m2_t *v1, vint16m2_t
    *v2, vint16m2_t *v3, const int16_t *base, size_t vl);
void vlseg4b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
    *v2, vint32m1_t *v3, const int32_t *base, size_t vl);
void vlseg4b_v_i32m2 (vint32m2_t *v0, vint32m2_t *v1, vint32m2_t
    *v2, vint32m2_t *v3, const int32_t *base, size_t vl);
void vlseg4b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
    *v2, vint64m1_t *v3, const int64_t *base, size_t vl);
void vlseg4b_v_i64m2 (vint64m2_t *v0, vint64m2_t *v1, vint64m2_t
    *v2, vint64m2_t *v3, const int64_t *base, size_t vl);
void vlseg4b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, const int8_t *base, size_t vl);
void vlseg4b_v_i8m2 (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, vint8m2_t *v3, const int8_t *base, size_t vl);
void vlseg4bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, const uint16_t *base,
    size_t vl);
void vlseg4bu_v_u16m2 (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vuint16m2_t *v3, const uint16_t *base,
    size_t vl);
void vlseg4bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, const uint32_t *base,
    size_t vl);
void vlseg4bu_v_u32m2 (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, const uint32_t *base,
    size_t vl);
void vlseg4bu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, const uint64_t *base,
    size_t vl);
void vlseg4bu_v_u64m2 (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, const uint64_t *base,
    size_t vl);
void vlseg4bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, vuint8m1_t
    *v2, vuint8m1_t *v3, const uint8_t *base, size_t vl);
void vlseg4bu_v_u8m2 (vuint8m2_t *v0, vuint8m2_t *v1, vuint8m2_t
    *v2, vuint8m2_t *v3, const uint8_t *base, size_t vl);
void vlseg5b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, vint16m1_t
    *v2, vint16m1_t *v3, vint16m1_t *v4, const int16_t *base,
    size_t vl);
void vlseg5b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
    *v2, vint32m1_t *v3, vint32m1_t *v4, const int32_t *base,
    size_t vl);

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void vlseg5b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
    *v2, vint64m1_t *v3, vint64m1_t *v4, const int64_t *base,
    size_t vl);
void vlseg5b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, const int8_t *base,
    size_t vl);
void vlseg5bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, const
    uint16_t *base, size_t vl);
void vlseg5bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, const
    uint32_t *base, size_t vl);
void vlseg5bu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, const
    uint64_t *base, size_t vl);
void vlseg5bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, vuint8m1_t
    *v2, vuint8m1_t *v3, vuint8m1_t *v4, const uint8_t *base,
    size_t vl);
void vlseg6b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, vint16m1_t
    *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t *v5, const
    int16_t *base, size_t vl);
void vlseg6b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
    *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t *v5, const
    int32_t *base, size_t vl);
void vlseg6b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
    *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t *v5, const
    int64_t *base, size_t vl);
void vlseg6b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, const
    int8_t *base, size_t vl);
void vlseg6bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, const uint16_t *base, size_t vl);
void vlseg6bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, const uint32_t *base, size_t vl);
void vlseg6bu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, const uint64_t *base, size_t vl);
void vlseg6bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, vuint8m1_t
    *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t *v5, const
    uint8_t *base, size_t vl);
void vlseg7b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, vint16m1_t
    *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t *v5,
    vint16m1_t *v6, const int16_t *base, size_t vl);

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void vlseg7b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
*v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t *v5,
vint32m1_t *v6, const int32_t *base, size_t vl);
void vlseg7b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
*v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t *v5,
vint64m1_t *v6, const int64_t *base, size_t vl);
void vlseg7b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
*v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
*v6, const int8_t *base, size_t vl);
void vlseg7bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vuint16m1_t *v6, const uint16_t *base,
size_t vl);
void vlseg7bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vuint32m1_t *v6, const uint32_t *base,
size_t vl);
void vlseg7bu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
vuint64m1_t *v5, vuint64m1_t *v6, const uint64_t *base,
size_t vl);
void vlseg7bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, vuint8m1_t
*v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t *v5,
vuint8m1_t *v6, const uint8_t *base, size_t vl);
void vlseg8b_v_i16m1 (vint16m1_t *v0, vint16m1_t *v1, vint16m1_t
*v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t *v5,
vint16m1_t *v6, vint16m1_t *v7, const int16_t *base, size_t
vl);
void vlseg8b_v_i32m1 (vint32m1_t *v0, vint32m1_t *v1, vint32m1_t
*v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t *v5,
vint32m1_t *v6, vint32m1_t *v7, const int32_t *base, size_t
vl);
void vlseg8b_v_i64m1 (vint64m1_t *v0, vint64m1_t *v1, vint64m1_t
*v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t *v5,
vint64m1_t *v6, vint64m1_t *v7, const int64_t *base, size_t
vl);
void vlseg8b_v_i8m1 (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
*v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
*v6, vint8m1_t *v7, const int8_t *base, size_t vl);
void vlseg8bu_v_u16m1 (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, const
uint16_t *base, size_t vl);
void vlseg8bu_v_u32m1 (vuint32m1_t *v0, vuint32m1_t *v1,
vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, const

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uint32_t *base, size_t vl);
void vlseg8bu_v_u64m1 (vuint64m1_t *v0, vuint64m1_t *v1,
vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, const
uint64_t *base, size_t vl);
void vlseg8bu_v_u8m1 (vuint8m1_t *v0, vuint8m1_t *v1, vuint8m1_t
*v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t *v5,
vuint8m1_t *v6, vuint8m1_t *v7, const uint8_t *base, size_t
vl);
void vsseg2b_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
v1, size_t vl);
void vsseg2b_v_i16m2 (int16_t *base, vint16m2_t v0, vint16m2_t
v1, size_t vl);
void vsseg2b_v_i16m4 (int16_t *base, vint16m4_t v0, vint16m4_t
v1, size_t vl);
void vsseg2b_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
v1, size_t vl);
void vsseg2b_v_i32m2 (int32_t *base, vint32m2_t v0, vint32m2_t
v1, size_t vl);
void vsseg2b_v_i32m4 (int32_t *base, vint32m4_t v0, vint32m4_t
v1, size_t vl);
void vsseg2b_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
v1, size_t vl);
void vsseg2b_v_i64m2 (int64_t *base, vint64m2_t v0, vint64m2_t
v1, size_t vl);
void vsseg2b_v_i64m4 (int64_t *base, vint64m4_t v0, vint64m4_t
v1, size_t vl);
void vsseg2b_v_i8m1 (int8_t *base, vint8m1_t v0, vint8m1_t v1,
size_t vl);
void vsseg2b_v_i8m2 (int8_t *base, vint8m2_t v0, vint8m2_t v1,
size_t vl);
void vsseg2b_v_i8m4 (int8_t *base, vint8m4_t v0, vint8m4_t v1,
size_t vl);
void vsseg2b_v_u16m1 (uint16_t *base, vuint16m1_t v0,
vuint16m1_t v1, size_t vl);
void vsseg2b_v_u16m2 (uint16_t *base, vuint16m2_t v0,
vuint16m2_t v1, size_t vl);
void vsseg2b_v_u16m4 (uint16_t *base, vuint16m4_t v0,
vuint16m4_t v1, size_t vl);
void vsseg2b_v_u32m1 (uint32_t *base, vuint32m1_t v0,
vuint32m1_t v1, size_t vl);
void vsseg2b_v_u32m2 (uint32_t *base, vuint32m2_t v0,
vuint32m2_t v1, size_t vl);
void vsseg2b_v_u32m4 (uint32_t *base, vuint32m4_t v0,
vuint32m4_t v1, size_t vl);

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void vsseg2b_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, size_t vl);
void vsseg2b_v_u64m2 (uint64_t *base, vuint64m2_t v0,
    vuint64m2_t v1, size_t vl);
void vsseg2b_v_u64m4 (uint64_t *base, vuint64m4_t v0,
    vuint64m4_t v1, size_t vl);
void vsseg2b_v_u8m1 (uint8_t *base, vuint8m1_t v0, vuint8m1_t
    v1, size_t vl);
void vsseg2b_v_u8m2 (uint8_t *base, vuint8m2_t v0, vuint8m2_t
    v1, size_t vl);
void vsseg2b_v_u8m4 (uint8_t *base, vuint8m4_t v0, vuint8m4_t
    v1, size_t vl);
void vsseg3b_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, size_t vl);
void vsseg3b_v_i16m2 (int16_t *base, vint16m2_t v0, vint16m2_t
    v1, vint16m2_t v2, size_t vl);
void vsseg3b_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, size_t vl);
void vsseg3b_v_i32m2 (int32_t *base, vint32m2_t v0, vint32m2_t
    v1, vint32m2_t v2, size_t vl);
void vsseg3b_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, size_t vl);
void vsseg3b_v_i64m2 (int64_t *base, vint64m2_t v0, vint64m2_t
    v1, vint64m2_t v2, size_t vl);
void vsseg3b_v_i8m1 (int8_t *base, vint8m1_t v0, vint8m1_t v1,
    vint8m1_t v2, size_t vl);
void vsseg3b_v_i8m2 (int8_t *base, vint8m2_t v0, vint8m2_t v1,
    vint8m2_t v2, size_t vl);
void vsseg3b_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, size_t vl);
void vsseg3b_v_u16m2 (uint16_t *base, vuint16m2_t v0,
    vuint16m2_t v1, vuint16m2_t v2, size_t vl);
void vsseg3b_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, size_t vl);
void vsseg3b_v_u32m2 (uint32_t *base, vuint32m2_t v0,
    vuint32m2_t v1, vuint32m2_t v2, size_t vl);
void vsseg3b_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, size_t vl);
void vsseg3b_v_u64m2 (uint64_t *base, vuint64m2_t v0,
    vuint64m2_t v1, vuint64m2_t v2, size_t vl);
void vsseg3b_v_u8m1 (uint8_t *base, vuint8m1_t v0, vuint8m1_t
    v1, vuint8m1_t v2, size_t vl);
void vsseg3b_v_u8m2 (uint8_t *base, vuint8m2_t v0, vuint8m2_t
    v1, vuint8m2_t v2, size_t vl);
void vsseg4b_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, vint16m1_t v3, size_t vl);

```



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void vsseg4b_v_i16m2 (int16_t *base, vint16m2_t v0, vint16m2_t
    v1, vint16m2_t v2, vint16m2_t v3, size_t vl);
void vsseg4b_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, size_t vl);
void vsseg4b_v_i32m2 (int32_t *base, vint32m2_t v0, vint32m2_t
    v1, vint32m2_t v2, vint32m2_t v3, size_t vl);
void vsseg4b_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, size_t vl);
void vsseg4b_v_i64m2 (int64_t *base, vint64m2_t v0, vint64m2_t
    v1, vint64m2_t v2, vint64m2_t v3, size_t vl);
void vsseg4b_v_i8m1 (int8_t *base, vint8m1_t v0, vint8m1_t v1,
    vint8m1_t v2, vint8m1_t v3, size_t vl);
void vsseg4b_v_i8m2 (int8_t *base, vint8m2_t v0, vint8m2_t v1,
    vint8m2_t v2, vint8m2_t v3, size_t vl);
void vsseg4b_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t v3, size_t vl);
void vsseg4b_v_u16m2 (uint16_t *base, vuint16m2_t v0,
    vuint16m2_t v1, vuint16m2_t v2, vuint16m2_t v3, size_t vl);
void vsseg4b_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, size_t vl);
void vsseg4b_v_u32m2 (uint32_t *base, vuint32m2_t v0,
    vuint32m2_t v1, vuint32m2_t v2, vuint32m2_t v3, size_t vl);
void vsseg4b_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, size_t vl);
void vsseg4b_v_u64m2 (uint64_t *base, vuint64m2_t v0,
    vuint64m2_t v1, vuint64m2_t v2, vuint64m2_t v3, size_t vl);
void vsseg4b_v_u8m1 (uint8_t *base, vuint8m1_t v0, vuint8m1_t
    v1, vuint8m1_t v2, vuint8m1_t v3, size_t vl);
void vsseg4b_v_u8m2 (uint8_t *base, vuint8m2_t v0, vuint8m2_t
    v1, vuint8m2_t v2, vuint8m2_t v3, size_t vl);
void vsseg5b_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, vint16m1_t v3, vint16m1_t v4, size_t vl);
void vsseg5b_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, vint32m1_t v4, size_t vl);
void vsseg5b_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, vint64m1_t v4, size_t vl);
void vsseg5b_v_i8m1 (int8_t *base, vint8m1_t v0, vint8m1_t v1,
    vint8m1_t v2, vint8m1_t v3, vint8m1_t v4, size_t vl);
void vsseg5b_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t
    v4, size_t vl);
void vsseg5b_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t
    v4, size_t vl);
void vsseg5b_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t

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    v4, size_t vl);
void vsseg5b_v_u8m1 (uint8_t *base, vuint8m1_t v0, vuint8m1_t
    v1, vuint8m1_t v2, vuint8m1_t v3, vuint8m1_t v4, size_t vl);
void vsseg6b_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t
    v5, size_t vl);
void vsseg6b_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t
    v5, size_t vl);
void vsseg6b_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t
    v5, size_t vl);
void vsseg6b_v_i8m1 (int8_t *base, vint8m1_t v0, vint8m1_t v1,
    vint8m1_t v2, vint8m1_t v3, vint8m1_t v4, vint8m1_t v5,
    size_t vl);
void vsseg6b_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t
    v4, vuint16m1_t v5, size_t vl);
void vsseg6b_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t
    v4, vuint32m1_t v5, size_t vl);
void vsseg6b_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t
    v4, vuint64m1_t v5, size_t vl);
void vsseg6b_v_u8m1 (uint8_t *base, vuint8m1_t v0, vuint8m1_t
    v1, vuint8m1_t v2, vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t
    v5, size_t vl);
void vsseg7b_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t
    v5, vint16m1_t v6, size_t vl);
void vsseg7b_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t
    v5, vint32m1_t v6, size_t vl);
void vsseg7b_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t
    v5, vint64m1_t v6, size_t vl);
void vsseg7b_v_i8m1 (int8_t *base, vint8m1_t v0, vint8m1_t v1,
    vint8m1_t v2, vint8m1_t v3, vint8m1_t v4, vint8m1_t v5,
    vint8m1_t v6, size_t vl);
void vsseg7b_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t
    v4, vuint16m1_t v5, vuint16m1_t v6, size_t vl);
void vsseg7b_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t
    v4, vuint32m1_t v5, vuint32m1_t v6, size_t vl);

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void vsseg7b_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t
    v4, vuint64m1_t v5, vuint64m1_t v6, size_t vl);
void vsseg7b_v_u8m1 (uint8_t *base, vuint8m1_t v0, vuint8m1_t
    v1, vuint8m1_t v2, vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t
    v5, vuint8m1_t v6, size_t vl);
void vsseg8b_v_i16m1 (int16_t *base, vint16m1_t v0, vint16m1_t
    v1, vint16m1_t v2, vint16m1_t v3, vint16m1_t v4, vint16m1_t
    v5, vint16m1_t v6, vint16m1_t v7, size_t vl);
void vsseg8b_v_i32m1 (int32_t *base, vint32m1_t v0, vint32m1_t
    v1, vint32m1_t v2, vint32m1_t v3, vint32m1_t v4, vint32m1_t
    v5, vint32m1_t v6, vint32m1_t v7, size_t vl);
void vsseg8b_v_i64m1 (int64_t *base, vint64m1_t v0, vint64m1_t
    v1, vint64m1_t v2, vint64m1_t v3, vint64m1_t v4, vint64m1_t
    v5, vint64m1_t v6, vint64m1_t v7, size_t vl);
void vsseg8b_v_i8m1 (int8_t *base, vint8m1_t v0, vint8m1_t v1,
    vint8m1_t v2, vint8m1_t v3, vint8m1_t v4, vint8m1_t v5,
    vint8m1_t v6, vint8m1_t v7, size_t vl);
void vsseg8b_v_u16m1 (uint16_t *base, vuint16m1_t v0,
    vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t v3, vuint16m1_t
    v4, vuint16m1_t v5, vuint16m1_t v6, vuint16m1_t v7, size_t
    vl);
void vsseg8b_v_u32m1 (uint32_t *base, vuint32m1_t v0,
    vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t v3, vuint32m1_t
    v4, vuint32m1_t v5, vuint32m1_t v6, vuint32m1_t v7, size_t
    vl);
void vsseg8b_v_u64m1 (uint64_t *base, vuint64m1_t v0,
    vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t v3, vuint64m1_t
    v4, vuint64m1_t v5, vuint64m1_t v6, vuint64m1_t v7, size_t
    vl);
void vsseg8b_v_u8m1 (uint8_t *base, vuint8m1_t v0, vuint8m1_t
    v1, vuint8m1_t v2, vuint8m1_t v3, vuint8m1_t v4, vuint8m1_t
    v5, vuint8m1_t v6, vuint8m1_t v7, size_t vl);
// masked functions
void vlseg2b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
    maskedoff1, const int16_t *base, size_t vl);
void vlseg2b_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1, vbool8_t
    mask, vint16m2_t maskedoff0, vint16m2_t maskedoff1, const
    int16_t *base, size_t vl);
void vlseg2b_v_i16m4_m (vint16m4_t *v0, vint16m4_t *v1, vbool4_t
    mask, vint16m4_t maskedoff0, vint16m4_t maskedoff1, const
    int16_t *base, size_t vl);
void vlseg2b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, const int32_t *base, size_t vl);

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void vlseg2b_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vbool16_t mask, vint32m2_t maskedoff0, vint32m2_t
    maskedoff1, const int32_t *base, size_t vl);
void vlseg2b_v_i32m4_m (vint32m4_t *v0, vint32m4_t *v1, vbool8_t
    mask, vint32m4_t maskedoff0, vint32m4_t maskedoff1, const
    int32_t *base, size_t vl);
void vlseg2b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, const int64_t *base, size_t vl);
void vlseg2b_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vbool32_t mask, vint64m2_t maskedoff0, vint64m2_t
    maskedoff1, const int64_t *base, size_t vl);
void vlseg2b_v_i64m4_m (vint64m4_t *v0, vint64m4_t *v1,
    vbool16_t mask, vint64m4_t maskedoff0, vint64m4_t
    maskedoff1, const int64_t *base, size_t vl);
void vlseg2b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vbool8_t
    mask, vint8m1_t maskedoff0, vint8m1_t maskedoff1, const
    int8_t *base, size_t vl);
void vlseg2b_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1, vbool4_t
    mask, vint8m2_t maskedoff0, vint8m2_t maskedoff1, const
    int8_t *base, size_t vl);
void vlseg2b_v_i8m4_m (vint8m4_t *v0, vint8m4_t *v1, vbool2_t
    mask, vint8m4_t maskedoff0, vint8m4_t maskedoff1, const
    int8_t *base, size_t vl);
void vlseg2bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vbool16_t mask, vuint16m1_t maskedoff0, vuint16m1_t
    maskedoff1, const uint16_t *base, size_t vl);
void vlseg2bu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vbool8_t mask, vuint16m2_t maskedoff0, vuint16m2_t
    maskedoff1, const uint16_t *base, size_t vl);
void vlseg2bu_v_u16m4_m (vuint16m4_t *v0, vuint16m4_t *v1,
    vbool4_t mask, vuint16m4_t maskedoff0, vuint16m4_t
    maskedoff1, const uint16_t *base, size_t vl);
void vlseg2bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vbool32_t mask, vuint32m1_t maskedoff0, vuint32m1_t
    maskedoff1, const uint32_t *base, size_t vl);
void vlseg2bu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vbool16_t mask, vuint32m2_t maskedoff0, vuint32m2_t
    maskedoff1, const uint32_t *base, size_t vl);
void vlseg2bu_v_u32m4_m (vuint32m4_t *v0, vuint32m4_t *v1,
    vbool8_t mask, vuint32m4_t maskedoff0, vuint32m4_t
    maskedoff1, const uint32_t *base, size_t vl);
void vlseg2bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vbool64_t mask, vuint64m1_t maskedoff0, vuint64m1_t
    maskedoff1, const uint64_t *base, size_t vl);

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void vlseg2bu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vbool32_t mask, vuint64m2_t maskedoff0, vuint64m2_t
    maskedoff1, const uint64_t *base, size_t vl);
void vlseg2bu_v_u64m4_m (vuint64m4_t *v0, vuint64m4_t *v1,
    vbool16_t mask, vuint64m4_t maskedoff0, vuint64m4_t
    maskedoff1, const uint64_t *base, size_t vl);
void vlseg2bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1, vbool8_t
    mask, vuint8m1_t maskedoff0, vuint8m1_t maskedoff1, const
    uint8_t *base, size_t vl);
void vlseg2bu_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1, vbool4_t
    mask, vuint8m2_t maskedoff0, vuint8m2_t maskedoff1, const
    uint8_t *base, size_t vl);
void vlseg2bu_v_u8m4_m (vuint8m4_t *v0, vuint8m4_t *v1, vbool2_t
    mask, vuint8m4_t maskedoff0, vuint8m4_t maskedoff1, const
    uint8_t *base, size_t vl);
void vlseg3b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vbool16_t mask, vint16m1_t maskedoff0,
    vint16m1_t maskedoff1, vint16m1_t maskedoff2, const int16_t
    *base, size_t vl);
void vlseg3b_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vbool8_t mask, vint16m2_t maskedoff0,
    vint16m2_t maskedoff1, vint16m2_t maskedoff2, const int16_t
    *base, size_t vl);
void vlseg3b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, const int32_t
    *base, size_t vl);
void vlseg3b_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vbool16_t mask, vint32m2_t maskedoff0,
    vint32m2_t maskedoff1, vint32m2_t maskedoff2, const int32_t
    *base, size_t vl);
void vlseg3b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, const int64_t
    *base, size_t vl);
void vlseg3b_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vbool32_t mask, vint64m2_t maskedoff0,
    vint64m2_t maskedoff1, vint64m2_t maskedoff2, const int64_t
    *base, size_t vl);
void vlseg3b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vbool8_t mask, vint8m1_t maskedoff0, vint8m1_t
    maskedoff1, vint8m1_t maskedoff2, const int8_t *base, size_t
    vl);
void vlseg3b_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, vbool4_t mask, vint8m2_t maskedoff0, vint8m2_t
    maskedoff1, vint8m2_t maskedoff2, const int8_t *base, size_t

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    vl);
void vlseg3bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vbool16_t mask, vuint16m1_t maskedoff0,
    vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, const
    uint16_t *base, size_t vl);
void vlseg3bu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vbool8_t mask, vuint16m2_t maskedoff0,
    vuint16m2_t maskedoff1, vuint16m2_t maskedoff2, const
    uint16_t *base, size_t vl);
void vlseg3bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, const
    uint32_t *base, size_t vl);
void vlseg3bu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vbool16_t mask, vuint32m2_t maskedoff0,
    vuint32m2_t maskedoff1, vuint32m2_t maskedoff2, const
    uint32_t *base, size_t vl);
void vlseg3bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, const
    uint64_t *base, size_t vl);
void vlseg3bu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vbool32_t mask, vuint64m2_t maskedoff0,
    vuint64m2_t maskedoff1, vuint64m2_t maskedoff2, const
    uint64_t *base, size_t vl);
void vlseg3bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vbool8_t mask, vuint8m1_t maskedoff0,
    vuint8m1_t maskedoff1, vuint8m1_t maskedoff2, const uint8_t
    *base, size_t vl);
void vlseg3bu_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, vbool4_t mask, vuint8m2_t maskedoff0,
    vuint8m2_t maskedoff1, vuint8m2_t maskedoff2, const uint8_t
    *base, size_t vl);
void vlseg4b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vbool16_t mask, vint16m1_t
    maskedoff0, vint16m1_t maskedoff1, vint16m1_t maskedoff2,
    vint16m1_t maskedoff3, const int16_t *base, size_t vl);
void vlseg4b_v_i16m2_m (vint16m2_t *v0, vint16m2_t *v1,
    vint16m2_t *v2, vint16m2_t *v3, vbool8_t mask, vint16m2_t
    maskedoff0, vint16m2_t maskedoff1, vint16m2_t maskedoff2,
    vint16m2_t maskedoff3, const int16_t *base, size_t vl);
void vlseg4b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vbool32_t mask, vint32m1_t
    maskedoff0, vint32m1_t maskedoff1, vint32m1_t maskedoff2,
    vint32m1_t maskedoff3, const int32_t *base, size_t vl);

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void vlseg4b_v_i32m2_m (vint32m2_t *v0, vint32m2_t *v1,
    vint32m2_t *v2, vint32m2_t *v3, vbool16_t mask, vint32m2_t
    maskedoff0, vint32m2_t maskedoff1, vint32m2_t maskedoff2,
    vint32m2_t maskedoff3, const int32_t *base, size_t vl);
void vlseg4b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vbool64_t mask, vint64m1_t
    maskedoff0, vint64m1_t maskedoff1, vint64m1_t maskedoff2,
    vint64m1_t maskedoff3, const int64_t *base, size_t vl);
void vlseg4b_v_i64m2_m (vint64m2_t *v0, vint64m2_t *v1,
    vint64m2_t *v2, vint64m2_t *v3, vbool32_t mask, vint64m2_t
    maskedoff0, vint64m2_t maskedoff1, vint64m2_t maskedoff2,
    vint64m2_t maskedoff3, const int64_t *base, size_t vl);
void vlseg4b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vbool8_t mask, vint8m1_t maskedoff0,
    vint8m1_t maskedoff1, vint8m1_t maskedoff2, vint8m1_t
    maskedoff3, const int8_t *base, size_t vl);
void vlseg4b_v_i8m2_m (vint8m2_t *v0, vint8m2_t *v1, vint8m2_t
    *v2, vint8m2_t *v3, vbool4_t mask, vint8m2_t maskedoff0,
    vint8m2_t maskedoff1, vint8m2_t maskedoff2, vint8m2_t
    maskedoff3, const int8_t *base, size_t vl);
void vlseg4bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vbool16_t mask,
    vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
    maskedoff2, vuint16m1_t maskedoff3, const uint16_t *base,
    size_t vl);
void vlseg4bu_v_u16m2_m (vuint16m2_t *v0, vuint16m2_t *v1,
    vuint16m2_t *v2, vuint16m2_t *v3, vbool8_t mask, vuint16m2_t
    maskedoff0, vuint16m2_t maskedoff1, vuint16m2_t maskedoff2,
    vuint16m2_t maskedoff3, const uint16_t *base, size_t vl);
void vlseg4bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, const uint32_t *base,
    size_t vl);
void vlseg4bu_v_u32m2_m (vuint32m2_t *v0, vuint32m2_t *v1,
    vuint32m2_t *v2, vuint32m2_t *v3, vbool16_t mask,
    vuint32m2_t maskedoff0, vuint32m2_t maskedoff1, vuint32m2_t
    maskedoff2, vuint32m2_t maskedoff3, const uint32_t *base,
    size_t vl);
void vlseg4bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vbool64_t mask,
    vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
    maskedoff2, vuint64m1_t maskedoff3, const uint64_t *base,
    size_t vl);
void vlseg4bu_v_u64m2_m (vuint64m2_t *v0, vuint64m2_t *v1,
    vuint64m2_t *v2, vuint64m2_t *v3, vbool32_t mask,

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    vuint64m2_t maskedoff0, vuint64m2_t maskedoff1, vuint64m2_t
    maskedoff2, vuint64m2_t maskedoff3, const uint64_t *base,
    size_t vl);
void vlseg4bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vbool8_t mask, vuint8m1_t
    maskedoff0, vuint8m1_t maskedoff1, vuint8m1_t maskedoff2,
    vuint8m1_t maskedoff3, const uint8_t *base, size_t vl);
void vlseg4bu_v_u8m2_m (vuint8m2_t *v0, vuint8m2_t *v1,
    vuint8m2_t *v2, vuint8m2_t *v3, vbool4_t mask, vuint8m2_t
    maskedoff0, vuint8m2_t maskedoff1, vuint8m2_t maskedoff2,
    vuint8m2_t maskedoff3, const uint8_t *base, size_t vl);
void vlseg5b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vbool16_t
    mask, vint16m1_t maskedoff0, vint16m1_t maskedoff1,
    vint16m1_t maskedoff2, vint16m1_t maskedoff3, vint16m1_t
    maskedoff4, const int16_t *base, size_t vl);
void vlseg5b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vbool32_t
    mask, vint32m1_t maskedoff0, vint32m1_t maskedoff1,
    vint32m1_t maskedoff2, vint32m1_t maskedoff3, vint32m1_t
    maskedoff4, const int32_t *base, size_t vl);
void vlseg5b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vbool64_t
    mask, vint64m1_t maskedoff0, vint64m1_t maskedoff1,
    vint64m1_t maskedoff2, vint64m1_t maskedoff3, vint64m1_t
    maskedoff4, const int64_t *base, size_t vl);
void vlseg5b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vbool8_t mask, vint8m1_t
    maskedoff0, vint8m1_t maskedoff1, vint8m1_t maskedoff2,
    vint8m1_t maskedoff3, vint8m1_t maskedoff4, const int8_t
    *base, size_t vl);
void vlseg5bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4, vbool16_t
    mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
    vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
    maskedoff4, const uint16_t *base, size_t vl);
void vlseg5bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, const uint32_t *base, size_t vl);
void vlseg5bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, const uint64_t *base, size_t vl);

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void vlseg5bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vbool8_t
    mask, vuint8m1_t maskedoff0, vuint8m1_t maskedoff1,
    vuint8m1_t maskedoff2, vuint8m1_t maskedoff3, vuint8m1_t
    maskedoff4, const uint8_t *base, size_t vl);
void vlseg6b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vbool16_t mask, vint16m1_t maskedoff0, vint16m1_t
    maskedoff1, vint16m1_t maskedoff2, vint16m1_t maskedoff3,
    vint16m1_t maskedoff4, vint16m1_t maskedoff5, const int16_t
    *base, size_t vl);
void vlseg6b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vbool32_t mask, vint32m1_t maskedoff0, vint32m1_t
    maskedoff1, vint32m1_t maskedoff2, vint32m1_t maskedoff3,
    vint32m1_t maskedoff4, vint32m1_t maskedoff5, const int32_t
    *base, size_t vl);
void vlseg6b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vbool64_t mask, vint64m1_t maskedoff0, vint64m1_t
    maskedoff1, vint64m1_t maskedoff2, vint64m1_t maskedoff3,
    vint64m1_t maskedoff4, vint64m1_t maskedoff5, const int64_t
    *base, size_t vl);
void vlseg6b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vbool8_t
    mask, vint8m1_t maskedoff0, vint8m1_t maskedoff1, vint8m1_t
    maskedoff2, vint8m1_t maskedoff3, vint8m1_t maskedoff4,
    vint8m1_t maskedoff5, const int8_t *base, size_t vl);
void vlseg6bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vbool16_t mask, vuint16m1_t maskedoff0,
    vuint16m1_t maskedoff1, vuint16m1_t maskedoff2, vuint16m1_t
    maskedoff3, vuint16m1_t maskedoff4, vuint16m1_t maskedoff5,
    const uint16_t *base, size_t vl);
void vlseg6bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vbool32_t mask, vuint32m1_t maskedoff0,
    vuint32m1_t maskedoff1, vuint32m1_t maskedoff2, vuint32m1_t
    maskedoff3, vuint32m1_t maskedoff4, vuint32m1_t maskedoff5,
    const uint32_t *base, size_t vl);
void vlseg6bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vbool64_t mask, vuint64m1_t maskedoff0,
    vuint64m1_t maskedoff1, vuint64m1_t maskedoff2, vuint64m1_t
    maskedoff3, vuint64m1_t maskedoff4, vuint64m1_t maskedoff5,
    const uint64_t *base, size_t vl);

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void vlseg6bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vbool8_t mask, vuint8m1_t maskedoff0, vuint8m1_t
    maskedoff1, vuint8m1_t maskedoff2, vuint8m1_t maskedoff3,
    vuint8m1_t maskedoff4, vuint8m1_t maskedoff5, const uint8_t
    *base, size_t vl);
void vlseg7b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
    vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
    *v5, vint16m1_t *v6, vbool16_t mask, vint16m1_t maskedoff0,
    vint16m1_t maskedoff1, vint16m1_t maskedoff2, vint16m1_t
    maskedoff3, vint16m1_t maskedoff4, vint16m1_t maskedoff5,
    vint16m1_t maskedoff6, const int16_t *base, size_t vl);
void vlseg7b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
    vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
    *v5, vint32m1_t *v6, vbool32_t mask, vint32m1_t maskedoff0,
    vint32m1_t maskedoff1, vint32m1_t maskedoff2, vint32m1_t
    maskedoff3, vint32m1_t maskedoff4, vint32m1_t maskedoff5,
    vint32m1_t maskedoff6, const int32_t *base, size_t vl);
void vlseg7b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
    vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
    *v5, vint64m1_t *v6, vbool64_t mask, vint64m1_t maskedoff0,
    vint64m1_t maskedoff1, vint64m1_t maskedoff2, vint64m1_t
    maskedoff3, vint64m1_t maskedoff4, vint64m1_t maskedoff5,
    vint64m1_t maskedoff6, const int64_t *base, size_t vl);
void vlseg7b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
    *v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
    *v6, vbool8_t mask, vint8m1_t maskedoff0, vint8m1_t
    maskedoff1, vint8m1_t maskedoff2, vint8m1_t maskedoff3,
    vint8m1_t maskedoff4, vint8m1_t maskedoff5, vint8m1_t
    maskedoff6, const int8_t *base, size_t vl);
void vlseg7bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
    vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
    vuint16m1_t *v5, vuint16m1_t *v6, vbool16_t mask,
    vuint16m1_t maskedoff0, vuint16m1_t maskedoff1, vuint16m1_t
    maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t maskedoff4,
    vuint16m1_t maskedoff5, vuint16m1_t maskedoff6, const
    uint16_t *base, size_t vl);
void vlseg7bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vbool32_t mask,
    vuint32m1_t maskedoff0, vuint32m1_t maskedoff1, vuint32m1_t
    maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t maskedoff4,
    vuint32m1_t maskedoff5, vuint32m1_t maskedoff6, const
    uint32_t *base, size_t vl);
void vlseg7bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,

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vuint64m1_t *v5, vuint64m1_t *v6, vbool64_t mask,
vuint64m1_t maskedoff0, vuint64m1_t maskedoff1, vuint64m1_t
maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t maskedoff4,
vuint64m1_t maskedoff5, vuint64m1_t maskedoff6, const
uint64_t *base, size_t vl);
void vlseg7bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
*v5, vuint8m1_t *v6, vbool8_t mask, vuint8m1_t maskedoff0,
vuint8m1_t maskedoff1, vuint8m1_t maskedoff2, vuint8m1_t
maskedoff3, vuint8m1_t maskedoff4, vuint8m1_t maskedoff5,
vuint8m1_t maskedoff6, const uint8_t *base, size_t vl);
void vlseg8b_v_i16m1_m (vint16m1_t *v0, vint16m1_t *v1,
vint16m1_t *v2, vint16m1_t *v3, vint16m1_t *v4, vint16m1_t
*v5, vint16m1_t *v6, vint16m1_t *v7, vbool16_t mask,
vint16m1_t maskedoff0, vint16m1_t maskedoff1, vint16m1_t
maskedoff2, vint16m1_t maskedoff3, vint16m1_t maskedoff4,
vint16m1_t maskedoff5, vint16m1_t maskedoff6, vint16m1_t
maskedoff7, const int16_t *base, size_t vl);
void vlseg8b_v_i32m1_m (vint32m1_t *v0, vint32m1_t *v1,
vint32m1_t *v2, vint32m1_t *v3, vint32m1_t *v4, vint32m1_t
*v5, vint32m1_t *v6, vint32m1_t *v7, vbool32_t mask,
vint32m1_t maskedoff0, vint32m1_t maskedoff1, vint32m1_t
maskedoff2, vint32m1_t maskedoff3, vint32m1_t maskedoff4,
vint32m1_t maskedoff5, vint32m1_t maskedoff6, vint32m1_t
maskedoff7, const int32_t *base, size_t vl);
void vlseg8b_v_i64m1_m (vint64m1_t *v0, vint64m1_t *v1,
vint64m1_t *v2, vint64m1_t *v3, vint64m1_t *v4, vint64m1_t
*v5, vint64m1_t *v6, vint64m1_t *v7, vbool64_t mask,
vint64m1_t maskedoff0, vint64m1_t maskedoff1, vint64m1_t
maskedoff2, vint64m1_t maskedoff3, vint64m1_t maskedoff4,
vint64m1_t maskedoff5, vint64m1_t maskedoff6, vint64m1_t
maskedoff7, const int64_t *base, size_t vl);
void vlseg8b_v_i8m1_m (vint8m1_t *v0, vint8m1_t *v1, vint8m1_t
*v2, vint8m1_t *v3, vint8m1_t *v4, vint8m1_t *v5, vint8m1_t
*v6, vint8m1_t *v7, vbool8_t mask, vint8m1_t maskedoff0,
vint8m1_t maskedoff1, vint8m1_t maskedoff2, vint8m1_t
maskedoff3, vint8m1_t maskedoff4, vint8m1_t maskedoff5,
vint8m1_t maskedoff6, vint8m1_t maskedoff7, const int8_t
*base, size_t vl);
void vlseg8bu_v_u16m1_m (vuint16m1_t *v0, vuint16m1_t *v1,
vuint16m1_t *v2, vuint16m1_t *v3, vuint16m1_t *v4,
vuint16m1_t *v5, vuint16m1_t *v6, vuint16m1_t *v7, vbool16_t
mask, vuint16m1_t maskedoff0, vuint16m1_t maskedoff1,
vuint16m1_t maskedoff2, vuint16m1_t maskedoff3, vuint16m1_t
maskedoff4, vuint16m1_t maskedoff5, vuint16m1_t maskedoff6,
vuint16m1_t maskedoff7, const uint16_t *base, size_t vl);

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void vlseg8bu_v_u32m1_m (vuint32m1_t *v0, vuint32m1_t *v1,
    vuint32m1_t *v2, vuint32m1_t *v3, vuint32m1_t *v4,
    vuint32m1_t *v5, vuint32m1_t *v6, vuint32m1_t *v7, vbool32_t
    mask, vuint32m1_t maskedoff0, vuint32m1_t maskedoff1,
    vuint32m1_t maskedoff2, vuint32m1_t maskedoff3, vuint32m1_t
    maskedoff4, vuint32m1_t maskedoff5, vuint32m1_t maskedoff6,
    vuint32m1_t maskedoff7, const uint32_t *base, size_t vl);
void vlseg8bu_v_u64m1_m (vuint64m1_t *v0, vuint64m1_t *v1,
    vuint64m1_t *v2, vuint64m1_t *v3, vuint64m1_t *v4,
    vuint64m1_t *v5, vuint64m1_t *v6, vuint64m1_t *v7, vbool64_t
    mask, vuint64m1_t maskedoff0, vuint64m1_t maskedoff1,
    vuint64m1_t maskedoff2, vuint64m1_t maskedoff3, vuint64m1_t
    maskedoff4, vuint64m1_t maskedoff5, vuint64m1_t maskedoff6,
    vuint64m1_t maskedoff7, const uint64_t *base, size_t vl);
void vlseg8bu_v_u8m1_m (vuint8m1_t *v0, vuint8m1_t *v1,
    vuint8m1_t *v2, vuint8m1_t *v3, vuint8m1_t *v4, vuint8m1_t
    *v5, vuint8m1_t *v6, vuint8m1_t *v7, vbool8_t mask,
    vuint8m1_t maskedoff0, vuint8m1_t maskedoff1, vuint8m1_t
    maskedoff2, vuint8m1_t maskedoff3, vuint8m1_t maskedoff4,
    vuint8m1_t maskedoff5, vuint8m1_t maskedoff6, vuint8m1_t
    maskedoff7, const uint8_t *base, size_t vl);
void vsseg2b_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, size_t vl);
void vsseg2b_v_i16m2_m (vbool8_t mask, int16_t *base, vint16m2_t
    v0, vint16m2_t v1, size_t vl);
void vsseg2b_v_i16m4_m (vbool4_t mask, int16_t *base, vint16m4_t
    v0, vint16m4_t v1, size_t vl);
void vsseg2b_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, size_t vl);
void vsseg2b_v_i32m2_m (vbool16_t mask, int32_t *base,
    vint32m2_t v0, vint32m2_t v1, size_t vl);
void vsseg2b_v_i32m4_m (vbool8_t mask, int32_t *base, vint32m4_t
    v0, vint32m4_t v1, size_t vl);
void vsseg2b_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, size_t vl);
void vsseg2b_v_i64m2_m (vbool32_t mask, int64_t *base,
    vint64m2_t v0, vint64m2_t v1, size_t vl);
void vsseg2b_v_i64m4_m (vbool16_t mask, int64_t *base,
    vint64m4_t v0, vint64m4_t v1, size_t vl);
void vsseg2b_v_i8m1_m (vbool8_t mask, int8_t *base, vint8m1_t
    v0, vint8m1_t v1, size_t vl);
void vsseg2b_v_i8m2_m (vbool4_t mask, int8_t *base, vint8m2_t
    v0, vint8m2_t v1, size_t vl);
void vsseg2b_v_i8m4_m (vbool2_t mask, int8_t *base, vint8m4_t
    v0, vint8m4_t v1, size_t vl);

```

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void vsseg2b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, size_t vl);
void vsseg2b_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t v0, vuint16m2_t v1, size_t vl);
void vsseg2b_v_u16m4_m (vbool4_t mask, uint16_t *base,
    vuint16m4_t v0, vuint16m4_t v1, size_t vl);
void vsseg2b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, size_t vl);
void vsseg2b_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t v0, vuint32m2_t v1, size_t vl);
void vsseg2b_v_u32m4_m (vbool8_t mask, uint32_t *base,
    vuint32m4_t v0, vuint32m4_t v1, size_t vl);
void vsseg2b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, size_t vl);
void vsseg2b_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t v0, vuint64m2_t v1, size_t vl);
void vsseg2b_v_u64m4_m (vbool16_t mask, uint64_t *base,
    vuint64m4_t v0, vuint64m4_t v1, size_t vl);
void vsseg2b_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    v0, vuint8m1_t v1, size_t vl);
void vsseg2b_v_u8m2_m (vbool4_t mask, uint8_t *base, vuint8m2_t
    v0, vuint8m2_t v1, size_t vl);
void vsseg2b_v_u8m4_m (vbool2_t mask, uint8_t *base, vuint8m4_t
    v0, vuint8m4_t v1, size_t vl);
void vsseg3b_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, size_t vl);
void vsseg3b_v_i16m2_m (vbool8_t mask, int16_t *base, vint16m2_t
    v0, vint16m2_t v1, vint16m2_t v2, size_t vl);
void vsseg3b_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, size_t vl);
void vsseg3b_v_i32m2_m (vbool16_t mask, int32_t *base,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, size_t vl);
void vsseg3b_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, size_t vl);
void vsseg3b_v_i64m2_m (vbool32_t mask, int64_t *base,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, size_t vl);
void vsseg3b_v_i8m1_m (vbool8_t mask, int8_t *base, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, size_t vl);
void vsseg3b_v_i8m2_m (vbool4_t mask, int8_t *base, vint8m2_t
    v0, vint8m2_t v1, vint8m2_t v2, size_t vl);
void vsseg3b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, size_t vl);
void vsseg3b_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, size_t vl);
void vsseg3b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, size_t vl);

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void vsseg3b_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, size_t vl);
void vsseg3b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, size_t vl);
void vsseg3b_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, size_t vl);
void vsseg3b_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    v0, vuint8m1_t v1, vuint8m1_t v2, size_t vl);
void vsseg3b_v_u8m2_m (vbool4_t mask, uint8_t *base, vuint8m2_t
    v0, vuint8m2_t v1, vuint8m2_t v2, size_t vl);
void vsseg4b_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    size_t vl);
void vsseg4b_v_i16m2_m (vbool8_t mask, int16_t *base, vint16m2_t
    v0, vint16m2_t v1, vint16m2_t v2, vint16m2_t v3, size_t vl);
void vsseg4b_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    size_t vl);
void vsseg4b_v_i32m2_m (vbool16_t mask, int32_t *base,
    vint32m2_t v0, vint32m2_t v1, vint32m2_t v2, vint32m2_t v3,
    size_t vl);
void vsseg4b_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    size_t vl);
void vsseg4b_v_i64m2_m (vbool32_t mask, int64_t *base,
    vint64m2_t v0, vint64m2_t v1, vint64m2_t v2, vint64m2_t v3,
    size_t vl);
void vsseg4b_v_i8m1_m (vbool8_t mask, int8_t *base, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, size_t vl);
void vsseg4b_v_i8m2_m (vbool4_t mask, int8_t *base, vint8m2_t
    v0, vint8m2_t v1, vint8m2_t v2, vint8m2_t v3, size_t vl);
void vsseg4b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, size_t vl);
void vsseg4b_v_u16m2_m (vbool8_t mask, uint16_t *base,
    vuint16m2_t v0, vuint16m2_t v1, vuint16m2_t v2, vuint16m2_t
    v3, size_t vl);
void vsseg4b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, size_t vl);
void vsseg4b_v_u32m2_m (vbool16_t mask, uint32_t *base,
    vuint32m2_t v0, vuint32m2_t v1, vuint32m2_t v2, vuint32m2_t
    v3, size_t vl);
void vsseg4b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, size_t vl);

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void vsseg4b_v_u64m2_m (vbool32_t mask, uint64_t *base,
    vuint64m2_t v0, vuint64m2_t v1, vuint64m2_t v2, vuint64m2_t
    v3, size_t vl);
void vsseg4b_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3, size_t vl);
void vsseg4b_v_u8m2_m (vbool4_t mask, uint8_t *base, vuint8m2_t
    v0, vuint8m2_t v1, vuint8m2_t v2, vuint8m2_t v3, size_t vl);
void vsseg5b_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, size_t vl);
void vsseg5b_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, size_t vl);
void vsseg5b_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, size_t vl);
void vsseg5b_v_i8m1_m (vbool8_t mask, int8_t *base, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, vint8m1_t v4,
    size_t vl);
void vsseg5b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, size_t vl);
void vsseg5b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, size_t vl);
void vsseg5b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, size_t vl);
void vsseg5b_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3, vuint8m1_t
    v4, size_t vl);
void vsseg6b_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, size_t vl);
void vsseg6b_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, size_t vl);
void vsseg6b_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, size_t vl);
void vsseg6b_v_i8m1_m (vbool8_t mask, int8_t *base, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, vint8m1_t v4,
    vint8m1_t v5, size_t vl);
void vsseg6b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, size_t vl);

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void vsseg6b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, size_t vl);
void vsseg6b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, size_t vl);
void vsseg6b_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3, vuint8m1_t
    v4, vuint8m1_t v5, size_t vl);
void vsseg7b_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, size_t vl);
void vsseg7b_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, size_t vl);
void vsseg7b_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,
    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, size_t vl);
void vsseg7b_v_i8m1_m (vbool8_t mask, int8_t *base, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, vint8m1_t v4,
    vint8m1_t v5, vint8m1_t v6, size_t vl);
void vsseg7b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6, size_t
    vl);
void vsseg7b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6, size_t
    vl);
void vsseg7b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6, size_t
    vl);
void vsseg7b_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3, vuint8m1_t
    v4, vuint8m1_t v5, vuint8m1_t v6, size_t vl);
void vsseg8b_v_i16m1_m (vbool16_t mask, int16_t *base,
    vint16m1_t v0, vint16m1_t v1, vint16m1_t v2, vint16m1_t v3,
    vint16m1_t v4, vint16m1_t v5, vint16m1_t v6, vint16m1_t v7,
    size_t vl);
void vsseg8b_v_i32m1_m (vbool32_t mask, int32_t *base,
    vint32m1_t v0, vint32m1_t v1, vint32m1_t v2, vint32m1_t v3,
    vint32m1_t v4, vint32m1_t v5, vint32m1_t v6, vint32m1_t v7,
    size_t vl);
void vsseg8b_v_i64m1_m (vbool64_t mask, int64_t *base,
    vint64m1_t v0, vint64m1_t v1, vint64m1_t v2, vint64m1_t v3,

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    vint64m1_t v4, vint64m1_t v5, vint64m1_t v6, vint64m1_t v7,
    size_t vl);
void vsseg8b_v_i8m1_m (vbool8_t mask, int8_t *base, vint8m1_t
    v0, vint8m1_t v1, vint8m1_t v2, vint8m1_t v3, vint8m1_t v4,
    vint8m1_t v5, vint8m1_t v6, vint8m1_t v7, size_t vl);
void vsseg8b_v_u16m1_m (vbool16_t mask, uint16_t *base,
    vuint16m1_t v0, vuint16m1_t v1, vuint16m1_t v2, vuint16m1_t
    v3, vuint16m1_t v4, vuint16m1_t v5, vuint16m1_t v6,
    vuint16m1_t v7, size_t vl);
void vsseg8b_v_u32m1_m (vbool32_t mask, uint32_t *base,
    vuint32m1_t v0, vuint32m1_t v1, vuint32m1_t v2, vuint32m1_t
    v3, vuint32m1_t v4, vuint32m1_t v5, vuint32m1_t v6,
    vuint32m1_t v7, size_t vl);
void vsseg8b_v_u64m1_m (vbool64_t mask, uint64_t *base,
    vuint64m1_t v0, vuint64m1_t v1, vuint64m1_t v2, vuint64m1_t
    v3, vuint64m1_t v4, vuint64m1_t v5, vuint64m1_t v6,
    vuint64m1_t v7, size_t vl);
void vsseg8b_v_u8m1_m (vbool8_t mask, uint8_t *base, vuint8m1_t
    v0, vuint8m1_t v1, vuint8m1_t v2, vuint8m1_t v3, vuint8m1_t
    v4, vuint8m1_t v5, vuint8m1_t v6, vuint8m1_t v7, size_t vl);

```

## Vector Integer Arithmetic Functions:

### Vector Single-Width Integer Add and Subtract Functions:

#### Prototypes:

```

vint8m1_t vadd_vv_i8m1 (vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vadd_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vadd_vv_i8m2 (vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vadd_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vadd_vv_i8m4 (vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vadd_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vadd_vv_i8m8 (vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vadd_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vadd_vv_i16m1 (vint16m1_t op1, vint16m1_t op2, size_t
    vl);
vint16m1_t vadd_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint16m2_t vadd_vv_i16m2 (vint16m2_t op1, vint16m2_t op2, size_t
    vl);
vint16m2_t vadd_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint16m4_t vadd_vv_i16m4 (vint16m4_t op1, vint16m4_t op2, size_t
    vl);

```

```

vint16m4_t vadd_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vint16m8_t vadd_vv_i16m8 (vint16m8_t op1, vint16m8_t op2, size_t
    vl);
vint16m8_t vadd_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t
    vl);
vint32m1_t vadd_vv_i32m1 (vint32m1_t op1, vint32m1_t op2, size_t
    vl);
vint32m1_t vadd_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint32m2_t vadd_vv_i32m2 (vint32m2_t op1, vint32m2_t op2, size_t
    vl);
vint32m2_t vadd_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint32m4_t vadd_vv_i32m4 (vint32m4_t op1, vint32m4_t op2, size_t
    vl);
vint32m4_t vadd_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint32m8_t vadd_vv_i32m8 (vint32m8_t op1, vint32m8_t op2, size_t
    vl);
vint32m8_t vadd_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t
    vl);
vint64m1_t vadd_vv_i64m1 (vint64m1_t op1, vint64m1_t op2, size_t
    vl);
vint64m1_t vadd_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t
    vl);
vint64m2_t vadd_vv_i64m2 (vint64m2_t op1, vint64m2_t op2, size_t
    vl);
vint64m2_t vadd_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t
    vl);
vint64m4_t vadd_vv_i64m4 (vint64m4_t op1, vint64m4_t op2, size_t
    vl);
vint64m4_t vadd_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t
    vl);
vint64m8_t vadd_vv_i64m8 (vint64m8_t op1, vint64m8_t op2, size_t
    vl);
vint64m8_t vadd_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vuint8m1_t vadd_vv_u8m1 (vuint8m1_t op1, vuint8m1_t op2, size_t
    vl);
vuint8m1_t vadd_vx_u8m1 (vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vadd_vv_u8m2 (vuint8m2_t op1, vuint8m2_t op2, size_t
    vl);
vuint8m2_t vadd_vx_u8m2 (vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vadd_vv_u8m4 (vuint8m4_t op1, vuint8m4_t op2, size_t
    vl);

```

```

vuint8m4_t vadd_vx_u8m4 (vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vadd_vv_u8m8 (vuint8m8_t op1, vuint8m8_t op2, size_t
    vl);
vuint8m8_t vadd_vx_u8m8 (vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vadd_vv_u16m1 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vuint16m1_t vadd_vx_u16m1 (vuint16m1_t op1, uint16_t op2, size_t
    vl);
vuint16m2_t vadd_vv_u16m2 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vuint16m2_t vadd_vx_u16m2 (vuint16m2_t op1, uint16_t op2, size_t
    vl);
vuint16m4_t vadd_vv_u16m4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vuint16m4_t vadd_vx_u16m4 (vuint16m4_t op1, uint16_t op2, size_t
    vl);
vuint16m8_t vadd_vv_u16m8 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vuint16m8_t vadd_vx_u16m8 (vuint16m8_t op1, uint16_t op2, size_t
    vl);
vuint32m1_t vadd_vv_u32m1 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vuint32m1_t vadd_vx_u32m1 (vuint32m1_t op1, uint32_t op2, size_t
    vl);
vuint32m2_t vadd_vv_u32m2 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vuint32m2_t vadd_vx_u32m2 (vuint32m2_t op1, uint32_t op2, size_t
    vl);
vuint32m4_t vadd_vv_u32m4 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vuint32m4_t vadd_vx_u32m4 (vuint32m4_t op1, uint32_t op2, size_t
    vl);
vuint32m8_t vadd_vv_u32m8 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vuint32m8_t vadd_vx_u32m8 (vuint32m8_t op1, uint32_t op2, size_t
    vl);
vuint64m1_t vadd_vv_u64m1 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vuint64m1_t vadd_vx_u64m1 (vuint64m1_t op1, uint64_t op2, size_t
    vl);
vuint64m2_t vadd_vv_u64m2 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vuint64m2_t vadd_vx_u64m2 (vuint64m2_t op1, uint64_t op2, size_t
    vl);
vuint64m4_t vadd_vv_u64m4 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);

```

```

vuint64m4_t vadd_vx_u64m4 (vuint64m4_t op1, uint64_t op2, size_t
    vl);
vuint64m8_t vadd_vv_u64m8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vuint64m8_t vadd_vx_u64m8 (vuint64m8_t op1, uint64_t op2, size_t
    vl);
vint8m1_t vsub_vv_i8m1 (vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vsub_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vsub_vv_i8m2 (vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vsub_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vsub_vv_i8m4 (vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vsub_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vsub_vv_i8m8 (vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vsub_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vsub_vv_i16m1 (vint16m1_t op1, vint16m1_t op2, size_t
    vl);
vint16m1_t vsub_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint16m2_t vsub_vv_i16m2 (vint16m2_t op1, vint16m2_t op2, size_t
    vl);
vint16m2_t vsub_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint16m4_t vsub_vv_i16m4 (vint16m4_t op1, vint16m4_t op2, size_t
    vl);
vint16m4_t vsub_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vint16m8_t vsub_vv_i16m8 (vint16m8_t op1, vint16m8_t op2, size_t
    vl);
vint16m8_t vsub_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t
    vl);
vint32m1_t vsub_vv_i32m1 (vint32m1_t op1, vint32m1_t op2, size_t
    vl);
vint32m1_t vsub_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint32m2_t vsub_vv_i32m2 (vint32m2_t op1, vint32m2_t op2, size_t
    vl);
vint32m2_t vsub_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint32m4_t vsub_vv_i32m4 (vint32m4_t op1, vint32m4_t op2, size_t
    vl);
vint32m4_t vsub_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint32m8_t vsub_vv_i32m8 (vint32m8_t op1, vint32m8_t op2, size_t
    vl);
vint32m8_t vsub_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t
    vl);

```

```

vint64m1_t vsub_vv_i64m1 (vint64m1_t op1, vint64m1_t op2, size_t
    vl);
vint64m1_t vsub_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t
    vl);
vint64m2_t vsub_vv_i64m2 (vint64m2_t op1, vint64m2_t op2, size_t
    vl);
vint64m2_t vsub_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t
    vl);
vint64m4_t vsub_vv_i64m4 (vint64m4_t op1, vint64m4_t op2, size_t
    vl);
vint64m4_t vsub_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t
    vl);
vint64m8_t vsub_vv_i64m8 (vint64m8_t op1, vint64m8_t op2, size_t
    vl);
vint64m8_t vsub_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vuint8m1_t vsub_vv_u8m1 (vuint8m1_t op1, vuint8m1_t op2, size_t
    vl);
vuint8m1_t vsub_vx_u8m1 (vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vsub_vv_u8m2 (vuint8m2_t op1, vuint8m2_t op2, size_t
    vl);
vuint8m2_t vsub_vx_u8m2 (vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vsub_vv_u8m4 (vuint8m4_t op1, vuint8m4_t op2, size_t
    vl);
vuint8m4_t vsub_vx_u8m4 (vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vsub_vv_u8m8 (vuint8m8_t op1, vuint8m8_t op2, size_t
    vl);
vuint8m8_t vsub_vx_u8m8 (vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vsub_vv_u16m1 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vuint16m1_t vsub_vx_u16m1 (vuint16m1_t op1, uint16_t op2, size_t
    vl);
vuint16m2_t vsub_vv_u16m2 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vuint16m2_t vsub_vx_u16m2 (vuint16m2_t op1, uint16_t op2, size_t
    vl);
vuint16m4_t vsub_vv_u16m4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vuint16m4_t vsub_vx_u16m4 (vuint16m4_t op1, uint16_t op2, size_t
    vl);
vuint16m8_t vsub_vv_u16m8 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vuint16m8_t vsub_vx_u16m8 (vuint16m8_t op1, uint16_t op2, size_t
    vl);
vuint32m1_t vsub_vv_u32m1 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);

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```

vuint32m1_t vsub_vx_u32m1 (vuint32m1_t op1, uint32_t op2, size_t
    vl);
vuint32m2_t vsub_vv_u32m2 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vuint32m2_t vsub_vx_u32m2 (vuint32m2_t op1, uint32_t op2, size_t
    vl);
vuint32m4_t vsub_vv_u32m4 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vuint32m4_t vsub_vx_u32m4 (vuint32m4_t op1, uint32_t op2, size_t
    vl);
vuint32m8_t vsub_vv_u32m8 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vuint32m8_t vsub_vx_u32m8 (vuint32m8_t op1, uint32_t op2, size_t
    vl);
vuint64m1_t vsub_vv_u64m1 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vuint64m1_t vsub_vx_u64m1 (vuint64m1_t op1, uint64_t op2, size_t
    vl);
vuint64m2_t vsub_vv_u64m2 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vuint64m2_t vsub_vx_u64m2 (vuint64m2_t op1, uint64_t op2, size_t
    vl);
vuint64m4_t vsub_vv_u64m4 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vuint64m4_t vsub_vx_u64m4 (vuint64m4_t op1, uint64_t op2, size_t
    vl);
vuint64m8_t vsub_vv_u64m8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vuint64m8_t vsub_vx_u64m8 (vuint64m8_t op1, uint64_t op2, size_t
    vl);
vint8m1_t vrsb_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vrsb_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vrsb_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vrsb_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vrsb_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint16m2_t vrsb_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint16m4_t vrsb_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vint16m8_t vrsb_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t
    vl);
vint32m1_t vrsb_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint32m2_t vrsb_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t
    vl);

```

```

vint32m4_t vrsub_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint32m8_t vrsub_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t
    vl);
vint64m1_t vrsub_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t
    vl);
vint64m2_t vrsub_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t
    vl);
vint64m4_t vrsub_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t
    vl);
vint64m8_t vrsub_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vuint8m1_t vrsub_vx_u8m1 (vuint8m1_t op1, uint8_t op2, size_t
    vl);
vuint8m2_t vrsub_vx_u8m2 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vuint8m4_t vrsub_vx_u8m4 (vuint8m4_t op1, uint8_t op2, size_t
    vl);
vuint8m8_t vrsub_vx_u8m8 (vuint8m8_t op1, uint8_t op2, size_t
    vl);
vuint16m1_t vrsub_vx_u16m1 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vuint16m2_t vrsub_vx_u16m2 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vuint16m4_t vrsub_vx_u16m4 (vuint16m4_t op1, uint16_t op2,
    size_t vl);
vuint16m8_t vrsub_vx_u16m8 (vuint16m8_t op1, uint16_t op2,
    size_t vl);
vuint32m1_t vrsub_vx_u32m1 (vuint32m1_t op1, uint32_t op2,
    size_t vl);
vuint32m2_t vrsub_vx_u32m2 (vuint32m2_t op1, uint32_t op2,
    size_t vl);
vuint32m4_t vrsub_vx_u32m4 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vuint32m8_t vrsub_vx_u32m8 (vuint32m8_t op1, uint32_t op2,
    size_t vl);
vuint64m1_t vrsub_vx_u64m1 (vuint64m1_t op1, uint64_t op2,
    size_t vl);
vuint64m2_t vrsub_vx_u64m2 (vuint64m2_t op1, uint64_t op2,
    size_t vl);
vuint64m4_t vrsub_vx_u64m4 (vuint64m4_t op1, uint64_t op2,
    size_t vl);
vuint64m8_t vrsub_vx_u64m8 (vuint64m8_t op1, uint64_t op2,
    size_t vl);
vint8m1_t vneg_v_i8m1 (vint8m1_t op1, size_t vl);
vint8m2_t vneg_v_i8m2 (vint8m2_t op1, size_t vl);

```

```

vint8m4_t vneg_v_i8m4 (vint8m4_t op1, size_t vl);
vint8m8_t vneg_v_i8m8 (vint8m8_t op1, size_t vl);
vint16m1_t vneg_v_i16m1 (vint16m1_t op1, size_t vl);
vint16m2_t vneg_v_i16m2 (vint16m2_t op1, size_t vl);
vint16m4_t vneg_v_i16m4 (vint16m4_t op1, size_t vl);
vint16m8_t vneg_v_i16m8 (vint16m8_t op1, size_t vl);
vint32m1_t vneg_v_i32m1 (vint32m1_t op1, size_t vl);
vint32m2_t vneg_v_i32m2 (vint32m2_t op1, size_t vl);
vint32m4_t vneg_v_i32m4 (vint32m4_t op1, size_t vl);
vint32m8_t vneg_v_i32m8 (vint32m8_t op1, size_t vl);
vint64m1_t vneg_v_i64m1 (vint64m1_t op1, size_t vl);
vint64m2_t vneg_v_i64m2 (vint64m2_t op1, size_t vl);
vint64m4_t vneg_v_i64m4 (vint64m4_t op1, size_t vl);
vint64m8_t vneg_v_i64m8 (vint64m8_t op1, size_t vl);
// masked functions
vint8m1_t vadd_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vadd_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vadd_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vadd_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vadd_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vadd_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vadd_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vadd_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vadd_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint16m1_t vadd_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vint16m2_t vadd_vv_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, vint16m2_t op2, size_t vl);
vint16m2_t vadd_vx_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vadd_vv_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, vint16m4_t op2, size_t vl);
vint16m4_t vadd_vx_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vadd_vv_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, vint16m8_t op2, size_t vl);

```



```

vint16m8_t vadd_vx_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vadd_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint32m1_t vadd_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vadd_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vint32m2_t vadd_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint32m4_t vadd_vv_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, vint32m4_t op2, size_t vl);
vint32m4_t vadd_vx_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vadd_vv_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, vint32m8_t op2, size_t vl);
vint32m8_t vadd_vx_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vadd_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vint64m1_t vadd_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vadd_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vint64m2_t vadd_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vint64m4_t vadd_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vint64m4_t vadd_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vadd_vv_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, vint64m8_t op2, size_t vl);
vint64m8_t vadd_vx_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, int64_t op2, size_t vl);
vuint8m1_t vadd_vv_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vuint8m1_t vadd_vx_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vadd_vv_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vuint8m2_t vadd_vx_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vadd_vv_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vuint8m4_t vadd_vx_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);

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vuint8m8_t vadd_vv_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vuint8m8_t vadd_vx_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vadd_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vuint16m1_t vadd_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint16m2_t vadd_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vuint16m2_t vadd_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint16m4_t vadd_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vuint16m4_t vadd_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vuint16m8_t vadd_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vuint16m8_t vadd_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vuint32m1_t vadd_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vuint32m1_t vadd_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vuint32m2_t vadd_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vuint32m2_t vadd_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint32m4_t vadd_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vuint32m4_t vadd_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vuint32m8_t vadd_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vuint32m8_t vadd_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);
vuint64m1_t vadd_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vuint64m1_t vadd_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vuint64m2_t vadd_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vuint64m2_t vadd_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vuint64m4_t vadd_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);

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vuint64m4_t vadd_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vuint64m8_t vadd_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vuint64m8_t vadd_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);
vint8m1_t vsub_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vsub_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vsub_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vsub_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vsub_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vsub_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vsub_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vsub_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vsub_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint16m1_t vsub_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vint16m2_t vsub_vv_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, vint16m2_t op2, size_t vl);
vint16m2_t vsub_vx_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vsub_vv_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, vint16m4_t op2, size_t vl);
vint16m4_t vsub_vx_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vsub_vv_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, vint16m8_t op2, size_t vl);
vint16m8_t vsub_vx_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vsub_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint32m1_t vsub_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vsub_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vint32m2_t vsub_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);

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vint32m4_t vsub_vv_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, vint32m4_t op2, size_t vl);
vint32m4_t vsub_vx_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vsub_vv_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, vint32m8_t op2, size_t vl);
vint32m8_t vsub_vx_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vsub_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vint64m1_t vsub_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vsub_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vint64m2_t vsub_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vint64m4_t vsub_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vint64m4_t vsub_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vsub_vv_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, vint64m8_t op2, size_t vl);
vint64m8_t vsub_vx_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, int64_t op2, size_t vl);
vuint8m1_t vsub_vv_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vuint8m1_t vsub_vx_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vsub_vv_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vuint8m2_t vsub_vx_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vsub_vv_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vuint8m4_t vsub_vx_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vsub_vv_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vuint8m8_t vsub_vx_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vsub_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vuint16m1_t vsub_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint16m2_t vsub_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);

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vuint16m2_t vsub_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint16m4_t vsub_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vuint16m4_t vsub_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vuint16m8_t vsub_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vuint16m8_t vsub_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vuint32m1_t vsub_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vuint32m1_t vsub_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vuint32m2_t vsub_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vuint32m2_t vsub_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint32m4_t vsub_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vuint32m4_t vsub_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vuint32m8_t vsub_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vuint32m8_t vsub_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);
vuint64m1_t vsub_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vuint64m1_t vsub_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vuint64m2_t vsub_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vuint64m2_t vsub_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vuint64m4_t vsub_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);
vuint64m4_t vsub_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vuint64m8_t vsub_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vuint64m8_t vsub_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);
vint8m1_t vrsub_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vrsub_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);

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vint8m4_t vrsub_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vrsub_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vrsub_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vint16m2_t vrsub_vx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vrsub_vx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vrsub_vx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vrsub_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vrsub_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint32m4_t vrsub_vx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vrsub_vx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vrsub_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vrsub_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vint64m4_t vrsub_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vrsub_vx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, int64_t op2, size_t vl);
vuint8m1_t vrsub_vx_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vrsub_vx_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vrsub_vx_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vrsub_vx_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vrsub_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint16m2_t vrsub_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint16m4_t vrsub_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vuint16m8_t vrsub_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vuint32m1_t vrsub_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);

```

```

vuint32m2_t vrsb_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint32m4_t vrsb_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vuint32m8_t vrsb_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);
vuint64m1_t vrsb_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vuint64m2_t vrsb_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vuint64m4_t vrsb_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vuint64m8_t vrsb_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);
vint8m1_t vneg_v_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, size_t vl);
vint8m2_t vneg_v_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, size_t vl);
vint8m4_t vneg_v_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, size_t vl);
vint8m8_t vneg_v_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, size_t vl);
vint16m1_t vneg_v_i16m1_m (vbool16_t mask, vint16m1_t maskedoff,
    vint16m1_t op1, size_t vl);
vint16m2_t vneg_v_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, size_t vl);
vint16m4_t vneg_v_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, size_t vl);
vint16m8_t vneg_v_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, size_t vl);
vint32m1_t vneg_v_i32m1_m (vbool32_t mask, vint32m1_t maskedoff,
    vint32m1_t op1, size_t vl);
vint32m2_t vneg_v_i32m2_m (vbool16_t mask, vint32m2_t maskedoff,
    vint32m2_t op1, size_t vl);
vint32m4_t vneg_v_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, size_t vl);
vint32m8_t vneg_v_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, size_t vl);
vint64m1_t vneg_v_i64m1_m (vbool64_t mask, vint64m1_t maskedoff,
    vint64m1_t op1, size_t vl);
vint64m2_t vneg_v_i64m2_m (vbool32_t mask, vint64m2_t maskedoff,
    vint64m2_t op1, size_t vl);
vint64m4_t vneg_v_i64m4_m (vbool16_t mask, vint64m4_t maskedoff,
    vint64m4_t op1, size_t vl);
vint64m8_t vneg_v_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, size_t vl);

```

## Vector Widening Integer Add/Subtract Functions:

### Prototypes:

```
vint16m2_t vwadd_vv_i16m2 (vint8m1_t op1, vint8m1_t op2, size_t
    vl);
vint16m2_t vwadd_vx_i16m2 (vint8m1_t op1, int8_t op2, size_t vl);
vint16m2_t vwadd_wv_i16m2 (vint16m2_t op1, vint8m1_t op2, size_t
    vl);
vint16m2_t vwadd_wx_i16m2 (vint16m2_t op1, int8_t op2, size_t
    vl);
vint16m4_t vwadd_vv_i16m4 (vint8m2_t op1, vint8m2_t op2, size_t
    vl);
vint16m4_t vwadd_vx_i16m4 (vint8m2_t op1, int8_t op2, size_t vl);
vint16m4_t vwadd_wv_i16m4 (vint16m4_t op1, vint8m2_t op2, size_t
    vl);
vint16m4_t vwadd_wx_i16m4 (vint16m4_t op1, int8_t op2, size_t
    vl);
vint16m8_t vwadd_vv_i16m8 (vint8m4_t op1, vint8m4_t op2, size_t
    vl);
vint16m8_t vwadd_vx_i16m8 (vint8m4_t op1, int8_t op2, size_t vl);
vint16m8_t vwadd_wv_i16m8 (vint16m8_t op1, vint8m4_t op2, size_t
    vl);
vint16m8_t vwadd_wx_i16m8 (vint16m8_t op1, int8_t op2, size_t
    vl);
vint32m2_t vwadd_vv_i32m2 (vint16m1_t op1, vint16m1_t op2,
    size_t vl);
vint32m2_t vwadd_vx_i32m2 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint32m2_t vwadd_wv_i32m2 (vint32m2_t op1, vint16m1_t op2,
    size_t vl);
vint32m2_t vwadd_wx_i32m2 (vint32m2_t op1, int16_t op2, size_t
    vl);
vint32m4_t vwadd_vv_i32m4 (vint16m2_t op1, vint16m2_t op2,
    size_t vl);
vint32m4_t vwadd_vx_i32m4 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint32m4_t vwadd_wv_i32m4 (vint32m4_t op1, vint16m2_t op2,
    size_t vl);
vint32m4_t vwadd_wx_i32m4 (vint32m4_t op1, int16_t op2, size_t
    vl);
vint32m8_t vwadd_vv_i32m8 (vint16m4_t op1, vint16m4_t op2,
    size_t vl);
vint32m8_t vwadd_vx_i32m8 (vint16m4_t op1, int16_t op2, size_t
    vl);
vint32m8_t vwadd_wv_i32m8 (vint32m8_t op1, vint16m4_t op2,
    size_t vl);
```



```

vint32m8_t vwadd_wx_i32m8 (vint32m8_t op1, int16_t op2, size_t
    vl);
vint64m2_t vwadd_vv_i64m2 (vint32m1_t op1, vint32m1_t op2,
    size_t vl);
vint64m2_t vwadd_vx_i64m2 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint64m2_t vwadd_wv_i64m2 (vint64m2_t op1, vint32m1_t op2,
    size_t vl);
vint64m2_t vwadd_wx_i64m2 (vint64m2_t op1, int32_t op2, size_t
    vl);
vint64m4_t vwadd_vv_i64m4 (vint32m2_t op1, vint32m2_t op2,
    size_t vl);
vint64m4_t vwadd_vx_i64m4 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint64m4_t vwadd_wv_i64m4 (vint64m4_t op1, vint32m2_t op2,
    size_t vl);
vint64m4_t vwadd_wx_i64m4 (vint64m4_t op1, int32_t op2, size_t
    vl);
vint64m8_t vwadd_vv_i64m8 (vint32m4_t op1, vint32m4_t op2,
    size_t vl);
vint64m8_t vwadd_vx_i64m8 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint64m8_t vwadd_wv_i64m8 (vint64m8_t op1, vint32m4_t op2,
    size_t vl);
vint64m8_t vwadd_wx_i64m8 (vint64m8_t op1, int32_t op2, size_t
    vl);
vuint16m2_t vwaddu_vv_u16m2 (vuint8m1_t op1, vuint8m1_t op2,
    size_t vl);
vuint16m2_t vwaddu_vx_u16m2 (vuint8m1_t op1, uint8_t op2, size_t
    vl);
vuint16m2_t vwaddu_wv_u16m2 (vuint16m2_t op1, vuint8m1_t op2,
    size_t vl);
vuint16m2_t vwaddu_wx_u16m2 (vuint16m2_t op1, uint8_t op2,
    size_t vl);
vuint16m4_t vwaddu_vv_u16m4 (vuint8m2_t op1, vuint8m2_t op2,
    size_t vl);
vuint16m4_t vwaddu_vx_u16m4 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vuint16m4_t vwaddu_wv_u16m4 (vuint16m4_t op1, vuint8m2_t op2,
    size_t vl);
vuint16m4_t vwaddu_wx_u16m4 (vuint16m4_t op1, uint8_t op2,
    size_t vl);
vuint16m8_t vwaddu_vv_u16m8 (vuint8m4_t op1, vuint8m4_t op2,
    size_t vl);
vuint16m8_t vwaddu_vx_u16m8 (vuint8m4_t op1, uint8_t op2, size_t
    vl);

```

```

vuint16m8_t vwaddu_wv_u16m8 (vuint16m8_t op1, vuint8m4_t op2,
    size_t vl);
vuint16m8_t vwaddu_wx_u16m8 (vuint16m8_t op1, uint8_t op2,
    size_t vl);
vuint32m2_t vwaddu_vv_u32m2 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vuint32m2_t vwaddu_vx_u32m2 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vuint32m2_t vwaddu_wv_u32m2 (vuint32m2_t op1, vuint16m1_t op2,
    size_t vl);
vuint32m2_t vwaddu_wx_u32m2 (vuint32m2_t op1, uint16_t op2,
    size_t vl);
vuint32m4_t vwaddu_vv_u32m4 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vuint32m4_t vwaddu_vx_u32m4 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vuint32m4_t vwaddu_wv_u32m4 (vuint32m4_t op1, vuint16m2_t op2,
    size_t vl);
vuint32m4_t vwaddu_wx_u32m4 (vuint32m4_t op1, uint16_t op2,
    size_t vl);
vuint32m8_t vwaddu_vv_u32m8 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vuint32m8_t vwaddu_vx_u32m8 (vuint16m4_t op1, uint16_t op2,
    size_t vl);
vuint32m8_t vwaddu_wv_u32m8 (vuint32m8_t op1, vuint16m4_t op2,
    size_t vl);
vuint32m8_t vwaddu_wx_u32m8 (vuint32m8_t op1, uint16_t op2,
    size_t vl);
vuint64m2_t vwaddu_vv_u64m2 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vuint64m2_t vwaddu_vx_u64m2 (vuint32m1_t op1, uint32_t op2,
    size_t vl);
vuint64m2_t vwaddu_wv_u64m2 (vuint64m2_t op1, vuint32m1_t op2,
    size_t vl);
vuint64m2_t vwaddu_wx_u64m2 (vuint64m2_t op1, uint32_t op2,
    size_t vl);
vuint64m4_t vwaddu_vv_u64m4 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vuint64m4_t vwaddu_vx_u64m4 (vuint32m2_t op1, uint32_t op2,
    size_t vl);
vuint64m4_t vwaddu_wv_u64m4 (vuint64m4_t op1, vuint32m2_t op2,
    size_t vl);
vuint64m4_t vwaddu_wx_u64m4 (vuint64m4_t op1, uint32_t op2,
    size_t vl);
vuint64m8_t vwaddu_vv_u64m8 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);

```

```

vuint64m8_t vwaddu_vx_u64m8 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vuint64m8_t vwaddu_wv_u64m8 (vuint64m8_t op1, vuint32m4_t op2,
    size_t vl);
vuint64m8_t vwaddu_wx_u64m8 (vuint64m8_t op1, uint32_t op2,
    size_t vl);
vint16m2_t vwsb_vv_i16m2 (vint8m1_t op1, vint8m1_t op2, size_t
    vl);
vint16m2_t vwsb_vx_i16m2 (vint8m1_t op1, int8_t op2, size_t vl);
vint16m2_t vwsb_wv_i16m2 (vint16m2_t op1, vint8m1_t op2, size_t
    vl);
vint16m2_t vwsb_wx_i16m2 (vint16m2_t op1, int8_t op2, size_t
    vl);
vint16m4_t vwsb_vv_i16m4 (vint8m2_t op1, vint8m2_t op2, size_t
    vl);
vint16m4_t vwsb_vx_i16m4 (vint8m2_t op1, int8_t op2, size_t vl);
vint16m4_t vwsb_wv_i16m4 (vint16m4_t op1, vint8m2_t op2, size_t
    vl);
vint16m4_t vwsb_wx_i16m4 (vint16m4_t op1, int8_t op2, size_t
    vl);
vint16m8_t vwsb_vv_i16m8 (vint8m4_t op1, vint8m4_t op2, size_t
    vl);
vint16m8_t vwsb_vx_i16m8 (vint8m4_t op1, int8_t op2, size_t vl);
vint16m8_t vwsb_wv_i16m8 (vint16m8_t op1, vint8m4_t op2, size_t
    vl);
vint16m8_t vwsb_wx_i16m8 (vint16m8_t op1, int8_t op2, size_t
    vl);
vint32m2_t vwsb_vv_i32m2 (vint16m1_t op1, vint16m1_t op2,
    size_t vl);
vint32m2_t vwsb_vx_i32m2 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint32m2_t vwsb_wv_i32m2 (vint32m2_t op1, vint16m1_t op2,
    size_t vl);
vint32m2_t vwsb_wx_i32m2 (vint32m2_t op1, int16_t op2, size_t
    vl);
vint32m4_t vwsb_vv_i32m4 (vint16m2_t op1, vint16m2_t op2,
    size_t vl);
vint32m4_t vwsb_vx_i32m4 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint32m4_t vwsb_wv_i32m4 (vint32m4_t op1, vint16m2_t op2,
    size_t vl);
vint32m4_t vwsb_wx_i32m4 (vint32m4_t op1, int16_t op2, size_t
    vl);
vint32m8_t vwsb_vv_i32m8 (vint16m4_t op1, vint16m4_t op2,
    size_t vl);

```

```

vint32m8_t vwsb_vx_i32m8 (vint16m4_t op1, int16_t op2, size_t
    vl);
vint32m8_t vwsb_wv_i32m8 (vint32m8_t op1, vint16m4_t op2,
    size_t vl);
vint32m8_t vwsb_wx_i32m8 (vint32m8_t op1, int16_t op2, size_t
    vl);
vint64m2_t vwsb_vv_i64m2 (vint32m1_t op1, vint32m1_t op2,
    size_t vl);
vint64m2_t vwsb_vx_i64m2 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint64m2_t vwsb_wv_i64m2 (vint64m2_t op1, vint32m1_t op2,
    size_t vl);
vint64m2_t vwsb_wx_i64m2 (vint64m2_t op1, int32_t op2, size_t
    vl);
vint64m4_t vwsb_vv_i64m4 (vint32m2_t op1, vint32m2_t op2,
    size_t vl);
vint64m4_t vwsb_vx_i64m4 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint64m4_t vwsb_wv_i64m4 (vint64m4_t op1, vint32m2_t op2,
    size_t vl);
vint64m4_t vwsb_wx_i64m4 (vint64m4_t op1, int32_t op2, size_t
    vl);
vint64m8_t vwsb_vv_i64m8 (vint32m4_t op1, vint32m4_t op2,
    size_t vl);
vint64m8_t vwsb_vx_i64m8 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint64m8_t vwsb_wv_i64m8 (vint64m8_t op1, vint32m4_t op2,
    size_t vl);
vint64m8_t vwsb_wx_i64m8 (vint64m8_t op1, int32_t op2, size_t
    vl);
vuint16m2_t vwsbu_vv_u16m2 (vuint8m1_t op1, vuint8m1_t op2,
    size_t vl);
vuint16m2_t vwsbu_vx_u16m2 (vuint8m1_t op1, uint8_t op2, size_t
    vl);
vuint16m2_t vwsbu_wv_u16m2 (vuint16m2_t op1, vuint8m1_t op2,
    size_t vl);
vuint16m2_t vwsbu_wx_u16m2 (vuint16m2_t op1, uint8_t op2,
    size_t vl);
vuint16m4_t vwsbu_vv_u16m4 (vuint8m2_t op1, vuint8m2_t op2,
    size_t vl);
vuint16m4_t vwsbu_vx_u16m4 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vuint16m4_t vwsbu_wv_u16m4 (vuint16m4_t op1, vuint8m2_t op2,
    size_t vl);
vuint16m4_t vwsbu_wx_u16m4 (vuint16m4_t op1, uint8_t op2,
    size_t vl);

```

```

vuint16m8_t vwsbu_vv_u16m8 (vuint8m4_t op1, vuint8m4_t op2,
    size_t vl);
vuint16m8_t vwsbu_vx_u16m8 (vuint8m4_t op1, uint8_t op2, size_t
    vl);
vuint16m8_t vwsbu_wv_u16m8 (vuint16m8_t op1, vuint8m4_t op2,
    size_t vl);
vuint16m8_t vwsbu_wx_u16m8 (vuint16m8_t op1, uint8_t op2,
    size_t vl);
vuint32m2_t vwsbu_vv_u32m2 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vuint32m2_t vwsbu_vx_u32m2 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vuint32m2_t vwsbu_wv_u32m2 (vuint32m2_t op1, vuint16m1_t op2,
    size_t vl);
vuint32m2_t vwsbu_wx_u32m2 (vuint32m2_t op1, uint16_t op2,
    size_t vl);
vuint32m4_t vwsbu_vv_u32m4 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vuint32m4_t vwsbu_vx_u32m4 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vuint32m4_t vwsbu_wv_u32m4 (vuint32m4_t op1, vuint16m2_t op2,
    size_t vl);
vuint32m4_t vwsbu_wx_u32m4 (vuint32m4_t op1, uint16_t op2,
    size_t vl);
vuint32m8_t vwsbu_vv_u32m8 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vuint32m8_t vwsbu_vx_u32m8 (vuint16m4_t op1, uint16_t op2,
    size_t vl);
vuint32m8_t vwsbu_wv_u32m8 (vuint32m8_t op1, vuint16m4_t op2,
    size_t vl);
vuint32m8_t vwsbu_wx_u32m8 (vuint32m8_t op1, uint16_t op2,
    size_t vl);
vuint64m2_t vwsbu_vv_u64m2 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vuint64m2_t vwsbu_vx_u64m2 (vuint32m1_t op1, uint32_t op2,
    size_t vl);
vuint64m2_t vwsbu_wv_u64m2 (vuint64m2_t op1, vuint32m1_t op2,
    size_t vl);
vuint64m2_t vwsbu_wx_u64m2 (vuint64m2_t op1, uint32_t op2,
    size_t vl);
vuint64m4_t vwsbu_vv_u64m4 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vuint64m4_t vwsbu_vx_u64m4 (vuint32m2_t op1, uint32_t op2,
    size_t vl);
vuint64m4_t vwsbu_wv_u64m4 (vuint64m4_t op1, vuint32m2_t op2,
    size_t vl);

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vuint64m4_t vwsu_u64m4 (vuint64m4_t op1, uint32_t op2,
    size_t vl);
vuint64m8_t vwsu_vv_u64m8 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vuint64m8_t vwsu_vx_u64m8 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vuint64m8_t vwsu_wv_u64m8 (vuint64m8_t op1, vuint32m4_t op2,
    size_t vl);
vuint64m8_t vwsu_wx_u64m8 (vuint64m8_t op1, uint32_t op2,
    size_t vl);
// masked functions
vint16m2_t vwadd_vv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint8m1_t op1, vint8m1_t op2, size_t vl);
vint16m2_t vwadd_vx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint8m1_t op1, int8_t op2, size_t vl);
vint16m2_t vwadd_wv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, vint8m1_t op2, size_t vl);
vint16m2_t vwadd_wx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, int8_t op2, size_t vl);
vint16m4_t vwadd_vv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint8m2_t op1, vint8m2_t op2, size_t vl);
vint16m4_t vwadd_vx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint8m2_t op1, int8_t op2, size_t vl);
vint16m4_t vwadd_wv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, vint8m2_t op2, size_t vl);
vint16m4_t vwadd_wx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, int8_t op2, size_t vl);
vint16m8_t vwadd_vv_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint8m4_t op1, vint8m4_t op2, size_t vl);
vint16m8_t vwadd_vx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint8m4_t op1, int8_t op2, size_t vl);
vint16m8_t vwadd_wv_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, vint8m4_t op2, size_t vl);
vint16m8_t vwadd_wx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, int8_t op2, size_t vl);
vint32m2_t vwadd_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint32m2_t vwadd_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vint32m2_t vwadd_wv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint16m1_t op2, size_t vl);
vint32m2_t vwadd_wx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int16_t op2, size_t vl);
vint32m4_t vwadd_vv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint16m2_t op1, vint16m2_t op2, size_t vl);

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vint32m4_t vwadd_vx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint16m2_t op1, int16_t op2, size_t vl);
vint32m4_t vwadd_wv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, vint16m2_t op2, size_t vl);
vint32m4_t vwadd_wx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, int16_t op2, size_t vl);
vint32m8_t vwadd_vv_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint16m4_t op1, vint16m4_t op2, size_t vl);
vint32m8_t vwadd_vx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint16m4_t op1, int16_t op2, size_t vl);
vint32m8_t vwadd_wv_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, vint16m4_t op2, size_t vl);
vint32m8_t vwadd_wx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, int16_t op2, size_t vl);
vint64m2_t vwadd_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint64m2_t vwadd_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint64m2_t vwadd_wv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint32m1_t op2, size_t vl);
vint64m2_t vwadd_wx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int32_t op2, size_t vl);
vint64m4_t vwadd_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vint64m4_t vwadd_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint64m4_t vwadd_wv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint32m2_t op2, size_t vl);
vint64m4_t vwadd_wx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int32_t op2, size_t vl);
vint64m8_t vwadd_vv_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint32m4_t op1, vint32m4_t op2, size_t vl);
vint64m8_t vwadd_vx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint32m4_t op1, int32_t op2, size_t vl);
vint64m8_t vwadd_wv_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, vint32m4_t op2, size_t vl);
vint64m8_t vwadd_wx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, int32_t op2, size_t vl);
vuint16m2_t vwaddu_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vuint16m2_t vwaddu_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint8m1_t op1, uint8_t op2, size_t vl);
vuint16m2_t vwaddu_wv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint8m1_t op2, size_t vl);
vuint16m2_t vwaddu_wx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, uint8_t op2, size_t vl);

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vuint16m4_t vwaddu_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vuint16m4_t vwaddu_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint8m2_t op1, uint8_t op2, size_t vl);
vuint16m4_t vwaddu_wv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint8m2_t op2, size_t vl);
vuint16m4_t vwaddu_wx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, uint8_t op2, size_t vl);
vuint16m8_t vwaddu_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vuint16m8_t vwaddu_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint8m4_t op1, uint8_t op2, size_t vl);
vuint16m8_t vwaddu_wv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint8m4_t op2, size_t vl);
vuint16m8_t vwaddu_wx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, uint8_t op2, size_t vl);
vuint32m2_t vwaddu_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vuint32m2_t vwaddu_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint32m2_t vwaddu_wv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint16m1_t op2, size_t vl);
vuint32m2_t vwaddu_wx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, uint16_t op2, size_t vl);
vuint32m4_t vwaddu_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vuint32m4_t vwaddu_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint32m4_t vwaddu_wv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint16m2_t op2, size_t vl);
vuint32m4_t vwaddu_wx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, uint16_t op2, size_t vl);
vuint32m8_t vwaddu_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vuint32m8_t vwaddu_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vuint32m8_t vwaddu_wv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint16m4_t op2, size_t vl);
vuint32m8_t vwaddu_wx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, uint16_t op2, size_t vl);
vuint64m2_t vwaddu_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vuint64m2_t vwaddu_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vuint64m2_t vwaddu_wv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint32m1_t op2, size_t vl);

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vuint64m2_t vwaddu_wx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, uint32_t op2, size_t vl);
vuint64m4_t vwaddu_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vuint64m4_t vwaddu_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint64m4_t vwaddu_wv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint32m2_t op2, size_t vl);
vuint64m4_t vwaddu_wx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, uint32_t op2, size_t vl);
vuint64m8_t vwaddu_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vuint64m8_t vwaddu_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vuint64m8_t vwaddu_wv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint32m4_t op2, size_t vl);
vuint64m8_t vwaddu_wx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, uint32_t op2, size_t vl);
vint16m2_t vwsb_vv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint8m1_t op1, vint8m1_t op2, size_t vl);
vint16m2_t vwsb_vx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint8m1_t op1, int8_t op2, size_t vl);
vint16m2_t vwsb_wv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, vint8m1_t op2, size_t vl);
vint16m2_t vwsb_wx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, int8_t op2, size_t vl);
vint16m4_t vwsb_vv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint8m2_t op1, vint8m2_t op2, size_t vl);
vint16m4_t vwsb_vx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint8m2_t op1, int8_t op2, size_t vl);
vint16m4_t vwsb_wv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, vint8m2_t op2, size_t vl);
vint16m4_t vwsb_wx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, int8_t op2, size_t vl);
vint16m8_t vwsb_vv_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint8m4_t op1, vint8m4_t op2, size_t vl);
vint16m8_t vwsb_vx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint8m4_t op1, int8_t op2, size_t vl);
vint16m8_t vwsb_wv_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, vint8m4_t op2, size_t vl);
vint16m8_t vwsb_wx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, int8_t op2, size_t vl);
vint32m2_t vwsb_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint32m2_t vwsb_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);

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vint32m2_t vwsb_wv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint16m1_t op2, size_t vl);
vint32m2_t vwsb_wx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int16_t op2, size_t vl);
vint32m4_t vwsb_vv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint16m2_t op1, vint16m2_t op2, size_t vl);
vint32m4_t vwsb_vx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint16m2_t op1, int16_t op2, size_t vl);
vint32m4_t vwsb_wv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, vint16m2_t op2, size_t vl);
vint32m4_t vwsb_wx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, int16_t op2, size_t vl);
vint32m8_t vwsb_vv_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint16m4_t op1, vint16m4_t op2, size_t vl);
vint32m8_t vwsb_vx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint16m4_t op1, int16_t op2, size_t vl);
vint32m8_t vwsb_wv_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, vint16m4_t op2, size_t vl);
vint32m8_t vwsb_wx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, int16_t op2, size_t vl);
vint64m2_t vwsb_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint64m2_t vwsb_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint64m2_t vwsb_wv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint32m1_t op2, size_t vl);
vint64m2_t vwsb_wx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int32_t op2, size_t vl);
vint64m4_t vwsb_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vint64m4_t vwsb_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint64m4_t vwsb_wv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint32m2_t op2, size_t vl);
vint64m4_t vwsb_wx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int32_t op2, size_t vl);
vint64m8_t vwsb_vv_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint32m4_t op1, vint32m4_t op2, size_t vl);
vint64m8_t vwsb_vx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint32m4_t op1, int32_t op2, size_t vl);
vint64m8_t vwsb_wv_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, vint32m4_t op2, size_t vl);
vint64m8_t vwsb_wx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, int32_t op2, size_t vl);
vuint16m2_t vwsbu_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint8m1_t op1, vuint8m1_t op2, size_t vl);

```

```

vuint16m2_t vwsu_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint8m1_t op1, uint8_t op2, size_t vl);
vuint16m2_t vwsu_wv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint8m1_t op2, size_t vl);
vuint16m2_t vwsu_wx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, uint8_t op2, size_t vl);
vuint16m4_t vwsu_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vuint16m4_t vwsu_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint8m2_t op1, uint8_t op2, size_t vl);
vuint16m4_t vwsu_wv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint8m2_t op2, size_t vl);
vuint16m4_t vwsu_wx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, uint8_t op2, size_t vl);
vuint16m8_t vwsu_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vuint16m8_t vwsu_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint8m4_t op1, uint8_t op2, size_t vl);
vuint16m8_t vwsu_wv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint8m4_t op2, size_t vl);
vuint16m8_t vwsu_wx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, uint8_t op2, size_t vl);
vuint32m2_t vwsu_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vuint32m2_t vwsu_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint32m2_t vwsu_wv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint16m1_t op2, size_t vl);
vuint32m2_t vwsu_wx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, uint16_t op2, size_t vl);
vuint32m4_t vwsu_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vuint32m4_t vwsu_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint32m4_t vwsu_wv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint16m2_t op2, size_t vl);
vuint32m4_t vwsu_wx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, uint16_t op2, size_t vl);
vuint32m8_t vwsu_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vuint32m8_t vwsu_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vuint32m8_t vwsu_wv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint16m4_t op2, size_t vl);
vuint32m8_t vwsu_wx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, uint16_t op2, size_t vl);

```

```

vuint64m2_t vwsu_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vuint64m2_t vwsu_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vuint64m2_t vwsu_wv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint32m1_t op2, size_t vl);
vuint64m2_t vwsu_wx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, uint32_t op2, size_t vl);
vuint64m4_t vwsu_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vuint64m4_t vwsu_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint64m4_t vwsu_wv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint32m2_t op2, size_t vl);
vuint64m4_t vwsu_wx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, uint32_t op2, size_t vl);
vuint64m8_t vwsu_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vuint64m8_t vwsu_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vuint64m8_t vwsu_wv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint32m4_t op2, size_t vl);
vuint64m8_t vwsu_wx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, uint32_t op2, size_t vl);

```

## Vector Integer Add-with-Carry / Subtract-with-Borrow Functions:

### Prototypes:

```

vint8m1_t vadc_vvm_i8m1 (vint8m1_t op1, vint8m1_t op2, vbool8_t
    carryin, size_t vl);
vint8m1_t vadc_vxm_i8m1 (vint8m1_t op1, int8_t op2, vbool8_t
    carryin, size_t vl);
vint8m2_t vadc_vvm_i8m2 (vint8m2_t op1, vint8m2_t op2, vbool4_t
    carryin, size_t vl);
vint8m2_t vadc_vxm_i8m2 (vint8m2_t op1, int8_t op2, vbool4_t
    carryin, size_t vl);
vint8m4_t vadc_vvm_i8m4 (vint8m4_t op1, vint8m4_t op2, vbool2_t
    carryin, size_t vl);
vint8m4_t vadc_vxm_i8m4 (vint8m4_t op1, int8_t op2, vbool2_t
    carryin, size_t vl);
vint8m8_t vadc_vvm_i8m8 (vint8m8_t op1, vint8m8_t op2, vbool1_t
    carryin, size_t vl);
vint8m8_t vadc_vxm_i8m8 (vint8m8_t op1, int8_t op2, vbool1_t
    carryin, size_t vl);

```

```

vint16m1_t vadc_vvm_i16m1 (vint16m1_t op1, vint16m1_t op2,
    vbool16_t carryin, size_t vl);
vint16m1_t vadc_vxm_i16m1 (vint16m1_t op1, int16_t op2,
    vbool16_t carryin, size_t vl);
vint16m2_t vadc_vvm_i16m2 (vint16m2_t op1, vint16m2_t op2,
    vbool8_t carryin, size_t vl);
vint16m2_t vadc_vxm_i16m2 (vint16m2_t op1, int16_t op2, vbool8_t
    carryin, size_t vl);
vint16m4_t vadc_vvm_i16m4 (vint16m4_t op1, vint16m4_t op2,
    vbool4_t carryin, size_t vl);
vint16m4_t vadc_vxm_i16m4 (vint16m4_t op1, int16_t op2, vbool4_t
    carryin, size_t vl);
vint16m8_t vadc_vvm_i16m8 (vint16m8_t op1, vint16m8_t op2,
    vbool2_t carryin, size_t vl);
vint16m8_t vadc_vxm_i16m8 (vint16m8_t op1, int16_t op2, vbool2_t
    carryin, size_t vl);
vint32m1_t vadc_vvm_i32m1 (vint32m1_t op1, vint32m1_t op2,
    vbool32_t carryin, size_t vl);
vint32m1_t vadc_vxm_i32m1 (vint32m1_t op1, int32_t op2,
    vbool32_t carryin, size_t vl);
vint32m2_t vadc_vvm_i32m2 (vint32m2_t op1, vint32m2_t op2,
    vbool16_t carryin, size_t vl);
vint32m2_t vadc_vxm_i32m2 (vint32m2_t op1, int32_t op2,
    vbool16_t carryin, size_t vl);
vint32m4_t vadc_vvm_i32m4 (vint32m4_t op1, vint32m4_t op2,
    vbool8_t carryin, size_t vl);
vint32m4_t vadc_vxm_i32m4 (vint32m4_t op1, int32_t op2, vbool8_t
    carryin, size_t vl);
vint32m8_t vadc_vvm_i32m8 (vint32m8_t op1, vint32m8_t op2,
    vbool4_t carryin, size_t vl);
vint32m8_t vadc_vxm_i32m8 (vint32m8_t op1, int32_t op2, vbool4_t
    carryin, size_t vl);
vint64m1_t vadc_vvm_i64m1 (vint64m1_t op1, vint64m1_t op2,
    vbool64_t carryin, size_t vl);
vint64m1_t vadc_vxm_i64m1 (vint64m1_t op1, int64_t op2,
    vbool64_t carryin, size_t vl);
vint64m2_t vadc_vvm_i64m2 (vint64m2_t op1, vint64m2_t op2,
    vbool32_t carryin, size_t vl);
vint64m2_t vadc_vxm_i64m2 (vint64m2_t op1, int64_t op2,
    vbool32_t carryin, size_t vl);
vint64m4_t vadc_vvm_i64m4 (vint64m4_t op1, vint64m4_t op2,
    vbool16_t carryin, size_t vl);
vint64m4_t vadc_vxm_i64m4 (vint64m4_t op1, int64_t op2,
    vbool16_t carryin, size_t vl);
vint64m8_t vadc_vvm_i64m8 (vint64m8_t op1, vint64m8_t op2,
    vbool8_t carryin, size_t vl);

```

```

vint64m8_t vadc_vxm_i64m8 (vint64m8_t op1, int64_t op2, vbool8_t
    carryin, size_t vl);
vuint8m1_t vadc_vvm_u8m1 (vuint8m1_t op1, vuint8m1_t op2,
    vbool8_t carryin, size_t vl);
vuint8m1_t vadc_vxm_u8m1 (vuint8m1_t op1, uint8_t op2, vbool8_t
    carryin, size_t vl);
vuint8m2_t vadc_vvm_u8m2 (vuint8m2_t op1, vuint8m2_t op2,
    vbool4_t carryin, size_t vl);
vuint8m2_t vadc_vxm_u8m2 (vuint8m2_t op1, uint8_t op2, vbool4_t
    carryin, size_t vl);
vuint8m4_t vadc_vvm_u8m4 (vuint8m4_t op1, vuint8m4_t op2,
    vbool2_t carryin, size_t vl);
vuint8m4_t vadc_vxm_u8m4 (vuint8m4_t op1, uint8_t op2, vbool2_t
    carryin, size_t vl);
vuint8m8_t vadc_vvm_u8m8 (vuint8m8_t op1, vuint8m8_t op2,
    vbool1_t carryin, size_t vl);
vuint8m8_t vadc_vxm_u8m8 (vuint8m8_t op1, uint8_t op2, vbool1_t
    carryin, size_t vl);
vuint16m1_t vadc_vvm_u16m1 (vuint16m1_t op1, vuint16m1_t op2,
    vbool16_t carryin, size_t vl);
vuint16m1_t vadc_vxm_u16m1 (vuint16m1_t op1, uint16_t op2,
    vbool16_t carryin, size_t vl);
vuint16m2_t vadc_vvm_u16m2 (vuint16m2_t op1, vuint16m2_t op2,
    vbool8_t carryin, size_t vl);
vuint16m2_t vadc_vxm_u16m2 (vuint16m2_t op1, uint16_t op2,
    vbool8_t carryin, size_t vl);
vuint16m4_t vadc_vvm_u16m4 (vuint16m4_t op1, vuint16m4_t op2,
    vbool4_t carryin, size_t vl);
vuint16m4_t vadc_vxm_u16m4 (vuint16m4_t op1, uint16_t op2,
    vbool4_t carryin, size_t vl);
vuint16m8_t vadc_vvm_u16m8 (vuint16m8_t op1, vuint16m8_t op2,
    vbool2_t carryin, size_t vl);
vuint16m8_t vadc_vxm_u16m8 (vuint16m8_t op1, uint16_t op2,
    vbool2_t carryin, size_t vl);
vuint32m1_t vadc_vvm_u32m1 (vuint32m1_t op1, vuint32m1_t op2,
    vbool32_t carryin, size_t vl);
vuint32m1_t vadc_vxm_u32m1 (vuint32m1_t op1, uint32_t op2,
    vbool32_t carryin, size_t vl);
vuint32m2_t vadc_vvm_u32m2 (vuint32m2_t op1, vuint32m2_t op2,
    vbool16_t carryin, size_t vl);
vuint32m2_t vadc_vxm_u32m2 (vuint32m2_t op1, uint32_t op2,
    vbool16_t carryin, size_t vl);
vuint32m4_t vadc_vvm_u32m4 (vuint32m4_t op1, vuint32m4_t op2,
    vbool8_t carryin, size_t vl);
vuint32m4_t vadc_vxm_u32m4 (vuint32m4_t op1, uint32_t op2,
    vbool8_t carryin, size_t vl);

```

```

vuint32m8_t vadc_vvm_u32m8 (vuint32m8_t op1, vuint32m8_t op2,
    vbool4_t carryin, size_t vl);
vuint32m8_t vadc_vxm_u32m8 (vuint32m8_t op1, uint32_t op2,
    vbool4_t carryin, size_t vl);
vuint64m1_t vadc_vvm_u64m1 (vuint64m1_t op1, vuint64m1_t op2,
    vbool64_t carryin, size_t vl);
vuint64m1_t vadc_vxm_u64m1 (vuint64m1_t op1, uint64_t op2,
    vbool64_t carryin, size_t vl);
vuint64m2_t vadc_vvm_u64m2 (vuint64m2_t op1, vuint64m2_t op2,
    vbool32_t carryin, size_t vl);
vuint64m2_t vadc_vxm_u64m2 (vuint64m2_t op1, uint64_t op2,
    vbool32_t carryin, size_t vl);
vuint64m4_t vadc_vvm_u64m4 (vuint64m4_t op1, vuint64m4_t op2,
    vbool16_t carryin, size_t vl);
vuint64m4_t vadc_vxm_u64m4 (vuint64m4_t op1, uint64_t op2,
    vbool16_t carryin, size_t vl);
vuint64m8_t vadc_vvm_u64m8 (vuint64m8_t op1, vuint64m8_t op2,
    vbool8_t carryin, size_t vl);
vuint64m8_t vadc_vxm_u64m8 (vuint64m8_t op1, uint64_t op2,
    vbool8_t carryin, size_t vl);
vbool8_t vmadc_vvm_i8m1_b8 (vint8m1_t op1, vint8m1_t op2,
    vbool8_t carryin, size_t vl);
vbool8_t vmadc_vxm_i8m1_b8 (vint8m1_t op1, int8_t op2, vbool8_t
    carryin, size_t vl);
vbool4_t vmadc_vvm_i8m2_b4 (vint8m2_t op1, vint8m2_t op2,
    vbool4_t carryin, size_t vl);
vbool4_t vmadc_vxm_i8m2_b4 (vint8m2_t op1, int8_t op2, vbool4_t
    carryin, size_t vl);
vbool2_t vmadc_vvm_i8m4_b2 (vint8m4_t op1, vint8m4_t op2,
    vbool2_t carryin, size_t vl);
vbool2_t vmadc_vxm_i8m4_b2 (vint8m4_t op1, int8_t op2, vbool2_t
    carryin, size_t vl);
vbool1_t vmadc_vvm_i8m8_b1 (vint8m8_t op1, vint8m8_t op2,
    vbool1_t carryin, size_t vl);
vbool1_t vmadc_vxm_i8m8_b1 (vint8m8_t op1, int8_t op2, vbool1_t
    carryin, size_t vl);
vbool16_t vmadc_vvm_i16m1_b16 (vint16m1_t op1, vint16m1_t op2,
    vbool16_t carryin, size_t vl);
vbool16_t vmadc_vxm_i16m1_b16 (vint16m1_t op1, int16_t op2,
    vbool16_t carryin, size_t vl);
vbool8_t vmadc_vvm_i16m2_b8 (vint16m2_t op1, vint16m2_t op2,
    vbool8_t carryin, size_t vl);
vbool8_t vmadc_vxm_i16m2_b8 (vint16m2_t op1, int16_t op2,
    vbool8_t carryin, size_t vl);
vbool4_t vmadc_vvm_i16m4_b4 (vint16m4_t op1, vint16m4_t op2,
    vbool4_t carryin, size_t vl);

```

```

vbool4_t vmadc_vxm_i16m4_b4 (vint16m4_t op1, int16_t op2,
    vbool4_t carryin, size_t vl);
vbool2_t vmadc_vvm_i16m8_b2 (vint16m8_t op1, vint16m8_t op2,
    vbool2_t carryin, size_t vl);
vbool2_t vmadc_vxm_i16m8_b2 (vint16m8_t op1, int16_t op2,
    vbool2_t carryin, size_t vl);
vbool32_t vmadc_vvm_i32m1_b32 (vint32m1_t op1, vint32m1_t op2,
    vbool32_t carryin, size_t vl);
vbool32_t vmadc_vxm_i32m1_b32 (vint32m1_t op1, int32_t op2,
    vbool32_t carryin, size_t vl);
vbool16_t vmadc_vvm_i32m2_b16 (vint32m2_t op1, vint32m2_t op2,
    vbool16_t carryin, size_t vl);
vbool16_t vmadc_vxm_i32m2_b16 (vint32m2_t op1, int32_t op2,
    vbool16_t carryin, size_t vl);
vbool8_t vmadc_vvm_i32m4_b8 (vint32m4_t op1, vint32m4_t op2,
    vbool8_t carryin, size_t vl);
vbool8_t vmadc_vxm_i32m4_b8 (vint32m4_t op1, int32_t op2,
    vbool8_t carryin, size_t vl);
vbool4_t vmadc_vvm_i32m8_b4 (vint32m8_t op1, vint32m8_t op2,
    vbool4_t carryin, size_t vl);
vbool4_t vmadc_vxm_i32m8_b4 (vint32m8_t op1, int32_t op2,
    vbool4_t carryin, size_t vl);
vbool64_t vmadc_vvm_i64m1_b64 (vint64m1_t op1, vint64m1_t op2,
    vbool64_t carryin, size_t vl);
vbool64_t vmadc_vxm_i64m1_b64 (vint64m1_t op1, int64_t op2,
    vbool64_t carryin, size_t vl);
vbool32_t vmadc_vvm_i64m2_b32 (vint64m2_t op1, vint64m2_t op2,
    vbool32_t carryin, size_t vl);
vbool32_t vmadc_vxm_i64m2_b32 (vint64m2_t op1, int64_t op2,
    vbool32_t carryin, size_t vl);
vbool16_t vmadc_vvm_i64m4_b16 (vint64m4_t op1, vint64m4_t op2,
    vbool16_t carryin, size_t vl);
vbool16_t vmadc_vxm_i64m4_b16 (vint64m4_t op1, int64_t op2,
    vbool16_t carryin, size_t vl);
vbool8_t vmadc_vvm_i64m8_b8 (vint64m8_t op1, vint64m8_t op2,
    vbool8_t carryin, size_t vl);
vbool8_t vmadc_vxm_i64m8_b8 (vint64m8_t op1, int64_t op2,
    vbool8_t carryin, size_t vl);
vbool8_t vmadc_vvm_u8m1_b8 (vuint8m1_t op1, vuint8m1_t op2,
    vbool8_t carryin, size_t vl);
vbool8_t vmadc_vxm_u8m1_b8 (vuint8m1_t op1, uint8_t op2,
    vbool8_t carryin, size_t vl);
vbool4_t vmadc_vvm_u8m2_b4 (vuint8m2_t op1, vuint8m2_t op2,
    vbool4_t carryin, size_t vl);
vbool4_t vmadc_vxm_u8m2_b4 (vuint8m2_t op1, uint8_t op2,
    vbool4_t carryin, size_t vl);

```



```

vbool12_t vmadc_vvm_u8m4_b2 (vuint8m4_t op1, vuint8m4_t op2,
    vbool12_t carryin, size_t vl);
vbool12_t vmadc_vxm_u8m4_b2 (vuint8m4_t op1, uint8_t op2,
    vbool12_t carryin, size_t vl);
vbool11_t vmadc_vvm_u8m8_b1 (vuint8m8_t op1, vuint8m8_t op2,
    vbool11_t carryin, size_t vl);
vbool11_t vmadc_vxm_u8m8_b1 (vuint8m8_t op1, uint8_t op2,
    vbool11_t carryin, size_t vl);
vbool16_t vmadc_vvm_u16m1_b16 (vuint16m1_t op1, vuint16m1_t op2,
    vbool16_t carryin, size_t vl);
vbool16_t vmadc_vxm_u16m1_b16 (vuint16m1_t op1, uint16_t op2,
    vbool16_t carryin, size_t vl);
vbool8_t vmadc_vvm_u16m2_b8 (vuint16m2_t op1, vuint16m2_t op2,
    vbool8_t carryin, size_t vl);
vbool8_t vmadc_vxm_u16m2_b8 (vuint16m2_t op1, uint16_t op2,
    vbool8_t carryin, size_t vl);
vbool4_t vmadc_vvm_u16m4_b4 (vuint16m4_t op1, vuint16m4_t op2,
    vbool4_t carryin, size_t vl);
vbool4_t vmadc_vxm_u16m4_b4 (vuint16m4_t op1, uint16_t op2,
    vbool4_t carryin, size_t vl);
vbool12_t vmadc_vvm_u16m8_b2 (vuint16m8_t op1, vuint16m8_t op2,
    vbool12_t carryin, size_t vl);
vbool12_t vmadc_vxm_u16m8_b2 (vuint16m8_t op1, uint16_t op2,
    vbool12_t carryin, size_t vl);
vbool32_t vmadc_vvm_u32m1_b32 (vuint32m1_t op1, vuint32m1_t op2,
    vbool32_t carryin, size_t vl);
vbool32_t vmadc_vxm_u32m1_b32 (vuint32m1_t op1, uint32_t op2,
    vbool32_t carryin, size_t vl);
vbool16_t vmadc_vvm_u32m2_b16 (vuint32m2_t op1, vuint32m2_t op2,
    vbool16_t carryin, size_t vl);
vbool16_t vmadc_vxm_u32m2_b16 (vuint32m2_t op1, uint32_t op2,
    vbool16_t carryin, size_t vl);
vbool8_t vmadc_vvm_u32m4_b8 (vuint32m4_t op1, vuint32m4_t op2,
    vbool8_t carryin, size_t vl);
vbool8_t vmadc_vxm_u32m4_b8 (vuint32m4_t op1, uint32_t op2,
    vbool8_t carryin, size_t vl);
vbool4_t vmadc_vvm_u32m8_b4 (vuint32m8_t op1, vuint32m8_t op2,
    vbool4_t carryin, size_t vl);
vbool4_t vmadc_vxm_u32m8_b4 (vuint32m8_t op1, uint32_t op2,
    vbool4_t carryin, size_t vl);
vbool64_t vmadc_vvm_u64m1_b64 (vuint64m1_t op1, vuint64m1_t op2,
    vbool64_t carryin, size_t vl);
vbool64_t vmadc_vxm_u64m1_b64 (vuint64m1_t op1, uint64_t op2,
    vbool64_t carryin, size_t vl);
vbool32_t vmadc_vvm_u64m2_b32 (vuint64m2_t op1, vuint64m2_t op2,
    vbool32_t carryin, size_t vl);

```

```

vbool32_t vmadc_vxm_u64m2_b32 (vuint64m2_t op1, uint64_t op2,
    vbool32_t carryin, size_t vl);
vbool16_t vmadc_vvm_u64m4_b16 (vuint64m4_t op1, vuint64m4_t op2,
    vbool16_t carryin, size_t vl);
vbool16_t vmadc_vxm_u64m4_b16 (vuint64m4_t op1, uint64_t op2,
    vbool16_t carryin, size_t vl);
vbool8_t vmadc_vvm_u64m8_b8 (vuint64m8_t op1, vuint64m8_t op2,
    vbool8_t carryin, size_t vl);
vbool8_t vmadc_vxm_u64m8_b8 (vuint64m8_t op1, uint64_t op2,
    vbool8_t carryin, size_t vl);
vint8m1_t vsbc_vvm_i8m1 (vint8m1_t op1, vint8m1_t op2, vbool8_t
    borrowin, size_t vl);
vint8m1_t vsbc_vxm_i8m1 (vint8m1_t op1, int8_t op2, vbool8_t
    borrowin, size_t vl);
vint8m2_t vsbc_vvm_i8m2 (vint8m2_t op1, vint8m2_t op2, vbool4_t
    borrowin, size_t vl);
vint8m2_t vsbc_vxm_i8m2 (vint8m2_t op1, int8_t op2, vbool4_t
    borrowin, size_t vl);
vint8m4_t vsbc_vvm_i8m4 (vint8m4_t op1, vint8m4_t op2, vbool2_t
    borrowin, size_t vl);
vint8m4_t vsbc_vxm_i8m4 (vint8m4_t op1, int8_t op2, vbool2_t
    borrowin, size_t vl);
vint8m8_t vsbc_vvm_i8m8 (vint8m8_t op1, vint8m8_t op2, vbool1_t
    borrowin, size_t vl);
vint8m8_t vsbc_vxm_i8m8 (vint8m8_t op1, int8_t op2, vbool1_t
    borrowin, size_t vl);
vint16m1_t vsbc_vvm_i16m1 (vint16m1_t op1, vint16m1_t op2,
    vbool16_t borrowin, size_t vl);
vint16m1_t vsbc_vxm_i16m1 (vint16m1_t op1, int16_t op2,
    vbool16_t borrowin, size_t vl);
vint16m2_t vsbc_vvm_i16m2 (vint16m2_t op1, vint16m2_t op2,
    vbool8_t borrowin, size_t vl);
vint16m2_t vsbc_vxm_i16m2 (vint16m2_t op1, int16_t op2, vbool8_t
    borrowin, size_t vl);
vint16m4_t vsbc_vvm_i16m4 (vint16m4_t op1, vint16m4_t op2,
    vbool4_t borrowin, size_t vl);
vint16m4_t vsbc_vxm_i16m4 (vint16m4_t op1, int16_t op2, vbool4_t
    borrowin, size_t vl);
vint16m8_t vsbc_vvm_i16m8 (vint16m8_t op1, vint16m8_t op2,
    vbool2_t borrowin, size_t vl);
vint16m8_t vsbc_vxm_i16m8 (vint16m8_t op1, int16_t op2, vbool2_t
    borrowin, size_t vl);
vint32m1_t vsbc_vvm_i32m1 (vint32m1_t op1, vint32m1_t op2,
    vbool32_t borrowin, size_t vl);
vint32m1_t vsbc_vxm_i32m1 (vint32m1_t op1, int32_t op2,
    vbool32_t borrowin, size_t vl);

```

```

vint32m2_t vsbc_vvm_i32m2 (vint32m2_t op1, vint32m2_t op2,
    vbool16_t borrowin, size_t vl);
vint32m2_t vsbc_vxm_i32m2 (vint32m2_t op1, int32_t op2,
    vbool16_t borrowin, size_t vl);
vint32m4_t vsbc_vvm_i32m4 (vint32m4_t op1, vint32m4_t op2,
    vbool8_t borrowin, size_t vl);
vint32m4_t vsbc_vxm_i32m4 (vint32m4_t op1, int32_t op2, vbool8_t
    borrowin, size_t vl);
vint32m8_t vsbc_vvm_i32m8 (vint32m8_t op1, vint32m8_t op2,
    vbool4_t borrowin, size_t vl);
vint32m8_t vsbc_vxm_i32m8 (vint32m8_t op1, int32_t op2, vbool4_t
    borrowin, size_t vl);
vint64m1_t vsbc_vvm_i64m1 (vint64m1_t op1, vint64m1_t op2,
    vbool64_t borrowin, size_t vl);
vint64m1_t vsbc_vxm_i64m1 (vint64m1_t op1, int64_t op2,
    vbool64_t borrowin, size_t vl);
vint64m2_t vsbc_vvm_i64m2 (vint64m2_t op1, vint64m2_t op2,
    vbool32_t borrowin, size_t vl);
vint64m2_t vsbc_vxm_i64m2 (vint64m2_t op1, int64_t op2,
    vbool32_t borrowin, size_t vl);
vint64m4_t vsbc_vvm_i64m4 (vint64m4_t op1, vint64m4_t op2,
    vbool16_t borrowin, size_t vl);
vint64m4_t vsbc_vxm_i64m4 (vint64m4_t op1, int64_t op2,
    vbool16_t borrowin, size_t vl);
vint64m8_t vsbc_vvm_i64m8 (vint64m8_t op1, vint64m8_t op2,
    vbool8_t borrowin, size_t vl);
vint64m8_t vsbc_vxm_i64m8 (vint64m8_t op1, int64_t op2, vbool8_t
    borrowin, size_t vl);
vuint8m1_t vsbc_vvm_u8m1 (vuint8m1_t op1, vuint8m1_t op2,
    vbool8_t borrowin, size_t vl);
vuint8m1_t vsbc_vxm_u8m1 (vuint8m1_t op1, uint8_t op2, vbool8_t
    borrowin, size_t vl);
vuint8m2_t vsbc_vvm_u8m2 (vuint8m2_t op1, vuint8m2_t op2,
    vbool4_t borrowin, size_t vl);
vuint8m2_t vsbc_vxm_u8m2 (vuint8m2_t op1, uint8_t op2, vbool4_t
    borrowin, size_t vl);
vuint8m4_t vsbc_vvm_u8m4 (vuint8m4_t op1, vuint8m4_t op2,
    vbool2_t borrowin, size_t vl);
vuint8m4_t vsbc_vxm_u8m4 (vuint8m4_t op1, uint8_t op2, vbool2_t
    borrowin, size_t vl);
vuint8m8_t vsbc_vvm_u8m8 (vuint8m8_t op1, vuint8m8_t op2,
    vbool1_t borrowin, size_t vl);
vuint8m8_t vsbc_vxm_u8m8 (vuint8m8_t op1, uint8_t op2, vbool1_t
    borrowin, size_t vl);
vuint16m1_t vsbc_vvm_u16m1 (vuint16m1_t op1, vuint16m1_t op2,
    vbool16_t borrowin, size_t vl);

```

```

vuint16m1_t vsbc_vxm_u16m1 (vuint16m1_t op1, uint16_t op2,
    vbool16_t borrowin, size_t vl);
vuint16m2_t vsbc_vvm_u16m2 (vuint16m2_t op1, vuint16m2_t op2,
    vbool8_t borrowin, size_t vl);
vuint16m2_t vsbc_vxm_u16m2 (vuint16m2_t op1, uint16_t op2,
    vbool8_t borrowin, size_t vl);
vuint16m4_t vsbc_vvm_u16m4 (vuint16m4_t op1, vuint16m4_t op2,
    vbool4_t borrowin, size_t vl);
vuint16m4_t vsbc_vxm_u16m4 (vuint16m4_t op1, uint16_t op2,
    vbool4_t borrowin, size_t vl);
vuint16m8_t vsbc_vvm_u16m8 (vuint16m8_t op1, vuint16m8_t op2,
    vbool2_t borrowin, size_t vl);
vuint16m8_t vsbc_vxm_u16m8 (vuint16m8_t op1, uint16_t op2,
    vbool2_t borrowin, size_t vl);
vuint32m1_t vsbc_vvm_u32m1 (vuint32m1_t op1, vuint32m1_t op2,
    vbool32_t borrowin, size_t vl);
vuint32m1_t vsbc_vxm_u32m1 (vuint32m1_t op1, uint32_t op2,
    vbool32_t borrowin, size_t vl);
vuint32m2_t vsbc_vvm_u32m2 (vuint32m2_t op1, vuint32m2_t op2,
    vbool16_t borrowin, size_t vl);
vuint32m2_t vsbc_vxm_u32m2 (vuint32m2_t op1, uint32_t op2,
    vbool16_t borrowin, size_t vl);
vuint32m4_t vsbc_vvm_u32m4 (vuint32m4_t op1, vuint32m4_t op2,
    vbool8_t borrowin, size_t vl);
vuint32m4_t vsbc_vxm_u32m4 (vuint32m4_t op1, uint32_t op2,
    vbool8_t borrowin, size_t vl);
vuint32m8_t vsbc_vvm_u32m8 (vuint32m8_t op1, vuint32m8_t op2,
    vbool4_t borrowin, size_t vl);
vuint32m8_t vsbc_vxm_u32m8 (vuint32m8_t op1, uint32_t op2,
    vbool4_t borrowin, size_t vl);
vuint64m1_t vsbc_vvm_u64m1 (vuint64m1_t op1, vuint64m1_t op2,
    vbool64_t borrowin, size_t vl);
vuint64m1_t vsbc_vxm_u64m1 (vuint64m1_t op1, uint64_t op2,
    vbool64_t borrowin, size_t vl);
vuint64m2_t vsbc_vvm_u64m2 (vuint64m2_t op1, vuint64m2_t op2,
    vbool32_t borrowin, size_t vl);
vuint64m2_t vsbc_vxm_u64m2 (vuint64m2_t op1, uint64_t op2,
    vbool32_t borrowin, size_t vl);
vuint64m4_t vsbc_vvm_u64m4 (vuint64m4_t op1, vuint64m4_t op2,
    vbool16_t borrowin, size_t vl);
vuint64m4_t vsbc_vxm_u64m4 (vuint64m4_t op1, uint64_t op2,
    vbool16_t borrowin, size_t vl);
vuint64m8_t vsbc_vvm_u64m8 (vuint64m8_t op1, vuint64m8_t op2,
    vbool8_t borrowin, size_t vl);
vuint64m8_t vsbc_vxm_u64m8 (vuint64m8_t op1, uint64_t op2,
    vbool8_t borrowin, size_t vl);

```

```

vbool8_t vmsbc_vvm_i8m1_b8 (vint8m1_t op1, vint8m1_t op2,
    vbool8_t borrowin, size_t vl);
vbool8_t vmsbc_vxm_i8m1_b8 (vint8m1_t op1, int8_t op2, vbool8_t
    borrowin, size_t vl);
vbool4_t vmsbc_vvm_i8m2_b4 (vint8m2_t op1, vint8m2_t op2,
    vbool4_t borrowin, size_t vl);
vbool4_t vmsbc_vxm_i8m2_b4 (vint8m2_t op1, int8_t op2, vbool4_t
    borrowin, size_t vl);
vbool2_t vmsbc_vvm_i8m4_b2 (vint8m4_t op1, vint8m4_t op2,
    vbool2_t borrowin, size_t vl);
vbool2_t vmsbc_vxm_i8m4_b2 (vint8m4_t op1, int8_t op2, vbool2_t
    borrowin, size_t vl);
vbool1_t vmsbc_vvm_i8m8_b1 (vint8m8_t op1, vint8m8_t op2,
    vbool1_t borrowin, size_t vl);
vbool1_t vmsbc_vxm_i8m8_b1 (vint8m8_t op1, int8_t op2, vbool1_t
    borrowin, size_t vl);
vbool16_t vmsbc_vvm_i16m1_b16 (vint16m1_t op1, vint16m1_t op2,
    vbool16_t borrowin, size_t vl);
vbool16_t vmsbc_vxm_i16m1_b16 (vint16m1_t op1, int16_t op2,
    vbool16_t borrowin, size_t vl);
vbool8_t vmsbc_vvm_i16m2_b8 (vint16m2_t op1, vint16m2_t op2,
    vbool8_t borrowin, size_t vl);
vbool8_t vmsbc_vxm_i16m2_b8 (vint16m2_t op1, int16_t op2,
    vbool8_t borrowin, size_t vl);
vbool4_t vmsbc_vvm_i16m4_b4 (vint16m4_t op1, vint16m4_t op2,
    vbool4_t borrowin, size_t vl);
vbool4_t vmsbc_vxm_i16m4_b4 (vint16m4_t op1, int16_t op2,
    vbool4_t borrowin, size_t vl);
vbool2_t vmsbc_vvm_i16m8_b2 (vint16m8_t op1, vint16m8_t op2,
    vbool2_t borrowin, size_t vl);
vbool2_t vmsbc_vxm_i16m8_b2 (vint16m8_t op1, int16_t op2,
    vbool2_t borrowin, size_t vl);
vbool32_t vmsbc_vvm_i32m1_b32 (vint32m1_t op1, vint32m1_t op2,
    vbool32_t borrowin, size_t vl);
vbool32_t vmsbc_vxm_i32m1_b32 (vint32m1_t op1, int32_t op2,
    vbool32_t borrowin, size_t vl);
vbool16_t vmsbc_vvm_i32m2_b16 (vint32m2_t op1, vint32m2_t op2,
    vbool16_t borrowin, size_t vl);
vbool16_t vmsbc_vxm_i32m2_b16 (vint32m2_t op1, int32_t op2,
    vbool16_t borrowin, size_t vl);
vbool8_t vmsbc_vvm_i32m4_b8 (vint32m4_t op1, vint32m4_t op2,
    vbool8_t borrowin, size_t vl);
vbool8_t vmsbc_vxm_i32m4_b8 (vint32m4_t op1, int32_t op2,
    vbool8_t borrowin, size_t vl);
vbool4_t vmsbc_vvm_i32m8_b4 (vint32m8_t op1, vint32m8_t op2,
    vbool4_t borrowin, size_t vl);

```

```

vbool4_t vmsbc_vxm_i32m8_b4 (vint32m8_t op1, int32_t op2,
    vbool4_t borrowin, size_t vl);
vbool64_t vmsbc_vvm_i64m1_b64 (vint64m1_t op1, vint64m1_t op2,
    vbool64_t borrowin, size_t vl);
vbool64_t vmsbc_vxm_i64m1_b64 (vint64m1_t op1, int64_t op2,
    vbool64_t borrowin, size_t vl);
vbool32_t vmsbc_vvm_i64m2_b32 (vint64m2_t op1, vint64m2_t op2,
    vbool32_t borrowin, size_t vl);
vbool32_t vmsbc_vxm_i64m2_b32 (vint64m2_t op1, int64_t op2,
    vbool32_t borrowin, size_t vl);
vbool16_t vmsbc_vvm_i64m4_b16 (vint64m4_t op1, vint64m4_t op2,
    vbool16_t borrowin, size_t vl);
vbool16_t vmsbc_vxm_i64m4_b16 (vint64m4_t op1, int64_t op2,
    vbool16_t borrowin, size_t vl);
vbool8_t vmsbc_vvm_i64m8_b8 (vint64m8_t op1, vint64m8_t op2,
    vbool8_t borrowin, size_t vl);
vbool8_t vmsbc_vxm_i64m8_b8 (vint64m8_t op1, int64_t op2,
    vbool8_t borrowin, size_t vl);
vbool8_t vmsbc_vvm_u8m1_b8 (vuint8m1_t op1, vuint8m1_t op2,
    vbool8_t borrowin, size_t vl);
vbool8_t vmsbc_vxm_u8m1_b8 (vuint8m1_t op1, uint8_t op2,
    vbool8_t borrowin, size_t vl);
vbool4_t vmsbc_vvm_u8m2_b4 (vuint8m2_t op1, vuint8m2_t op2,
    vbool4_t borrowin, size_t vl);
vbool4_t vmsbc_vxm_u8m2_b4 (vuint8m2_t op1, uint8_t op2,
    vbool4_t borrowin, size_t vl);
vbool2_t vmsbc_vvm_u8m4_b2 (vuint8m4_t op1, vuint8m4_t op2,
    vbool2_t borrowin, size_t vl);
vbool2_t vmsbc_vxm_u8m4_b2 (vuint8m4_t op1, uint8_t op2,
    vbool2_t borrowin, size_t vl);
vbool1_t vmsbc_vvm_u8m8_b1 (vuint8m8_t op1, vuint8m8_t op2,
    vbool1_t borrowin, size_t vl);
vbool1_t vmsbc_vxm_u8m8_b1 (vuint8m8_t op1, uint8_t op2,
    vbool1_t borrowin, size_t vl);
vbool16_t vmsbc_vvm_u16m1_b16 (vuint16m1_t op1, vuint16m1_t op2,
    vbool16_t borrowin, size_t vl);
vbool16_t vmsbc_vxm_u16m1_b16 (vuint16m1_t op1, uint16_t op2,
    vbool16_t borrowin, size_t vl);
vbool8_t vmsbc_vvm_u16m2_b8 (vuint16m2_t op1, vuint16m2_t op2,
    vbool8_t borrowin, size_t vl);
vbool8_t vmsbc_vxm_u16m2_b8 (vuint16m2_t op1, uint16_t op2,
    vbool8_t borrowin, size_t vl);
vbool4_t vmsbc_vvm_u16m4_b4 (vuint16m4_t op1, vuint16m4_t op2,
    vbool4_t borrowin, size_t vl);
vbool4_t vmsbc_vxm_u16m4_b4 (vuint16m4_t op1, uint16_t op2,
    vbool4_t borrowin, size_t vl);

```

```

vbool12_t vmsbc_vvm_u16m8_b2 (vuint16m8_t op1, vuint16m8_t op2,
    vbool12_t borrowin, size_t vl);
vbool12_t vmsbc_vxm_u16m8_b2 (vuint16m8_t op1, uint16_t op2,
    vbool12_t borrowin, size_t vl);
vbool32_t vmsbc_vvm_u32m1_b32 (vuint32m1_t op1, vuint32m1_t op2,
    vbool32_t borrowin, size_t vl);
vbool32_t vmsbc_vxm_u32m1_b32 (vuint32m1_t op1, uint32_t op2,
    vbool32_t borrowin, size_t vl);
vbool16_t vmsbc_vvm_u32m2_b16 (vuint32m2_t op1, vuint32m2_t op2,
    vbool16_t borrowin, size_t vl);
vbool16_t vmsbc_vxm_u32m2_b16 (vuint32m2_t op1, uint32_t op2,
    vbool16_t borrowin, size_t vl);
vbool8_t vmsbc_vvm_u32m4_b8 (vuint32m4_t op1, vuint32m4_t op2,
    vbool8_t borrowin, size_t vl);
vbool8_t vmsbc_vxm_u32m4_b8 (vuint32m4_t op1, uint32_t op2,
    vbool8_t borrowin, size_t vl);
vbool4_t vmsbc_vvm_u32m8_b4 (vuint32m8_t op1, vuint32m8_t op2,
    vbool4_t borrowin, size_t vl);
vbool4_t vmsbc_vxm_u32m8_b4 (vuint32m8_t op1, uint32_t op2,
    vbool4_t borrowin, size_t vl);
vbool64_t vmsbc_vvm_u64m1_b64 (vuint64m1_t op1, vuint64m1_t op2,
    vbool64_t borrowin, size_t vl);
vbool64_t vmsbc_vxm_u64m1_b64 (vuint64m1_t op1, uint64_t op2,
    vbool64_t borrowin, size_t vl);
vbool32_t vmsbc_vvm_u64m2_b32 (vuint64m2_t op1, vuint64m2_t op2,
    vbool32_t borrowin, size_t vl);
vbool32_t vmsbc_vxm_u64m2_b32 (vuint64m2_t op1, uint64_t op2,
    vbool32_t borrowin, size_t vl);
vbool16_t vmsbc_vvm_u64m4_b16 (vuint64m4_t op1, vuint64m4_t op2,
    vbool16_t borrowin, size_t vl);
vbool16_t vmsbc_vxm_u64m4_b16 (vuint64m4_t op1, uint64_t op2,
    vbool16_t borrowin, size_t vl);
vbool8_t vmsbc_vvm_u64m8_b8 (vuint64m8_t op1, vuint64m8_t op2,
    vbool8_t borrowin, size_t vl);
vbool8_t vmsbc_vxm_u64m8_b8 (vuint64m8_t op1, uint64_t op2,
    vbool8_t borrowin, size_t vl);

```

## Vector Bitwise Logical Functions:

### Prototypes:

```

vint8m1_t vand_vv_i8m1 (vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vand_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vand_vv_i8m2 (vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vand_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vand_vv_i8m4 (vint8m4_t op1, vint8m4_t op2, size_t vl);

```

```

vint8m4_t vand_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vand_vv_i8m8 (vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vand_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vand_vv_i16m1 (vint16m1_t op1, vint16m1_t op2, size_t
    vl);
vint16m1_t vand_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint16m2_t vand_vv_i16m2 (vint16m2_t op1, vint16m2_t op2, size_t
    vl);
vint16m2_t vand_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint16m4_t vand_vv_i16m4 (vint16m4_t op1, vint16m4_t op2, size_t
    vl);
vint16m4_t vand_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vint16m8_t vand_vv_i16m8 (vint16m8_t op1, vint16m8_t op2, size_t
    vl);
vint16m8_t vand_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t
    vl);
vint32m1_t vand_vv_i32m1 (vint32m1_t op1, vint32m1_t op2, size_t
    vl);
vint32m1_t vand_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint32m2_t vand_vv_i32m2 (vint32m2_t op1, vint32m2_t op2, size_t
    vl);
vint32m2_t vand_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint32m4_t vand_vv_i32m4 (vint32m4_t op1, vint32m4_t op2, size_t
    vl);
vint32m4_t vand_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint32m8_t vand_vv_i32m8 (vint32m8_t op1, vint32m8_t op2, size_t
    vl);
vint32m8_t vand_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t
    vl);
vint64m1_t vand_vv_i64m1 (vint64m1_t op1, vint64m1_t op2, size_t
    vl);
vint64m1_t vand_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t
    vl);
vint64m2_t vand_vv_i64m2 (vint64m2_t op1, vint64m2_t op2, size_t
    vl);
vint64m2_t vand_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t
    vl);
vint64m4_t vand_vv_i64m4 (vint64m4_t op1, vint64m4_t op2, size_t
    vl);

```



```

vint64m4_t vand_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t
    vl);
vint64m8_t vand_vv_i64m8 (vint64m8_t op1, vint64m8_t op2, size_t
    vl);
vint64m8_t vand_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vuint8m1_t vand_vv_u8m1 (vuint8m1_t op1, vuint8m1_t op2, size_t
    vl);
vuint8m1_t vand_vx_u8m1 (vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vand_vv_u8m2 (vuint8m2_t op1, vuint8m2_t op2, size_t
    vl);
vuint8m2_t vand_vx_u8m2 (vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vand_vv_u8m4 (vuint8m4_t op1, vuint8m4_t op2, size_t
    vl);
vuint8m4_t vand_vx_u8m4 (vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vand_vv_u8m8 (vuint8m8_t op1, vuint8m8_t op2, size_t
    vl);
vuint8m8_t vand_vx_u8m8 (vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vand_vv_u16m1 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vuint16m1_t vand_vx_u16m1 (vuint16m1_t op1, uint16_t op2, size_t
    vl);
vuint16m2_t vand_vv_u16m2 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vuint16m2_t vand_vx_u16m2 (vuint16m2_t op1, uint16_t op2, size_t
    vl);
vuint16m4_t vand_vv_u16m4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vuint16m4_t vand_vx_u16m4 (vuint16m4_t op1, uint16_t op2, size_t
    vl);
vuint16m8_t vand_vv_u16m8 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vuint16m8_t vand_vx_u16m8 (vuint16m8_t op1, uint16_t op2, size_t
    vl);
vuint32m1_t vand_vv_u32m1 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vuint32m1_t vand_vx_u32m1 (vuint32m1_t op1, uint32_t op2, size_t
    vl);
vuint32m2_t vand_vv_u32m2 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vuint32m2_t vand_vx_u32m2 (vuint32m2_t op1, uint32_t op2, size_t
    vl);
vuint32m4_t vand_vv_u32m4 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vuint32m4_t vand_vx_u32m4 (vuint32m4_t op1, uint32_t op2, size_t
    vl);

```

```

vuint32m8_t vand_vv_u32m8 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vuint32m8_t vand_vx_u32m8 (vuint32m8_t op1, uint32_t op2, size_t
    vl);
vuint64m1_t vand_vv_u64m1 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vuint64m1_t vand_vx_u64m1 (vuint64m1_t op1, uint64_t op2, size_t
    vl);
vuint64m2_t vand_vv_u64m2 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vuint64m2_t vand_vx_u64m2 (vuint64m2_t op1, uint64_t op2, size_t
    vl);
vuint64m4_t vand_vv_u64m4 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vuint64m4_t vand_vx_u64m4 (vuint64m4_t op1, uint64_t op2, size_t
    vl);
vuint64m8_t vand_vv_u64m8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vuint64m8_t vand_vx_u64m8 (vuint64m8_t op1, uint64_t op2, size_t
    vl);
vint8m1_t vor_vv_i8m1 (vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vor_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vor_vv_i8m2 (vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vor_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vor_vv_i8m4 (vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vor_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vor_vv_i8m8 (vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vor_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vor_vv_i16m1 (vint16m1_t op1, vint16m1_t op2, size_t
    vl);
vint16m1_t vor_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t vl);
vint16m2_t vor_vv_i16m2 (vint16m2_t op1, vint16m2_t op2, size_t
    vl);
vint16m2_t vor_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vor_vv_i16m4 (vint16m4_t op1, vint16m4_t op2, size_t
    vl);
vint16m4_t vor_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vor_vv_i16m8 (vint16m8_t op1, vint16m8_t op2, size_t
    vl);
vint16m8_t vor_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vor_vv_i32m1 (vint32m1_t op1, vint32m1_t op2, size_t
    vl);
vint32m1_t vor_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vor_vv_i32m2 (vint32m2_t op1, vint32m2_t op2, size_t
    vl);
vint32m2_t vor_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t vl);

```

```

vint32m4_t vor_vv_i32m4 (vint32m4_t op1, vint32m4_t op2, size_t
    vl);
vint32m4_t vor_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vor_vv_i32m8 (vint32m8_t op1, vint32m8_t op2, size_t
    vl);
vint32m8_t vor_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vor_vv_i64m1 (vint64m1_t op1, vint64m1_t op2, size_t
    vl);
vint64m1_t vor_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vor_vv_i64m2 (vint64m2_t op1, vint64m2_t op2, size_t
    vl);
vint64m2_t vor_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t vl);
vint64m4_t vor_vv_i64m4 (vint64m4_t op1, vint64m4_t op2, size_t
    vl);
vint64m4_t vor_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vor_vv_i64m8 (vint64m8_t op1, vint64m8_t op2, size_t
    vl);
vint64m8_t vor_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t vl);
vuint8m1_t vor_vv_u8m1 (vuint8m1_t op1, vuint8m1_t op2, size_t
    vl);
vuint8m1_t vor_vx_u8m1 (vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vor_vv_u8m2 (vuint8m2_t op1, vuint8m2_t op2, size_t
    vl);
vuint8m2_t vor_vx_u8m2 (vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vor_vv_u8m4 (vuint8m4_t op1, vuint8m4_t op2, size_t
    vl);
vuint8m4_t vor_vx_u8m4 (vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vor_vv_u8m8 (vuint8m8_t op1, vuint8m8_t op2, size_t
    vl);
vuint8m8_t vor_vx_u8m8 (vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vor_vv_u16m1 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vuint16m1_t vor_vx_u16m1 (vuint16m1_t op1, uint16_t op2, size_t
    vl);
vuint16m2_t vor_vv_u16m2 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vuint16m2_t vor_vx_u16m2 (vuint16m2_t op1, uint16_t op2, size_t
    vl);
vuint16m4_t vor_vv_u16m4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vuint16m4_t vor_vx_u16m4 (vuint16m4_t op1, uint16_t op2, size_t
    vl);
vuint16m8_t vor_vv_u16m8 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vuint16m8_t vor_vx_u16m8 (vuint16m8_t op1, uint16_t op2, size_t
    vl);

```

```

vuint32m1_t vor_vv_u32m1 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vuint32m1_t vor_vx_u32m1 (vuint32m1_t op1, uint32_t op2, size_t
    vl);
vuint32m2_t vor_vv_u32m2 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vuint32m2_t vor_vx_u32m2 (vuint32m2_t op1, uint32_t op2, size_t
    vl);
vuint32m4_t vor_vv_u32m4 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vuint32m4_t vor_vx_u32m4 (vuint32m4_t op1, uint32_t op2, size_t
    vl);
vuint32m8_t vor_vv_u32m8 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vuint32m8_t vor_vx_u32m8 (vuint32m8_t op1, uint32_t op2, size_t
    vl);
vuint64m1_t vor_vv_u64m1 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vuint64m1_t vor_vx_u64m1 (vuint64m1_t op1, uint64_t op2, size_t
    vl);
vuint64m2_t vor_vv_u64m2 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vuint64m2_t vor_vx_u64m2 (vuint64m2_t op1, uint64_t op2, size_t
    vl);
vuint64m4_t vor_vv_u64m4 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vuint64m4_t vor_vx_u64m4 (vuint64m4_t op1, uint64_t op2, size_t
    vl);
vuint64m8_t vor_vv_u64m8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vuint64m8_t vor_vx_u64m8 (vuint64m8_t op1, uint64_t op2, size_t
    vl);
vint8m1_t vxor_vv_i8m1 (vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vxor_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vxor_vv_i8m2 (vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vxor_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vxor_vv_i8m4 (vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vxor_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vxor_vv_i8m8 (vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vxor_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vxor_vv_i16m1 (vint16m1_t op1, vint16m1_t op2, size_t
    vl);
vint16m1_t vxor_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint16m2_t vxor_vv_i16m2 (vint16m2_t op1, vint16m2_t op2, size_t
    vl);

```

```

vint16m2_t vxor_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint16m4_t vxor_vv_i16m4 (vint16m4_t op1, vint16m4_t op2, size_t
    vl);
vint16m4_t vxor_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vint16m8_t vxor_vv_i16m8 (vint16m8_t op1, vint16m8_t op2, size_t
    vl);
vint16m8_t vxor_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t
    vl);
vint32m1_t vxor_vv_i32m1 (vint32m1_t op1, vint32m1_t op2, size_t
    vl);
vint32m1_t vxor_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint32m2_t vxor_vv_i32m2 (vint32m2_t op1, vint32m2_t op2, size_t
    vl);
vint32m2_t vxor_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint32m4_t vxor_vv_i32m4 (vint32m4_t op1, vint32m4_t op2, size_t
    vl);
vint32m4_t vxor_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint32m8_t vxor_vv_i32m8 (vint32m8_t op1, vint32m8_t op2, size_t
    vl);
vint32m8_t vxor_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t
    vl);
vint64m1_t vxor_vv_i64m1 (vint64m1_t op1, vint64m1_t op2, size_t
    vl);
vint64m1_t vxor_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t
    vl);
vint64m2_t vxor_vv_i64m2 (vint64m2_t op1, vint64m2_t op2, size_t
    vl);
vint64m2_t vxor_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t
    vl);
vint64m4_t vxor_vv_i64m4 (vint64m4_t op1, vint64m4_t op2, size_t
    vl);
vint64m4_t vxor_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t
    vl);
vint64m8_t vxor_vv_i64m8 (vint64m8_t op1, vint64m8_t op2, size_t
    vl);
vint64m8_t vxor_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vuint8m1_t vxor_vv_u8m1 (vuint8m1_t op1, vuint8m1_t op2, size_t
    vl);
vuint8m1_t vxor_vx_u8m1 (vuint8m1_t op1, uint8_t op2, size_t vl);

```

```

vuint8m2_t vxor_vv_u8m2 (vuint8m2_t op1, vuint8m2_t op2, size_t
    vl);
vuint8m2_t vxor_vx_u8m2 (vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vxor_vv_u8m4 (vuint8m4_t op1, vuint8m4_t op2, size_t
    vl);
vuint8m4_t vxor_vx_u8m4 (vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vxor_vv_u8m8 (vuint8m8_t op1, vuint8m8_t op2, size_t
    vl);
vuint8m8_t vxor_vx_u8m8 (vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vxor_vv_u16m1 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vuint16m1_t vxor_vx_u16m1 (vuint16m1_t op1, uint16_t op2, size_t
    vl);
vuint16m2_t vxor_vv_u16m2 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vuint16m2_t vxor_vx_u16m2 (vuint16m2_t op1, uint16_t op2, size_t
    vl);
vuint16m4_t vxor_vv_u16m4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vuint16m4_t vxor_vx_u16m4 (vuint16m4_t op1, uint16_t op2, size_t
    vl);
vuint16m8_t vxor_vv_u16m8 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vuint16m8_t vxor_vx_u16m8 (vuint16m8_t op1, uint16_t op2, size_t
    vl);
vuint32m1_t vxor_vv_u32m1 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vuint32m1_t vxor_vx_u32m1 (vuint32m1_t op1, uint32_t op2, size_t
    vl);
vuint32m2_t vxor_vv_u32m2 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vuint32m2_t vxor_vx_u32m2 (vuint32m2_t op1, uint32_t op2, size_t
    vl);
vuint32m4_t vxor_vv_u32m4 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vuint32m4_t vxor_vx_u32m4 (vuint32m4_t op1, uint32_t op2, size_t
    vl);
vuint32m8_t vxor_vv_u32m8 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vuint32m8_t vxor_vx_u32m8 (vuint32m8_t op1, uint32_t op2, size_t
    vl);
vuint64m1_t vxor_vv_u64m1 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vuint64m1_t vxor_vx_u64m1 (vuint64m1_t op1, uint64_t op2, size_t
    vl);

```

```

vuint64m2_t vxor_vv_u64m2 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vuint64m2_t vxor_vx_u64m2 (vuint64m2_t op1, uint64_t op2, size_t
    vl);
vuint64m4_t vxor_vv_u64m4 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vuint64m4_t vxor_vx_u64m4 (vuint64m4_t op1, uint64_t op2, size_t
    vl);
vuint64m8_t vxor_vv_u64m8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vuint64m8_t vxor_vx_u64m8 (vuint64m8_t op1, uint64_t op2, size_t
    vl);
vint8m1_t vnot_v_i8m1 (vint8m1_t op1, size_t vl);
vint8m2_t vnot_v_i8m2 (vint8m2_t op1, size_t vl);
vint8m4_t vnot_v_i8m4 (vint8m4_t op1, size_t vl);
vint8m8_t vnot_v_i8m8 (vint8m8_t op1, size_t vl);
vint16m1_t vnot_v_i16m1 (vint16m1_t op1, size_t vl);
vint16m2_t vnot_v_i16m2 (vint16m2_t op1, size_t vl);
vint16m4_t vnot_v_i16m4 (vint16m4_t op1, size_t vl);
vint16m8_t vnot_v_i16m8 (vint16m8_t op1, size_t vl);
vint32m1_t vnot_v_i32m1 (vint32m1_t op1, size_t vl);
vint32m2_t vnot_v_i32m2 (vint32m2_t op1, size_t vl);
vint32m4_t vnot_v_i32m4 (vint32m4_t op1, size_t vl);
vint32m8_t vnot_v_i32m8 (vint32m8_t op1, size_t vl);
vint64m1_t vnot_v_i64m1 (vint64m1_t op1, size_t vl);
vint64m2_t vnot_v_i64m2 (vint64m2_t op1, size_t vl);
vint64m4_t vnot_v_i64m4 (vint64m4_t op1, size_t vl);
vint64m8_t vnot_v_i64m8 (vint64m8_t op1, size_t vl);
vuint8m1_t vnot_v_u8m1 (vuint8m1_t op1, size_t vl);
vuint8m2_t vnot_v_u8m2 (vuint8m2_t op1, size_t vl);
vuint8m4_t vnot_v_u8m4 (vuint8m4_t op1, size_t vl);
vuint8m8_t vnot_v_u8m8 (vuint8m8_t op1, size_t vl);
vuint16m1_t vnot_v_u16m1 (vuint16m1_t op1, size_t vl);
vuint16m2_t vnot_v_u16m2 (vuint16m2_t op1, size_t vl);
vuint16m4_t vnot_v_u16m4 (vuint16m4_t op1, size_t vl);
vuint16m8_t vnot_v_u16m8 (vuint16m8_t op1, size_t vl);
vuint32m1_t vnot_v_u32m1 (vuint32m1_t op1, size_t vl);
vuint32m2_t vnot_v_u32m2 (vuint32m2_t op1, size_t vl);
vuint32m4_t vnot_v_u32m4 (vuint32m4_t op1, size_t vl);
vuint32m8_t vnot_v_u32m8 (vuint32m8_t op1, size_t vl);
vuint64m1_t vnot_v_u64m1 (vuint64m1_t op1, size_t vl);
vuint64m2_t vnot_v_u64m2 (vuint64m2_t op1, size_t vl);
vuint64m4_t vnot_v_u64m4 (vuint64m4_t op1, size_t vl);
vuint64m8_t vnot_v_u64m8 (vuint64m8_t op1, size_t vl);
// masked functions

```

```

vint8m1_t vand_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vand_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vand_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vand_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vand_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vand_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vand_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vand_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vand_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint16m1_t vand_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vint16m2_t vand_vv_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, vint16m2_t op2, size_t vl);
vint16m2_t vand_vx_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vand_vv_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, vint16m4_t op2, size_t vl);
vint16m4_t vand_vx_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vand_vv_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, vint16m8_t op2, size_t vl);
vint16m8_t vand_vx_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vand_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint32m1_t vand_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vand_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vint32m2_t vand_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint32m4_t vand_vv_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, vint32m4_t op2, size_t vl);
vint32m4_t vand_vx_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vand_vv_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, vint32m8_t op2, size_t vl);

```



```

vint32m8_t vand_vx_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vand_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vint64m1_t vand_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vand_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vint64m2_t vand_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vint64m4_t vand_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vint64m4_t vand_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vand_vv_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, vint64m8_t op2, size_t vl);
vint64m8_t vand_vx_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, int64_t op2, size_t vl);
vuint8m1_t vand_vv_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vuint8m1_t vand_vx_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vand_vv_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vuint8m2_t vand_vx_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vand_vv_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vuint8m4_t vand_vx_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vand_vv_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vuint8m8_t vand_vx_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vand_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vuint16m1_t vand_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint16m2_t vand_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vuint16m2_t vand_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint16m4_t vand_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vuint16m4_t vand_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);

```

```

vuint16m8_t vand_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vuint16m8_t vand_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vuint32m1_t vand_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vuint32m1_t vand_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vuint32m2_t vand_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vuint32m2_t vand_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint32m4_t vand_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vuint32m4_t vand_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vuint32m8_t vand_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vuint32m8_t vand_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);
vuint64m1_t vand_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vuint64m1_t vand_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vuint64m2_t vand_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vuint64m2_t vand_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vuint64m4_t vand_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);
vuint64m4_t vand_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vuint64m8_t vand_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vuint64m8_t vand_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);
vint8m1_t vor_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vor_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vor_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vor_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vor_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);

```

```

vint8m4_t vor_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vor_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vor_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vor_vv_i16m1_m (vbool16_t mask, vint16m1_t maskedoff,
    vint16m1_t op1, vint16m1_t op2, size_t vl);
vint16m1_t vor_vx_i16m1_m (vbool16_t mask, vint16m1_t maskedoff,
    vint16m1_t op1, int16_t op2, size_t vl);
vint16m2_t vor_vv_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, vint16m2_t op2, size_t vl);
vint16m2_t vor_vx_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vor_vv_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, vint16m4_t op2, size_t vl);
vint16m4_t vor_vx_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vor_vv_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, vint16m8_t op2, size_t vl);
vint16m8_t vor_vx_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vor_vv_i32m1_m (vbool32_t mask, vint32m1_t maskedoff,
    vint32m1_t op1, vint32m1_t op2, size_t vl);
vint32m1_t vor_vx_i32m1_m (vbool32_t mask, vint32m1_t maskedoff,
    vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vor_vv_i32m2_m (vbool16_t mask, vint32m2_t maskedoff,
    vint32m2_t op1, vint32m2_t op2, size_t vl);
vint32m2_t vor_vx_i32m2_m (vbool16_t mask, vint32m2_t maskedoff,
    vint32m2_t op1, int32_t op2, size_t vl);
vint32m4_t vor_vv_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, vint32m4_t op2, size_t vl);
vint32m4_t vor_vx_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vor_vv_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, vint32m8_t op2, size_t vl);
vint32m8_t vor_vx_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vor_vv_i64m1_m (vbool64_t mask, vint64m1_t maskedoff,
    vint64m1_t op1, vint64m1_t op2, size_t vl);
vint64m1_t vor_vx_i64m1_m (vbool64_t mask, vint64m1_t maskedoff,
    vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vor_vv_i64m2_m (vbool32_t mask, vint64m2_t maskedoff,
    vint64m2_t op1, vint64m2_t op2, size_t vl);
vint64m2_t vor_vx_i64m2_m (vbool32_t mask, vint64m2_t maskedoff,
    vint64m2_t op1, int64_t op2, size_t vl);

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vint64m4_t vor_vv_i64m4_m (vbool16_t mask, vint64m4_t maskedoff,
    vint64m4_t op1, vint64m4_t op2, size_t vl);
vint64m4_t vor_vx_i64m4_m (vbool16_t mask, vint64m4_t maskedoff,
    vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vor_vv_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, vint64m8_t op2, size_t vl);
vint64m8_t vor_vx_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, int64_t op2, size_t vl);
vuint8m1_t vor_vv_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vuint8m1_t vor_vx_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vor_vv_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vuint8m2_t vor_vx_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vor_vv_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vuint8m4_t vor_vx_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vor_vv_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vuint8m8_t vor_vx_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vor_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vuint16m1_t vor_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint16m2_t vor_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vuint16m2_t vor_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint16m4_t vor_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vuint16m4_t vor_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vuint16m8_t vor_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vuint16m8_t vor_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vuint32m1_t vor_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vuint32m1_t vor_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vuint32m2_t vor_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);

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vuint32m2_t vor_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint32m4_t vor_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vuint32m4_t vor_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vuint32m8_t vor_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vuint32m8_t vor_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);
vuint64m1_t vor_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vuint64m1_t vor_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vuint64m2_t vor_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vuint64m2_t vor_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vuint64m4_t vor_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);
vuint64m4_t vor_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vuint64m8_t vor_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vuint64m8_t vor_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);
vint8m1_t vxor_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vxor_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vxor_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vxor_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vxor_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vxor_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vxor_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vxor_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vxor_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint16m1_t vxor_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);

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vint16m2_t vxor_vv_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, vint16m2_t op2, size_t vl);
vint16m2_t vxor_vx_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vxor_vv_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, vint16m4_t op2, size_t vl);
vint16m4_t vxor_vx_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vxor_vv_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, vint16m8_t op2, size_t vl);
vint16m8_t vxor_vx_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vxor_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint32m1_t vxor_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vxor_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vint32m2_t vxor_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint32m4_t vxor_vv_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, vint32m4_t op2, size_t vl);
vint32m4_t vxor_vx_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vxor_vv_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, vint32m8_t op2, size_t vl);
vint32m8_t vxor_vx_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vxor_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vint64m1_t vxor_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vxor_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vint64m2_t vxor_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vint64m4_t vxor_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vint64m4_t vxor_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vxor_vv_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, vint64m8_t op2, size_t vl);
vint64m8_t vxor_vx_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, int64_t op2, size_t vl);
vuint8m1_t vxor_vv_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, vuint8m1_t op2, size_t vl);

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vuint8m1_t vxor_vx_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vxor_vv_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vuint8m2_t vxor_vx_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vxor_vv_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vuint8m4_t vxor_vx_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vxor_vv_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vuint8m8_t vxor_vx_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vxor_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vuint16m1_t vxor_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint16m2_t vxor_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vuint16m2_t vxor_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint16m4_t vxor_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vuint16m4_t vxor_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vuint16m8_t vxor_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vuint16m8_t vxor_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vuint32m1_t vxor_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vuint32m1_t vxor_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vuint32m2_t vxor_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vuint32m2_t vxor_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint32m4_t vxor_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vuint32m4_t vxor_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vuint32m8_t vxor_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vuint32m8_t vxor_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);

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vuint64m1_t vxor_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vuint64m1_t vxor_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vuint64m2_t vxor_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vuint64m2_t vxor_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vuint64m4_t vxor_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);
vuint64m4_t vxor_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vuint64m8_t vxor_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vuint64m8_t vxor_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);
vint8m1_t vnot_v_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, size_t vl);
vint8m2_t vnot_v_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, size_t vl);
vint8m4_t vnot_v_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, size_t vl);
vint8m8_t vnot_v_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, size_t vl);
vint16m1_t vnot_v_i16m1_m (vbool16_t mask, vint16m1_t maskedoff,
    vint16m1_t op1, size_t vl);
vint16m2_t vnot_v_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, size_t vl);
vint16m4_t vnot_v_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, size_t vl);
vint16m8_t vnot_v_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, size_t vl);
vint32m1_t vnot_v_i32m1_m (vbool32_t mask, vint32m1_t maskedoff,
    vint32m1_t op1, size_t vl);
vint32m2_t vnot_v_i32m2_m (vbool16_t mask, vint32m2_t maskedoff,
    vint32m2_t op1, size_t vl);
vint32m4_t vnot_v_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, size_t vl);
vint32m8_t vnot_v_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, size_t vl);
vint64m1_t vnot_v_i64m1_m (vbool64_t mask, vint64m1_t maskedoff,
    vint64m1_t op1, size_t vl);
vint64m2_t vnot_v_i64m2_m (vbool32_t mask, vint64m2_t maskedoff,
    vint64m2_t op1, size_t vl);
vint64m4_t vnot_v_i64m4_m (vbool16_t mask, vint64m4_t maskedoff,
    vint64m4_t op1, size_t vl);

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```

vint64m8_t vnot_v_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, size_t vl);
vuint8m1_t vnot_v_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, size_t vl);
vuint8m2_t vnot_v_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, size_t vl);
vuint8m4_t vnot_v_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, size_t vl);
vuint8m8_t vnot_v_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, size_t vl);
vuint16m1_t vnot_v_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, size_t vl);
vuint16m2_t vnot_v_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, size_t vl);
vuint16m4_t vnot_v_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, size_t vl);
vuint16m8_t vnot_v_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, size_t vl);
vuint32m1_t vnot_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, size_t vl);
vuint32m2_t vnot_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, size_t vl);
vuint32m4_t vnot_v_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, size_t vl);
vuint32m8_t vnot_v_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, size_t vl);
vuint64m1_t vnot_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, size_t vl);
vuint64m2_t vnot_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, size_t vl);
vuint64m4_t vnot_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, size_t vl);
vuint64m8_t vnot_v_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, size_t vl);

```

## Vector Single-Width Bit Shift Functions:

### Prototypes:

```

vint8m1_t vsll_vv_i8m1 (vint8m1_t op1, vuint8m1_t shift, size_t
    vl);
vint8m1_t vsll_vx_i8m1 (vint8m1_t op1, size_t shift, size_t vl);
vint8m2_t vsll_vv_i8m2 (vint8m2_t op1, vuint8m2_t shift, size_t
    vl);
vint8m2_t vsll_vx_i8m2 (vint8m2_t op1, size_t shift, size_t vl);

```

```

vint8m4_t vsll_vv_i8m4 (vint8m4_t op1, vuint8m4_t shift, size_t
    vl);
vint8m4_t vsll_vx_i8m4 (vint8m4_t op1, size_t shift, size_t vl);
vint8m8_t vsll_vv_i8m8 (vint8m8_t op1, vuint8m8_t shift, size_t
    vl);
vint8m8_t vsll_vx_i8m8 (vint8m8_t op1, size_t shift, size_t vl);
vint16m1_t vsll_vv_i16m1 (vint16m1_t op1, vuint16m1_t shift,
    size_t vl);
vint16m1_t vsll_vx_i16m1 (vint16m1_t op1, size_t shift, size_t
    vl);
vint16m2_t vsll_vv_i16m2 (vint16m2_t op1, vuint16m2_t shift,
    size_t vl);
vint16m2_t vsll_vx_i16m2 (vint16m2_t op1, size_t shift, size_t
    vl);
vint16m4_t vsll_vv_i16m4 (vint16m4_t op1, vuint16m4_t shift,
    size_t vl);
vint16m4_t vsll_vx_i16m4 (vint16m4_t op1, size_t shift, size_t
    vl);
vint16m8_t vsll_vv_i16m8 (vint16m8_t op1, vuint16m8_t shift,
    size_t vl);
vint16m8_t vsll_vx_i16m8 (vint16m8_t op1, size_t shift, size_t
    vl);
vint32m1_t vsll_vv_i32m1 (vint32m1_t op1, vuint32m1_t shift,
    size_t vl);
vint32m1_t vsll_vx_i32m1 (vint32m1_t op1, size_t shift, size_t
    vl);
vint32m2_t vsll_vv_i32m2 (vint32m2_t op1, vuint32m2_t shift,
    size_t vl);
vint32m2_t vsll_vx_i32m2 (vint32m2_t op1, size_t shift, size_t
    vl);
vint32m4_t vsll_vv_i32m4 (vint32m4_t op1, vuint32m4_t shift,
    size_t vl);
vint32m4_t vsll_vx_i32m4 (vint32m4_t op1, size_t shift, size_t
    vl);
vint32m8_t vsll_vv_i32m8 (vint32m8_t op1, vuint32m8_t shift,
    size_t vl);
vint32m8_t vsll_vx_i32m8 (vint32m8_t op1, size_t shift, size_t
    vl);
vint64m1_t vsll_vv_i64m1 (vint64m1_t op1, vuint64m1_t shift,
    size_t vl);
vint64m1_t vsll_vx_i64m1 (vint64m1_t op1, size_t shift, size_t
    vl);
vint64m2_t vsll_vv_i64m2 (vint64m2_t op1, vuint64m2_t shift,
    size_t vl);
vint64m2_t vsll_vx_i64m2 (vint64m2_t op1, size_t shift, size_t
    vl);

```

```

vint64m4_t vsll_vv_i64m4 (vint64m4_t op1, vuint64m4_t shift,
    size_t vl);
vint64m4_t vsll_vx_i64m4 (vint64m4_t op1, size_t shift, size_t
    vl);
vint64m8_t vsll_vv_i64m8 (vint64m8_t op1, vuint64m8_t shift,
    size_t vl);
vint64m8_t vsll_vx_i64m8 (vint64m8_t op1, size_t shift, size_t
    vl);
vuint8m1_t vsll_vv_u8m1 (vuint8m1_t op1, vuint8m1_t shift,
    size_t vl);
vuint8m1_t vsll_vx_u8m1 (vuint8m1_t op1, size_t shift, size_t
    vl);
vuint8m2_t vsll_vv_u8m2 (vuint8m2_t op1, vuint8m2_t shift,
    size_t vl);
vuint8m2_t vsll_vx_u8m2 (vuint8m2_t op1, size_t shift, size_t
    vl);
vuint8m4_t vsll_vv_u8m4 (vuint8m4_t op1, vuint8m4_t shift,
    size_t vl);
vuint8m4_t vsll_vx_u8m4 (vuint8m4_t op1, size_t shift, size_t
    vl);
vuint8m8_t vsll_vv_u8m8 (vuint8m8_t op1, vuint8m8_t shift,
    size_t vl);
vuint8m8_t vsll_vx_u8m8 (vuint8m8_t op1, size_t shift, size_t
    vl);
vuint16m1_t vsll_vv_u16m1 (vuint16m1_t op1, vuint16m1_t shift,
    size_t vl);
vuint16m1_t vsll_vx_u16m1 (vuint16m1_t op1, size_t shift, size_t
    vl);
vuint16m2_t vsll_vv_u16m2 (vuint16m2_t op1, vuint16m2_t shift,
    size_t vl);
vuint16m2_t vsll_vx_u16m2 (vuint16m2_t op1, size_t shift, size_t
    vl);
vuint16m4_t vsll_vv_u16m4 (vuint16m4_t op1, vuint16m4_t shift,
    size_t vl);
vuint16m4_t vsll_vx_u16m4 (vuint16m4_t op1, size_t shift, size_t
    vl);
vuint16m8_t vsll_vv_u16m8 (vuint16m8_t op1, vuint16m8_t shift,
    size_t vl);
vuint16m8_t vsll_vx_u16m8 (vuint16m8_t op1, size_t shift, size_t
    vl);
vuint32m1_t vsll_vv_u32m1 (vuint32m1_t op1, vuint32m1_t shift,
    size_t vl);
vuint32m1_t vsll_vx_u32m1 (vuint32m1_t op1, size_t shift, size_t
    vl);
vuint32m2_t vsll_vv_u32m2 (vuint32m2_t op1, vuint32m2_t shift,
    size_t vl);

```

```

vuint32m2_t vsll_vx_u32m2 (vuint32m2_t op1, size_t shift, size_t
    vl);
vuint32m4_t vsll_vv_u32m4 (vuint32m4_t op1, vuint32m4_t shift,
    size_t vl);
vuint32m4_t vsll_vx_u32m4 (vuint32m4_t op1, size_t shift, size_t
    vl);
vuint32m8_t vsll_vv_u32m8 (vuint32m8_t op1, vuint32m8_t shift,
    size_t vl);
vuint32m8_t vsll_vx_u32m8 (vuint32m8_t op1, size_t shift, size_t
    vl);
vuint64m1_t vsll_vv_u64m1 (vuint64m1_t op1, vuint64m1_t shift,
    size_t vl);
vuint64m1_t vsll_vx_u64m1 (vuint64m1_t op1, size_t shift, size_t
    vl);
vuint64m2_t vsll_vv_u64m2 (vuint64m2_t op1, vuint64m2_t shift,
    size_t vl);
vuint64m2_t vsll_vx_u64m2 (vuint64m2_t op1, size_t shift, size_t
    vl);
vuint64m4_t vsll_vv_u64m4 (vuint64m4_t op1, vuint64m4_t shift,
    size_t vl);
vuint64m4_t vsll_vx_u64m4 (vuint64m4_t op1, size_t shift, size_t
    vl);
vuint64m8_t vsll_vv_u64m8 (vuint64m8_t op1, vuint64m8_t shift,
    size_t vl);
vuint64m8_t vsll_vx_u64m8 (vuint64m8_t op1, size_t shift, size_t
    vl);
vuint8m1_t vsrl_vv_u8m1 (vuint8m1_t op1, vuint8m1_t shift,
    size_t vl);
vuint8m1_t vsrl_vx_u8m1 (vuint8m1_t op1, size_t shift, size_t
    vl);
vuint8m2_t vsrl_vv_u8m2 (vuint8m2_t op1, vuint8m2_t shift,
    size_t vl);
vuint8m2_t vsrl_vx_u8m2 (vuint8m2_t op1, size_t shift, size_t
    vl);
vuint8m4_t vsrl_vv_u8m4 (vuint8m4_t op1, vuint8m4_t shift,
    size_t vl);
vuint8m4_t vsrl_vx_u8m4 (vuint8m4_t op1, size_t shift, size_t
    vl);
vuint8m8_t vsrl_vv_u8m8 (vuint8m8_t op1, vuint8m8_t shift,
    size_t vl);
vuint8m8_t vsrl_vx_u8m8 (vuint8m8_t op1, size_t shift, size_t
    vl);
vuint16m1_t vsrl_vv_u16m1 (vuint16m1_t op1, vuint16m1_t shift,
    size_t vl);
vuint16m1_t vsrl_vx_u16m1 (vuint16m1_t op1, size_t shift, size_t
    vl);

```

```

vuint16m2_t vsrl_vv_u16m2 (vuint16m2_t op1, vuint16m2_t shift,
    size_t vl);
vuint16m2_t vsrl_vx_u16m2 (vuint16m2_t op1, size_t shift, size_t
    vl);
vuint16m4_t vsrl_vv_u16m4 (vuint16m4_t op1, vuint16m4_t shift,
    size_t vl);
vuint16m4_t vsrl_vx_u16m4 (vuint16m4_t op1, size_t shift, size_t
    vl);
vuint16m8_t vsrl_vv_u16m8 (vuint16m8_t op1, vuint16m8_t shift,
    size_t vl);
vuint16m8_t vsrl_vx_u16m8 (vuint16m8_t op1, size_t shift, size_t
    vl);
vuint32m1_t vsrl_vv_u32m1 (vuint32m1_t op1, vuint32m1_t shift,
    size_t vl);
vuint32m1_t vsrl_vx_u32m1 (vuint32m1_t op1, size_t shift, size_t
    vl);
vuint32m2_t vsrl_vv_u32m2 (vuint32m2_t op1, vuint32m2_t shift,
    size_t vl);
vuint32m2_t vsrl_vx_u32m2 (vuint32m2_t op1, size_t shift, size_t
    vl);
vuint32m4_t vsrl_vv_u32m4 (vuint32m4_t op1, vuint32m4_t shift,
    size_t vl);
vuint32m4_t vsrl_vx_u32m4 (vuint32m4_t op1, size_t shift, size_t
    vl);
vuint32m8_t vsrl_vv_u32m8 (vuint32m8_t op1, vuint32m8_t shift,
    size_t vl);
vuint32m8_t vsrl_vx_u32m8 (vuint32m8_t op1, size_t shift, size_t
    vl);
vuint64m1_t vsrl_vv_u64m1 (vuint64m1_t op1, vuint64m1_t shift,
    size_t vl);
vuint64m1_t vsrl_vx_u64m1 (vuint64m1_t op1, size_t shift, size_t
    vl);
vuint64m2_t vsrl_vv_u64m2 (vuint64m2_t op1, vuint64m2_t shift,
    size_t vl);
vuint64m2_t vsrl_vx_u64m2 (vuint64m2_t op1, size_t shift, size_t
    vl);
vuint64m4_t vsrl_vv_u64m4 (vuint64m4_t op1, vuint64m4_t shift,
    size_t vl);
vuint64m4_t vsrl_vx_u64m4 (vuint64m4_t op1, size_t shift, size_t
    vl);
vuint64m8_t vsrl_vv_u64m8 (vuint64m8_t op1, vuint64m8_t shift,
    size_t vl);
vuint64m8_t vsrl_vx_u64m8 (vuint64m8_t op1, size_t shift, size_t
    vl);
vint8m1_t vsra_vv_i8m1 (vint8m1_t op1, vuint8m1_t shift, size_t
    vl);

```

```

vint8m1_t vsra_vx_i8m1 (vint8m1_t op1, size_t shift, size_t vl);
vint8m2_t vsra_vv_i8m2 (vint8m2_t op1, vuint8m2_t shift, size_t
    vl);
vint8m2_t vsra_vx_i8m2 (vint8m2_t op1, size_t shift, size_t vl);
vint8m4_t vsra_vv_i8m4 (vint8m4_t op1, vuint8m4_t shift, size_t
    vl);
vint8m4_t vsra_vx_i8m4 (vint8m4_t op1, size_t shift, size_t vl);
vint8m8_t vsra_vv_i8m8 (vint8m8_t op1, vuint8m8_t shift, size_t
    vl);
vint8m8_t vsra_vx_i8m8 (vint8m8_t op1, size_t shift, size_t vl);
vint16m1_t vsra_vv_i16m1 (vint16m1_t op1, vuint16m1_t shift,
    size_t vl);
vint16m1_t vsra_vx_i16m1 (vint16m1_t op1, size_t shift, size_t
    vl);
vint16m2_t vsra_vv_i16m2 (vint16m2_t op1, vuint16m2_t shift,
    size_t vl);
vint16m2_t vsra_vx_i16m2 (vint16m2_t op1, size_t shift, size_t
    vl);
vint16m4_t vsra_vv_i16m4 (vint16m4_t op1, vuint16m4_t shift,
    size_t vl);
vint16m4_t vsra_vx_i16m4 (vint16m4_t op1, size_t shift, size_t
    vl);
vint16m8_t vsra_vv_i16m8 (vint16m8_t op1, vuint16m8_t shift,
    size_t vl);
vint16m8_t vsra_vx_i16m8 (vint16m8_t op1, size_t shift, size_t
    vl);
vint32m1_t vsra_vv_i32m1 (vint32m1_t op1, vuint32m1_t shift,
    size_t vl);
vint32m1_t vsra_vx_i32m1 (vint32m1_t op1, size_t shift, size_t
    vl);
vint32m2_t vsra_vv_i32m2 (vint32m2_t op1, vuint32m2_t shift,
    size_t vl);
vint32m2_t vsra_vx_i32m2 (vint32m2_t op1, size_t shift, size_t
    vl);
vint32m4_t vsra_vv_i32m4 (vint32m4_t op1, vuint32m4_t shift,
    size_t vl);
vint32m4_t vsra_vx_i32m4 (vint32m4_t op1, size_t shift, size_t
    vl);
vint32m8_t vsra_vv_i32m8 (vint32m8_t op1, vuint32m8_t shift,
    size_t vl);
vint32m8_t vsra_vx_i32m8 (vint32m8_t op1, size_t shift, size_t
    vl);
vint64m1_t vsra_vv_i64m1 (vint64m1_t op1, vuint64m1_t shift,
    size_t vl);
vint64m1_t vsra_vx_i64m1 (vint64m1_t op1, size_t shift, size_t
    vl);

```

```

vint64m2_t vsra_vv_i64m2 (vint64m2_t op1, vuint64m2_t shift,
    size_t vl);
vint64m2_t vsra_vx_i64m2 (vint64m2_t op1, size_t shift, size_t
    vl);
vint64m4_t vsra_vv_i64m4 (vint64m4_t op1, vuint64m4_t shift,
    size_t vl);
vint64m4_t vsra_vx_i64m4 (vint64m4_t op1, size_t shift, size_t
    vl);
vint64m8_t vsra_vv_i64m8 (vint64m8_t op1, vuint64m8_t shift,
    size_t vl);
vint64m8_t vsra_vx_i64m8 (vint64m8_t op1, size_t shift, size_t
    vl);
// masked functions
vint8m1_t vsll_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vuint8m1_t shift, size_t vl);
vint8m1_t vsll_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, size_t shift, size_t vl);
vint8m2_t vsll_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vuint8m2_t shift, size_t vl);
vint8m2_t vsll_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, size_t shift, size_t vl);
vint8m4_t vsll_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vuint8m4_t shift, size_t vl);
vint8m4_t vsll_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, size_t shift, size_t vl);
vint8m8_t vsll_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vuint8m8_t shift, size_t vl);
vint8m8_t vsll_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, size_t shift, size_t vl);
vint16m1_t vsll_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vuint16m1_t shift, size_t vl);
vint16m1_t vsll_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, size_t shift, size_t vl);
vint16m2_t vsll_vv_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, vuint16m2_t shift, size_t vl);
vint16m2_t vsll_vx_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, size_t shift, size_t vl);
vint16m4_t vsll_vv_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, vuint16m4_t shift, size_t vl);
vint16m4_t vsll_vx_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, size_t shift, size_t vl);
vint16m8_t vsll_vv_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, vuint16m8_t shift, size_t vl);
vint16m8_t vsll_vx_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, size_t shift, size_t vl);

```

```

vint32m1_t vsll_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vuint32m1_t shift, size_t vl);
vint32m1_t vsll_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, size_t shift, size_t vl);
vint32m2_t vsll_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vuint32m2_t shift, size_t vl);
vint32m2_t vsll_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, size_t shift, size_t vl);
vint32m4_t vsll_vv_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, vuint32m4_t shift, size_t vl);
vint32m4_t vsll_vx_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, size_t shift, size_t vl);
vint32m8_t vsll_vv_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, vuint32m8_t shift, size_t vl);
vint32m8_t vsll_vx_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, size_t shift, size_t vl);
vint64m1_t vsll_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vuint64m1_t shift, size_t vl);
vint64m1_t vsll_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, size_t shift, size_t vl);
vint64m2_t vsll_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vuint64m2_t shift, size_t vl);
vint64m2_t vsll_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, size_t shift, size_t vl);
vint64m4_t vsll_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vuint64m4_t shift, size_t vl);
vint64m4_t vsll_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, size_t shift, size_t vl);
vint64m8_t vsll_vv_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, vuint64m8_t shift, size_t vl);
vint64m8_t vsll_vx_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, size_t shift, size_t vl);
vuint8m1_t vsll_vv_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, vuint8m1_t shift, size_t vl);
vuint8m1_t vsll_vx_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, size_t shift, size_t vl);
vuint8m2_t vsll_vv_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, vuint8m2_t shift, size_t vl);
vuint8m2_t vsll_vx_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, size_t shift, size_t vl);
vuint8m4_t vsll_vv_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, vuint8m4_t shift, size_t vl);
vuint8m4_t vsll_vx_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, size_t shift, size_t vl);
vuint8m8_t vsll_vv_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, vuint8m8_t shift, size_t vl);

```



```

vuint8m8_t vsll_vx_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, size_t shift, size_t vl);
vuint16m1_t vsll_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t shift, size_t vl);
vuint16m1_t vsll_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, size_t shift, size_t vl);
vuint16m2_t vsll_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t shift, size_t vl);
vuint16m2_t vsll_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, size_t shift, size_t vl);
vuint16m4_t vsll_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t shift, size_t vl);
vuint16m4_t vsll_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, size_t shift, size_t vl);
vuint16m8_t vsll_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t shift, size_t vl);
vuint16m8_t vsll_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, size_t shift, size_t vl);
vuint32m1_t vsll_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t shift, size_t vl);
vuint32m1_t vsll_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, size_t shift, size_t vl);
vuint32m2_t vsll_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t shift, size_t vl);
vuint32m2_t vsll_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, size_t shift, size_t vl);
vuint32m4_t vsll_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t shift, size_t vl);
vuint32m4_t vsll_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, size_t shift, size_t vl);
vuint32m8_t vsll_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t shift, size_t vl);
vuint32m8_t vsll_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, size_t shift, size_t vl);
vuint64m1_t vsll_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t shift, size_t vl);
vuint64m1_t vsll_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, size_t shift, size_t vl);
vuint64m2_t vsll_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t shift, size_t vl);
vuint64m2_t vsll_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, size_t shift, size_t vl);
vuint64m4_t vsll_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t shift, size_t vl);
vuint64m4_t vsll_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, size_t shift, size_t vl);

```

```

vuint64m8_t vsll_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t shift, size_t vl);
vuint64m8_t vsll_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, size_t shift, size_t vl);
vuint8m1_t vsrl_vv_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, vuint8m1_t shift, size_t vl);
vuint8m1_t vsrl_vx_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, size_t shift, size_t vl);
vuint8m2_t vsrl_vv_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, vuint8m2_t shift, size_t vl);
vuint8m2_t vsrl_vx_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, size_t shift, size_t vl);
vuint8m4_t vsrl_vv_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, vuint8m4_t shift, size_t vl);
vuint8m4_t vsrl_vx_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, size_t shift, size_t vl);
vuint8m8_t vsrl_vv_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, vuint8m8_t shift, size_t vl);
vuint8m8_t vsrl_vx_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, size_t shift, size_t vl);
vuint16m1_t vsrl_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t shift, size_t vl);
vuint16m1_t vsrl_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, size_t shift, size_t vl);
vuint16m2_t vsrl_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t shift, size_t vl);
vuint16m2_t vsrl_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, size_t shift, size_t vl);
vuint16m4_t vsrl_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t shift, size_t vl);
vuint16m4_t vsrl_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, size_t shift, size_t vl);
vuint16m8_t vsrl_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t shift, size_t vl);
vuint16m8_t vsrl_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, size_t shift, size_t vl);
vuint32m1_t vsrl_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t shift, size_t vl);
vuint32m1_t vsrl_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, size_t shift, size_t vl);
vuint32m2_t vsrl_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t shift, size_t vl);
vuint32m2_t vsrl_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, size_t shift, size_t vl);
vuint32m4_t vsrl_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t shift, size_t vl);

```

```

vuint32m4_t vsrl_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, size_t shift, size_t vl);
vuint32m8_t vsrl_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t shift, size_t vl);
vuint32m8_t vsrl_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, size_t shift, size_t vl);
vuint64m1_t vsrl_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t shift, size_t vl);
vuint64m1_t vsrl_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, size_t shift, size_t vl);
vuint64m2_t vsrl_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t shift, size_t vl);
vuint64m2_t vsrl_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, size_t shift, size_t vl);
vuint64m4_t vsrl_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t shift, size_t vl);
vuint64m4_t vsrl_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, size_t shift, size_t vl);
vuint64m8_t vsrl_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t shift, size_t vl);
vuint64m8_t vsrl_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, size_t shift, size_t vl);
vint8m1_t vsra_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vuint8m1_t shift, size_t vl);
vint8m1_t vsra_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, size_t shift, size_t vl);
vint8m2_t vsra_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vuint8m2_t shift, size_t vl);
vint8m2_t vsra_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, size_t shift, size_t vl);
vint8m4_t vsra_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vuint8m4_t shift, size_t vl);
vint8m4_t vsra_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, size_t shift, size_t vl);
vint8m8_t vsra_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vuint8m8_t shift, size_t vl);
vint8m8_t vsra_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, size_t shift, size_t vl);
vint16m1_t vsra_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vuint16m1_t shift, size_t vl);
vint16m1_t vsra_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, size_t shift, size_t vl);
vint16m2_t vsra_vv_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, vuint16m2_t shift, size_t vl);
vint16m2_t vsra_vx_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, size_t shift, size_t vl);

```

```

vint16m4_t vsra_vv_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, vuint16m4_t shift, size_t vl);
vint16m4_t vsra_vx_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, size_t shift, size_t vl);
vint16m8_t vsra_vv_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, vuint16m8_t shift, size_t vl);
vint16m8_t vsra_vx_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, size_t shift, size_t vl);
vint32m1_t vsra_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vuint32m1_t shift, size_t vl);
vint32m1_t vsra_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, size_t shift, size_t vl);
vint32m2_t vsra_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vuint32m2_t shift, size_t vl);
vint32m2_t vsra_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, size_t shift, size_t vl);
vint32m4_t vsra_vv_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, vuint32m4_t shift, size_t vl);
vint32m4_t vsra_vx_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, size_t shift, size_t vl);
vint32m8_t vsra_vv_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, vuint32m8_t shift, size_t vl);
vint32m8_t vsra_vx_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, size_t shift, size_t vl);
vint64m1_t vsra_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vuint64m1_t shift, size_t vl);
vint64m1_t vsra_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, size_t shift, size_t vl);
vint64m2_t vsra_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vuint64m2_t shift, size_t vl);
vint64m2_t vsra_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, size_t shift, size_t vl);
vint64m4_t vsra_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vuint64m4_t shift, size_t vl);
vint64m4_t vsra_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, size_t shift, size_t vl);
vint64m8_t vsra_vv_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, vuint64m8_t shift, size_t vl);
vint64m8_t vsra_vx_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, size_t shift, size_t vl);

```

## Vector Narrowing Integer Right Shift Functions:

### Prototypes:

```

vuint8m1_t vnsrl_wv_u8m1 (vuint16m2_t op1, vuint8m1_t shift,
    size_t vl);
vuint8m1_t vnsrl_wx_u8m1 (vuint16m2_t op1, size_t shift, size_t
    vl);
vuint8m2_t vnsrl_wv_u8m2 (vuint16m4_t op1, vuint8m2_t shift,
    size_t vl);
vuint8m2_t vnsrl_wx_u8m2 (vuint16m4_t op1, size_t shift, size_t
    vl);
vuint8m4_t vnsrl_wv_u8m4 (vuint16m8_t op1, vuint8m4_t shift,
    size_t vl);
vuint8m4_t vnsrl_wx_u8m4 (vuint16m8_t op1, size_t shift, size_t
    vl);
vuint16m1_t vnsrl_wv_u16m1 (vuint32m2_t op1, vuint16m1_t shift,
    size_t vl);
vuint16m1_t vnsrl_wx_u16m1 (vuint32m2_t op1, size_t shift,
    size_t vl);
vuint16m2_t vnsrl_wv_u16m2 (vuint32m4_t op1, vuint16m2_t shift,
    size_t vl);
vuint16m2_t vnsrl_wx_u16m2 (vuint32m4_t op1, size_t shift,
    size_t vl);
vuint16m4_t vnsrl_wv_u16m4 (vuint32m8_t op1, vuint16m4_t shift,
    size_t vl);
vuint16m4_t vnsrl_wx_u16m4 (vuint32m8_t op1, size_t shift,
    size_t vl);
vuint32m1_t vnsrl_wv_u32m1 (vuint64m2_t op1, vuint32m1_t shift,
    size_t vl);
vuint32m1_t vnsrl_wx_u32m1 (vuint64m2_t op1, size_t shift,
    size_t vl);
vuint32m2_t vnsrl_wv_u32m2 (vuint64m4_t op1, vuint32m2_t shift,
    size_t vl);
vuint32m2_t vnsrl_wx_u32m2 (vuint64m4_t op1, size_t shift,
    size_t vl);
vuint32m4_t vnsrl_wv_u32m4 (vuint64m8_t op1, vuint32m4_t shift,
    size_t vl);
vuint32m4_t vnsrl_wx_u32m4 (vuint64m8_t op1, size_t shift,
    size_t vl);
vint8m1_t vnsra_wv_i8m1 (vint16m2_t op1, vuint8m1_t shift,
    size_t vl);
vint8m1_t vnsra_wx_i8m1 (vint16m2_t op1, size_t shift, size_t
    vl);
vint8m2_t vnsra_wv_i8m2 (vint16m4_t op1, vuint8m2_t shift,
    size_t vl);
vint8m2_t vnsra_wx_i8m2 (vint16m4_t op1, size_t shift, size_t
    vl);
vint8m4_t vnsra_wv_i8m4 (vint16m8_t op1, vuint8m4_t shift,
    size_t vl);

```

```

vint8m4_t vnsra_wx_i8m4 (vint16m8_t op1, size_t shift, size_t
    vl);
vint16m1_t vnsra_wv_i16m1 (vint32m2_t op1, vuint16m1_t shift,
    size_t vl);
vint16m1_t vnsra_wx_i16m1 (vint32m2_t op1, size_t shift, size_t
    vl);
vint16m2_t vnsra_wv_i16m2 (vint32m4_t op1, vuint16m2_t shift,
    size_t vl);
vint16m2_t vnsra_wx_i16m2 (vint32m4_t op1, size_t shift, size_t
    vl);
vint16m4_t vnsra_wv_i16m4 (vint32m8_t op1, vuint16m4_t shift,
    size_t vl);
vint16m4_t vnsra_wx_i16m4 (vint32m8_t op1, size_t shift, size_t
    vl);
vint32m1_t vnsra_wv_i32m1 (vint64m2_t op1, vuint32m1_t shift,
    size_t vl);
vint32m1_t vnsra_wx_i32m1 (vint64m2_t op1, size_t shift, size_t
    vl);
vint32m2_t vnsra_wv_i32m2 (vint64m4_t op1, vuint32m2_t shift,
    size_t vl);
vint32m2_t vnsra_wx_i32m2 (vint64m4_t op1, size_t shift, size_t
    vl);
vint32m4_t vnsra_wv_i32m4 (vint64m8_t op1, vuint32m4_t shift,
    size_t vl);
vint32m4_t vnsra_wx_i32m4 (vint64m8_t op1, size_t shift, size_t
    vl);
// masked functions
vuint8m1_t vnsrl_wv_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint16m2_t op1, vuint8m1_t shift, size_t vl);
vuint8m1_t vnsrl_wx_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint16m2_t op1, size_t shift, size_t vl);
vuint8m2_t vnsrl_wv_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint16m4_t op1, vuint8m2_t shift, size_t vl);
vuint8m2_t vnsrl_wx_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint16m4_t op1, size_t shift, size_t vl);
vuint8m4_t vnsrl_wv_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint16m8_t op1, vuint8m4_t shift, size_t vl);
vuint8m4_t vnsrl_wx_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint16m8_t op1, size_t shift, size_t vl);
vuint16m1_t vnsrl_wv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint32m2_t op1, vuint16m1_t shift, size_t vl);
vuint16m1_t vnsrl_wx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint32m2_t op1, size_t shift, size_t vl);
vuint16m2_t vnsrl_wv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint32m4_t op1, vuint16m2_t shift, size_t vl);

```

```

vuint16m2_t vnsrl_wx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint32m4_t op1, size_t shift, size_t vl);
vuint16m4_t vnsrl_wv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint32m8_t op1, vuint16m4_t shift, size_t vl);
vuint16m4_t vnsrl_wx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint32m8_t op1, size_t shift, size_t vl);
vuint32m1_t vnsrl_wv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint64m2_t op1, vuint32m1_t shift, size_t vl);
vuint32m1_t vnsrl_wx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint64m2_t op1, size_t shift, size_t vl);
vuint32m2_t vnsrl_wv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint64m4_t op1, vuint32m2_t shift, size_t vl);
vuint32m2_t vnsrl_wx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint64m4_t op1, size_t shift, size_t vl);
vuint32m4_t vnsrl_wv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint64m8_t op1, vuint32m4_t shift, size_t vl);
vuint32m4_t vnsrl_wx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint64m8_t op1, size_t shift, size_t vl);
vint8m1_t vnsra_wv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint16m2_t op1, vuint8m1_t shift, size_t vl);
vint8m1_t vnsra_wx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint16m2_t op1, size_t shift, size_t vl);
vint8m2_t vnsra_wv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint16m4_t op1, vuint8m2_t shift, size_t vl);
vint8m2_t vnsra_wx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint16m4_t op1, size_t shift, size_t vl);
vint8m4_t vnsra_wv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint16m8_t op1, vuint8m4_t shift, size_t vl);
vint8m4_t vnsra_wx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint16m8_t op1, size_t shift, size_t vl);
vint16m1_t vnsra_wv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint32m2_t op1, vuint16m1_t shift, size_t vl);
vint16m1_t vnsra_wx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint32m2_t op1, size_t shift, size_t vl);
vint16m2_t vnsra_wv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint32m4_t op1, vuint16m2_t shift, size_t vl);
vint16m2_t vnsra_wx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint32m4_t op1, size_t shift, size_t vl);
vint16m4_t vnsra_wv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint32m8_t op1, vuint16m4_t shift, size_t vl);
vint16m4_t vnsra_wx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint32m8_t op1, size_t shift, size_t vl);
vint32m1_t vnsra_wv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint64m2_t op1, vuint32m1_t shift, size_t vl);
vint32m1_t vnsra_wx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint64m2_t op1, size_t shift, size_t vl);

```

```

vint32m2_t vnsra_wv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint64m4_t op1, vuint32m2_t shift, size_t vl);
vint32m2_t vnsra_wx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint64m4_t op1, size_t shift, size_t vl);
vint32m4_t vnsra_wv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint64m8_t op1, vuint32m4_t shift, size_t vl);
vint32m4_t vnsra_wx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint64m8_t op1, size_t shift, size_t vl);

```

## Vector Integer Comparison Functions:

### Prototypes:

```

vbool8_t vmseq_vv_i8m1_b8 (vint8m1_t op1, vint8m1_t op2, size_t
    vl);
vbool8_t vmseq_vx_i8m1_b8 (vint8m1_t op1, int8_t op2, size_t vl);
vbool4_t vmseq_vv_i8m2_b4 (vint8m2_t op1, vint8m2_t op2, size_t
    vl);
vbool4_t vmseq_vx_i8m2_b4 (vint8m2_t op1, int8_t op2, size_t vl);
vbool2_t vmseq_vv_i8m4_b2 (vint8m4_t op1, vint8m4_t op2, size_t
    vl);
vbool2_t vmseq_vx_i8m4_b2 (vint8m4_t op1, int8_t op2, size_t vl);
vbool1_t vmseq_vv_i8m8_b1 (vint8m8_t op1, vint8m8_t op2, size_t
    vl);
vbool1_t vmseq_vx_i8m8_b1 (vint8m8_t op1, int8_t op2, size_t vl);
vbool16_t vmseq_vv_i16m1_b16 (vint16m1_t op1, vint16m1_t op2,
    size_t vl);
vbool16_t vmseq_vx_i16m1_b16 (vint16m1_t op1, int16_t op2,
    size_t vl);
vbool8_t vmseq_vv_i16m2_b8 (vint16m2_t op1, vint16m2_t op2,
    size_t vl);
vbool8_t vmseq_vx_i16m2_b8 (vint16m2_t op1, int16_t op2, size_t
    vl);
vbool4_t vmseq_vv_i16m4_b4 (vint16m4_t op1, vint16m4_t op2,
    size_t vl);
vbool4_t vmseq_vx_i16m4_b4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vbool2_t vmseq_vv_i16m8_b2 (vint16m8_t op1, vint16m8_t op2,
    size_t vl);
vbool2_t vmseq_vx_i16m8_b2 (vint16m8_t op1, int16_t op2, size_t
    vl);
vbool32_t vmseq_vv_i32m1_b32 (vint32m1_t op1, vint32m1_t op2,
    size_t vl);
vbool32_t vmseq_vx_i32m1_b32 (vint32m1_t op1, int32_t op2,
    size_t vl);

```



```

vbool16_t vmseq_vv_i32m2_b16 (vint32m2_t op1, vint32m2_t op2,
    size_t vl);
vbool16_t vmseq_vx_i32m2_b16 (vint32m2_t op1, int32_t op2,
    size_t vl);
vbool8_t vmseq_vv_i32m4_b8 (vint32m4_t op1, vint32m4_t op2,
    size_t vl);
vbool8_t vmseq_vx_i32m4_b8 (vint32m4_t op1, int32_t op2, size_t
    vl);
vbool4_t vmseq_vv_i32m8_b4 (vint32m8_t op1, vint32m8_t op2,
    size_t vl);
vbool4_t vmseq_vx_i32m8_b4 (vint32m8_t op1, int32_t op2, size_t
    vl);
vbool64_t vmseq_vv_i64m1_b64 (vint64m1_t op1, vint64m1_t op2,
    size_t vl);
vbool64_t vmseq_vx_i64m1_b64 (vint64m1_t op1, int64_t op2,
    size_t vl);
vbool32_t vmseq_vv_i64m2_b32 (vint64m2_t op1, vint64m2_t op2,
    size_t vl);
vbool32_t vmseq_vx_i64m2_b32 (vint64m2_t op1, int64_t op2,
    size_t vl);
vbool16_t vmseq_vv_i64m4_b16 (vint64m4_t op1, vint64m4_t op2,
    size_t vl);
vbool16_t vmseq_vx_i64m4_b16 (vint64m4_t op1, int64_t op2,
    size_t vl);
vbool8_t vmseq_vv_i64m8_b8 (vint64m8_t op1, vint64m8_t op2,
    size_t vl);
vbool8_t vmseq_vx_i64m8_b8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vbool8_t vmseq_vv_u8m1_b8 (vuint8m1_t op1, vuint8m1_t op2,
    size_t vl);
vbool8_t vmseq_vx_u8m1_b8 (vuint8m1_t op1, uint8_t op2, size_t
    vl);
vbool4_t vmseq_vv_u8m2_b4 (vuint8m2_t op1, vuint8m2_t op2,
    size_t vl);
vbool4_t vmseq_vx_u8m2_b4 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vbool2_t vmseq_vv_u8m4_b2 (vuint8m4_t op1, vuint8m4_t op2,
    size_t vl);
vbool2_t vmseq_vx_u8m4_b2 (vuint8m4_t op1, uint8_t op2, size_t
    vl);
vbool1_t vmseq_vv_u8m8_b1 (vuint8m8_t op1, vuint8m8_t op2,
    size_t vl);
vbool1_t vmseq_vx_u8m8_b1 (vuint8m8_t op1, uint8_t op2, size_t
    vl);
vbool16_t vmseq_vv_u16m1_b16 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);

```

```

vbool16_t vmseq_vx_u16m1_b16 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vbool8_t vmseq_vv_u16m2_b8 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vbool8_t vmseq_vx_u16m2_b8 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vbool4_t vmseq_vv_u16m4_b4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vbool4_t vmseq_vx_u16m4_b4 (vuint16m4_t op1, uint16_t op2,
    size_t vl);
vbool2_t vmseq_vv_u16m8_b2 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vbool2_t vmseq_vx_u16m8_b2 (vuint16m8_t op1, uint16_t op2,
    size_t vl);
vbool32_t vmseq_vv_u32m1_b32 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vbool32_t vmseq_vx_u32m1_b32 (vuint32m1_t op1, uint32_t op2,
    size_t vl);
vbool16_t vmseq_vv_u32m2_b16 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vbool16_t vmseq_vx_u32m2_b16 (vuint32m2_t op1, uint32_t op2,
    size_t vl);
vbool8_t vmseq_vv_u32m4_b8 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vbool8_t vmseq_vx_u32m4_b8 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vbool4_t vmseq_vv_u32m8_b4 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vbool4_t vmseq_vx_u32m8_b4 (vuint32m8_t op1, uint32_t op2,
    size_t vl);
vbool64_t vmseq_vv_u64m1_b64 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vbool64_t vmseq_vx_u64m1_b64 (vuint64m1_t op1, uint64_t op2,
    size_t vl);
vbool32_t vmseq_vv_u64m2_b32 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vbool32_t vmseq_vx_u64m2_b32 (vuint64m2_t op1, uint64_t op2,
    size_t vl);
vbool16_t vmseq_vv_u64m4_b16 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vbool16_t vmseq_vx_u64m4_b16 (vuint64m4_t op1, uint64_t op2,
    size_t vl);
vbool8_t vmseq_vv_u64m8_b8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vbool8_t vmseq_vx_u64m8_b8 (vuint64m8_t op1, uint64_t op2,
    size_t vl);

```

```

vbool8_t vmsne_vv_i8m1_b8 (vint8m1_t op1, vint8m1_t op2, size_t
    vl);
vbool8_t vmsne_vx_i8m1_b8 (vint8m1_t op1, int8_t op2, size_t vl);
vbool4_t vmsne_vv_i8m2_b4 (vint8m2_t op1, vint8m2_t op2, size_t
    vl);
vbool4_t vmsne_vx_i8m2_b4 (vint8m2_t op1, int8_t op2, size_t vl);
vbool2_t vmsne_vv_i8m4_b2 (vint8m4_t op1, vint8m4_t op2, size_t
    vl);
vbool2_t vmsne_vx_i8m4_b2 (vint8m4_t op1, int8_t op2, size_t vl);
vbool1_t vmsne_vv_i8m8_b1 (vint8m8_t op1, vint8m8_t op2, size_t
    vl);
vbool1_t vmsne_vx_i8m8_b1 (vint8m8_t op1, int8_t op2, size_t vl);
vbool16_t vmsne_vv_i16m1_b16 (vint16m1_t op1, vint16m1_t op2,
    size_t vl);
vbool16_t vmsne_vx_i16m1_b16 (vint16m1_t op1, int16_t op2,
    size_t vl);
vbool8_t vmsne_vv_i16m2_b8 (vint16m2_t op1, vint16m2_t op2,
    size_t vl);
vbool8_t vmsne_vx_i16m2_b8 (vint16m2_t op1, int16_t op2, size_t
    vl);
vbool4_t vmsne_vv_i16m4_b4 (vint16m4_t op1, vint16m4_t op2,
    size_t vl);
vbool4_t vmsne_vx_i16m4_b4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vbool2_t vmsne_vv_i16m8_b2 (vint16m8_t op1, vint16m8_t op2,
    size_t vl);
vbool2_t vmsne_vx_i16m8_b2 (vint16m8_t op1, int16_t op2, size_t
    vl);
vbool32_t vmsne_vv_i32m1_b32 (vint32m1_t op1, vint32m1_t op2,
    size_t vl);
vbool32_t vmsne_vx_i32m1_b32 (vint32m1_t op1, int32_t op2,
    size_t vl);
vbool16_t vmsne_vv_i32m2_b16 (vint32m2_t op1, vint32m2_t op2,
    size_t vl);
vbool16_t vmsne_vx_i32m2_b16 (vint32m2_t op1, int32_t op2,
    size_t vl);
vbool8_t vmsne_vv_i32m4_b8 (vint32m4_t op1, vint32m4_t op2,
    size_t vl);
vbool8_t vmsne_vx_i32m4_b8 (vint32m4_t op1, int32_t op2, size_t
    vl);
vbool4_t vmsne_vv_i32m8_b4 (vint32m8_t op1, vint32m8_t op2,
    size_t vl);
vbool4_t vmsne_vx_i32m8_b4 (vint32m8_t op1, int32_t op2, size_t
    vl);
vbool64_t vmsne_vv_i64m1_b64 (vint64m1_t op1, vint64m1_t op2,
    size_t vl);

```

```

vbool64_t vmsne_vx_i64m1_b64 (vint64m1_t op1, int64_t op2,
    size_t vl);
vbool32_t vmsne_vv_i64m2_b32 (vint64m2_t op1, vint64m2_t op2,
    size_t vl);
vbool32_t vmsne_vx_i64m2_b32 (vint64m2_t op1, int64_t op2,
    size_t vl);
vbool16_t vmsne_vv_i64m4_b16 (vint64m4_t op1, vint64m4_t op2,
    size_t vl);
vbool16_t vmsne_vx_i64m4_b16 (vint64m4_t op1, int64_t op2,
    size_t vl);
vbool8_t vmsne_vv_i64m8_b8 (vint64m8_t op1, vint64m8_t op2,
    size_t vl);
vbool8_t vmsne_vx_i64m8_b8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vbool8_t vmsne_vv_u8m1_b8 (vuint8m1_t op1, vuint8m1_t op2,
    size_t vl);
vbool8_t vmsne_vx_u8m1_b8 (vuint8m1_t op1, uint8_t op2, size_t
    vl);
vbool4_t vmsne_vv_u8m2_b4 (vuint8m2_t op1, vuint8m2_t op2,
    size_t vl);
vbool4_t vmsne_vx_u8m2_b4 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vbool2_t vmsne_vv_u8m4_b2 (vuint8m4_t op1, vuint8m4_t op2,
    size_t vl);
vbool2_t vmsne_vx_u8m4_b2 (vuint8m4_t op1, uint8_t op2, size_t
    vl);
vbool1_t vmsne_vv_u8m8_b1 (vuint8m8_t op1, vuint8m8_t op2,
    size_t vl);
vbool1_t vmsne_vx_u8m8_b1 (vuint8m8_t op1, uint8_t op2, size_t
    vl);
vbool16_t vmsne_vv_u16m1_b16 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vbool16_t vmsne_vx_u16m1_b16 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vbool8_t vmsne_vv_u16m2_b8 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vbool8_t vmsne_vx_u16m2_b8 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vbool4_t vmsne_vv_u16m4_b4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vbool4_t vmsne_vx_u16m4_b4 (vuint16m4_t op1, uint16_t op2,
    size_t vl);
vbool2_t vmsne_vv_u16m8_b2 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vbool2_t vmsne_vx_u16m8_b2 (vuint16m8_t op1, uint16_t op2,
    size_t vl);

```

```

vbool32_t vmsne_vv_u32m1_b32 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vbool32_t vmsne_vx_u32m1_b32 (vuint32m1_t op1, uint32_t op2,
    size_t vl);
vbool16_t vmsne_vv_u32m2_b16 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vbool16_t vmsne_vx_u32m2_b16 (vuint32m2_t op1, uint32_t op2,
    size_t vl);
vbool8_t vmsne_vv_u32m4_b8 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vbool8_t vmsne_vx_u32m4_b8 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vbool4_t vmsne_vv_u32m8_b4 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vbool4_t vmsne_vx_u32m8_b4 (vuint32m8_t op1, uint32_t op2,
    size_t vl);
vbool64_t vmsne_vv_u64m1_b64 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vbool64_t vmsne_vx_u64m1_b64 (vuint64m1_t op1, uint64_t op2,
    size_t vl);
vbool32_t vmsne_vv_u64m2_b32 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vbool32_t vmsne_vx_u64m2_b32 (vuint64m2_t op1, uint64_t op2,
    size_t vl);
vbool16_t vmsne_vv_u64m4_b16 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vbool16_t vmsne_vx_u64m4_b16 (vuint64m4_t op1, uint64_t op2,
    size_t vl);
vbool8_t vmsne_vv_u64m8_b8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vbool8_t vmsne_vx_u64m8_b8 (vuint64m8_t op1, uint64_t op2,
    size_t vl);
vbool8_t vmslt_vv_i8m1_b8 (vint8m1_t op1, vint8m1_t op2, size_t
    vl);
vbool8_t vmslt_vx_i8m1_b8 (vint8m1_t op1, int8_t op2, size_t vl);
vbool4_t vmslt_vv_i8m2_b4 (vint8m2_t op1, vint8m2_t op2, size_t
    vl);
vbool4_t vmslt_vx_i8m2_b4 (vint8m2_t op1, int8_t op2, size_t vl);
vbool2_t vmslt_vv_i8m4_b2 (vint8m4_t op1, vint8m4_t op2, size_t
    vl);
vbool2_t vmslt_vx_i8m4_b2 (vint8m4_t op1, int8_t op2, size_t vl);
vbool1_t vmslt_vv_i8m8_b1 (vint8m8_t op1, vint8m8_t op2, size_t
    vl);
vbool1_t vmslt_vx_i8m8_b1 (vint8m8_t op1, int8_t op2, size_t vl);
vbool16_t vmslt_vv_i16m1_b16 (vint16m1_t op1, vint16m1_t op2,
    size_t vl);

```

```

vbool16_t vmslt_vx_i16m1_b16 (vint16m1_t op1, int16_t op2,
    size_t vl);
vbool8_t vmslt_vv_i16m2_b8 (vint16m2_t op1, vint16m2_t op2,
    size_t vl);
vbool8_t vmslt_vx_i16m2_b8 (vint16m2_t op1, int16_t op2, size_t
    vl);
vbool4_t vmslt_vv_i16m4_b4 (vint16m4_t op1, vint16m4_t op2,
    size_t vl);
vbool4_t vmslt_vx_i16m4_b4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vbool2_t vmslt_vv_i16m8_b2 (vint16m8_t op1, vint16m8_t op2,
    size_t vl);
vbool2_t vmslt_vx_i16m8_b2 (vint16m8_t op1, int16_t op2, size_t
    vl);
vbool32_t vmslt_vv_i32m1_b32 (vint32m1_t op1, vint32m1_t op2,
    size_t vl);
vbool32_t vmslt_vx_i32m1_b32 (vint32m1_t op1, int32_t op2,
    size_t vl);
vbool16_t vmslt_vv_i32m2_b16 (vint32m2_t op1, vint32m2_t op2,
    size_t vl);
vbool16_t vmslt_vx_i32m2_b16 (vint32m2_t op1, int32_t op2,
    size_t vl);
vbool8_t vmslt_vv_i32m4_b8 (vint32m4_t op1, vint32m4_t op2,
    size_t vl);
vbool8_t vmslt_vx_i32m4_b8 (vint32m4_t op1, int32_t op2, size_t
    vl);
vbool4_t vmslt_vv_i32m8_b4 (vint32m8_t op1, vint32m8_t op2,
    size_t vl);
vbool4_t vmslt_vx_i32m8_b4 (vint32m8_t op1, int32_t op2, size_t
    vl);
vbool64_t vmslt_vv_i64m1_b64 (vint64m1_t op1, vint64m1_t op2,
    size_t vl);
vbool64_t vmslt_vx_i64m1_b64 (vint64m1_t op1, int64_t op2,
    size_t vl);
vbool32_t vmslt_vv_i64m2_b32 (vint64m2_t op1, vint64m2_t op2,
    size_t vl);
vbool32_t vmslt_vx_i64m2_b32 (vint64m2_t op1, int64_t op2,
    size_t vl);
vbool16_t vmslt_vv_i64m4_b16 (vint64m4_t op1, vint64m4_t op2,
    size_t vl);
vbool16_t vmslt_vx_i64m4_b16 (vint64m4_t op1, int64_t op2,
    size_t vl);
vbool8_t vmslt_vv_i64m8_b8 (vint64m8_t op1, vint64m8_t op2,
    size_t vl);
vbool8_t vmslt_vx_i64m8_b8 (vint64m8_t op1, int64_t op2, size_t
    vl);

```

```

vbool8_t vmsltu_vv_u8m1_b8 (vuint8m1_t op1, vuint8m1_t op2,
    size_t vl);
vbool8_t vmsltu_vx_u8m1_b8 (vuint8m1_t op1, uint8_t op2, size_t
    vl);
vbool4_t vmsltu_vv_u8m2_b4 (vuint8m2_t op1, vuint8m2_t op2,
    size_t vl);
vbool4_t vmsltu_vx_u8m2_b4 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vbool2_t vmsltu_vv_u8m4_b2 (vuint8m4_t op1, vuint8m4_t op2,
    size_t vl);
vbool2_t vmsltu_vx_u8m4_b2 (vuint8m4_t op1, uint8_t op2, size_t
    vl);
vbool1_t vmsltu_vv_u8m8_b1 (vuint8m8_t op1, vuint8m8_t op2,
    size_t vl);
vbool1_t vmsltu_vx_u8m8_b1 (vuint8m8_t op1, uint8_t op2, size_t
    vl);
vbool16_t vmsltu_vv_u16m1_b16 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vbool16_t vmsltu_vx_u16m1_b16 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vbool8_t vmsltu_vv_u16m2_b8 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vbool8_t vmsltu_vx_u16m2_b8 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vbool4_t vmsltu_vv_u16m4_b4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vbool4_t vmsltu_vx_u16m4_b4 (vuint16m4_t op1, uint16_t op2,
    size_t vl);
vbool2_t vmsltu_vv_u16m8_b2 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vbool2_t vmsltu_vx_u16m8_b2 (vuint16m8_t op1, uint16_t op2,
    size_t vl);
vbool32_t vmsltu_vv_u32m1_b32 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vbool32_t vmsltu_vx_u32m1_b32 (vuint32m1_t op1, uint32_t op2,
    size_t vl);
vbool16_t vmsltu_vv_u32m2_b16 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vbool16_t vmsltu_vx_u32m2_b16 (vuint32m2_t op1, uint32_t op2,
    size_t vl);
vbool8_t vmsltu_vv_u32m4_b8 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vbool8_t vmsltu_vx_u32m4_b8 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vbool4_t vmsltu_vv_u32m8_b4 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);

```

```

vbool4_t vmsltu_vx_u32m8_b4 (vuint32m8_t op1, uint32_t op2,
    size_t vl);
vbool64_t vmsltu_vv_u64m1_b64 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vbool64_t vmsltu_vx_u64m1_b64 (vuint64m1_t op1, uint64_t op2,
    size_t vl);
vbool32_t vmsltu_vv_u64m2_b32 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vbool32_t vmsltu_vx_u64m2_b32 (vuint64m2_t op1, uint64_t op2,
    size_t vl);
vbool16_t vmsltu_vv_u64m4_b16 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vbool16_t vmsltu_vx_u64m4_b16 (vuint64m4_t op1, uint64_t op2,
    size_t vl);
vbool8_t vmsltu_vv_u64m8_b8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vbool8_t vmsltu_vx_u64m8_b8 (vuint64m8_t op1, uint64_t op2,
    size_t vl);
vbool8_t vmsle_vv_i8m1_b8 (vint8m1_t op1, vint8m1_t op2, size_t
    vl);
vbool8_t vmsle_vx_i8m1_b8 (vint8m1_t op1, int8_t op2, size_t vl);
vbool4_t vmsle_vv_i8m2_b4 (vint8m2_t op1, vint8m2_t op2, size_t
    vl);
vbool4_t vmsle_vx_i8m2_b4 (vint8m2_t op1, int8_t op2, size_t vl);
vbool2_t vmsle_vv_i8m4_b2 (vint8m4_t op1, vint8m4_t op2, size_t
    vl);
vbool2_t vmsle_vx_i8m4_b2 (vint8m4_t op1, int8_t op2, size_t vl);
vbool1_t vmsle_vv_i8m8_b1 (vint8m8_t op1, vint8m8_t op2, size_t
    vl);
vbool1_t vmsle_vx_i8m8_b1 (vint8m8_t op1, int8_t op2, size_t vl);
vbool16_t vmsle_vv_i16m1_b16 (vint16m1_t op1, vint16m1_t op2,
    size_t vl);
vbool16_t vmsle_vx_i16m1_b16 (vint16m1_t op1, int16_t op2,
    size_t vl);
vbool8_t vmsle_vv_i16m2_b8 (vint16m2_t op1, vint16m2_t op2,
    size_t vl);
vbool8_t vmsle_vx_i16m2_b8 (vint16m2_t op1, int16_t op2, size_t
    vl);
vbool4_t vmsle_vv_i16m4_b4 (vint16m4_t op1, vint16m4_t op2,
    size_t vl);
vbool4_t vmsle_vx_i16m4_b4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vbool2_t vmsle_vv_i16m8_b2 (vint16m8_t op1, vint16m8_t op2,
    size_t vl);
vbool2_t vmsle_vx_i16m8_b2 (vint16m8_t op1, int16_t op2, size_t
    vl);

```



```

vbool32_t vmsle_vv_i32m1_b32 (vint32m1_t op1, vint32m1_t op2,
    size_t vl);
vbool32_t vmsle_vx_i32m1_b32 (vint32m1_t op1, int32_t op2,
    size_t vl);
vbool16_t vmsle_vv_i32m2_b16 (vint32m2_t op1, vint32m2_t op2,
    size_t vl);
vbool16_t vmsle_vx_i32m2_b16 (vint32m2_t op1, int32_t op2,
    size_t vl);
vbool8_t vmsle_vv_i32m4_b8 (vint32m4_t op1, vint32m4_t op2,
    size_t vl);
vbool8_t vmsle_vx_i32m4_b8 (vint32m4_t op1, int32_t op2, size_t
    vl);
vbool4_t vmsle_vv_i32m8_b4 (vint32m8_t op1, vint32m8_t op2,
    size_t vl);
vbool4_t vmsle_vx_i32m8_b4 (vint32m8_t op1, int32_t op2, size_t
    vl);
vbool64_t vmsle_vv_i64m1_b64 (vint64m1_t op1, vint64m1_t op2,
    size_t vl);
vbool64_t vmsle_vx_i64m1_b64 (vint64m1_t op1, int64_t op2,
    size_t vl);
vbool32_t vmsle_vv_i64m2_b32 (vint64m2_t op1, vint64m2_t op2,
    size_t vl);
vbool32_t vmsle_vx_i64m2_b32 (vint64m2_t op1, int64_t op2,
    size_t vl);
vbool16_t vmsle_vv_i64m4_b16 (vint64m4_t op1, vint64m4_t op2,
    size_t vl);
vbool16_t vmsle_vx_i64m4_b16 (vint64m4_t op1, int64_t op2,
    size_t vl);
vbool8_t vmsle_vv_i64m8_b8 (vint64m8_t op1, vint64m8_t op2,
    size_t vl);
vbool8_t vmsle_vx_i64m8_b8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vbool8_t vmsleu_vv_u8m1_b8 (vuint8m1_t op1, vuint8m1_t op2,
    size_t vl);
vbool8_t vmsleu_vx_u8m1_b8 (vuint8m1_t op1, uint8_t op2, size_t
    vl);
vbool4_t vmsleu_vv_u8m2_b4 (vuint8m2_t op1, vuint8m2_t op2,
    size_t vl);
vbool4_t vmsleu_vx_u8m2_b4 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vbool2_t vmsleu_vv_u8m4_b2 (vuint8m4_t op1, vuint8m4_t op2,
    size_t vl);
vbool2_t vmsleu_vx_u8m4_b2 (vuint8m4_t op1, uint8_t op2, size_t
    vl);
vbool1_t vmsleu_vv_u8m8_b1 (vuint8m8_t op1, vuint8m8_t op2,
    size_t vl);

```

```

vbool1_t vmsleu_vx_u8m8_b1 (vuint8m8_t op1, uint8_t op2, size_t
    vl);
vbool16_t vmsleu_vv_u16m1_b16 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vbool16_t vmsleu_vx_u16m1_b16 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vbool8_t vmsleu_vv_u16m2_b8 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vbool8_t vmsleu_vx_u16m2_b8 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vbool4_t vmsleu_vv_u16m4_b4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vbool4_t vmsleu_vx_u16m4_b4 (vuint16m4_t op1, uint16_t op2,
    size_t vl);
vbool2_t vmsleu_vv_u16m8_b2 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vbool2_t vmsleu_vx_u16m8_b2 (vuint16m8_t op1, uint16_t op2,
    size_t vl);
vbool32_t vmsleu_vv_u32m1_b32 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vbool32_t vmsleu_vx_u32m1_b32 (vuint32m1_t op1, uint32_t op2,
    size_t vl);
vbool16_t vmsleu_vv_u32m2_b16 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vbool16_t vmsleu_vx_u32m2_b16 (vuint32m2_t op1, uint32_t op2,
    size_t vl);
vbool8_t vmsleu_vv_u32m4_b8 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vbool8_t vmsleu_vx_u32m4_b8 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vbool4_t vmsleu_vv_u32m8_b4 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vbool4_t vmsleu_vx_u32m8_b4 (vuint32m8_t op1, uint32_t op2,
    size_t vl);
vbool64_t vmsleu_vv_u64m1_b64 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vbool64_t vmsleu_vx_u64m1_b64 (vuint64m1_t op1, uint64_t op2,
    size_t vl);
vbool32_t vmsleu_vv_u64m2_b32 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vbool32_t vmsleu_vx_u64m2_b32 (vuint64m2_t op1, uint64_t op2,
    size_t vl);
vbool16_t vmsleu_vv_u64m4_b16 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vbool16_t vmsleu_vx_u64m4_b16 (vuint64m4_t op1, uint64_t op2,
    size_t vl);

```

```

vbool8_t vmsleu_vv_u64m8_b8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vbool8_t vmsleu_vx_u64m8_b8 (vuint64m8_t op1, uint64_t op2,
    size_t vl);
vbool8_t vmsgt_vv_i8m1_b8 (vint8m1_t op1, vint8m1_t op2, size_t
    vl);
vbool8_t vmsgt_vx_i8m1_b8 (vint8m1_t op1, int8_t op2, size_t vl);
vbool4_t vmsgt_vv_i8m2_b4 (vint8m2_t op1, vint8m2_t op2, size_t
    vl);
vbool4_t vmsgt_vx_i8m2_b4 (vint8m2_t op1, int8_t op2, size_t vl);
vbool2_t vmsgt_vv_i8m4_b2 (vint8m4_t op1, vint8m4_t op2, size_t
    vl);
vbool2_t vmsgt_vx_i8m4_b2 (vint8m4_t op1, int8_t op2, size_t vl);
vbool1_t vmsgt_vv_i8m8_b1 (vint8m8_t op1, vint8m8_t op2, size_t
    vl);
vbool1_t vmsgt_vx_i8m8_b1 (vint8m8_t op1, int8_t op2, size_t vl);
vbool16_t vmsgt_vv_i16m1_b16 (vint16m1_t op1, vint16m1_t op2,
    size_t vl);
vbool16_t vmsgt_vx_i16m1_b16 (vint16m1_t op1, int16_t op2,
    size_t vl);
vbool8_t vmsgt_vv_i16m2_b8 (vint16m2_t op1, vint16m2_t op2,
    size_t vl);
vbool8_t vmsgt_vx_i16m2_b8 (vint16m2_t op1, int16_t op2, size_t
    vl);
vbool4_t vmsgt_vv_i16m4_b4 (vint16m4_t op1, vint16m4_t op2,
    size_t vl);
vbool4_t vmsgt_vx_i16m4_b4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vbool2_t vmsgt_vv_i16m8_b2 (vint16m8_t op1, vint16m8_t op2,
    size_t vl);
vbool2_t vmsgt_vx_i16m8_b2 (vint16m8_t op1, int16_t op2, size_t
    vl);
vbool32_t vmsgt_vv_i32m1_b32 (vint32m1_t op1, vint32m1_t op2,
    size_t vl);
vbool32_t vmsgt_vx_i32m1_b32 (vint32m1_t op1, int32_t op2,
    size_t vl);
vbool16_t vmsgt_vv_i32m2_b16 (vint32m2_t op1, vint32m2_t op2,
    size_t vl);
vbool16_t vmsgt_vx_i32m2_b16 (vint32m2_t op1, int32_t op2,
    size_t vl);
vbool8_t vmsgt_vv_i32m4_b8 (vint32m4_t op1, vint32m4_t op2,
    size_t vl);
vbool8_t vmsgt_vx_i32m4_b8 (vint32m4_t op1, int32_t op2, size_t
    vl);
vbool4_t vmsgt_vv_i32m8_b4 (vint32m8_t op1, vint32m8_t op2,
    size_t vl);

```

```

vbool4_t vmsgt_vx_i32m8_b4 (vint32m8_t op1, int32_t op2, size_t
    vl);
vbool64_t vmsgt_vv_i64m1_b64 (vint64m1_t op1, vint64m1_t op2,
    size_t vl);
vbool64_t vmsgt_vx_i64m1_b64 (vint64m1_t op1, int64_t op2,
    size_t vl);
vbool32_t vmsgt_vv_i64m2_b32 (vint64m2_t op1, vint64m2_t op2,
    size_t vl);
vbool32_t vmsgt_vx_i64m2_b32 (vint64m2_t op1, int64_t op2,
    size_t vl);
vbool16_t vmsgt_vv_i64m4_b16 (vint64m4_t op1, vint64m4_t op2,
    size_t vl);
vbool16_t vmsgt_vx_i64m4_b16 (vint64m4_t op1, int64_t op2,
    size_t vl);
vbool8_t vmsgt_vv_i64m8_b8 (vint64m8_t op1, vint64m8_t op2,
    size_t vl);
vbool8_t vmsgt_vx_i64m8_b8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vbool8_t vmsgtu_vv_u8m1_b8 (vuint8m1_t op1, vuint8m1_t op2,
    size_t vl);
vbool8_t vmsgtu_vx_u8m1_b8 (vuint8m1_t op1, uint8_t op2, size_t
    vl);
vbool4_t vmsgtu_vv_u8m2_b4 (vuint8m2_t op1, vuint8m2_t op2,
    size_t vl);
vbool4_t vmsgtu_vx_u8m2_b4 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vbool2_t vmsgtu_vv_u8m4_b2 (vuint8m4_t op1, vuint8m4_t op2,
    size_t vl);
vbool2_t vmsgtu_vx_u8m4_b2 (vuint8m4_t op1, uint8_t op2, size_t
    vl);
vbool1_t vmsgtu_vv_u8m8_b1 (vuint8m8_t op1, vuint8m8_t op2,
    size_t vl);
vbool1_t vmsgtu_vx_u8m8_b1 (vuint8m8_t op1, uint8_t op2, size_t
    vl);
vbool16_t vmsgtu_vv_u16m1_b16 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vbool16_t vmsgtu_vx_u16m1_b16 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vbool8_t vmsgtu_vv_u16m2_b8 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vbool8_t vmsgtu_vx_u16m2_b8 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vbool4_t vmsgtu_vv_u16m4_b4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vbool4_t vmsgtu_vx_u16m4_b4 (vuint16m4_t op1, uint16_t op2,
    size_t vl);

```

```

vbool12_t vmsgtu_vv_u16m8_b2 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vbool12_t vmsgtu_vx_u16m8_b2 (vuint16m8_t op1, uint16_t op2,
    size_t vl);
vbool32_t vmsgtu_vv_u32m1_b32 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vbool32_t vmsgtu_vx_u32m1_b32 (vuint32m1_t op1, uint32_t op2,
    size_t vl);
vbool16_t vmsgtu_vv_u32m2_b16 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vbool16_t vmsgtu_vx_u32m2_b16 (vuint32m2_t op1, uint32_t op2,
    size_t vl);
vbool8_t vmsgtu_vv_u32m4_b8 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vbool8_t vmsgtu_vx_u32m4_b8 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vbool4_t vmsgtu_vv_u32m8_b4 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vbool4_t vmsgtu_vx_u32m8_b4 (vuint32m8_t op1, uint32_t op2,
    size_t vl);
vbool64_t vmsgtu_vv_u64m1_b64 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vbool64_t vmsgtu_vx_u64m1_b64 (vuint64m1_t op1, uint64_t op2,
    size_t vl);
vbool32_t vmsgtu_vv_u64m2_b32 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vbool32_t vmsgtu_vx_u64m2_b32 (vuint64m2_t op1, uint64_t op2,
    size_t vl);
vbool16_t vmsgtu_vv_u64m4_b16 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vbool16_t vmsgtu_vx_u64m4_b16 (vuint64m4_t op1, uint64_t op2,
    size_t vl);
vbool8_t vmsgtu_vv_u64m8_b8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vbool8_t vmsgtu_vx_u64m8_b8 (vuint64m8_t op1, uint64_t op2,
    size_t vl);
vbool8_t vmsge_vv_i8m1_b8 (vint8m1_t op1, vint8m1_t op2, size_t
    vl);
vbool8_t vmsge_vx_i8m1_b8 (vint8m1_t op1, int8_t op2, size_t vl);
vbool4_t vmsge_vv_i8m2_b4 (vint8m2_t op1, vint8m2_t op2, size_t
    vl);
vbool4_t vmsge_vx_i8m2_b4 (vint8m2_t op1, int8_t op2, size_t vl);
vbool2_t vmsge_vv_i8m4_b2 (vint8m4_t op1, vint8m4_t op2, size_t
    vl);
vbool2_t vmsge_vx_i8m4_b2 (vint8m4_t op1, int8_t op2, size_t vl);

```

```

vbool1_t vmsge_vv_i8m8_b1 (vint8m8_t op1, vint8m8_t op2, size_t
    vl);
vbool1_t vmsge_vx_i8m8_b1 (vint8m8_t op1, int8_t op2, size_t vl);
vbool16_t vmsge_vv_i16m1_b16 (vint16m1_t op1, vint16m1_t op2,
    size_t vl);
vbool16_t vmsge_vx_i16m1_b16 (vint16m1_t op1, int16_t op2,
    size_t vl);
vbool8_t vmsge_vv_i16m2_b8 (vint16m2_t op1, vint16m2_t op2,
    size_t vl);
vbool8_t vmsge_vx_i16m2_b8 (vint16m2_t op1, int16_t op2, size_t
    vl);
vbool4_t vmsge_vv_i16m4_b4 (vint16m4_t op1, vint16m4_t op2,
    size_t vl);
vbool4_t vmsge_vx_i16m4_b4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vbool2_t vmsge_vv_i16m8_b2 (vint16m8_t op1, vint16m8_t op2,
    size_t vl);
vbool2_t vmsge_vx_i16m8_b2 (vint16m8_t op1, int16_t op2, size_t
    vl);
vbool32_t vmsge_vv_i32m1_b32 (vint32m1_t op1, vint32m1_t op2,
    size_t vl);
vbool32_t vmsge_vx_i32m1_b32 (vint32m1_t op1, int32_t op2,
    size_t vl);
vbool16_t vmsge_vv_i32m2_b16 (vint32m2_t op1, vint32m2_t op2,
    size_t vl);
vbool16_t vmsge_vx_i32m2_b16 (vint32m2_t op1, int32_t op2,
    size_t vl);
vbool8_t vmsge_vv_i32m4_b8 (vint32m4_t op1, vint32m4_t op2,
    size_t vl);
vbool8_t vmsge_vx_i32m4_b8 (vint32m4_t op1, int32_t op2, size_t
    vl);
vbool4_t vmsge_vv_i32m8_b4 (vint32m8_t op1, vint32m8_t op2,
    size_t vl);
vbool4_t vmsge_vx_i32m8_b4 (vint32m8_t op1, int32_t op2, size_t
    vl);
vbool64_t vmsge_vv_i64m1_b64 (vint64m1_t op1, vint64m1_t op2,
    size_t vl);
vbool64_t vmsge_vx_i64m1_b64 (vint64m1_t op1, int64_t op2,
    size_t vl);
vbool32_t vmsge_vv_i64m2_b32 (vint64m2_t op1, vint64m2_t op2,
    size_t vl);
vbool32_t vmsge_vx_i64m2_b32 (vint64m2_t op1, int64_t op2,
    size_t vl);
vbool16_t vmsge_vv_i64m4_b16 (vint64m4_t op1, vint64m4_t op2,
    size_t vl);

```

```

vbool16_t vmsge_vx_i64m4_b16 (vint64m4_t op1, int64_t op2,
    size_t vl);
vbool8_t vmsge_vv_i64m8_b8 (vint64m8_t op1, vint64m8_t op2,
    size_t vl);
vbool8_t vmsge_vx_i64m8_b8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vbool8_t vmsgeu_vv_u8m1_b8 (vuint8m1_t op1, vuint8m1_t op2,
    size_t vl);
vbool8_t vmsgeu_vx_u8m1_b8 (vuint8m1_t op1, uint8_t op2, size_t
    vl);
vbool4_t vmsgeu_vv_u8m2_b4 (vuint8m2_t op1, vuint8m2_t op2,
    size_t vl);
vbool4_t vmsgeu_vx_u8m2_b4 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vbool2_t vmsgeu_vv_u8m4_b2 (vuint8m4_t op1, vuint8m4_t op2,
    size_t vl);
vbool2_t vmsgeu_vx_u8m4_b2 (vuint8m4_t op1, uint8_t op2, size_t
    vl);
vbool1_t vmsgeu_vv_u8m8_b1 (vuint8m8_t op1, vuint8m8_t op2,
    size_t vl);
vbool1_t vmsgeu_vx_u8m8_b1 (vuint8m8_t op1, uint8_t op2, size_t
    vl);
vbool16_t vmsgeu_vv_u16m1_b16 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vbool16_t vmsgeu_vx_u16m1_b16 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vbool8_t vmsgeu_vv_u16m2_b8 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vbool8_t vmsgeu_vx_u16m2_b8 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vbool4_t vmsgeu_vv_u16m4_b4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vbool4_t vmsgeu_vx_u16m4_b4 (vuint16m4_t op1, uint16_t op2,
    size_t vl);
vbool2_t vmsgeu_vv_u16m8_b2 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vbool2_t vmsgeu_vx_u16m8_b2 (vuint16m8_t op1, uint16_t op2,
    size_t vl);
vbool32_t vmsgeu_vv_u32m1_b32 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vbool32_t vmsgeu_vx_u32m1_b32 (vuint32m1_t op1, uint32_t op2,
    size_t vl);
vbool16_t vmsgeu_vv_u32m2_b16 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vbool16_t vmsgeu_vx_u32m2_b16 (vuint32m2_t op1, uint32_t op2,
    size_t vl);

```

```

vbool8_t vmsgeu_vv_u32m4_b8 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vbool8_t vmsgeu_vx_u32m4_b8 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vbool4_t vmsgeu_vv_u32m8_b4 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vbool4_t vmsgeu_vx_u32m8_b4 (vuint32m8_t op1, uint32_t op2,
    size_t vl);
vbool64_t vmsgeu_vv_u64m1_b64 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vbool64_t vmsgeu_vx_u64m1_b64 (vuint64m1_t op1, uint64_t op2,
    size_t vl);
vbool32_t vmsgeu_vv_u64m2_b32 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vbool32_t vmsgeu_vx_u64m2_b32 (vuint64m2_t op1, uint64_t op2,
    size_t vl);
vbool16_t vmsgeu_vv_u64m4_b16 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vbool16_t vmsgeu_vx_u64m4_b16 (vuint64m4_t op1, uint64_t op2,
    size_t vl);
vbool8_t vmsgeu_vv_u64m8_b8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vbool8_t vmsgeu_vx_u64m8_b8 (vuint64m8_t op1, uint64_t op2,
    size_t vl);
// masked functions
vbool8_t vmseq_vv_i8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vbool8_t vmseq_vx_i8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vbool4_t vmseq_vv_i8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vbool4_t vmseq_vx_i8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vbool2_t vmseq_vv_i8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);
vbool2_t vmseq_vx_i8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vbool1_t vmseq_vv_i8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vbool1_t vmseq_vx_i8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vbool16_t vmseq_vv_i16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vbool16_t vmseq_vx_i16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);

```



```

vbool8_t vmseq_vv_i16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint16m2_t op1, vint16m2_t op2, size_t vl);
vbool8_t vmseq_vx_i16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint16m2_t op1, int16_t op2, size_t vl);
vbool4_t vmseq_vv_i16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint16m4_t op1, vint16m4_t op2, size_t vl);
vbool4_t vmseq_vx_i16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint16m4_t op1, int16_t op2, size_t vl);
vbool2_t vmseq_vv_i16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vint16m8_t op1, vint16m8_t op2, size_t vl);
vbool2_t vmseq_vx_i16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vint16m8_t op1, int16_t op2, size_t vl);
vbool32_t vmseq_vv_i32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vbool32_t vmseq_vx_i32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vbool16_t vmseq_vv_i32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vbool16_t vmseq_vx_i32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vbool8_t vmseq_vv_i32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint32m4_t op1, vint32m4_t op2, size_t vl);
vbool8_t vmseq_vx_i32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint32m4_t op1, int32_t op2, size_t vl);
vbool4_t vmseq_vv_i32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint32m8_t op1, vint32m8_t op2, size_t vl);
vbool4_t vmseq_vx_i32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint32m8_t op1, int32_t op2, size_t vl);
vbool64_t vmseq_vv_i64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vbool64_t vmseq_vx_i64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vbool32_t vmseq_vv_i64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vbool32_t vmseq_vx_i64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vbool16_t vmseq_vv_i64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vbool16_t vmseq_vx_i64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vbool8_t vmseq_vv_i64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint64m8_t op1, vint64m8_t op2, size_t vl);
vbool8_t vmseq_vx_i64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint64m8_t op1, int64_t op2, size_t vl);
vbool8_t vmseq_vv_u8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint8m1_t op1, vuint8m1_t op2, size_t vl);

```

```

vbool8_t vmseq_vx_u8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vbool4_t vmseq_vv_u8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vbool4_t vmseq_vx_u8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);
vbool2_t vmseq_vv_u8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vbool2_t vmseq_vx_u8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);
vbool1_t vmseq_vv_u8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vbool1_t vmseq_vx_u8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vbool16_t vmseq_vv_u16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vbool16_t vmseq_vx_u16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vbool8_t vmseq_vv_u16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vbool8_t vmseq_vx_u16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint16m2_t op1, uint16_t op2, size_t vl);
vbool4_t vmseq_vv_u16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vbool4_t vmseq_vx_u16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint16m4_t op1, uint16_t op2, size_t vl);
vbool2_t vmseq_vv_u16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vbool2_t vmseq_vx_u16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vuint16m8_t op1, uint16_t op2, size_t vl);
vbool32_t vmseq_vv_u32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vbool32_t vmseq_vx_u32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vbool16_t vmseq_vv_u32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vbool16_t vmseq_vx_u32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vbool8_t vmseq_vv_u32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vbool8_t vmseq_vx_u32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint32m4_t op1, uint32_t op2, size_t vl);
vbool4_t vmseq_vv_u32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vbool4_t vmseq_vx_u32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint32m8_t op1, uint32_t op2, size_t vl);

```

```

vbool64_t vmseq_vv_u64m1_b64_m (vbool64_t mask, vbool64_t
maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vbool64_t vmseq_vx_u64m1_b64_m (vbool64_t mask, vbool64_t
maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vbool32_t vmseq_vv_u64m2_b32_m (vbool32_t mask, vbool32_t
maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vbool32_t vmseq_vx_u64m2_b32_m (vbool32_t mask, vbool32_t
maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vbool16_t vmseq_vv_u64m4_b16_m (vbool16_t mask, vbool16_t
maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);
vbool16_t vmseq_vx_u64m4_b16_m (vbool16_t mask, vbool16_t
maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vbool8_t vmseq_vv_u64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vbool8_t vmseq_vx_u64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
vuint64m8_t op1, uint64_t op2, size_t vl);
vbool8_t vmsne_vv_i8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
vint8m1_t op1, vint8m1_t op2, size_t vl);
vbool8_t vmsne_vx_i8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
vint8m1_t op1, int8_t op2, size_t vl);
vbool4_t vmsne_vv_i8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
vint8m2_t op1, vint8m2_t op2, size_t vl);
vbool4_t vmsne_vx_i8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
vint8m2_t op1, int8_t op2, size_t vl);
vbool2_t vmsne_vv_i8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
vint8m4_t op1, vint8m4_t op2, size_t vl);
vbool2_t vmsne_vx_i8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
vint8m4_t op1, int8_t op2, size_t vl);
vbool1_t vmsne_vv_i8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
vint8m8_t op1, vint8m8_t op2, size_t vl);
vbool1_t vmsne_vx_i8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
vint8m8_t op1, int8_t op2, size_t vl);
vbool16_t vmsne_vv_i16m1_b16_m (vbool16_t mask, vbool16_t
maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vbool16_t vmsne_vx_i16m1_b16_m (vbool16_t mask, vbool16_t
maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vbool8_t vmsne_vv_i16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
vint16m2_t op1, vint16m2_t op2, size_t vl);
vbool8_t vmsne_vx_i16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
vint16m2_t op1, int16_t op2, size_t vl);
vbool4_t vmsne_vv_i16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
vint16m4_t op1, vint16m4_t op2, size_t vl);
vbool4_t vmsne_vx_i16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
vint16m4_t op1, int16_t op2, size_t vl);
vbool2_t vmsne_vv_i16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
vint16m8_t op1, vint16m8_t op2, size_t vl);

```

```

vbool12_t vmsne_vx_i16m8_b2_m (vbool12_t mask, vbool12_t maskedoff,
    vint16m8_t op1, int16_t op2, size_t vl);
vbool32_t vmsne_vv_i32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vbool32_t vmsne_vx_i32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vbool16_t vmsne_vv_i32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vbool16_t vmsne_vx_i32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vbool8_t vmsne_vv_i32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint32m4_t op1, vint32m4_t op2, size_t vl);
vbool8_t vmsne_vx_i32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint32m4_t op1, int32_t op2, size_t vl);
vbool4_t vmsne_vv_i32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint32m8_t op1, vint32m8_t op2, size_t vl);
vbool4_t vmsne_vx_i32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint32m8_t op1, int32_t op2, size_t vl);
vbool64_t vmsne_vv_i64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vbool64_t vmsne_vx_i64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vbool32_t vmsne_vv_i64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vbool32_t vmsne_vx_i64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vbool16_t vmsne_vv_i64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vbool16_t vmsne_vx_i64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vbool8_t vmsne_vv_i64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint64m8_t op1, vint64m8_t op2, size_t vl);
vbool8_t vmsne_vx_i64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint64m8_t op1, int64_t op2, size_t vl);
vbool8_t vmsne_vv_u8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vbool8_t vmsne_vx_u8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vbool4_t vmsne_vv_u8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vbool4_t vmsne_vx_u8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);
vbool2_t vmsne_vv_u8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vbool2_t vmsne_vx_u8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);

```

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vbool1_t vmsne_vv_u8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vbool1_t vmsne_vx_u8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vbool16_t vmsne_vv_u16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vbool16_t vmsne_vx_u16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vbool8_t vmsne_vv_u16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vbool8_t vmsne_vx_u16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint16m2_t op1, uint16_t op2, size_t vl);
vbool4_t vmsne_vv_u16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vbool4_t vmsne_vx_u16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint16m4_t op1, uint16_t op2, size_t vl);
vbool2_t vmsne_vv_u16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vbool2_t vmsne_vx_u16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vuint16m8_t op1, uint16_t op2, size_t vl);
vbool32_t vmsne_vv_u32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vbool32_t vmsne_vx_u32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vbool16_t vmsne_vv_u32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vbool16_t vmsne_vx_u32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vbool8_t vmsne_vv_u32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vbool8_t vmsne_vx_u32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint32m4_t op1, uint32_t op2, size_t vl);
vbool4_t vmsne_vv_u32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vbool4_t vmsne_vx_u32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint32m8_t op1, uint32_t op2, size_t vl);
vbool64_t vmsne_vv_u64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vbool64_t vmsne_vx_u64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vbool32_t vmsne_vv_u64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vbool32_t vmsne_vx_u64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vbool16_t vmsne_vv_u64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);

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vbool16_t vmsne_vx_u64m4_b16_m (vbool16_t mask, vbool16_t
maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vbool8_t vmsne_vv_u64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vbool8_t vmsne_vx_u64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
vuint64m8_t op1, uint64_t op2, size_t vl);
vbool8_t vmslt_vv_i8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
vint8m1_t op1, vint8m1_t op2, size_t vl);
vbool8_t vmslt_vx_i8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
vint8m1_t op1, int8_t op2, size_t vl);
vbool4_t vmslt_vv_i8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
vint8m2_t op1, vint8m2_t op2, size_t vl);
vbool4_t vmslt_vx_i8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
vint8m2_t op1, int8_t op2, size_t vl);
vbool2_t vmslt_vv_i8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
vint8m4_t op1, vint8m4_t op2, size_t vl);
vbool2_t vmslt_vx_i8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
vint8m4_t op1, int8_t op2, size_t vl);
vbool1_t vmslt_vv_i8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
vint8m8_t op1, vint8m8_t op2, size_t vl);
vbool1_t vmslt_vx_i8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
vint8m8_t op1, int8_t op2, size_t vl);
vbool16_t vmslt_vv_i16m1_b16_m (vbool16_t mask, vbool16_t
maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vbool16_t vmslt_vx_i16m1_b16_m (vbool16_t mask, vbool16_t
maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vbool8_t vmslt_vv_i16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
vint16m2_t op1, vint16m2_t op2, size_t vl);
vbool8_t vmslt_vx_i16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
vint16m2_t op1, int16_t op2, size_t vl);
vbool4_t vmslt_vv_i16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
vint16m4_t op1, vint16m4_t op2, size_t vl);
vbool4_t vmslt_vx_i16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
vint16m4_t op1, int16_t op2, size_t vl);
vbool2_t vmslt_vv_i16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
vint16m8_t op1, vint16m8_t op2, size_t vl);
vbool2_t vmslt_vx_i16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
vint16m8_t op1, int16_t op2, size_t vl);
vbool32_t vmslt_vv_i32m1_b32_m (vbool32_t mask, vbool32_t
maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vbool32_t vmslt_vx_i32m1_b32_m (vbool32_t mask, vbool32_t
maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vbool16_t vmslt_vv_i32m2_b16_m (vbool16_t mask, vbool16_t
maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vbool16_t vmslt_vx_i32m2_b16_m (vbool16_t mask, vbool16_t
maskedoff, vint32m2_t op1, int32_t op2, size_t vl);

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```

vbool8_t vmslt_vv_i32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint32m4_t op1, vint32m4_t op2, size_t vl);
vbool8_t vmslt_vx_i32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint32m4_t op1, int32_t op2, size_t vl);
vbool4_t vmslt_vv_i32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint32m8_t op1, vint32m8_t op2, size_t vl);
vbool4_t vmslt_vx_i32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint32m8_t op1, int32_t op2, size_t vl);
vbool64_t vmslt_vv_i64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vbool64_t vmslt_vx_i64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vbool32_t vmslt_vv_i64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vbool32_t vmslt_vx_i64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vbool16_t vmslt_vv_i64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vbool16_t vmslt_vx_i64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vbool8_t vmslt_vv_i64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint64m8_t op1, vint64m8_t op2, size_t vl);
vbool8_t vmslt_vx_i64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint64m8_t op1, int64_t op2, size_t vl);
vbool8_t vmsltu_vv_u8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vbool8_t vmsltu_vx_u8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vbool4_t vmsltu_vv_u8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vbool4_t vmsltu_vx_u8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);
vbool2_t vmsltu_vv_u8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vbool2_t vmsltu_vx_u8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);
vbool1_t vmsltu_vv_u8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vbool1_t vmsltu_vx_u8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vbool16_t vmsltu_vv_u16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vbool16_t vmsltu_vx_u16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vbool8_t vmsltu_vv_u16m2_b8_m (vbool8_t mask, vbool8_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);

```

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vbool8_t vmsltu_vx_u16m2_b8_m (vbool8_t mask, vbool8_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vbool4_t vmsltu_vv_u16m4_b4_m (vbool4_t mask, vbool4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vbool4_t vmsltu_vx_u16m4_b4_m (vbool4_t mask, vbool4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vbool2_t vmsltu_vv_u16m8_b2_m (vbool2_t mask, vbool2_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vbool2_t vmsltu_vx_u16m8_b2_m (vbool2_t mask, vbool2_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vbool32_t vmsltu_vv_u32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vbool32_t vmsltu_vx_u32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vbool16_t vmsltu_vv_u32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vbool16_t vmsltu_vx_u32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vbool8_t vmsltu_vv_u32m4_b8_m (vbool8_t mask, vbool8_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vbool8_t vmsltu_vx_u32m4_b8_m (vbool8_t mask, vbool8_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vbool4_t vmsltu_vv_u32m8_b4_m (vbool4_t mask, vbool4_t
    maskedoff, vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vbool4_t vmsltu_vx_u32m8_b4_m (vbool4_t mask, vbool4_t
    maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);
vbool64_t vmsltu_vv_u64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vbool64_t vmsltu_vx_u64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vbool32_t vmsltu_vv_u64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vbool32_t vmsltu_vx_u64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vbool16_t vmsltu_vv_u64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);
vbool16_t vmsltu_vx_u64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vbool8_t vmsltu_vv_u64m8_b8_m (vbool8_t mask, vbool8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vbool8_t vmsltu_vx_u64m8_b8_m (vbool8_t mask, vbool8_t
    maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);
vbool8_t vmsle_vv_i8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vbool8_t vmsle_vx_i8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);

```



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vbool4_t vmsle_vv_i8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vbool4_t vmsle_vx_i8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vbool2_t vmsle_vv_i8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);
vbool2_t vmsle_vx_i8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vbool1_t vmsle_vv_i8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vbool1_t vmsle_vx_i8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vbool16_t vmsle_vv_i16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vbool16_t vmsle_vx_i16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vbool8_t vmsle_vv_i16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint16m2_t op1, vint16m2_t op2, size_t vl);
vbool8_t vmsle_vx_i16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint16m2_t op1, int16_t op2, size_t vl);
vbool4_t vmsle_vv_i16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint16m4_t op1, vint16m4_t op2, size_t vl);
vbool4_t vmsle_vx_i16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint16m4_t op1, int16_t op2, size_t vl);
vbool2_t vmsle_vv_i16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vint16m8_t op1, vint16m8_t op2, size_t vl);
vbool2_t vmsle_vx_i16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vint16m8_t op1, int16_t op2, size_t vl);
vbool32_t vmsle_vv_i32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vbool32_t vmsle_vx_i32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vbool16_t vmsle_vv_i32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vbool16_t vmsle_vx_i32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vbool8_t vmsle_vv_i32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint32m4_t op1, vint32m4_t op2, size_t vl);
vbool8_t vmsle_vx_i32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint32m4_t op1, int32_t op2, size_t vl);
vbool4_t vmsle_vv_i32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint32m8_t op1, vint32m8_t op2, size_t vl);
vbool4_t vmsle_vx_i32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint32m8_t op1, int32_t op2, size_t vl);
vbool64_t vmsle_vv_i64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);

```

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vbool64_t vmsle_vx_i64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vbool32_t vmsle_vv_i64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vbool32_t vmsle_vx_i64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vbool16_t vmsle_vv_i64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vbool16_t vmsle_vx_i64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vbool8_t vmsle_vv_i64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint64m8_t op1, vint64m8_t op2, size_t vl);
vbool8_t vmsle_vx_i64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint64m8_t op1, int64_t op2, size_t vl);
vbool8_t vmsleu_vv_u8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vbool8_t vmsleu_vx_u8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vbool4_t vmsleu_vv_u8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vbool4_t vmsleu_vx_u8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);
vbool2_t vmsleu_vv_u8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vbool2_t vmsleu_vx_u8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);
vbool1_t vmsleu_vv_u8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vbool1_t vmsleu_vx_u8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vbool16_t vmsleu_vv_u16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vbool16_t vmsleu_vx_u16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vbool8_t vmsleu_vv_u16m2_b8_m (vbool8_t mask, vbool8_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vbool8_t vmsleu_vx_u16m2_b8_m (vbool8_t mask, vbool8_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vbool4_t vmsleu_vv_u16m4_b4_m (vbool4_t mask, vbool4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vbool4_t vmsleu_vx_u16m4_b4_m (vbool4_t mask, vbool4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vbool2_t vmsleu_vv_u16m8_b2_m (vbool2_t mask, vbool2_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vbool2_t vmsleu_vx_u16m8_b2_m (vbool2_t mask, vbool2_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);

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vbool32_t vmsleu_vv_u32m1_b32_m (vbool32_t mask, vbool32_t
maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vbool32_t vmsleu_vx_u32m1_b32_m (vbool32_t mask, vbool32_t
maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vbool16_t vmsleu_vv_u32m2_b16_m (vbool16_t mask, vbool16_t
maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vbool16_t vmsleu_vx_u32m2_b16_m (vbool16_t mask, vbool16_t
maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vbool8_t vmsleu_vv_u32m4_b8_m (vbool8_t mask, vbool8_t
maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vbool8_t vmsleu_vx_u32m4_b8_m (vbool8_t mask, vbool8_t
maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vbool4_t vmsleu_vv_u32m8_b4_m (vbool4_t mask, vbool4_t
maskedoff, vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vbool4_t vmsleu_vx_u32m8_b4_m (vbool4_t mask, vbool4_t
maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);
vbool64_t vmsleu_vv_u64m1_b64_m (vbool64_t mask, vbool64_t
maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vbool64_t vmsleu_vx_u64m1_b64_m (vbool64_t mask, vbool64_t
maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vbool32_t vmsleu_vv_u64m2_b32_m (vbool32_t mask, vbool32_t
maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vbool32_t vmsleu_vx_u64m2_b32_m (vbool32_t mask, vbool32_t
maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vbool16_t vmsleu_vv_u64m4_b16_m (vbool16_t mask, vbool16_t
maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);
vbool16_t vmsleu_vx_u64m4_b16_m (vbool16_t mask, vbool16_t
maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vbool8_t vmsleu_vv_u64m8_b8_m (vbool8_t mask, vbool8_t
maskedoff, vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vbool8_t vmsleu_vx_u64m8_b8_m (vbool8_t mask, vbool8_t
maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);
vbool8_t vmsgt_vv_i8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
vint8m1_t op1, vint8m1_t op2, size_t vl);
vbool8_t vmsgt_vx_i8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
vint8m1_t op1, int8_t op2, size_t vl);
vbool4_t vmsgt_vv_i8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
vint8m2_t op1, vint8m2_t op2, size_t vl);
vbool4_t vmsgt_vx_i8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
vint8m2_t op1, int8_t op2, size_t vl);
vbool2_t vmsgt_vv_i8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
vint8m4_t op1, vint8m4_t op2, size_t vl);
vbool2_t vmsgt_vx_i8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
vint8m4_t op1, int8_t op2, size_t vl);
vbool1_t vmsgt_vv_i8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
vint8m8_t op1, vint8m8_t op2, size_t vl);

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vbool1_t vmsgt_vx_i8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vbool16_t vmsgt_vv_i16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vbool16_t vmsgt_vx_i16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vbool8_t vmsgt_vv_i16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint16m2_t op1, vint16m2_t op2, size_t vl);
vbool8_t vmsgt_vx_i16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint16m2_t op1, int16_t op2, size_t vl);
vbool4_t vmsgt_vv_i16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint16m4_t op1, vint16m4_t op2, size_t vl);
vbool4_t vmsgt_vx_i16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint16m4_t op1, int16_t op2, size_t vl);
vbool2_t vmsgt_vv_i16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vint16m8_t op1, vint16m8_t op2, size_t vl);
vbool2_t vmsgt_vx_i16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vint16m8_t op1, int16_t op2, size_t vl);
vbool32_t vmsgt_vv_i32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vbool32_t vmsgt_vx_i32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vbool16_t vmsgt_vv_i32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vbool16_t vmsgt_vx_i32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vbool8_t vmsgt_vv_i32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint32m4_t op1, vint32m4_t op2, size_t vl);
vbool8_t vmsgt_vx_i32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint32m4_t op1, int32_t op2, size_t vl);
vbool4_t vmsgt_vv_i32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint32m8_t op1, vint32m8_t op2, size_t vl);
vbool4_t vmsgt_vx_i32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint32m8_t op1, int32_t op2, size_t vl);
vbool64_t vmsgt_vv_i64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vbool64_t vmsgt_vx_i64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vbool32_t vmsgt_vv_i64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vbool32_t vmsgt_vx_i64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vbool16_t vmsgt_vv_i64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vbool16_t vmsgt_vx_i64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);

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vbool8_t vmsgt_vv_i64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint64m8_t op1, vint64m8_t op2, size_t vl);
vbool8_t vmsgt_vx_i64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint64m8_t op1, int64_t op2, size_t vl);
vbool8_t vmsgtu_vv_u8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vbool8_t vmsgtu_vx_u8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vbool4_t vmsgtu_vv_u8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vbool4_t vmsgtu_vx_u8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);
vbool2_t vmsgtu_vv_u8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vbool2_t vmsgtu_vx_u8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);
vbool1_t vmsgtu_vv_u8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vbool1_t vmsgtu_vx_u8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vbool16_t vmsgtu_vv_u16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vbool16_t vmsgtu_vx_u16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vbool8_t vmsgtu_vv_u16m2_b8_m (vbool8_t mask, vbool8_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vbool8_t vmsgtu_vx_u16m2_b8_m (vbool8_t mask, vbool8_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vbool4_t vmsgtu_vv_u16m4_b4_m (vbool4_t mask, vbool4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vbool4_t vmsgtu_vx_u16m4_b4_m (vbool4_t mask, vbool4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vbool2_t vmsgtu_vv_u16m8_b2_m (vbool2_t mask, vbool2_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vbool2_t vmsgtu_vx_u16m8_b2_m (vbool2_t mask, vbool2_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vbool32_t vmsgtu_vv_u32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vbool32_t vmsgtu_vx_u32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vbool16_t vmsgtu_vv_u32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vbool16_t vmsgtu_vx_u32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vbool8_t vmsgtu_vv_u32m4_b8_m (vbool8_t mask, vbool8_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);

```

```

vbool8_t vmsgtu_vx_u32m4_b8_m (vbool8_t mask, vbool8_t
    maskedoff, uint32_t op1, uint32_t op2, size_t vl);
vbool4_t vmsgtu_vv_u32m8_b4_m (vbool4_t mask, vbool4_t
    maskedoff, uint32_t op1, uint32_t op2, size_t vl);
vbool4_t vmsgtu_vx_u32m8_b4_m (vbool4_t mask, vbool4_t
    maskedoff, uint32_t op1, uint32_t op2, size_t vl);
vbool64_t vmsgtu_vv_u64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, uint64_t op1, uint64_t op2, size_t vl);
vbool64_t vmsgtu_vx_u64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, uint64_t op1, uint64_t op2, size_t vl);
vbool32_t vmsgtu_vv_u64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, uint64_t op1, uint64_t op2, size_t vl);
vbool32_t vmsgtu_vx_u64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, uint64_t op1, uint64_t op2, size_t vl);
vbool16_t vmsgtu_vv_u64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, uint64_t op1, uint64_t op2, size_t vl);
vbool16_t vmsgtu_vx_u64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, uint64_t op1, uint64_t op2, size_t vl);
vbool8_t vmsgtu_vv_u64m8_b8_m (vbool8_t mask, vbool8_t
    maskedoff, uint64_t op1, uint64_t op2, size_t vl);
vbool8_t vmsgtu_vx_u64m8_b8_m (vbool8_t mask, vbool8_t
    maskedoff, uint64_t op1, uint64_t op2, size_t vl);
vbool8_t vmsge_vv_i8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint8_t op1, vint8_t op2, size_t vl);
vbool8_t vmsge_vx_i8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint8_t op1, int8_t op2, size_t vl);
vbool4_t vmsge_vv_i8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint8_t op1, vint8_t op2, size_t vl);
vbool4_t vmsge_vx_i8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint8_t op1, int8_t op2, size_t vl);
vbool2_t vmsge_vv_i8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vint8_t op1, vint8_t op2, size_t vl);
vbool2_t vmsge_vx_i8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vint8_t op1, int8_t op2, size_t vl);
vbool1_t vmsge_vv_i8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vint8_t op1, vint8_t op2, size_t vl);
vbool1_t vmsge_vx_i8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vint8_t op1, int8_t op2, size_t vl);
vbool16_t vmsge_vv_i16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint16_t op1, vint16_t op2, size_t vl);
vbool16_t vmsge_vx_i16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint16_t op1, int16_t op2, size_t vl);
vbool8_t vmsge_vv_i16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint16_t op1, vint16_t op2, size_t vl);
vbool8_t vmsge_vx_i16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint16_t op1, int16_t op2, size_t vl);

```

```

vbool4_t vmsge_vv_i16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint16m4_t op1, vint16m4_t op2, size_t vl);
vbool4_t vmsge_vx_i16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint16m4_t op1, int16_t op2, size_t vl);
vbool2_t vmsge_vv_i16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vint16m8_t op1, vint16m8_t op2, size_t vl);
vbool2_t vmsge_vx_i16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vint16m8_t op1, int16_t op2, size_t vl);
vbool32_t vmsge_vv_i32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vbool32_t vmsge_vx_i32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vbool16_t vmsge_vv_i32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vbool16_t vmsge_vx_i32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vbool8_t vmsge_vv_i32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint32m4_t op1, vint32m4_t op2, size_t vl);
vbool8_t vmsge_vx_i32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint32m4_t op1, int32_t op2, size_t vl);
vbool4_t vmsge_vv_i32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint32m8_t op1, vint32m8_t op2, size_t vl);
vbool4_t vmsge_vx_i32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vint32m8_t op1, int32_t op2, size_t vl);
vbool64_t vmsge_vv_i64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vbool64_t vmsge_vx_i64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vbool32_t vmsge_vv_i64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vbool32_t vmsge_vx_i64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vbool16_t vmsge_vv_i64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vbool16_t vmsge_vx_i64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vbool8_t vmsge_vv_i64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint64m8_t op1, vint64m8_t op2, size_t vl);
vbool8_t vmsge_vx_i64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vint64m8_t op1, int64_t op2, size_t vl);
vbool8_t vmsgeu_vv_u8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vbool8_t vmsgeu_vx_u8m1_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vbool4_t vmsgeu_vv_u8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint8m2_t op1, vuint8m2_t op2, size_t vl);

```

```

vbool4_t vmsgeu_vx_u8m2_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);
vbool2_t vmsgeu_vv_u8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vbool2_t vmsgeu_vx_u8m4_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);
vbool1_t vmsgeu_vv_u8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vbool1_t vmsgeu_vx_u8m8_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vbool16_t vmsgeu_vv_u16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vbool16_t vmsgeu_vx_u16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vbool8_t vmsgeu_vv_u16m2_b8_m (vbool8_t mask, vbool8_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vbool8_t vmsgeu_vx_u16m2_b8_m (vbool8_t mask, vbool8_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vbool4_t vmsgeu_vv_u16m4_b4_m (vbool4_t mask, vbool4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vbool4_t vmsgeu_vx_u16m4_b4_m (vbool4_t mask, vbool4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vbool2_t vmsgeu_vv_u16m8_b2_m (vbool2_t mask, vbool2_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vbool2_t vmsgeu_vx_u16m8_b2_m (vbool2_t mask, vbool2_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vbool32_t vmsgeu_vv_u32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vbool32_t vmsgeu_vx_u32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vbool16_t vmsgeu_vv_u32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vbool16_t vmsgeu_vx_u32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vbool8_t vmsgeu_vv_u32m4_b8_m (vbool8_t mask, vbool8_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vbool8_t vmsgeu_vx_u32m4_b8_m (vbool8_t mask, vbool8_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vbool4_t vmsgeu_vv_u32m8_b4_m (vbool4_t mask, vbool4_t
    maskedoff, vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vbool4_t vmsgeu_vx_u32m8_b4_m (vbool4_t mask, vbool4_t
    maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);
vbool64_t vmsgeu_vv_u64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vbool64_t vmsgeu_vx_u64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);

```



```

vbool32_t vmsgeu_vv_u64m2_b32_m (vbool32_t mask, vbool32_t
maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vbool32_t vmsgeu_vx_u64m2_b32_m (vbool32_t mask, vbool32_t
maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vbool16_t vmsgeu_vv_u64m4_b16_m (vbool16_t mask, vbool16_t
maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);
vbool16_t vmsgeu_vx_u64m4_b16_m (vbool16_t mask, vbool16_t
maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vbool8_t vmsgeu_vv_u64m8_b8_m (vbool8_t mask, vbool8_t
maskedoff, vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vbool8_t vmsgeu_vx_u64m8_b8_m (vbool8_t mask, vbool8_t
maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);

```

### Vector Integer Min/Max Functions:

#### Prototypes:

```

vint8m1_t vmin_vv_i8m1 (vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vmin_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vmin_vv_i8m2 (vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vmin_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vmin_vv_i8m4 (vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vmin_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vmin_vv_i8m8 (vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vmin_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vmin_vv_i16m1 (vint16m1_t op1, vint16m1_t op2, size_t
vl);
vint16m1_t vmin_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t
vl);
vint16m2_t vmin_vv_i16m2 (vint16m2_t op1, vint16m2_t op2, size_t
vl);
vint16m2_t vmin_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t
vl);
vint16m4_t vmin_vv_i16m4 (vint16m4_t op1, vint16m4_t op2, size_t
vl);
vint16m4_t vmin_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t
vl);
vint16m8_t vmin_vv_i16m8 (vint16m8_t op1, vint16m8_t op2, size_t
vl);
vint16m8_t vmin_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t
vl);
vint32m1_t vmin_vv_i32m1 (vint32m1_t op1, vint32m1_t op2, size_t
vl);
vint32m1_t vmin_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t
vl);

```

```

vint32m2_t vmin_vv_i32m2 (vint32m2_t op1, vint32m2_t op2, size_t
    vl);
vint32m2_t vmin_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint32m4_t vmin_vv_i32m4 (vint32m4_t op1, vint32m4_t op2, size_t
    vl);
vint32m4_t vmin_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint32m8_t vmin_vv_i32m8 (vint32m8_t op1, vint32m8_t op2, size_t
    vl);
vint32m8_t vmin_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t
    vl);
vint64m1_t vmin_vv_i64m1 (vint64m1_t op1, vint64m1_t op2, size_t
    vl);
vint64m1_t vmin_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t
    vl);
vint64m2_t vmin_vv_i64m2 (vint64m2_t op1, vint64m2_t op2, size_t
    vl);
vint64m2_t vmin_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t
    vl);
vint64m4_t vmin_vv_i64m4 (vint64m4_t op1, vint64m4_t op2, size_t
    vl);
vint64m4_t vmin_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t
    vl);
vint64m8_t vmin_vv_i64m8 (vint64m8_t op1, vint64m8_t op2, size_t
    vl);
vint64m8_t vmin_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vuint8m1_t vminu_vv_u8m1 (vuint8m1_t op1, vuint8m1_t op2, size_t
    vl);
vuint8m1_t vminu_vx_u8m1 (vuint8m1_t op1, uint8_t op2, size_t
    vl);
vuint8m2_t vminu_vv_u8m2 (vuint8m2_t op1, vuint8m2_t op2, size_t
    vl);
vuint8m2_t vminu_vx_u8m2 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vuint8m4_t vminu_vv_u8m4 (vuint8m4_t op1, vuint8m4_t op2, size_t
    vl);
vuint8m4_t vminu_vx_u8m4 (vuint8m4_t op1, uint8_t op2, size_t
    vl);
vuint8m8_t vminu_vv_u8m8 (vuint8m8_t op1, vuint8m8_t op2, size_t
    vl);
vuint8m8_t vminu_vx_u8m8 (vuint8m8_t op1, uint8_t op2, size_t
    vl);
vuint16m1_t vminu_vv_u16m1 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);

```

```

vuint16m1_t vminu_vx_u16m1 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vuint16m2_t vminu_vv_u16m2 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vuint16m2_t vminu_vx_u16m2 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vuint16m4_t vminu_vv_u16m4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vuint16m4_t vminu_vx_u16m4 (vuint16m4_t op1, uint16_t op2,
    size_t vl);
vuint16m8_t vminu_vv_u16m8 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vuint16m8_t vminu_vx_u16m8 (vuint16m8_t op1, uint16_t op2,
    size_t vl);
vuint32m1_t vminu_vv_u32m1 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vuint32m1_t vminu_vx_u32m1 (vuint32m1_t op1, uint32_t op2,
    size_t vl);
vuint32m2_t vminu_vv_u32m2 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vuint32m2_t vminu_vx_u32m2 (vuint32m2_t op1, uint32_t op2,
    size_t vl);
vuint32m4_t vminu_vv_u32m4 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vuint32m4_t vminu_vx_u32m4 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vuint32m8_t vminu_vv_u32m8 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vuint32m8_t vminu_vx_u32m8 (vuint32m8_t op1, uint32_t op2,
    size_t vl);
vuint64m1_t vminu_vv_u64m1 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vuint64m1_t vminu_vx_u64m1 (vuint64m1_t op1, uint64_t op2,
    size_t vl);
vuint64m2_t vminu_vv_u64m2 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vuint64m2_t vminu_vx_u64m2 (vuint64m2_t op1, uint64_t op2,
    size_t vl);
vuint64m4_t vminu_vv_u64m4 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vuint64m4_t vminu_vx_u64m4 (vuint64m4_t op1, uint64_t op2,
    size_t vl);
vuint64m8_t vminu_vv_u64m8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vuint64m8_t vminu_vx_u64m8 (vuint64m8_t op1, uint64_t op2,
    size_t vl);

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vint8m1_t vmax_vv_i8m1 (vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vmax_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vmax_vv_i8m2 (vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vmax_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vmax_vv_i8m4 (vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vmax_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vmax_vv_i8m8 (vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vmax_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vmax_vv_i16m1 (vint16m1_t op1, vint16m1_t op2, size_t
    vl);
vint16m1_t vmax_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint16m2_t vmax_vv_i16m2 (vint16m2_t op1, vint16m2_t op2, size_t
    vl);
vint16m2_t vmax_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint16m4_t vmax_vv_i16m4 (vint16m4_t op1, vint16m4_t op2, size_t
    vl);
vint16m4_t vmax_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vint16m8_t vmax_vv_i16m8 (vint16m8_t op1, vint16m8_t op2, size_t
    vl);
vint16m8_t vmax_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t
    vl);
vint32m1_t vmax_vv_i32m1 (vint32m1_t op1, vint32m1_t op2, size_t
    vl);
vint32m1_t vmax_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint32m2_t vmax_vv_i32m2 (vint32m2_t op1, vint32m2_t op2, size_t
    vl);
vint32m2_t vmax_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint32m4_t vmax_vv_i32m4 (vint32m4_t op1, vint32m4_t op2, size_t
    vl);
vint32m4_t vmax_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint32m8_t vmax_vv_i32m8 (vint32m8_t op1, vint32m8_t op2, size_t
    vl);
vint32m8_t vmax_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t
    vl);
vint64m1_t vmax_vv_i64m1 (vint64m1_t op1, vint64m1_t op2, size_t
    vl);
vint64m1_t vmax_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t
    vl);
vint64m2_t vmax_vv_i64m2 (vint64m2_t op1, vint64m2_t op2, size_t
    vl);

```

```

vint64m2_t vmax_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t
    vl);
vint64m4_t vmax_vv_i64m4 (vint64m4_t op1, vint64m4_t op2, size_t
    vl);
vint64m4_t vmax_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t
    vl);
vint64m8_t vmax_vv_i64m8 (vint64m8_t op1, vint64m8_t op2, size_t
    vl);
vint64m8_t vmax_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vuint8m1_t vmaxu_vv_u8m1 (vuint8m1_t op1, vuint8m1_t op2, size_t
    vl);
vuint8m1_t vmaxu_vx_u8m1 (vuint8m1_t op1, uint8_t op2, size_t
    vl);
vuint8m2_t vmaxu_vv_u8m2 (vuint8m2_t op1, vuint8m2_t op2, size_t
    vl);
vuint8m2_t vmaxu_vx_u8m2 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vuint8m4_t vmaxu_vv_u8m4 (vuint8m4_t op1, vuint8m4_t op2, size_t
    vl);
vuint8m4_t vmaxu_vx_u8m4 (vuint8m4_t op1, uint8_t op2, size_t
    vl);
vuint8m8_t vmaxu_vv_u8m8 (vuint8m8_t op1, vuint8m8_t op2, size_t
    vl);
vuint8m8_t vmaxu_vx_u8m8 (vuint8m8_t op1, uint8_t op2, size_t
    vl);
vuint16m1_t vmaxu_vv_u16m1 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vuint16m1_t vmaxu_vx_u16m1 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vuint16m2_t vmaxu_vv_u16m2 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vuint16m2_t vmaxu_vx_u16m2 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vuint16m4_t vmaxu_vv_u16m4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vuint16m4_t vmaxu_vx_u16m4 (vuint16m4_t op1, uint16_t op2,
    size_t vl);
vuint16m8_t vmaxu_vv_u16m8 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vuint16m8_t vmaxu_vx_u16m8 (vuint16m8_t op1, uint16_t op2,
    size_t vl);
vuint32m1_t vmaxu_vv_u32m1 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vuint32m1_t vmaxu_vx_u32m1 (vuint32m1_t op1, uint32_t op2,
    size_t vl);

```

```

vuint32m2_t vmaxu_vv_u32m2 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vuint32m2_t vmaxu_vx_u32m2 (vuint32m2_t op1, uint32_t op2,
    size_t vl);
vuint32m4_t vmaxu_vv_u32m4 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vuint32m4_t vmaxu_vx_u32m4 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vuint32m8_t vmaxu_vv_u32m8 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vuint32m8_t vmaxu_vx_u32m8 (vuint32m8_t op1, uint32_t op2,
    size_t vl);
vuint64m1_t vmaxu_vv_u64m1 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vuint64m1_t vmaxu_vx_u64m1 (vuint64m1_t op1, uint64_t op2,
    size_t vl);
vuint64m2_t vmaxu_vv_u64m2 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vuint64m2_t vmaxu_vx_u64m2 (vuint64m2_t op1, uint64_t op2,
    size_t vl);
vuint64m4_t vmaxu_vv_u64m4 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vuint64m4_t vmaxu_vx_u64m4 (vuint64m4_t op1, uint64_t op2,
    size_t vl);
vuint64m8_t vmaxu_vv_u64m8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vuint64m8_t vmaxu_vx_u64m8 (vuint64m8_t op1, uint64_t op2,
    size_t vl);
// masked functions
vint8m1_t vmin_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vmin_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vmin_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vmin_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vmin_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vmin_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vmin_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vmin_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);

```

```

vint16m1_t vmin_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint16m1_t vmin_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vint16m2_t vmin_vv_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, vint16m2_t op2, size_t vl);
vint16m2_t vmin_vx_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vmin_vv_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, vint16m4_t op2, size_t vl);
vint16m4_t vmin_vx_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vmin_vv_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, vint16m8_t op2, size_t vl);
vint16m8_t vmin_vx_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vmin_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint32m1_t vmin_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vmin_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vint32m2_t vmin_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint32m4_t vmin_vv_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, vint32m4_t op2, size_t vl);
vint32m4_t vmin_vx_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vmin_vv_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, vint32m8_t op2, size_t vl);
vint32m8_t vmin_vx_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vmin_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vint64m1_t vmin_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vmin_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vint64m2_t vmin_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vint64m4_t vmin_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vint64m4_t vmin_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vmin_vv_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, vint64m8_t op2, size_t vl);

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vint64m8_t vmin_vx_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, int64_t op2, size_t vl);
vuint8m1_t vminu_vv_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vuint8m1_t vminu_vx_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vminu_vv_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vuint8m2_t vminu_vx_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vminu_vv_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vuint8m4_t vminu_vx_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vminu_vv_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vuint8m8_t vminu_vx_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vminu_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vuint16m1_t vminu_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint16m2_t vminu_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vuint16m2_t vminu_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint16m4_t vminu_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vuint16m4_t vminu_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vuint16m8_t vminu_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vuint16m8_t vminu_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vuint32m1_t vminu_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vuint32m1_t vminu_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vuint32m2_t vminu_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vuint32m2_t vminu_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint32m4_t vminu_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vuint32m4_t vminu_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);

```



```

vuint32m8_t vminu_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vuint32m8_t vminu_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);
vuint64m1_t vminu_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vuint64m1_t vminu_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vuint64m2_t vminu_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vuint64m2_t vminu_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vuint64m4_t vminu_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);
vuint64m4_t vminu_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vuint64m8_t vminu_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vuint64m8_t vminu_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);
vint8m1_t vmax_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vmax_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vmax_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vmax_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vmax_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vmax_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vmax_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vmax_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vmax_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint16m1_t vmax_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vint16m2_t vmax_vv_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, vint16m2_t op2, size_t vl);
vint16m2_t vmax_vx_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vmax_vv_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, vint16m4_t op2, size_t vl);

```

```

vint16m4_t vmax_vx_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vmax_vv_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, vint16m8_t op2, size_t vl);
vint16m8_t vmax_vx_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vmax_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint32m1_t vmax_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vmax_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vint32m2_t vmax_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint32m4_t vmax_vv_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, vint32m4_t op2, size_t vl);
vint32m4_t vmax_vx_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vmax_vv_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, vint32m8_t op2, size_t vl);
vint32m8_t vmax_vx_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vmax_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vint64m1_t vmax_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vmax_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vint64m2_t vmax_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vint64m4_t vmax_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vint64m4_t vmax_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vmax_vv_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, vint64m8_t op2, size_t vl);
vint64m8_t vmax_vx_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, int64_t op2, size_t vl);
vuint8m1_t vmaxu_vv_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vuint8m1_t vmaxu_vx_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vmaxu_vv_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vuint8m2_t vmaxu_vx_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);

```

```

vuint8m4_t vmaxu_vv_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vuint8m4_t vmaxu_vx_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vmaxu_vv_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vuint8m8_t vmaxu_vx_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vmaxu_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vuint16m1_t vmaxu_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint16m2_t vmaxu_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vuint16m2_t vmaxu_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint16m4_t vmaxu_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vuint16m4_t vmaxu_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vuint16m8_t vmaxu_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vuint16m8_t vmaxu_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vuint32m1_t vmaxu_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vuint32m1_t vmaxu_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vuint32m2_t vmaxu_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vuint32m2_t vmaxu_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint32m4_t vmaxu_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vuint32m4_t vmaxu_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vuint32m8_t vmaxu_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vuint32m8_t vmaxu_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);
vuint64m1_t vmaxu_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vuint64m1_t vmaxu_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vuint64m2_t vmaxu_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);

```

```

vuint64m2_t vmaxu_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vuint64m4_t vmaxu_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);
vuint64m4_t vmaxu_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vuint64m8_t vmaxu_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vuint64m8_t vmaxu_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);

```

### Vector Single-Width Integer Multiply Functions:

#### Prototypes:

```

vint8m1_t vmul_vv_i8m1 (vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vmul_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vmul_vv_i8m2 (vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vmul_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vmul_vv_i8m4 (vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vmul_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vmul_vv_i8m8 (vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vmul_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vmul_vv_i16m1 (vint16m1_t op1, vint16m1_t op2, size_t
    vl);
vint16m1_t vmul_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint16m2_t vmul_vv_i16m2 (vint16m2_t op1, vint16m2_t op2, size_t
    vl);
vint16m2_t vmul_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint16m4_t vmul_vv_i16m4 (vint16m4_t op1, vint16m4_t op2, size_t
    vl);
vint16m4_t vmul_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vint16m8_t vmul_vv_i16m8 (vint16m8_t op1, vint16m8_t op2, size_t
    vl);
vint16m8_t vmul_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t
    vl);
vint32m1_t vmul_vv_i32m1 (vint32m1_t op1, vint32m1_t op2, size_t
    vl);
vint32m1_t vmul_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint32m2_t vmul_vv_i32m2 (vint32m2_t op1, vint32m2_t op2, size_t
    vl);

```

```

vint32m2_t vmul_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint32m4_t vmul_vv_i32m4 (vint32m4_t op1, vint32m4_t op2, size_t
    vl);
vint32m4_t vmul_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint32m8_t vmul_vv_i32m8 (vint32m8_t op1, vint32m8_t op2, size_t
    vl);
vint32m8_t vmul_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t
    vl);
vint64m1_t vmul_vv_i64m1 (vint64m1_t op1, vint64m1_t op2, size_t
    vl);
vint64m1_t vmul_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t
    vl);
vint64m2_t vmul_vv_i64m2 (vint64m2_t op1, vint64m2_t op2, size_t
    vl);
vint64m2_t vmul_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t
    vl);
vint64m4_t vmul_vv_i64m4 (vint64m4_t op1, vint64m4_t op2, size_t
    vl);
vint64m4_t vmul_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t
    vl);
vint64m8_t vmul_vv_i64m8 (vint64m8_t op1, vint64m8_t op2, size_t
    vl);
vint64m8_t vmul_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vuint8m1_t vmul_vv_u8m1 (vuint8m1_t op1, vuint8m1_t op2, size_t
    vl);
vuint8m1_t vmul_vx_u8m1 (vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vmul_vv_u8m2 (vuint8m2_t op1, vuint8m2_t op2, size_t
    vl);
vuint8m2_t vmul_vx_u8m2 (vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vmul_vv_u8m4 (vuint8m4_t op1, vuint8m4_t op2, size_t
    vl);
vuint8m4_t vmul_vx_u8m4 (vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vmul_vv_u8m8 (vuint8m8_t op1, vuint8m8_t op2, size_t
    vl);
vuint8m8_t vmul_vx_u8m8 (vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vmul_vv_u16m1 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vuint16m1_t vmul_vx_u16m1 (vuint16m1_t op1, uint16_t op2, size_t
    vl);
vuint16m2_t vmul_vv_u16m2 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vuint16m2_t vmul_vx_u16m2 (vuint16m2_t op1, uint16_t op2, size_t
    vl);

```

```

vuint16m4_t vmul_vv_u16m4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vuint16m4_t vmul_vx_u16m4 (vuint16m4_t op1, uint16_t op2, size_t
    vl);
vuint16m8_t vmul_vv_u16m8 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vuint16m8_t vmul_vx_u16m8 (vuint16m8_t op1, uint16_t op2, size_t
    vl);
vuint32m1_t vmul_vv_u32m1 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vuint32m1_t vmul_vx_u32m1 (vuint32m1_t op1, uint32_t op2, size_t
    vl);
vuint32m2_t vmul_vv_u32m2 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vuint32m2_t vmul_vx_u32m2 (vuint32m2_t op1, uint32_t op2, size_t
    vl);
vuint32m4_t vmul_vv_u32m4 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vuint32m4_t vmul_vx_u32m4 (vuint32m4_t op1, uint32_t op2, size_t
    vl);
vuint32m8_t vmul_vv_u32m8 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vuint32m8_t vmul_vx_u32m8 (vuint32m8_t op1, uint32_t op2, size_t
    vl);
vuint64m1_t vmul_vv_u64m1 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vuint64m1_t vmul_vx_u64m1 (vuint64m1_t op1, uint64_t op2, size_t
    vl);
vuint64m2_t vmul_vv_u64m2 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vuint64m2_t vmul_vx_u64m2 (vuint64m2_t op1, uint64_t op2, size_t
    vl);
vuint64m4_t vmul_vv_u64m4 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vuint64m4_t vmul_vx_u64m4 (vuint64m4_t op1, uint64_t op2, size_t
    vl);
vuint64m8_t vmul_vv_u64m8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vuint64m8_t vmul_vx_u64m8 (vuint64m8_t op1, uint64_t op2, size_t
    vl);
vint8m1_t vmulh_vv_i8m1 (vint8m1_t op1, vint8m1_t op2, size_t
    vl);
vint8m1_t vmulh_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vmulh_vv_i8m2 (vint8m2_t op1, vint8m2_t op2, size_t
    vl);
vint8m2_t vmulh_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);

```

```

vint8m4_t vmulh_vv_i8m4 (vint8m4_t op1, vint8m4_t op2, size_t
    vl);
vint8m4_t vmulh_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vmulh_vv_i8m8 (vint8m8_t op1, vint8m8_t op2, size_t
    vl);
vint8m8_t vmulh_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vmulh_vv_i16m1 (vint16m1_t op1, vint16m1_t op2,
    size_t vl);
vint16m1_t vmulh_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint16m2_t vmulh_vv_i16m2 (vint16m2_t op1, vint16m2_t op2,
    size_t vl);
vint16m2_t vmulh_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint16m4_t vmulh_vv_i16m4 (vint16m4_t op1, vint16m4_t op2,
    size_t vl);
vint16m4_t vmulh_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vint16m8_t vmulh_vv_i16m8 (vint16m8_t op1, vint16m8_t op2,
    size_t vl);
vint16m8_t vmulh_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t
    vl);
vint32m1_t vmulh_vv_i32m1 (vint32m1_t op1, vint32m1_t op2,
    size_t vl);
vint32m1_t vmulh_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint32m2_t vmulh_vv_i32m2 (vint32m2_t op1, vint32m2_t op2,
    size_t vl);
vint32m2_t vmulh_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint32m4_t vmulh_vv_i32m4 (vint32m4_t op1, vint32m4_t op2,
    size_t vl);
vint32m4_t vmulh_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint32m8_t vmulh_vv_i32m8 (vint32m8_t op1, vint32m8_t op2,
    size_t vl);
vint32m8_t vmulh_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t
    vl);
vint64m1_t vmulh_vv_i64m1 (vint64m1_t op1, vint64m1_t op2,
    size_t vl);
vint64m1_t vmulh_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t
    vl);
vint64m2_t vmulh_vv_i64m2 (vint64m2_t op1, vint64m2_t op2,
    size_t vl);
vint64m2_t vmulh_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t
    vl);

```

```

vint64m4_t vmulh_vv_i64m4 (vint64m4_t op1, vint64m4_t op2,
    size_t vl);
vint64m4_t vmulh_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t
    vl);
vint64m8_t vmulh_vv_i64m8 (vint64m8_t op1, vint64m8_t op2,
    size_t vl);
vint64m8_t vmulh_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vuint8m1_t vmulhu_vv_u8m1 (vuint8m1_t op1, vuint8m1_t op2,
    size_t vl);
vuint8m1_t vmulhu_vx_u8m1 (vuint8m1_t op1, uint8_t op2, size_t
    vl);
vuint8m2_t vmulhu_vv_u8m2 (vuint8m2_t op1, vuint8m2_t op2,
    size_t vl);
vuint8m2_t vmulhu_vx_u8m2 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vuint8m4_t vmulhu_vv_u8m4 (vuint8m4_t op1, vuint8m4_t op2,
    size_t vl);
vuint8m4_t vmulhu_vx_u8m4 (vuint8m4_t op1, uint8_t op2, size_t
    vl);
vuint8m8_t vmulhu_vv_u8m8 (vuint8m8_t op1, vuint8m8_t op2,
    size_t vl);
vuint8m8_t vmulhu_vx_u8m8 (vuint8m8_t op1, uint8_t op2, size_t
    vl);
vuint16m1_t vmulhu_vv_u16m1 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vuint16m1_t vmulhu_vx_u16m1 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vuint16m2_t vmulhu_vv_u16m2 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vuint16m2_t vmulhu_vx_u16m2 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vuint16m4_t vmulhu_vv_u16m4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vuint16m4_t vmulhu_vx_u16m4 (vuint16m4_t op1, uint16_t op2,
    size_t vl);
vuint16m8_t vmulhu_vv_u16m8 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vuint16m8_t vmulhu_vx_u16m8 (vuint16m8_t op1, uint16_t op2,
    size_t vl);
vuint32m1_t vmulhu_vv_u32m1 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vuint32m1_t vmulhu_vx_u32m1 (vuint32m1_t op1, uint32_t op2,
    size_t vl);
vuint32m2_t vmulhu_vv_u32m2 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);

```



```

vuint32m2_t vmulhu_vx_u32m2 (vuint32m2_t op1, uint32_t op2,
    size_t vl);
vuint32m4_t vmulhu_vv_u32m4 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vuint32m4_t vmulhu_vx_u32m4 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vuint32m8_t vmulhu_vv_u32m8 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vuint32m8_t vmulhu_vx_u32m8 (vuint32m8_t op1, uint32_t op2,
    size_t vl);
vuint64m1_t vmulhu_vv_u64m1 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vuint64m1_t vmulhu_vx_u64m1 (vuint64m1_t op1, uint64_t op2,
    size_t vl);
vuint64m2_t vmulhu_vv_u64m2 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vuint64m2_t vmulhu_vx_u64m2 (vuint64m2_t op1, uint64_t op2,
    size_t vl);
vuint64m4_t vmulhu_vv_u64m4 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vuint64m4_t vmulhu_vx_u64m4 (vuint64m4_t op1, uint64_t op2,
    size_t vl);
vuint64m8_t vmulhu_vv_u64m8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vuint64m8_t vmulhu_vx_u64m8 (vuint64m8_t op1, uint64_t op2,
    size_t vl);
vint8m1_t vmulhsu_vv_i8m1 (vint8m1_t op1, vuint8m1_t op2, size_t
    vl);
vint8m1_t vmulhsu_vx_i8m1 (vint8m1_t op1, uint8_t op2, size_t
    vl);
vint8m2_t vmulhsu_vv_i8m2 (vint8m2_t op1, vuint8m2_t op2, size_t
    vl);
vint8m2_t vmulhsu_vx_i8m2 (vint8m2_t op1, uint8_t op2, size_t
    vl);
vint8m4_t vmulhsu_vv_i8m4 (vint8m4_t op1, vuint8m4_t op2, size_t
    vl);
vint8m4_t vmulhsu_vx_i8m4 (vint8m4_t op1, uint8_t op2, size_t
    vl);
vint8m8_t vmulhsu_vv_i8m8 (vint8m8_t op1, vuint8m8_t op2, size_t
    vl);
vint8m8_t vmulhsu_vx_i8m8 (vint8m8_t op1, uint8_t op2, size_t
    vl);
vint16m1_t vmulhsu_vv_i16m1 (vint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vint16m1_t vmulhsu_vx_i16m1 (vint16m1_t op1, uint16_t op2,
    size_t vl);

```

```

vint16m2_t vmulhsu_vv_i16m2 (vint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vint16m2_t vmulhsu_vx_i16m2 (vint16m2_t op1, uint16_t op2,
    size_t vl);
vint16m4_t vmulhsu_vv_i16m4 (vint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vint16m4_t vmulhsu_vx_i16m4 (vint16m4_t op1, uint16_t op2,
    size_t vl);
vint16m8_t vmulhsu_vv_i16m8 (vint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vint16m8_t vmulhsu_vx_i16m8 (vint16m8_t op1, uint16_t op2,
    size_t vl);
vint32m1_t vmulhsu_vv_i32m1 (vint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vint32m1_t vmulhsu_vx_i32m1 (vint32m1_t op1, uint32_t op2,
    size_t vl);
vint32m2_t vmulhsu_vv_i32m2 (vint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vint32m2_t vmulhsu_vx_i32m2 (vint32m2_t op1, uint32_t op2,
    size_t vl);
vint32m4_t vmulhsu_vv_i32m4 (vint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vint32m4_t vmulhsu_vx_i32m4 (vint32m4_t op1, uint32_t op2,
    size_t vl);
vint32m8_t vmulhsu_vv_i32m8 (vint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vint32m8_t vmulhsu_vx_i32m8 (vint32m8_t op1, uint32_t op2,
    size_t vl);
vint64m1_t vmulhsu_vv_i64m1 (vint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vint64m1_t vmulhsu_vx_i64m1 (vint64m1_t op1, uint64_t op2,
    size_t vl);
vint64m2_t vmulhsu_vv_i64m2 (vint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vint64m2_t vmulhsu_vx_i64m2 (vint64m2_t op1, uint64_t op2,
    size_t vl);
vint64m4_t vmulhsu_vv_i64m4 (vint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vint64m4_t vmulhsu_vx_i64m4 (vint64m4_t op1, uint64_t op2,
    size_t vl);
vint64m8_t vmulhsu_vv_i64m8 (vint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vint64m8_t vmulhsu_vx_i64m8 (vint64m8_t op1, uint64_t op2,
    size_t vl);
// masked functions

```

```

vint8m1_t vmul_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vmul_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vmul_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vmul_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vmul_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vmul_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vmul_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vmul_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vmul_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint16m1_t vmul_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vint16m2_t vmul_vv_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, vint16m2_t op2, size_t vl);
vint16m2_t vmul_vx_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vmul_vv_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, vint16m4_t op2, size_t vl);
vint16m4_t vmul_vx_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vmul_vv_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, vint16m8_t op2, size_t vl);
vint16m8_t vmul_vx_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vmul_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint32m1_t vmul_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vmul_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vint32m2_t vmul_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint32m4_t vmul_vv_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, vint32m4_t op2, size_t vl);
vint32m4_t vmul_vx_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vmul_vv_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, vint32m8_t op2, size_t vl);

```

```

vint32m8_t vmul_vx_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vmul_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vint64m1_t vmul_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vmul_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vint64m2_t vmul_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vint64m4_t vmul_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vint64m4_t vmul_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vmul_vv_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, vint64m8_t op2, size_t vl);
vint64m8_t vmul_vx_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, int64_t op2, size_t vl);
vuint8m1_t vmul_vv_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vuint8m1_t vmul_vx_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vmul_vv_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vuint8m2_t vmul_vx_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vmul_vv_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vuint8m4_t vmul_vx_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vmul_vv_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vuint8m8_t vmul_vx_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vmul_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vuint16m1_t vmul_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint16m2_t vmul_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vuint16m2_t vmul_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint16m4_t vmul_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vuint16m4_t vmul_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);

```

```

vuint16m8_t vmul_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vuint16m8_t vmul_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vuint32m1_t vmul_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vuint32m1_t vmul_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vuint32m2_t vmul_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vuint32m2_t vmul_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint32m4_t vmul_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vuint32m4_t vmul_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vuint32m8_t vmul_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vuint32m8_t vmul_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);
vuint64m1_t vmul_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vuint64m1_t vmul_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vuint64m2_t vmul_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vuint64m2_t vmul_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vuint64m4_t vmul_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);
vuint64m4_t vmul_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vuint64m8_t vmul_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vuint64m8_t vmul_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);
vint8m1_t vmulh_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vmulh_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vmulh_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vmulh_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vmulh_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);

```

```

vint8m4_t vmulh_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vmulh_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vmulh_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vmulh_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint16m1_t vmulh_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vint16m2_t vmulh_vv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, vint16m2_t op2, size_t vl);
vint16m2_t vmulh_vx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vmulh_vv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, vint16m4_t op2, size_t vl);
vint16m4_t vmulh_vx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vmulh_vv_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, vint16m8_t op2, size_t vl);
vint16m8_t vmulh_vx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vmulh_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint32m1_t vmulh_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vmulh_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vint32m2_t vmulh_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint32m4_t vmulh_vv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, vint32m4_t op2, size_t vl);
vint32m4_t vmulh_vx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vmulh_vv_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, vint32m8_t op2, size_t vl);
vint32m8_t vmulh_vx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vmulh_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vint64m1_t vmulh_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vmulh_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vint64m2_t vmulh_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);

```

```

vint64m4_t vmulh_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vint64m4_t vmulh_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vmulh_vv_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, vint64m8_t op2, size_t vl);
vint64m8_t vmulh_vx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, int64_t op2, size_t vl);
vuint8m1_t vmulhu_vv_u8m1_m (vbool8_t mask, vuint8m1_t
    maskedoff, vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vuint8m1_t vmulhu_vx_u8m1_m (vbool8_t mask, vuint8m1_t
    maskedoff, vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vmulhu_vv_u8m2_m (vbool4_t mask, vuint8m2_t
    maskedoff, vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vuint8m2_t vmulhu_vx_u8m2_m (vbool4_t mask, vuint8m2_t
    maskedoff, vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vmulhu_vv_u8m4_m (vbool2_t mask, vuint8m4_t
    maskedoff, vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vuint8m4_t vmulhu_vx_u8m4_m (vbool2_t mask, vuint8m4_t
    maskedoff, vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vmulhu_vv_u8m8_m (vbool1_t mask, vuint8m8_t
    maskedoff, vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vuint8m8_t vmulhu_vx_u8m8_m (vbool1_t mask, vuint8m8_t
    maskedoff, vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vmulhu_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vuint16m1_t vmulhu_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint16m2_t vmulhu_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vuint16m2_t vmulhu_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint16m4_t vmulhu_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vuint16m4_t vmulhu_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vuint16m8_t vmulhu_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vuint16m8_t vmulhu_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vuint32m1_t vmulhu_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vuint32m1_t vmulhu_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vuint32m2_t vmulhu_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);

```

```

vuint32m2_t vmulhu_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint32m4_t vmulhu_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vuint32m4_t vmulhu_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vuint32m8_t vmulhu_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vuint32m8_t vmulhu_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);
vuint64m1_t vmulhu_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vuint64m1_t vmulhu_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vuint64m2_t vmulhu_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vuint64m2_t vmulhu_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vuint64m4_t vmulhu_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);
vuint64m4_t vmulhu_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vuint64m8_t vmulhu_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vuint64m8_t vmulhu_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);
vint8m1_t vmulhsu_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vuint8m1_t op2, size_t vl);
vint8m1_t vmulhsu_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, uint8_t op2, size_t vl);
vint8m2_t vmulhsu_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vuint8m2_t op2, size_t vl);
vint8m2_t vmulhsu_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, uint8_t op2, size_t vl);
vint8m4_t vmulhsu_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vuint8m4_t op2, size_t vl);
vint8m4_t vmulhsu_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, uint8_t op2, size_t vl);
vint8m8_t vmulhsu_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vuint8m8_t op2, size_t vl);
vint8m8_t vmulhsu_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, uint8_t op2, size_t vl);
vint16m1_t vmulhsu_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vuint16m1_t op2, size_t vl);
vint16m1_t vmulhsu_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, uint16_t op2, size_t vl);

```



```

vint16m2_t vmulhsu_vv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, vuint16m2_t op2, size_t vl);
vint16m2_t vmulhsu_vx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, uint16_t op2, size_t vl);
vint16m4_t vmulhsu_vv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, vuint16m4_t op2, size_t vl);
vint16m4_t vmulhsu_vx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, uint16_t op2, size_t vl);
vint16m8_t vmulhsu_vv_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, vuint16m8_t op2, size_t vl);
vint16m8_t vmulhsu_vx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, uint16_t op2, size_t vl);
vint32m1_t vmulhsu_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vuint32m1_t op2, size_t vl);
vint32m1_t vmulhsu_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, uint32_t op2, size_t vl);
vint32m2_t vmulhsu_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vuint32m2_t op2, size_t vl);
vint32m2_t vmulhsu_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, uint32_t op2, size_t vl);
vint32m4_t vmulhsu_vv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, vuint32m4_t op2, size_t vl);
vint32m4_t vmulhsu_vx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, uint32_t op2, size_t vl);
vint32m8_t vmulhsu_vv_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, vuint32m8_t op2, size_t vl);
vint32m8_t vmulhsu_vx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, uint32_t op2, size_t vl);
vint64m1_t vmulhsu_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vuint64m1_t op2, size_t vl);
vint64m1_t vmulhsu_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, uint64_t op2, size_t vl);
vint64m2_t vmulhsu_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vuint64m2_t op2, size_t vl);
vint64m2_t vmulhsu_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, uint64_t op2, size_t vl);
vint64m4_t vmulhsu_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vuint64m4_t op2, size_t vl);
vint64m4_t vmulhsu_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, uint64_t op2, size_t vl);
vint64m8_t vmulhsu_vv_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, vuint64m8_t op2, size_t vl);
vint64m8_t vmulhsu_vx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, uint64_t op2, size_t vl);

```

## Vector Integer Divide Functions:

### Prototypes:

```
vint8m1_t vdiv_vv_i8m1 (vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vdiv_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vdiv_vv_i8m2 (vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vdiv_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vdiv_vv_i8m4 (vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vdiv_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vdiv_vv_i8m8 (vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vdiv_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vdiv_vv_i16m1 (vint16m1_t op1, vint16m1_t op2, size_t
vl);
vint16m1_t vdiv_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t
vl);
vint16m2_t vdiv_vv_i16m2 (vint16m2_t op1, vint16m2_t op2, size_t
vl);
vint16m2_t vdiv_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t
vl);
vint16m4_t vdiv_vv_i16m4 (vint16m4_t op1, vint16m4_t op2, size_t
vl);
vint16m4_t vdiv_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t
vl);
vint16m8_t vdiv_vv_i16m8 (vint16m8_t op1, vint16m8_t op2, size_t
vl);
vint16m8_t vdiv_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t
vl);
vint32m1_t vdiv_vv_i32m1 (vint32m1_t op1, vint32m1_t op2, size_t
vl);
vint32m1_t vdiv_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t
vl);
vint32m2_t vdiv_vv_i32m2 (vint32m2_t op1, vint32m2_t op2, size_t
vl);
vint32m2_t vdiv_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t
vl);
vint32m4_t vdiv_vv_i32m4 (vint32m4_t op1, vint32m4_t op2, size_t
vl);
vint32m4_t vdiv_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t
vl);
vint32m8_t vdiv_vv_i32m8 (vint32m8_t op1, vint32m8_t op2, size_t
vl);
vint32m8_t vdiv_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t
vl);
vint64m1_t vdiv_vv_i64m1 (vint64m1_t op1, vint64m1_t op2, size_t
vl);
```

```

vint64m1_t vdiv_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t
    vl);
vint64m2_t vdiv_vv_i64m2 (vint64m2_t op1, vint64m2_t op2, size_t
    vl);
vint64m2_t vdiv_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t
    vl);
vint64m4_t vdiv_vv_i64m4 (vint64m4_t op1, vint64m4_t op2, size_t
    vl);
vint64m4_t vdiv_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t
    vl);
vint64m8_t vdiv_vv_i64m8 (vint64m8_t op1, vint64m8_t op2, size_t
    vl);
vint64m8_t vdiv_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vuint8m1_t vdivu_vv_u8m1 (vuint8m1_t op1, vuint8m1_t op2, size_t
    vl);
vuint8m1_t vdivu_vx_u8m1 (vuint8m1_t op1, uint8_t op2, size_t
    vl);
vuint8m2_t vdivu_vv_u8m2 (vuint8m2_t op1, vuint8m2_t op2, size_t
    vl);
vuint8m2_t vdivu_vx_u8m2 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vuint8m4_t vdivu_vv_u8m4 (vuint8m4_t op1, vuint8m4_t op2, size_t
    vl);
vuint8m4_t vdivu_vx_u8m4 (vuint8m4_t op1, uint8_t op2, size_t
    vl);
vuint8m8_t vdivu_vv_u8m8 (vuint8m8_t op1, vuint8m8_t op2, size_t
    vl);
vuint8m8_t vdivu_vx_u8m8 (vuint8m8_t op1, uint8_t op2, size_t
    vl);
vuint16m1_t vdivu_vv_u16m1 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vuint16m1_t vdivu_vx_u16m1 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vuint16m2_t vdivu_vv_u16m2 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vuint16m2_t vdivu_vx_u16m2 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vuint16m4_t vdivu_vv_u16m4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vuint16m4_t vdivu_vx_u16m4 (vuint16m4_t op1, uint16_t op2,
    size_t vl);
vuint16m8_t vdivu_vv_u16m8 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vuint16m8_t vdivu_vx_u16m8 (vuint16m8_t op1, uint16_t op2,
    size_t vl);

```

```

vuint32m1_t vdivu_vv_u32m1 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vuint32m1_t vdivu_vx_u32m1 (vuint32m1_t op1, uint32_t op2,
    size_t vl);
vuint32m2_t vdivu_vv_u32m2 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vuint32m2_t vdivu_vx_u32m2 (vuint32m2_t op1, uint32_t op2,
    size_t vl);
vuint32m4_t vdivu_vv_u32m4 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vuint32m4_t vdivu_vx_u32m4 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vuint32m8_t vdivu_vv_u32m8 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vuint32m8_t vdivu_vx_u32m8 (vuint32m8_t op1, uint32_t op2,
    size_t vl);
vuint64m1_t vdivu_vv_u64m1 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vuint64m1_t vdivu_vx_u64m1 (vuint64m1_t op1, uint64_t op2,
    size_t vl);
vuint64m2_t vdivu_vv_u64m2 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vuint64m2_t vdivu_vx_u64m2 (vuint64m2_t op1, uint64_t op2,
    size_t vl);
vuint64m4_t vdivu_vv_u64m4 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vuint64m4_t vdivu_vx_u64m4 (vuint64m4_t op1, uint64_t op2,
    size_t vl);
vuint64m8_t vdivu_vv_u64m8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vuint64m8_t vdivu_vx_u64m8 (vuint64m8_t op1, uint64_t op2,
    size_t vl);
vint8m1_t vrem_vv_i8m1 (vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vrem_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vrem_vv_i8m2 (vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vrem_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vrem_vv_i8m4 (vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vrem_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vrem_vv_i8m8 (vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vrem_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vrem_vv_i16m1 (vint16m1_t op1, vint16m1_t op2, size_t
    vl);
vint16m1_t vrem_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint16m2_t vrem_vv_i16m2 (vint16m2_t op1, vint16m2_t op2, size_t
    vl);

```

```

vint16m2_t vrem_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint16m4_t vrem_vv_i16m4 (vint16m4_t op1, vint16m4_t op2, size_t
    vl);
vint16m4_t vrem_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vint16m8_t vrem_vv_i16m8 (vint16m8_t op1, vint16m8_t op2, size_t
    vl);
vint16m8_t vrem_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t
    vl);
vint32m1_t vrem_vv_i32m1 (vint32m1_t op1, vint32m1_t op2, size_t
    vl);
vint32m1_t vrem_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint32m2_t vrem_vv_i32m2 (vint32m2_t op1, vint32m2_t op2, size_t
    vl);
vint32m2_t vrem_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint32m4_t vrem_vv_i32m4 (vint32m4_t op1, vint32m4_t op2, size_t
    vl);
vint32m4_t vrem_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint32m8_t vrem_vv_i32m8 (vint32m8_t op1, vint32m8_t op2, size_t
    vl);
vint32m8_t vrem_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t
    vl);
vint64m1_t vrem_vv_i64m1 (vint64m1_t op1, vint64m1_t op2, size_t
    vl);
vint64m1_t vrem_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t
    vl);
vint64m2_t vrem_vv_i64m2 (vint64m2_t op1, vint64m2_t op2, size_t
    vl);
vint64m2_t vrem_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t
    vl);
vint64m4_t vrem_vv_i64m4 (vint64m4_t op1, vint64m4_t op2, size_t
    vl);
vint64m4_t vrem_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t
    vl);
vint64m8_t vrem_vv_i64m8 (vint64m8_t op1, vint64m8_t op2, size_t
    vl);
vint64m8_t vrem_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vuint8m1_t vremu_vv_u8m1 (vuint8m1_t op1, vuint8m1_t op2, size_t
    vl);
vuint8m1_t vremu_vx_u8m1 (vuint8m1_t op1, uint8_t op2, size_t
    vl);

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```

vuint8m2_t vremu_vv_u8m2 (vuint8m2_t op1, vuint8m2_t op2, size_t
    vl);
vuint8m2_t vremu_vx_u8m2 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vuint8m4_t vremu_vv_u8m4 (vuint8m4_t op1, vuint8m4_t op2, size_t
    vl);
vuint8m4_t vremu_vx_u8m4 (vuint8m4_t op1, uint8_t op2, size_t
    vl);
vuint8m8_t vremu_vv_u8m8 (vuint8m8_t op1, vuint8m8_t op2, size_t
    vl);
vuint8m8_t vremu_vx_u8m8 (vuint8m8_t op1, uint8_t op2, size_t
    vl);
vuint16m1_t vremu_vv_u16m1 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vuint16m1_t vremu_vx_u16m1 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vuint16m2_t vremu_vv_u16m2 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vuint16m2_t vremu_vx_u16m2 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vuint16m4_t vremu_vv_u16m4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vuint16m4_t vremu_vx_u16m4 (vuint16m4_t op1, uint16_t op2,
    size_t vl);
vuint16m8_t vremu_vv_u16m8 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vuint16m8_t vremu_vx_u16m8 (vuint16m8_t op1, uint16_t op2,
    size_t vl);
vuint32m1_t vremu_vv_u32m1 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vuint32m1_t vremu_vx_u32m1 (vuint32m1_t op1, uint32_t op2,
    size_t vl);
vuint32m2_t vremu_vv_u32m2 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vuint32m2_t vremu_vx_u32m2 (vuint32m2_t op1, uint32_t op2,
    size_t vl);
vuint32m4_t vremu_vv_u32m4 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vuint32m4_t vremu_vx_u32m4 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vuint32m8_t vremu_vv_u32m8 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vuint32m8_t vremu_vx_u32m8 (vuint32m8_t op1, uint32_t op2,
    size_t vl);
vuint64m1_t vremu_vv_u64m1 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);

```

```

vuint64m1_t vremu_vx_u64m1 (vuint64m1_t op1, uint64_t op2,
    size_t vl);
vuint64m2_t vremu_vv_u64m2 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vuint64m2_t vremu_vx_u64m2 (vuint64m2_t op1, uint64_t op2,
    size_t vl);
vuint64m4_t vremu_vv_u64m4 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vuint64m4_t vremu_vx_u64m4 (vuint64m4_t op1, uint64_t op2,
    size_t vl);
vuint64m8_t vremu_vv_u64m8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vuint64m8_t vremu_vx_u64m8 (vuint64m8_t op1, uint64_t op2,
    size_t vl);
// masked functions
vint8m1_t vdiv_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vdiv_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vdiv_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vdiv_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vdiv_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vdiv_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vdiv_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vdiv_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vdiv_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint16m1_t vdiv_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vint16m2_t vdiv_vv_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, vint16m2_t op2, size_t vl);
vint16m2_t vdiv_vx_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vdiv_vv_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, vint16m4_t op2, size_t vl);
vint16m4_t vdiv_vx_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vdiv_vv_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, vint16m8_t op2, size_t vl);

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```

vint16m8_t vdiv_vx_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vdiv_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint32m1_t vdiv_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vdiv_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vint32m2_t vdiv_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint32m4_t vdiv_vv_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, vint32m4_t op2, size_t vl);
vint32m4_t vdiv_vx_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vdiv_vv_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, vint32m8_t op2, size_t vl);
vint32m8_t vdiv_vx_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vdiv_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vint64m1_t vdiv_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vdiv_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vint64m2_t vdiv_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vint64m4_t vdiv_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vint64m4_t vdiv_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vdiv_vv_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, vint64m8_t op2, size_t vl);
vint64m8_t vdiv_vx_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, int64_t op2, size_t vl);
vuint8m1_t vdivu_vv_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vuint8m1_t vdivu_vx_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vdivu_vv_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vuint8m2_t vdivu_vx_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vdivu_vv_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vuint8m4_t vdivu_vx_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);

```



```

vuint8m8_t vdivu_vv_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vuint8m8_t vdivu_vx_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vdivu_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vuint16m1_t vdivu_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint16m2_t vdivu_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vuint16m2_t vdivu_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint16m4_t vdivu_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vuint16m4_t vdivu_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vuint16m8_t vdivu_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vuint16m8_t vdivu_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vuint32m1_t vdivu_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vuint32m1_t vdivu_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vuint32m2_t vdivu_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vuint32m2_t vdivu_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint32m4_t vdivu_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vuint32m4_t vdivu_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vuint32m8_t vdivu_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vuint32m8_t vdivu_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);
vuint64m1_t vdivu_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vuint64m1_t vdivu_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vuint64m2_t vdivu_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vuint64m2_t vdivu_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vuint64m4_t vdivu_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);

```

```

vuint64m4_t vdivu_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vuint64m8_t vdivu_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vuint64m8_t vdivu_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);
vint8m1_t vrem_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vrem_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vrem_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vrem_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vrem_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vrem_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vrem_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vrem_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vrem_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint16m1_t vrem_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vint16m2_t vrem_vv_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, vint16m2_t op2, size_t vl);
vint16m2_t vrem_vx_i16m2_m (vbool8_t mask, vint16m2_t maskedoff,
    vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vrem_vv_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, vint16m4_t op2, size_t vl);
vint16m4_t vrem_vx_i16m4_m (vbool4_t mask, vint16m4_t maskedoff,
    vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vrem_vv_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, vint16m8_t op2, size_t vl);
vint16m8_t vrem_vx_i16m8_m (vbool2_t mask, vint16m8_t maskedoff,
    vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vrem_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint32m1_t vrem_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vrem_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vint32m2_t vrem_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);

```

```

vint32m4_t vrem_vv_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, vint32m4_t op2, size_t vl);
vint32m4_t vrem_vx_i32m4_m (vbool8_t mask, vint32m4_t maskedoff,
    vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vrem_vv_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, vint32m8_t op2, size_t vl);
vint32m8_t vrem_vx_i32m8_m (vbool4_t mask, vint32m8_t maskedoff,
    vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vrem_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vint64m1_t vrem_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vrem_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vint64m2_t vrem_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vint64m4_t vrem_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vint64m4_t vrem_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vrem_vv_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, vint64m8_t op2, size_t vl);
vint64m8_t vrem_vx_i64m8_m (vbool8_t mask, vint64m8_t maskedoff,
    vint64m8_t op1, int64_t op2, size_t vl);
vuint8m1_t vremu_vv_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vuint8m1_t vremu_vx_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vremu_vv_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vuint8m2_t vremu_vx_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vremu_vv_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vuint8m4_t vremu_vx_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vremu_vv_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vuint8m8_t vremu_vx_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vremu_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vuint16m1_t vremu_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint16m2_t vremu_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);

```

```

vuint16m2_t vremu_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint16m4_t vremu_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vuint16m4_t vremu_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vuint16m8_t vremu_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vuint16m8_t vremu_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vuint32m1_t vremu_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vuint32m1_t vremu_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vuint32m2_t vremu_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vuint32m2_t vremu_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint32m4_t vremu_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vuint32m4_t vremu_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vuint32m8_t vremu_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vuint32m8_t vremu_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);
vuint64m1_t vremu_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vuint64m1_t vremu_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vuint64m2_t vremu_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vuint64m2_t vremu_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vuint64m4_t vremu_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);
vuint64m4_t vremu_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vuint64m8_t vremu_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vuint64m8_t vremu_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);

```

### Vector Widening Integer Multiply Functions:

#### Prototypes:

```

vint16m2_t vwmul_vv_i16m2 (vint8m1_t op1, vint8m1_t op2, size_t
    vl);
vint16m2_t vwmul_vx_i16m2 (vint8m1_t op1, int8_t op2, size_t vl);
vint16m4_t vwmul_vv_i16m4 (vint8m2_t op1, vint8m2_t op2, size_t
    vl);
vint16m4_t vwmul_vx_i16m4 (vint8m2_t op1, int8_t op2, size_t vl);
vint16m8_t vwmul_vv_i16m8 (vint8m4_t op1, vint8m4_t op2, size_t
    vl);
vint16m8_t vwmul_vx_i16m8 (vint8m4_t op1, int8_t op2, size_t vl);
vint32m2_t vwmul_vv_i32m2 (vint16m1_t op1, vint16m1_t op2,
    size_t vl);
vint32m2_t vwmul_vx_i32m2 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint32m4_t vwmul_vv_i32m4 (vint16m2_t op1, vint16m2_t op2,
    size_t vl);
vint32m4_t vwmul_vx_i32m4 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint32m8_t vwmul_vv_i32m8 (vint16m4_t op1, vint16m4_t op2,
    size_t vl);
vint32m8_t vwmul_vx_i32m8 (vint16m4_t op1, int16_t op2, size_t
    vl);
vint64m2_t vwmul_vv_i64m2 (vint32m1_t op1, vint32m1_t op2,
    size_t vl);
vint64m2_t vwmul_vx_i64m2 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint64m4_t vwmul_vv_i64m4 (vint32m2_t op1, vint32m2_t op2,
    size_t vl);
vint64m4_t vwmul_vx_i64m4 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint64m8_t vwmul_vv_i64m8 (vint32m4_t op1, vint32m4_t op2,
    size_t vl);
vint64m8_t vwmul_vx_i64m8 (vint32m4_t op1, int32_t op2, size_t
    vl);
vuint16m2_t vwmulu_vv_u16m2 (vuint8m1_t op1, vuint8m1_t op2,
    size_t vl);
vuint16m2_t vwmulu_vx_u16m2 (vuint8m1_t op1, uint8_t op2, size_t
    vl);
vuint16m4_t vwmulu_vv_u16m4 (vuint8m2_t op1, vuint8m2_t op2,
    size_t vl);
vuint16m4_t vwmulu_vx_u16m4 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vuint16m8_t vwmulu_vv_u16m8 (vuint8m4_t op1, vuint8m4_t op2,
    size_t vl);
vuint16m8_t vwmulu_vx_u16m8 (vuint8m4_t op1, uint8_t op2, size_t
    vl);

```

```

vuint32m2_t vwmulu_vv_u32m2 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vuint32m2_t vwmulu_vx_u32m2 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vuint32m4_t vwmulu_vv_u32m4 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vuint32m4_t vwmulu_vx_u32m4 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vuint32m8_t vwmulu_vv_u32m8 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vuint32m8_t vwmulu_vx_u32m8 (vuint16m4_t op1, uint16_t op2,
    size_t vl);
vuint64m2_t vwmulu_vv_u64m2 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vuint64m2_t vwmulu_vx_u64m2 (vuint32m1_t op1, uint32_t op2,
    size_t vl);
vuint64m4_t vwmulu_vv_u64m4 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vuint64m4_t vwmulu_vx_u64m4 (vuint32m2_t op1, uint32_t op2,
    size_t vl);
vuint64m8_t vwmulu_vv_u64m8 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vuint64m8_t vwmulu_vx_u64m8 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vint16m2_t vwmulsu_vv_i16m2 (vint8m1_t op1, vuint8m1_t op2,
    size_t vl);
vint16m2_t vwmulsu_vx_i16m2 (vint8m1_t op1, uint8_t op2, size_t
    vl);
vint16m4_t vwmulsu_vv_i16m4 (vint8m2_t op1, vuint8m2_t op2,
    size_t vl);
vint16m4_t vwmulsu_vx_i16m4 (vint8m2_t op1, uint8_t op2, size_t
    vl);
vint16m8_t vwmulsu_vv_i16m8 (vint8m4_t op1, vuint8m4_t op2,
    size_t vl);
vint16m8_t vwmulsu_vx_i16m8 (vint8m4_t op1, uint8_t op2, size_t
    vl);
vint32m2_t vwmulsu_vv_i32m2 (vint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vint32m2_t vwmulsu_vx_i32m2 (vint16m1_t op1, uint16_t op2,
    size_t vl);
vint32m4_t vwmulsu_vv_i32m4 (vint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vint32m4_t vwmulsu_vx_i32m4 (vint16m2_t op1, uint16_t op2,
    size_t vl);
vint32m8_t vwmulsu_vv_i32m8 (vint16m4_t op1, vuint16m4_t op2,
    size_t vl);

```

```

vint32m8_t vwmulsv_vx_i32m8 (vint16m4_t op1, uint16_t op2,
    size_t vl);
vint64m2_t vwmulsv_vv_i64m2 (vint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vint64m2_t vwmulsv_vx_i64m2 (vint32m1_t op1, uint32_t op2,
    size_t vl);
vint64m4_t vwmulsv_vv_i64m4 (vint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vint64m4_t vwmulsv_vx_i64m4 (vint32m2_t op1, uint32_t op2,
    size_t vl);
vint64m8_t vwmulsv_vv_i64m8 (vint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vint64m8_t vwmulsv_vx_i64m8 (vint32m4_t op1, uint32_t op2,
    size_t vl);
// masked functions
vint16m2_t vwmul_vv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint8m1_t op1, vint8m1_t op2, size_t vl);
vint16m2_t vwmul_vx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint8m1_t op1, int8_t op2, size_t vl);
vint16m4_t vwmul_vv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint8m2_t op1, vint8m2_t op2, size_t vl);
vint16m4_t vwmul_vx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint8m2_t op1, int8_t op2, size_t vl);
vint16m8_t vwmul_vv_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint8m4_t op1, vint8m4_t op2, size_t vl);
vint16m8_t vwmul_vx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint8m4_t op1, int8_t op2, size_t vl);
vint32m2_t vwmul_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint32m2_t vwmul_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vint32m4_t vwmul_vv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint16m2_t op1, vint16m2_t op2, size_t vl);
vint32m4_t vwmul_vx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint16m2_t op1, int16_t op2, size_t vl);
vint32m8_t vwmul_vv_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint16m4_t op1, vint16m4_t op2, size_t vl);
vint32m8_t vwmul_vx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint16m4_t op1, int16_t op2, size_t vl);
vint64m2_t vwmul_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint64m2_t vwmul_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint64m4_t vwmul_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);

```

```

vint64m4_t vwmul_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint64m8_t vwmul_vv_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint32m4_t op1, vint32m4_t op2, size_t vl);
vint64m8_t vwmul_vx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint32m4_t op1, int32_t op2, size_t vl);
vuint16m2_t vwmulu_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vuint16m2_t vwmulu_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint8m1_t op1, uint8_t op2, size_t vl);
vuint16m4_t vwmulu_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vuint16m4_t vwmulu_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint8m2_t op1, uint8_t op2, size_t vl);
vuint16m8_t vwmulu_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vuint16m8_t vwmulu_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint8m4_t op1, uint8_t op2, size_t vl);
vuint32m2_t vwmulu_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vuint32m2_t vwmulu_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint32m4_t vwmulu_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vuint32m4_t vwmulu_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint32m8_t vwmulu_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vuint32m8_t vwmulu_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vuint64m2_t vwmulu_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vuint64m2_t vwmulu_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vuint64m4_t vwmulu_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vuint64m4_t vwmulu_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint64m8_t vwmulu_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vuint64m8_t vwmulu_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vint16m2_t vwmulsu_vv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint8m1_t op1, vuint8m1_t op2, size_t vl);
vint16m2_t vwmulsu_vx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint8m1_t op1, uint8_t op2, size_t vl);

```



```

vint16m4_t vwmulsu_vv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint8m2_t op1, vuint8m2_t op2, size_t vl);
vint16m4_t vwmulsu_vx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint8m2_t op1, uint8_t op2, size_t vl);
vint16m8_t vwmulsu_vv_i16m8_m (vbool12_t mask, vint16m8_t
    maskedoff, vint8m4_t op1, vuint8m4_t op2, size_t vl);
vint16m8_t vwmulsu_vx_i16m8_m (vbool12_t mask, vint16m8_t
    maskedoff, vint8m4_t op1, uint8_t op2, size_t vl);
vint32m2_t vwmulsu_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint16m1_t op1, vuint16m1_t op2, size_t vl);
vint32m2_t vwmulsu_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint16m1_t op1, uint16_t op2, size_t vl);
vint32m4_t vwmulsu_vv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint16m2_t op1, vuint16m2_t op2, size_t vl);
vint32m4_t vwmulsu_vx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint16m2_t op1, uint16_t op2, size_t vl);
vint32m8_t vwmulsu_vv_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint16m4_t op1, vuint16m4_t op2, size_t vl);
vint32m8_t vwmulsu_vx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint16m4_t op1, uint16_t op2, size_t vl);
vint64m2_t vwmulsu_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint32m1_t op1, vuint32m1_t op2, size_t vl);
vint64m2_t vwmulsu_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint32m1_t op1, uint32_t op2, size_t vl);
vint64m4_t vwmulsu_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint32m2_t op1, vuint32m2_t op2, size_t vl);
vint64m4_t vwmulsu_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint32m2_t op1, uint32_t op2, size_t vl);
vint64m8_t vwmulsu_vv_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint32m4_t op1, vuint32m4_t op2, size_t vl);
vint64m8_t vwmulsu_vx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint32m4_t op1, uint32_t op2, size_t vl);

```

## Vector Single-Width Integer Multiply-Add Functions:

### Prototypes:

```

vint8m1_t vmacc_vv_i8m1 (vint8m1_t vd, vint8m1_t vs1, vint8m1_t
    vs2, size_t vl);
vint8m1_t vmacc_vx_i8m1 (vint8m1_t vd, int8_t rs1, vint8m1_t
    vs2, size_t vl);
vint8m2_t vmacc_vv_i8m2 (vint8m2_t vd, vint8m2_t vs1, vint8m2_t
    vs2, size_t vl);
vint8m2_t vmacc_vx_i8m2 (vint8m2_t vd, int8_t rs1, vint8m2_t
    vs2, size_t vl);

```

```

vint8m4_t vmacc_vv_i8m4 (vint8m4_t vd, vint8m4_t vs1, vint8m4_t
    vs2, size_t vl);
vint8m4_t vmacc_vx_i8m4 (vint8m4_t vd, int8_t rs1, vint8m4_t
    vs2, size_t vl);
vint8m8_t vmacc_vv_i8m8 (vint8m8_t vd, vint8m8_t vs1, vint8m8_t
    vs2, size_t vl);
vint8m8_t vmacc_vx_i8m8 (vint8m8_t vd, int8_t rs1, vint8m8_t
    vs2, size_t vl);
vint16m1_t vmacc_vv_i16m1 (vint16m1_t vd, vint16m1_t vs1,
    vint16m1_t vs2, size_t vl);
vint16m1_t vmacc_vx_i16m1 (vint16m1_t vd, int16_t rs1,
    vint16m1_t vs2, size_t vl);
vint16m2_t vmacc_vv_i16m2 (vint16m2_t vd, vint16m2_t vs1,
    vint16m2_t vs2, size_t vl);
vint16m2_t vmacc_vx_i16m2 (vint16m2_t vd, int16_t rs1,
    vint16m2_t vs2, size_t vl);
vint16m4_t vmacc_vv_i16m4 (vint16m4_t vd, vint16m4_t vs1,
    vint16m4_t vs2, size_t vl);
vint16m4_t vmacc_vx_i16m4 (vint16m4_t vd, int16_t rs1,
    vint16m4_t vs2, size_t vl);
vint16m8_t vmacc_vv_i16m8 (vint16m8_t vd, vint16m8_t vs1,
    vint16m8_t vs2, size_t vl);
vint16m8_t vmacc_vx_i16m8 (vint16m8_t vd, int16_t rs1,
    vint16m8_t vs2, size_t vl);
vint32m1_t vmacc_vv_i32m1 (vint32m1_t vd, vint32m1_t vs1,
    vint32m1_t vs2, size_t vl);
vint32m1_t vmacc_vx_i32m1 (vint32m1_t vd, int32_t rs1,
    vint32m1_t vs2, size_t vl);
vint32m2_t vmacc_vv_i32m2 (vint32m2_t vd, vint32m2_t vs1,
    vint32m2_t vs2, size_t vl);
vint32m2_t vmacc_vx_i32m2 (vint32m2_t vd, int32_t rs1,
    vint32m2_t vs2, size_t vl);
vint32m4_t vmacc_vv_i32m4 (vint32m4_t vd, vint32m4_t vs1,
    vint32m4_t vs2, size_t vl);
vint32m4_t vmacc_vx_i32m4 (vint32m4_t vd, int32_t rs1,
    vint32m4_t vs2, size_t vl);
vint32m8_t vmacc_vv_i32m8 (vint32m8_t vd, vint32m8_t vs1,
    vint32m8_t vs2, size_t vl);
vint32m8_t vmacc_vx_i32m8 (vint32m8_t vd, int32_t rs1,
    vint32m8_t vs2, size_t vl);
vint64m1_t vmacc_vv_i64m1 (vint64m1_t vd, vint64m1_t vs1,
    vint64m1_t vs2, size_t vl);
vint64m1_t vmacc_vx_i64m1 (vint64m1_t vd, int64_t rs1,
    vint64m1_t vs2, size_t vl);
vint64m2_t vmacc_vv_i64m2 (vint64m2_t vd, vint64m2_t vs1,
    vint64m2_t vs2, size_t vl);

```

```

vint64m2_t vmacc_vx_i64m2 (vint64m2_t vd, int64_t rs1,
    vint64m2_t vs2, size_t vl);
vint64m4_t vmacc_vv_i64m4 (vint64m4_t vd, vint64m4_t vs1,
    vint64m4_t vs2, size_t vl);
vint64m4_t vmacc_vx_i64m4 (vint64m4_t vd, int64_t rs1,
    vint64m4_t vs2, size_t vl);
vint64m8_t vmacc_vv_i64m8 (vint64m8_t vd, vint64m8_t vs1,
    vint64m8_t vs2, size_t vl);
vint64m8_t vmacc_vx_i64m8 (vint64m8_t vd, int64_t rs1,
    vint64m8_t vs2, size_t vl);
vuint8m1_t vmacc_vv_u8m1 (vuint8m1_t vd, vuint8m1_t vs1,
    vuint8m1_t vs2, size_t vl);
vuint8m1_t vmacc_vx_u8m1 (vuint8m1_t vd, uint8_t rs1, vuint8m1_t
    vs2, size_t vl);
vuint8m2_t vmacc_vv_u8m2 (vuint8m2_t vd, vuint8m2_t vs1,
    vuint8m2_t vs2, size_t vl);
vuint8m2_t vmacc_vx_u8m2 (vuint8m2_t vd, uint8_t rs1, vuint8m2_t
    vs2, size_t vl);
vuint8m4_t vmacc_vv_u8m4 (vuint8m4_t vd, vuint8m4_t vs1,
    vuint8m4_t vs2, size_t vl);
vuint8m4_t vmacc_vx_u8m4 (vuint8m4_t vd, uint8_t rs1, vuint8m4_t
    vs2, size_t vl);
vuint8m8_t vmacc_vv_u8m8 (vuint8m8_t vd, vuint8m8_t vs1,
    vuint8m8_t vs2, size_t vl);
vuint8m8_t vmacc_vx_u8m8 (vuint8m8_t vd, uint8_t rs1, vuint8m8_t
    vs2, size_t vl);
vuint16m1_t vmacc_vv_u16m1 (vuint16m1_t vd, vuint16m1_t vs1,
    vuint16m1_t vs2, size_t vl);
vuint16m1_t vmacc_vx_u16m1 (vuint16m1_t vd, uint16_t rs1,
    vuint16m1_t vs2, size_t vl);
vuint16m2_t vmacc_vv_u16m2 (vuint16m2_t vd, vuint16m2_t vs1,
    vuint16m2_t vs2, size_t vl);
vuint16m2_t vmacc_vx_u16m2 (vuint16m2_t vd, uint16_t rs1,
    vuint16m2_t vs2, size_t vl);
vuint16m4_t vmacc_vv_u16m4 (vuint16m4_t vd, vuint16m4_t vs1,
    vuint16m4_t vs2, size_t vl);
vuint16m4_t vmacc_vx_u16m4 (vuint16m4_t vd, uint16_t rs1,
    vuint16m4_t vs2, size_t vl);
vuint16m8_t vmacc_vv_u16m8 (vuint16m8_t vd, vuint16m8_t vs1,
    vuint16m8_t vs2, size_t vl);
vuint16m8_t vmacc_vx_u16m8 (vuint16m8_t vd, uint16_t rs1,
    vuint16m8_t vs2, size_t vl);
vuint32m1_t vmacc_vv_u32m1 (vuint32m1_t vd, vuint32m1_t vs1,
    vuint32m1_t vs2, size_t vl);
vuint32m1_t vmacc_vx_u32m1 (vuint32m1_t vd, uint32_t rs1,
    vuint32m1_t vs2, size_t vl);

```

```

vuint32m2_t vmacc_vv_u32m2 (vuint32m2_t vd, vuint32m2_t vs1,
    vuint32m2_t vs2, size_t vl);
vuint32m2_t vmacc_vx_u32m2 (vuint32m2_t vd, uint32_t rs1,
    vuint32m2_t vs2, size_t vl);
vuint32m4_t vmacc_vv_u32m4 (vuint32m4_t vd, vuint32m4_t vs1,
    vuint32m4_t vs2, size_t vl);
vuint32m4_t vmacc_vx_u32m4 (vuint32m4_t vd, uint32_t rs1,
    vuint32m4_t vs2, size_t vl);
vuint32m8_t vmacc_vv_u32m8 (vuint32m8_t vd, vuint32m8_t vs1,
    vuint32m8_t vs2, size_t vl);
vuint32m8_t vmacc_vx_u32m8 (vuint32m8_t vd, uint32_t rs1,
    vuint32m8_t vs2, size_t vl);
vuint64m1_t vmacc_vv_u64m1 (vuint64m1_t vd, vuint64m1_t vs1,
    vuint64m1_t vs2, size_t vl);
vuint64m1_t vmacc_vx_u64m1 (vuint64m1_t vd, uint64_t rs1,
    vuint64m1_t vs2, size_t vl);
vuint64m2_t vmacc_vv_u64m2 (vuint64m2_t vd, vuint64m2_t vs1,
    vuint64m2_t vs2, size_t vl);
vuint64m2_t vmacc_vx_u64m2 (vuint64m2_t vd, uint64_t rs1,
    vuint64m2_t vs2, size_t vl);
vuint64m4_t vmacc_vv_u64m4 (vuint64m4_t vd, vuint64m4_t vs1,
    vuint64m4_t vs2, size_t vl);
vuint64m4_t vmacc_vx_u64m4 (vuint64m4_t vd, uint64_t rs1,
    vuint64m4_t vs2, size_t vl);
vuint64m8_t vmacc_vv_u64m8 (vuint64m8_t vd, vuint64m8_t vs1,
    vuint64m8_t vs2, size_t vl);
vuint64m8_t vmacc_vx_u64m8 (vuint64m8_t vd, uint64_t rs1,
    vuint64m8_t vs2, size_t vl);
vint8m1_t vnmsac_vv_i8m1 (vint8m1_t vd, vint8m1_t vs1, vint8m1_t
    vs2, size_t vl);
vint8m1_t vnmsac_vx_i8m1 (vint8m1_t vd, int8_t rs1, vint8m1_t
    vs2, size_t vl);
vint8m2_t vnmsac_vv_i8m2 (vint8m2_t vd, vint8m2_t vs1, vint8m2_t
    vs2, size_t vl);
vint8m2_t vnmsac_vx_i8m2 (vint8m2_t vd, int8_t rs1, vint8m2_t
    vs2, size_t vl);
vint8m4_t vnmsac_vv_i8m4 (vint8m4_t vd, vint8m4_t vs1, vint8m4_t
    vs2, size_t vl);
vint8m4_t vnmsac_vx_i8m4 (vint8m4_t vd, int8_t rs1, vint8m4_t
    vs2, size_t vl);
vint8m8_t vnmsac_vv_i8m8 (vint8m8_t vd, vint8m8_t vs1, vint8m8_t
    vs2, size_t vl);
vint8m8_t vnmsac_vx_i8m8 (vint8m8_t vd, int8_t rs1, vint8m8_t
    vs2, size_t vl);
vint16m1_t vnmsac_vv_i16m1 (vint16m1_t vd, vint16m1_t vs1,
    vint16m1_t vs2, size_t vl);

```

```

vint16m1_t vnmsac_vx_i16m1 (vint16m1_t vd, int16_t rs1,
    vint16m1_t vs2, size_t vl);
vint16m2_t vnmsac_vv_i16m2 (vint16m2_t vd, vint16m2_t vs1,
    vint16m2_t vs2, size_t vl);
vint16m2_t vnmsac_vx_i16m2 (vint16m2_t vd, int16_t rs1,
    vint16m2_t vs2, size_t vl);
vint16m4_t vnmsac_vv_i16m4 (vint16m4_t vd, vint16m4_t vs1,
    vint16m4_t vs2, size_t vl);
vint16m4_t vnmsac_vx_i16m4 (vint16m4_t vd, int16_t rs1,
    vint16m4_t vs2, size_t vl);
vint16m8_t vnmsac_vv_i16m8 (vint16m8_t vd, vint16m8_t vs1,
    vint16m8_t vs2, size_t vl);
vint16m8_t vnmsac_vx_i16m8 (vint16m8_t vd, int16_t rs1,
    vint16m8_t vs2, size_t vl);
vint32m1_t vnmsac_vv_i32m1 (vint32m1_t vd, vint32m1_t vs1,
    vint32m1_t vs2, size_t vl);
vint32m1_t vnmsac_vx_i32m1 (vint32m1_t vd, int32_t rs1,
    vint32m1_t vs2, size_t vl);
vint32m2_t vnmsac_vv_i32m2 (vint32m2_t vd, vint32m2_t vs1,
    vint32m2_t vs2, size_t vl);
vint32m2_t vnmsac_vx_i32m2 (vint32m2_t vd, int32_t rs1,
    vint32m2_t vs2, size_t vl);
vint32m4_t vnmsac_vv_i32m4 (vint32m4_t vd, vint32m4_t vs1,
    vint32m4_t vs2, size_t vl);
vint32m4_t vnmsac_vx_i32m4 (vint32m4_t vd, int32_t rs1,
    vint32m4_t vs2, size_t vl);
vint32m8_t vnmsac_vv_i32m8 (vint32m8_t vd, vint32m8_t vs1,
    vint32m8_t vs2, size_t vl);
vint32m8_t vnmsac_vx_i32m8 (vint32m8_t vd, int32_t rs1,
    vint32m8_t vs2, size_t vl);
vint64m1_t vnmsac_vv_i64m1 (vint64m1_t vd, vint64m1_t vs1,
    vint64m1_t vs2, size_t vl);
vint64m1_t vnmsac_vx_i64m1 (vint64m1_t vd, int64_t rs1,
    vint64m1_t vs2, size_t vl);
vint64m2_t vnmsac_vv_i64m2 (vint64m2_t vd, vint64m2_t vs1,
    vint64m2_t vs2, size_t vl);
vint64m2_t vnmsac_vx_i64m2 (vint64m2_t vd, int64_t rs1,
    vint64m2_t vs2, size_t vl);
vint64m4_t vnmsac_vv_i64m4 (vint64m4_t vd, vint64m4_t vs1,
    vint64m4_t vs2, size_t vl);
vint64m4_t vnmsac_vx_i64m4 (vint64m4_t vd, int64_t rs1,
    vint64m4_t vs2, size_t vl);
vint64m8_t vnmsac_vv_i64m8 (vint64m8_t vd, vint64m8_t vs1,
    vint64m8_t vs2, size_t vl);
vint64m8_t vnmsac_vx_i64m8 (vint64m8_t vd, int64_t rs1,
    vint64m8_t vs2, size_t vl);

```

```

vuint8m1_t vnmsac_vv_u8m1 (vuint8m1_t vd, vuint8m1_t vs1,
    vuint8m1_t vs2, size_t vl);
vuint8m1_t vnmsac_vx_u8m1 (vuint8m1_t vd, uint8_t rs1,
    vuint8m1_t vs2, size_t vl);
vuint8m2_t vnmsac_vv_u8m2 (vuint8m2_t vd, vuint8m2_t vs1,
    vuint8m2_t vs2, size_t vl);
vuint8m2_t vnmsac_vx_u8m2 (vuint8m2_t vd, uint8_t rs1,
    vuint8m2_t vs2, size_t vl);
vuint8m4_t vnmsac_vv_u8m4 (vuint8m4_t vd, vuint8m4_t vs1,
    vuint8m4_t vs2, size_t vl);
vuint8m4_t vnmsac_vx_u8m4 (vuint8m4_t vd, uint8_t rs1,
    vuint8m4_t vs2, size_t vl);
vuint8m8_t vnmsac_vv_u8m8 (vuint8m8_t vd, vuint8m8_t vs1,
    vuint8m8_t vs2, size_t vl);
vuint8m8_t vnmsac_vx_u8m8 (vuint8m8_t vd, uint8_t rs1,
    vuint8m8_t vs2, size_t vl);
vuint16m1_t vnmsac_vv_u16m1 (vuint16m1_t vd, vuint16m1_t vs1,
    vuint16m1_t vs2, size_t vl);
vuint16m1_t vnmsac_vx_u16m1 (vuint16m1_t vd, uint16_t rs1,
    vuint16m1_t vs2, size_t vl);
vuint16m2_t vnmsac_vv_u16m2 (vuint16m2_t vd, vuint16m2_t vs1,
    vuint16m2_t vs2, size_t vl);
vuint16m2_t vnmsac_vx_u16m2 (vuint16m2_t vd, uint16_t rs1,
    vuint16m2_t vs2, size_t vl);
vuint16m4_t vnmsac_vv_u16m4 (vuint16m4_t vd, vuint16m4_t vs1,
    vuint16m4_t vs2, size_t vl);
vuint16m4_t vnmsac_vx_u16m4 (vuint16m4_t vd, uint16_t rs1,
    vuint16m4_t vs2, size_t vl);
vuint16m8_t vnmsac_vv_u16m8 (vuint16m8_t vd, vuint16m8_t vs1,
    vuint16m8_t vs2, size_t vl);
vuint16m8_t vnmsac_vx_u16m8 (vuint16m8_t vd, uint16_t rs1,
    vuint16m8_t vs2, size_t vl);
vuint32m1_t vnmsac_vv_u32m1 (vuint32m1_t vd, vuint32m1_t vs1,
    vuint32m1_t vs2, size_t vl);
vuint32m1_t vnmsac_vx_u32m1 (vuint32m1_t vd, uint32_t rs1,
    vuint32m1_t vs2, size_t vl);
vuint32m2_t vnmsac_vv_u32m2 (vuint32m2_t vd, vuint32m2_t vs1,
    vuint32m2_t vs2, size_t vl);
vuint32m2_t vnmsac_vx_u32m2 (vuint32m2_t vd, uint32_t rs1,
    vuint32m2_t vs2, size_t vl);
vuint32m4_t vnmsac_vv_u32m4 (vuint32m4_t vd, vuint32m4_t vs1,
    vuint32m4_t vs2, size_t vl);
vuint32m4_t vnmsac_vx_u32m4 (vuint32m4_t vd, uint32_t rs1,
    vuint32m4_t vs2, size_t vl);
vuint32m8_t vnmsac_vv_u32m8 (vuint32m8_t vd, vuint32m8_t vs1,
    vuint32m8_t vs2, size_t vl);

```

```

vuint32m8_t vnmsac_vx_u32m8 (vuint32m8_t vd, uint32_t rs1,
    vuint32m8_t vs2, size_t vl);
vuint64m1_t vnmsac_vv_u64m1 (vuint64m1_t vd, vuint64m1_t vs1,
    vuint64m1_t vs2, size_t vl);
vuint64m1_t vnmsac_vx_u64m1 (vuint64m1_t vd, uint64_t rs1,
    vuint64m1_t vs2, size_t vl);
vuint64m2_t vnmsac_vv_u64m2 (vuint64m2_t vd, vuint64m2_t vs1,
    vuint64m2_t vs2, size_t vl);
vuint64m2_t vnmsac_vx_u64m2 (vuint64m2_t vd, uint64_t rs1,
    vuint64m2_t vs2, size_t vl);
vuint64m4_t vnmsac_vv_u64m4 (vuint64m4_t vd, vuint64m4_t vs1,
    vuint64m4_t vs2, size_t vl);
vuint64m4_t vnmsac_vx_u64m4 (vuint64m4_t vd, uint64_t rs1,
    vuint64m4_t vs2, size_t vl);
vuint64m8_t vnmsac_vv_u64m8 (vuint64m8_t vd, vuint64m8_t vs1,
    vuint64m8_t vs2, size_t vl);
vuint64m8_t vnmsac_vx_u64m8 (vuint64m8_t vd, uint64_t rs1,
    vuint64m8_t vs2, size_t vl);
vint8m1_t vmadd_vv_i8m1 (vint8m1_t vd, vint8m1_t vs1, vint8m1_t
    vs2, size_t vl);
vint8m1_t vmadd_vx_i8m1 (vint8m1_t vd, int8_t rs1, vint8m1_t
    vs2, size_t vl);
vint8m2_t vmadd_vv_i8m2 (vint8m2_t vd, vint8m2_t vs1, vint8m2_t
    vs2, size_t vl);
vint8m2_t vmadd_vx_i8m2 (vint8m2_t vd, int8_t rs1, vint8m2_t
    vs2, size_t vl);
vint8m4_t vmadd_vv_i8m4 (vint8m4_t vd, vint8m4_t vs1, vint8m4_t
    vs2, size_t vl);
vint8m4_t vmadd_vx_i8m4 (vint8m4_t vd, int8_t rs1, vint8m4_t
    vs2, size_t vl);
vint8m8_t vmadd_vv_i8m8 (vint8m8_t vd, vint8m8_t vs1, vint8m8_t
    vs2, size_t vl);
vint8m8_t vmadd_vx_i8m8 (vint8m8_t vd, int8_t rs1, vint8m8_t
    vs2, size_t vl);
vint16m1_t vmadd_vv_i16m1 (vint16m1_t vd, vint16m1_t vs1,
    vint16m1_t vs2, size_t vl);
vint16m1_t vmadd_vx_i16m1 (vint16m1_t vd, int16_t rs1,
    vint16m1_t vs2, size_t vl);
vint16m2_t vmadd_vv_i16m2 (vint16m2_t vd, vint16m2_t vs1,
    vint16m2_t vs2, size_t vl);
vint16m2_t vmadd_vx_i16m2 (vint16m2_t vd, int16_t rs1,
    vint16m2_t vs2, size_t vl);
vint16m4_t vmadd_vv_i16m4 (vint16m4_t vd, vint16m4_t vs1,
    vint16m4_t vs2, size_t vl);
vint16m4_t vmadd_vx_i16m4 (vint16m4_t vd, int16_t rs1,
    vint16m4_t vs2, size_t vl);

```

```

vint16m8_t vmadd_vv_i16m8 (vint16m8_t vd, vint16m8_t vs1,
    vint16m8_t vs2, size_t vl);
vint16m8_t vmadd_vx_i16m8 (vint16m8_t vd, int16_t rs1,
    vint16m8_t vs2, size_t vl);
vint32m1_t vmadd_vv_i32m1 (vint32m1_t vd, vint32m1_t vs1,
    vint32m1_t vs2, size_t vl);
vint32m1_t vmadd_vx_i32m1 (vint32m1_t vd, int32_t rs1,
    vint32m1_t vs2, size_t vl);
vint32m2_t vmadd_vv_i32m2 (vint32m2_t vd, vint32m2_t vs1,
    vint32m2_t vs2, size_t vl);
vint32m2_t vmadd_vx_i32m2 (vint32m2_t vd, int32_t rs1,
    vint32m2_t vs2, size_t vl);
vint32m4_t vmadd_vv_i32m4 (vint32m4_t vd, vint32m4_t vs1,
    vint32m4_t vs2, size_t vl);
vint32m4_t vmadd_vx_i32m4 (vint32m4_t vd, int32_t rs1,
    vint32m4_t vs2, size_t vl);
vint32m8_t vmadd_vv_i32m8 (vint32m8_t vd, vint32m8_t vs1,
    vint32m8_t vs2, size_t vl);
vint32m8_t vmadd_vx_i32m8 (vint32m8_t vd, int32_t rs1,
    vint32m8_t vs2, size_t vl);
vint64m1_t vmadd_vv_i64m1 (vint64m1_t vd, vint64m1_t vs1,
    vint64m1_t vs2, size_t vl);
vint64m1_t vmadd_vx_i64m1 (vint64m1_t vd, int64_t rs1,
    vint64m1_t vs2, size_t vl);
vint64m2_t vmadd_vv_i64m2 (vint64m2_t vd, vint64m2_t vs1,
    vint64m2_t vs2, size_t vl);
vint64m2_t vmadd_vx_i64m2 (vint64m2_t vd, int64_t rs1,
    vint64m2_t vs2, size_t vl);
vint64m4_t vmadd_vv_i64m4 (vint64m4_t vd, vint64m4_t vs1,
    vint64m4_t vs2, size_t vl);
vint64m4_t vmadd_vx_i64m4 (vint64m4_t vd, int64_t rs1,
    vint64m4_t vs2, size_t vl);
vint64m8_t vmadd_vv_i64m8 (vint64m8_t vd, vint64m8_t vs1,
    vint64m8_t vs2, size_t vl);
vint64m8_t vmadd_vx_i64m8 (vint64m8_t vd, int64_t rs1,
    vint64m8_t vs2, size_t vl);
vuint8m1_t vmadd_vv_u8m1 (vuint8m1_t vd, vuint8m1_t vs1,
    vuint8m1_t vs2, size_t vl);
vuint8m1_t vmadd_vx_u8m1 (vuint8m1_t vd, uint8_t rs1, vuint8m1_t
    vs2, size_t vl);
vuint8m2_t vmadd_vv_u8m2 (vuint8m2_t vd, vuint8m2_t vs1,
    vuint8m2_t vs2, size_t vl);
vuint8m2_t vmadd_vx_u8m2 (vuint8m2_t vd, uint8_t rs1, vuint8m2_t
    vs2, size_t vl);
vuint8m4_t vmadd_vv_u8m4 (vuint8m4_t vd, vuint8m4_t vs1,
    vuint8m4_t vs2, size_t vl);

```



```

vuint8m4_t vmadd_vx_u8m4 (vuint8m4_t vd, uint8_t rs1, vuint8m4_t
    vs2, size_t vl);
vuint8m8_t vmadd_vv_u8m8 (vuint8m8_t vd, vuint8m8_t vs1,
    vuint8m8_t vs2, size_t vl);
vuint8m8_t vmadd_vx_u8m8 (vuint8m8_t vd, uint8_t rs1, vuint8m8_t
    vs2, size_t vl);
vuint16m1_t vmadd_vv_u16m1 (vuint16m1_t vd, vuint16m1_t vs1,
    vuint16m1_t vs2, size_t vl);
vuint16m1_t vmadd_vx_u16m1 (vuint16m1_t vd, uint16_t rs1,
    vuint16m1_t vs2, size_t vl);
vuint16m2_t vmadd_vv_u16m2 (vuint16m2_t vd, vuint16m2_t vs1,
    vuint16m2_t vs2, size_t vl);
vuint16m2_t vmadd_vx_u16m2 (vuint16m2_t vd, uint16_t rs1,
    vuint16m2_t vs2, size_t vl);
vuint16m4_t vmadd_vv_u16m4 (vuint16m4_t vd, vuint16m4_t vs1,
    vuint16m4_t vs2, size_t vl);
vuint16m4_t vmadd_vx_u16m4 (vuint16m4_t vd, uint16_t rs1,
    vuint16m4_t vs2, size_t vl);
vuint16m8_t vmadd_vv_u16m8 (vuint16m8_t vd, vuint16m8_t vs1,
    vuint16m8_t vs2, size_t vl);
vuint16m8_t vmadd_vx_u16m8 (vuint16m8_t vd, uint16_t rs1,
    vuint16m8_t vs2, size_t vl);
vuint32m1_t vmadd_vv_u32m1 (vuint32m1_t vd, vuint32m1_t vs1,
    vuint32m1_t vs2, size_t vl);
vuint32m1_t vmadd_vx_u32m1 (vuint32m1_t vd, uint32_t rs1,
    vuint32m1_t vs2, size_t vl);
vuint32m2_t vmadd_vv_u32m2 (vuint32m2_t vd, vuint32m2_t vs1,
    vuint32m2_t vs2, size_t vl);
vuint32m2_t vmadd_vx_u32m2 (vuint32m2_t vd, uint32_t rs1,
    vuint32m2_t vs2, size_t vl);
vuint32m4_t vmadd_vv_u32m4 (vuint32m4_t vd, vuint32m4_t vs1,
    vuint32m4_t vs2, size_t vl);
vuint32m4_t vmadd_vx_u32m4 (vuint32m4_t vd, uint32_t rs1,
    vuint32m4_t vs2, size_t vl);
vuint32m8_t vmadd_vv_u32m8 (vuint32m8_t vd, vuint32m8_t vs1,
    vuint32m8_t vs2, size_t vl);
vuint32m8_t vmadd_vx_u32m8 (vuint32m8_t vd, uint32_t rs1,
    vuint32m8_t vs2, size_t vl);
vuint64m1_t vmadd_vv_u64m1 (vuint64m1_t vd, vuint64m1_t vs1,
    vuint64m1_t vs2, size_t vl);
vuint64m1_t vmadd_vx_u64m1 (vuint64m1_t vd, uint64_t rs1,
    vuint64m1_t vs2, size_t vl);
vuint64m2_t vmadd_vv_u64m2 (vuint64m2_t vd, vuint64m2_t vs1,
    vuint64m2_t vs2, size_t vl);
vuint64m2_t vmadd_vx_u64m2 (vuint64m2_t vd, uint64_t rs1,
    vuint64m2_t vs2, size_t vl);

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vuint64m4_t vmadd_vv_u64m4 (vuint64m4_t vd, vuint64m4_t vs1,
    vuint64m4_t vs2, size_t vl);
vuint64m4_t vmadd_vx_u64m4 (vuint64m4_t vd, uint64_t rs1,
    vuint64m4_t vs2, size_t vl);
vuint64m8_t vmadd_vv_u64m8 (vuint64m8_t vd, vuint64m8_t vs1,
    vuint64m8_t vs2, size_t vl);
vuint64m8_t vmadd_vx_u64m8 (vuint64m8_t vd, uint64_t rs1,
    vuint64m8_t vs2, size_t vl);
vint8m1_t vnmsub_vv_i8m1 (vint8m1_t vd, vint8m1_t vs1, vint8m1_t
    vs2, size_t vl);
vint8m1_t vnmsub_vx_i8m1 (vint8m1_t vd, int8_t rs1, vint8m1_t
    vs2, size_t vl);
vint8m2_t vnmsub_vv_i8m2 (vint8m2_t vd, vint8m2_t vs1, vint8m2_t
    vs2, size_t vl);
vint8m2_t vnmsub_vx_i8m2 (vint8m2_t vd, int8_t rs1, vint8m2_t
    vs2, size_t vl);
vint8m4_t vnmsub_vv_i8m4 (vint8m4_t vd, vint8m4_t vs1, vint8m4_t
    vs2, size_t vl);
vint8m4_t vnmsub_vx_i8m4 (vint8m4_t vd, int8_t rs1, vint8m4_t
    vs2, size_t vl);
vint8m8_t vnmsub_vv_i8m8 (vint8m8_t vd, vint8m8_t vs1, vint8m8_t
    vs2, size_t vl);
vint8m8_t vnmsub_vx_i8m8 (vint8m8_t vd, int8_t rs1, vint8m8_t
    vs2, size_t vl);
vint16m1_t vnmsub_vv_i16m1 (vint16m1_t vd, vint16m1_t vs1,
    vint16m1_t vs2, size_t vl);
vint16m1_t vnmsub_vx_i16m1 (vint16m1_t vd, int16_t rs1,
    vint16m1_t vs2, size_t vl);
vint16m2_t vnmsub_vv_i16m2 (vint16m2_t vd, vint16m2_t vs1,
    vint16m2_t vs2, size_t vl);
vint16m2_t vnmsub_vx_i16m2 (vint16m2_t vd, int16_t rs1,
    vint16m2_t vs2, size_t vl);
vint16m4_t vnmsub_vv_i16m4 (vint16m4_t vd, vint16m4_t vs1,
    vint16m4_t vs2, size_t vl);
vint16m4_t vnmsub_vx_i16m4 (vint16m4_t vd, int16_t rs1,
    vint16m4_t vs2, size_t vl);
vint16m8_t vnmsub_vv_i16m8 (vint16m8_t vd, vint16m8_t vs1,
    vint16m8_t vs2, size_t vl);
vint16m8_t vnmsub_vx_i16m8 (vint16m8_t vd, int16_t rs1,
    vint16m8_t vs2, size_t vl);
vint32m1_t vnmsub_vv_i32m1 (vint32m1_t vd, vint32m1_t vs1,
    vint32m1_t vs2, size_t vl);
vint32m1_t vnmsub_vx_i32m1 (vint32m1_t vd, int32_t rs1,
    vint32m1_t vs2, size_t vl);
vint32m2_t vnmsub_vv_i32m2 (vint32m2_t vd, vint32m2_t vs1,
    vint32m2_t vs2, size_t vl);

```

```

vint32m2_t vnmsub_vx_i32m2 (vint32m2_t vd, int32_t rs1,
    vint32m2_t vs2, size_t vl);
vint32m4_t vnmsub_vv_i32m4 (vint32m4_t vd, vint32m4_t vs1,
    vint32m4_t vs2, size_t vl);
vint32m4_t vnmsub_vx_i32m4 (vint32m4_t vd, int32_t rs1,
    vint32m4_t vs2, size_t vl);
vint32m8_t vnmsub_vv_i32m8 (vint32m8_t vd, vint32m8_t vs1,
    vint32m8_t vs2, size_t vl);
vint32m8_t vnmsub_vx_i32m8 (vint32m8_t vd, int32_t rs1,
    vint32m8_t vs2, size_t vl);
vint64m1_t vnmsub_vv_i64m1 (vint64m1_t vd, vint64m1_t vs1,
    vint64m1_t vs2, size_t vl);
vint64m1_t vnmsub_vx_i64m1 (vint64m1_t vd, int64_t rs1,
    vint64m1_t vs2, size_t vl);
vint64m2_t vnmsub_vv_i64m2 (vint64m2_t vd, vint64m2_t vs1,
    vint64m2_t vs2, size_t vl);
vint64m2_t vnmsub_vx_i64m2 (vint64m2_t vd, int64_t rs1,
    vint64m2_t vs2, size_t vl);
vint64m4_t vnmsub_vv_i64m4 (vint64m4_t vd, vint64m4_t vs1,
    vint64m4_t vs2, size_t vl);
vint64m4_t vnmsub_vx_i64m4 (vint64m4_t vd, int64_t rs1,
    vint64m4_t vs2, size_t vl);
vint64m8_t vnmsub_vv_i64m8 (vint64m8_t vd, vint64m8_t vs1,
    vint64m8_t vs2, size_t vl);
vint64m8_t vnmsub_vx_i64m8 (vint64m8_t vd, int64_t rs1,
    vint64m8_t vs2, size_t vl);
vuint8m1_t vnmsub_vv_u8m1 (vuint8m1_t vd, vuint8m1_t vs1,
    vuint8m1_t vs2, size_t vl);
vuint8m1_t vnmsub_vx_u8m1 (vuint8m1_t vd, uint8_t rs1,
    vuint8m1_t vs2, size_t vl);
vuint8m2_t vnmsub_vv_u8m2 (vuint8m2_t vd, vuint8m2_t vs1,
    vuint8m2_t vs2, size_t vl);
vuint8m2_t vnmsub_vx_u8m2 (vuint8m2_t vd, uint8_t rs1,
    vuint8m2_t vs2, size_t vl);
vuint8m4_t vnmsub_vv_u8m4 (vuint8m4_t vd, vuint8m4_t vs1,
    vuint8m4_t vs2, size_t vl);
vuint8m4_t vnmsub_vx_u8m4 (vuint8m4_t vd, uint8_t rs1,
    vuint8m4_t vs2, size_t vl);
vuint8m8_t vnmsub_vv_u8m8 (vuint8m8_t vd, vuint8m8_t vs1,
    vuint8m8_t vs2, size_t vl);
vuint8m8_t vnmsub_vx_u8m8 (vuint8m8_t vd, uint8_t rs1,
    vuint8m8_t vs2, size_t vl);
vuint16m1_t vnmsub_vv_u16m1 (vuint16m1_t vd, vuint16m1_t vs1,
    vuint16m1_t vs2, size_t vl);
vuint16m1_t vnmsub_vx_u16m1 (vuint16m1_t vd, uint16_t rs1,
    vuint16m1_t vs2, size_t vl);

```

```

vuint16m2_t vnmsub_vv_u16m2 (vuint16m2_t vd, vuint16m2_t vs1,
    vuint16m2_t vs2, size_t vl);
vuint16m2_t vnmsub_vx_u16m2 (vuint16m2_t vd, uint16_t rs1,
    vuint16m2_t vs2, size_t vl);
vuint16m4_t vnmsub_vv_u16m4 (vuint16m4_t vd, vuint16m4_t vs1,
    vuint16m4_t vs2, size_t vl);
vuint16m4_t vnmsub_vx_u16m4 (vuint16m4_t vd, uint16_t rs1,
    vuint16m4_t vs2, size_t vl);
vuint16m8_t vnmsub_vv_u16m8 (vuint16m8_t vd, vuint16m8_t vs1,
    vuint16m8_t vs2, size_t vl);
vuint16m8_t vnmsub_vx_u16m8 (vuint16m8_t vd, uint16_t rs1,
    vuint16m8_t vs2, size_t vl);
vuint32m1_t vnmsub_vv_u32m1 (vuint32m1_t vd, vuint32m1_t vs1,
    vuint32m1_t vs2, size_t vl);
vuint32m1_t vnmsub_vx_u32m1 (vuint32m1_t vd, uint32_t rs1,
    vuint32m1_t vs2, size_t vl);
vuint32m2_t vnmsub_vv_u32m2 (vuint32m2_t vd, vuint32m2_t vs1,
    vuint32m2_t vs2, size_t vl);
vuint32m2_t vnmsub_vx_u32m2 (vuint32m2_t vd, uint32_t rs1,
    vuint32m2_t vs2, size_t vl);
vuint32m4_t vnmsub_vv_u32m4 (vuint32m4_t vd, vuint32m4_t vs1,
    vuint32m4_t vs2, size_t vl);
vuint32m4_t vnmsub_vx_u32m4 (vuint32m4_t vd, uint32_t rs1,
    vuint32m4_t vs2, size_t vl);
vuint32m8_t vnmsub_vv_u32m8 (vuint32m8_t vd, vuint32m8_t vs1,
    vuint32m8_t vs2, size_t vl);
vuint32m8_t vnmsub_vx_u32m8 (vuint32m8_t vd, uint32_t rs1,
    vuint32m8_t vs2, size_t vl);
vuint64m1_t vnmsub_vv_u64m1 (vuint64m1_t vd, vuint64m1_t vs1,
    vuint64m1_t vs2, size_t vl);
vuint64m1_t vnmsub_vx_u64m1 (vuint64m1_t vd, uint64_t rs1,
    vuint64m1_t vs2, size_t vl);
vuint64m2_t vnmsub_vv_u64m2 (vuint64m2_t vd, vuint64m2_t vs1,
    vuint64m2_t vs2, size_t vl);
vuint64m2_t vnmsub_vx_u64m2 (vuint64m2_t vd, uint64_t rs1,
    vuint64m2_t vs2, size_t vl);
vuint64m4_t vnmsub_vv_u64m4 (vuint64m4_t vd, vuint64m4_t vs1,
    vuint64m4_t vs2, size_t vl);
vuint64m4_t vnmsub_vx_u64m4 (vuint64m4_t vd, uint64_t rs1,
    vuint64m4_t vs2, size_t vl);
vuint64m8_t vnmsub_vv_u64m8 (vuint64m8_t vd, vuint64m8_t vs1,
    vuint64m8_t vs2, size_t vl);
vuint64m8_t vnmsub_vx_u64m8 (vuint64m8_t vd, uint64_t rs1,
    vuint64m8_t vs2, size_t vl);
// masked functions

```

```

vint8m1_t vmacc_vv_i8m1_m (vbool8_t mask, vint8m1_t vd,
    vint8m1_t vs1, vint8m1_t vs2, size_t vl);
vint8m1_t vmacc_vx_i8m1_m (vbool8_t mask, vint8m1_t vd, int8_t
    rs1, vint8m1_t vs2, size_t vl);
vint8m2_t vmacc_vv_i8m2_m (vbool4_t mask, vint8m2_t vd,
    vint8m2_t vs1, vint8m2_t vs2, size_t vl);
vint8m2_t vmacc_vx_i8m2_m (vbool4_t mask, vint8m2_t vd, int8_t
    rs1, vint8m2_t vs2, size_t vl);
vint8m4_t vmacc_vv_i8m4_m (vbool2_t mask, vint8m4_t vd,
    vint8m4_t vs1, vint8m4_t vs2, size_t vl);
vint8m4_t vmacc_vx_i8m4_m (vbool2_t mask, vint8m4_t vd, int8_t
    rs1, vint8m4_t vs2, size_t vl);
vint8m8_t vmacc_vv_i8m8_m (vbool1_t mask, vint8m8_t vd,
    vint8m8_t vs1, vint8m8_t vs2, size_t vl);
vint8m8_t vmacc_vx_i8m8_m (vbool1_t mask, vint8m8_t vd, int8_t
    rs1, vint8m8_t vs2, size_t vl);
vint16m1_t vmacc_vv_i16m1_m (vbool16_t mask, vint16m1_t vd,
    vint16m1_t vs1, vint16m1_t vs2, size_t vl);
vint16m1_t vmacc_vx_i16m1_m (vbool16_t mask, vint16m1_t vd,
    int16_t rs1, vint16m1_t vs2, size_t vl);
vint16m2_t vmacc_vv_i16m2_m (vbool8_t mask, vint16m2_t vd,
    vint16m2_t vs1, vint16m2_t vs2, size_t vl);
vint16m2_t vmacc_vx_i16m2_m (vbool8_t mask, vint16m2_t vd,
    int16_t rs1, vint16m2_t vs2, size_t vl);
vint16m4_t vmacc_vv_i16m4_m (vbool4_t mask, vint16m4_t vd,
    vint16m4_t vs1, vint16m4_t vs2, size_t vl);
vint16m4_t vmacc_vx_i16m4_m (vbool4_t mask, vint16m4_t vd,
    int16_t rs1, vint16m4_t vs2, size_t vl);
vint16m8_t vmacc_vv_i16m8_m (vbool2_t mask, vint16m8_t vd,
    vint16m8_t vs1, vint16m8_t vs2, size_t vl);
vint16m8_t vmacc_vx_i16m8_m (vbool2_t mask, vint16m8_t vd,
    int16_t rs1, vint16m8_t vs2, size_t vl);
vint32m1_t vmacc_vv_i32m1_m (vbool32_t mask, vint32m1_t vd,
    vint32m1_t vs1, vint32m1_t vs2, size_t vl);
vint32m1_t vmacc_vx_i32m1_m (vbool32_t mask, vint32m1_t vd,
    int32_t rs1, vint32m1_t vs2, size_t vl);
vint32m2_t vmacc_vv_i32m2_m (vbool16_t mask, vint32m2_t vd,
    vint32m2_t vs1, vint32m2_t vs2, size_t vl);
vint32m2_t vmacc_vx_i32m2_m (vbool16_t mask, vint32m2_t vd,
    int32_t rs1, vint32m2_t vs2, size_t vl);
vint32m4_t vmacc_vv_i32m4_m (vbool8_t mask, vint32m4_t vd,
    vint32m4_t vs1, vint32m4_t vs2, size_t vl);
vint32m4_t vmacc_vx_i32m4_m (vbool8_t mask, vint32m4_t vd,
    int32_t rs1, vint32m4_t vs2, size_t vl);
vint32m8_t vmacc_vv_i32m8_m (vbool4_t mask, vint32m8_t vd,
    vint32m8_t vs1, vint32m8_t vs2, size_t vl);

```

```

vint32m8_t vmacc_vx_i32m8_m (vbool4_t mask, vint32m8_t vd,
    int32_t rs1, vint32m8_t vs2, size_t vl);
vint64m1_t vmacc_vv_i64m1_m (vbool64_t mask, vint64m1_t vd,
    vint64m1_t vs1, vint64m1_t vs2, size_t vl);
vint64m1_t vmacc_vx_i64m1_m (vbool64_t mask, vint64m1_t vd,
    int64_t rs1, vint64m1_t vs2, size_t vl);
vint64m2_t vmacc_vv_i64m2_m (vbool32_t mask, vint64m2_t vd,
    vint64m2_t vs1, vint64m2_t vs2, size_t vl);
vint64m2_t vmacc_vx_i64m2_m (vbool32_t mask, vint64m2_t vd,
    int64_t rs1, vint64m2_t vs2, size_t vl);
vint64m4_t vmacc_vv_i64m4_m (vbool16_t mask, vint64m4_t vd,
    vint64m4_t vs1, vint64m4_t vs2, size_t vl);
vint64m4_t vmacc_vx_i64m4_m (vbool16_t mask, vint64m4_t vd,
    int64_t rs1, vint64m4_t vs2, size_t vl);
vint64m8_t vmacc_vv_i64m8_m (vbool8_t mask, vint64m8_t vd,
    vint64m8_t vs1, vint64m8_t vs2, size_t vl);
vint64m8_t vmacc_vx_i64m8_m (vbool8_t mask, vint64m8_t vd,
    int64_t rs1, vint64m8_t vs2, size_t vl);
vuint8m1_t vmacc_vv_u8m1_m (vbool8_t mask, vuint8m1_t vd,
    vuint8m1_t vs1, vuint8m1_t vs2, size_t vl);
vuint8m1_t vmacc_vx_u8m1_m (vbool8_t mask, vuint8m1_t vd,
    uint8_t rs1, vuint8m1_t vs2, size_t vl);
vuint8m2_t vmacc_vv_u8m2_m (vbool4_t mask, vuint8m2_t vd,
    vuint8m2_t vs1, vuint8m2_t vs2, size_t vl);
vuint8m2_t vmacc_vx_u8m2_m (vbool4_t mask, vuint8m2_t vd,
    uint8_t rs1, vuint8m2_t vs2, size_t vl);
vuint8m4_t vmacc_vv_u8m4_m (vbool2_t mask, vuint8m4_t vd,
    vuint8m4_t vs1, vuint8m4_t vs2, size_t vl);
vuint8m4_t vmacc_vx_u8m4_m (vbool2_t mask, vuint8m4_t vd,
    uint8_t rs1, vuint8m4_t vs2, size_t vl);
vuint8m8_t vmacc_vv_u8m8_m (vbool1_t mask, vuint8m8_t vd,
    vuint8m8_t vs1, vuint8m8_t vs2, size_t vl);
vuint8m8_t vmacc_vx_u8m8_m (vbool1_t mask, vuint8m8_t vd,
    uint8_t rs1, vuint8m8_t vs2, size_t vl);
vuint16m1_t vmacc_vv_u16m1_m (vbool16_t mask, vuint16m1_t vd,
    vuint16m1_t vs1, vuint16m1_t vs2, size_t vl);
vuint16m1_t vmacc_vx_u16m1_m (vbool16_t mask, vuint16m1_t vd,
    uint16_t rs1, vuint16m1_t vs2, size_t vl);
vuint16m2_t vmacc_vv_u16m2_m (vbool8_t mask, vuint16m2_t vd,
    vuint16m2_t vs1, vuint16m2_t vs2, size_t vl);
vuint16m2_t vmacc_vx_u16m2_m (vbool8_t mask, vuint16m2_t vd,
    uint16_t rs1, vuint16m2_t vs2, size_t vl);
vuint16m4_t vmacc_vv_u16m4_m (vbool4_t mask, vuint16m4_t vd,
    vuint16m4_t vs1, vuint16m4_t vs2, size_t vl);
vuint16m4_t vmacc_vx_u16m4_m (vbool4_t mask, vuint16m4_t vd,
    uint16_t rs1, vuint16m4_t vs2, size_t vl);

```

```

vuint16m8_t vmacc_vv_u16m8_m (vbool2_t mask, vuint16m8_t vd,
    vuint16m8_t vs1, vuint16m8_t vs2, size_t vl);
vuint16m8_t vmacc_vx_u16m8_m (vbool2_t mask, vuint16m8_t vd,
    uint16_t rs1, vuint16m8_t vs2, size_t vl);
vuint32m1_t vmacc_vv_u32m1_m (vbool32_t mask, vuint32m1_t vd,
    vuint32m1_t vs1, vuint32m1_t vs2, size_t vl);
vuint32m1_t vmacc_vx_u32m1_m (vbool32_t mask, vuint32m1_t vd,
    uint32_t rs1, vuint32m1_t vs2, size_t vl);
vuint32m2_t vmacc_vv_u32m2_m (vbool16_t mask, vuint32m2_t vd,
    vuint32m2_t vs1, vuint32m2_t vs2, size_t vl);
vuint32m2_t vmacc_vx_u32m2_m (vbool16_t mask, vuint32m2_t vd,
    uint32_t rs1, vuint32m2_t vs2, size_t vl);
vuint32m4_t vmacc_vv_u32m4_m (vbool8_t mask, vuint32m4_t vd,
    vuint32m4_t vs1, vuint32m4_t vs2, size_t vl);
vuint32m4_t vmacc_vx_u32m4_m (vbool8_t mask, vuint32m4_t vd,
    uint32_t rs1, vuint32m4_t vs2, size_t vl);
vuint32m8_t vmacc_vv_u32m8_m (vbool4_t mask, vuint32m8_t vd,
    vuint32m8_t vs1, vuint32m8_t vs2, size_t vl);
vuint32m8_t vmacc_vx_u32m8_m (vbool4_t mask, vuint32m8_t vd,
    uint32_t rs1, vuint32m8_t vs2, size_t vl);
vuint64m1_t vmacc_vv_u64m1_m (vbool64_t mask, vuint64m1_t vd,
    vuint64m1_t vs1, vuint64m1_t vs2, size_t vl);
vuint64m1_t vmacc_vx_u64m1_m (vbool64_t mask, vuint64m1_t vd,
    uint64_t rs1, vuint64m1_t vs2, size_t vl);
vuint64m2_t vmacc_vv_u64m2_m (vbool32_t mask, vuint64m2_t vd,
    vuint64m2_t vs1, vuint64m2_t vs2, size_t vl);
vuint64m2_t vmacc_vx_u64m2_m (vbool32_t mask, vuint64m2_t vd,
    uint64_t rs1, vuint64m2_t vs2, size_t vl);
vuint64m4_t vmacc_vv_u64m4_m (vbool16_t mask, vuint64m4_t vd,
    vuint64m4_t vs1, vuint64m4_t vs2, size_t vl);
vuint64m4_t vmacc_vx_u64m4_m (vbool16_t mask, vuint64m4_t vd,
    uint64_t rs1, vuint64m4_t vs2, size_t vl);
vuint64m8_t vmacc_vv_u64m8_m (vbool8_t mask, vuint64m8_t vd,
    vuint64m8_t vs1, vuint64m8_t vs2, size_t vl);
vuint64m8_t vmacc_vx_u64m8_m (vbool8_t mask, vuint64m8_t vd,
    uint64_t rs1, vuint64m8_t vs2, size_t vl);
vint8m1_t vnmsac_vv_i8m1_m (vbool8_t mask, vint8m1_t vd,
    vint8m1_t vs1, vint8m1_t vs2, size_t vl);
vint8m1_t vnmsac_vx_i8m1_m (vbool8_t mask, vint8m1_t vd, int8_t
    rs1, vint8m1_t vs2, size_t vl);
vint8m2_t vnmsac_vv_i8m2_m (vbool4_t mask, vint8m2_t vd,
    vint8m2_t vs1, vint8m2_t vs2, size_t vl);
vint8m2_t vnmsac_vx_i8m2_m (vbool4_t mask, vint8m2_t vd, int8_t
    rs1, vint8m2_t vs2, size_t vl);
vint8m4_t vnmsac_vv_i8m4_m (vbool2_t mask, vint8m4_t vd,
    vint8m4_t vs1, vint8m4_t vs2, size_t vl);

```

```

vint8m4_t vnmsac_vx_i8m4_m (vbool12_t mask, vint8m4_t vd, int8_t
    rs1, vint8m4_t vs2, size_t vl);
vint8m8_t vnmsac_vv_i8m8_m (vbool11_t mask, vint8m8_t vd,
    vint8m8_t vs1, vint8m8_t vs2, size_t vl);
vint8m8_t vnmsac_vx_i8m8_m (vbool11_t mask, vint8m8_t vd, int8_t
    rs1, vint8m8_t vs2, size_t vl);
vint16m1_t vnmsac_vv_i16m1_m (vbool16_t mask, vint16m1_t vd,
    vint16m1_t vs1, vint16m1_t vs2, size_t vl);
vint16m1_t vnmsac_vx_i16m1_m (vbool16_t mask, vint16m1_t vd,
    int16_t rs1, vint16m1_t vs2, size_t vl);
vint16m2_t vnmsac_vv_i16m2_m (vbool8_t mask, vint16m2_t vd,
    vint16m2_t vs1, vint16m2_t vs2, size_t vl);
vint16m2_t vnmsac_vx_i16m2_m (vbool8_t mask, vint16m2_t vd,
    int16_t rs1, vint16m2_t vs2, size_t vl);
vint16m4_t vnmsac_vv_i16m4_m (vbool4_t mask, vint16m4_t vd,
    vint16m4_t vs1, vint16m4_t vs2, size_t vl);
vint16m4_t vnmsac_vx_i16m4_m (vbool4_t mask, vint16m4_t vd,
    int16_t rs1, vint16m4_t vs2, size_t vl);
vint16m8_t vnmsac_vv_i16m8_m (vbool2_t mask, vint16m8_t vd,
    vint16m8_t vs1, vint16m8_t vs2, size_t vl);
vint16m8_t vnmsac_vx_i16m8_m (vbool2_t mask, vint16m8_t vd,
    int16_t rs1, vint16m8_t vs2, size_t vl);
vint32m1_t vnmsac_vv_i32m1_m (vbool32_t mask, vint32m1_t vd,
    vint32m1_t vs1, vint32m1_t vs2, size_t vl);
vint32m1_t vnmsac_vx_i32m1_m (vbool32_t mask, vint32m1_t vd,
    int32_t rs1, vint32m1_t vs2, size_t vl);
vint32m2_t vnmsac_vv_i32m2_m (vbool16_t mask, vint32m2_t vd,
    vint32m2_t vs1, vint32m2_t vs2, size_t vl);
vint32m2_t vnmsac_vx_i32m2_m (vbool16_t mask, vint32m2_t vd,
    int32_t rs1, vint32m2_t vs2, size_t vl);
vint32m4_t vnmsac_vv_i32m4_m (vbool8_t mask, vint32m4_t vd,
    vint32m4_t vs1, vint32m4_t vs2, size_t vl);
vint32m4_t vnmsac_vx_i32m4_m (vbool8_t mask, vint32m4_t vd,
    int32_t rs1, vint32m4_t vs2, size_t vl);
vint32m8_t vnmsac_vv_i32m8_m (vbool4_t mask, vint32m8_t vd,
    vint32m8_t vs1, vint32m8_t vs2, size_t vl);
vint32m8_t vnmsac_vx_i32m8_m (vbool4_t mask, vint32m8_t vd,
    int32_t rs1, vint32m8_t vs2, size_t vl);
vint64m1_t vnmsac_vv_i64m1_m (vbool64_t mask, vint64m1_t vd,
    vint64m1_t vs1, vint64m1_t vs2, size_t vl);
vint64m1_t vnmsac_vx_i64m1_m (vbool64_t mask, vint64m1_t vd,
    int64_t rs1, vint64m1_t vs2, size_t vl);
vint64m2_t vnmsac_vv_i64m2_m (vbool32_t mask, vint64m2_t vd,
    vint64m2_t vs1, vint64m2_t vs2, size_t vl);
vint64m2_t vnmsac_vx_i64m2_m (vbool32_t mask, vint64m2_t vd,
    int64_t rs1, vint64m2_t vs2, size_t vl);

```



```

vint64m4_t vnmsac_vv_i64m4_m (vbool16_t mask, vint64m4_t vd,
    vint64m4_t vs1, vint64m4_t vs2, size_t vl);
vint64m4_t vnmsac_vx_i64m4_m (vbool16_t mask, vint64m4_t vd,
    int64_t rs1, vint64m4_t vs2, size_t vl);
vint64m8_t vnmsac_vv_i64m8_m (vbool8_t mask, vint64m8_t vd,
    vint64m8_t vs1, vint64m8_t vs2, size_t vl);
vint64m8_t vnmsac_vx_i64m8_m (vbool8_t mask, vint64m8_t vd,
    int64_t rs1, vint64m8_t vs2, size_t vl);
vuint8m1_t vnmsac_vv_u8m1_m (vbool8_t mask, vuint8m1_t vd,
    vuint8m1_t vs1, vuint8m1_t vs2, size_t vl);
vuint8m1_t vnmsac_vx_u8m1_m (vbool8_t mask, vuint8m1_t vd,
    uint8_t rs1, vuint8m1_t vs2, size_t vl);
vuint8m2_t vnmsac_vv_u8m2_m (vbool4_t mask, vuint8m2_t vd,
    vuint8m2_t vs1, vuint8m2_t vs2, size_t vl);
vuint8m2_t vnmsac_vx_u8m2_m (vbool4_t mask, vuint8m2_t vd,
    uint8_t rs1, vuint8m2_t vs2, size_t vl);
vuint8m4_t vnmsac_vv_u8m4_m (vbool2_t mask, vuint8m4_t vd,
    vuint8m4_t vs1, vuint8m4_t vs2, size_t vl);
vuint8m4_t vnmsac_vx_u8m4_m (vbool2_t mask, vuint8m4_t vd,
    uint8_t rs1, vuint8m4_t vs2, size_t vl);
vuint8m8_t vnmsac_vv_u8m8_m (vbool1_t mask, vuint8m8_t vd,
    vuint8m8_t vs1, vuint8m8_t vs2, size_t vl);
vuint8m8_t vnmsac_vx_u8m8_m (vbool1_t mask, vuint8m8_t vd,
    uint8_t rs1, vuint8m8_t vs2, size_t vl);
vuint16m1_t vnmsac_vv_u16m1_m (vbool16_t mask, vuint16m1_t vd,
    vuint16m1_t vs1, vuint16m1_t vs2, size_t vl);
vuint16m1_t vnmsac_vx_u16m1_m (vbool16_t mask, vuint16m1_t vd,
    uint16_t rs1, vuint16m1_t vs2, size_t vl);
vuint16m2_t vnmsac_vv_u16m2_m (vbool8_t mask, vuint16m2_t vd,
    vuint16m2_t vs1, vuint16m2_t vs2, size_t vl);
vuint16m2_t vnmsac_vx_u16m2_m (vbool8_t mask, vuint16m2_t vd,
    uint16_t rs1, vuint16m2_t vs2, size_t vl);
vuint16m4_t vnmsac_vv_u16m4_m (vbool4_t mask, vuint16m4_t vd,
    vuint16m4_t vs1, vuint16m4_t vs2, size_t vl);
vuint16m4_t vnmsac_vx_u16m4_m (vbool4_t mask, vuint16m4_t vd,
    uint16_t rs1, vuint16m4_t vs2, size_t vl);
vuint16m8_t vnmsac_vv_u16m8_m (vbool2_t mask, vuint16m8_t vd,
    vuint16m8_t vs1, vuint16m8_t vs2, size_t vl);
vuint16m8_t vnmsac_vx_u16m8_m (vbool2_t mask, vuint16m8_t vd,
    uint16_t rs1, vuint16m8_t vs2, size_t vl);
vuint32m1_t vnmsac_vv_u32m1_m (vbool32_t mask, vuint32m1_t vd,
    vuint32m1_t vs1, vuint32m1_t vs2, size_t vl);
vuint32m1_t vnmsac_vx_u32m1_m (vbool32_t mask, vuint32m1_t vd,
    uint32_t rs1, vuint32m1_t vs2, size_t vl);
vuint32m2_t vnmsac_vv_u32m2_m (vbool16_t mask, vuint32m2_t vd,
    vuint32m2_t vs1, vuint32m2_t vs2, size_t vl);

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vuint32m2_t vnmsac_vx_u32m2_m (vbool16_t mask, vuint32m2_t vd,
    uint32_t rs1, vuint32m2_t vs2, size_t vl);
vuint32m4_t vnmsac_vv_u32m4_m (vbool8_t mask, vuint32m4_t vd,
    vuint32m4_t vs1, vuint32m4_t vs2, size_t vl);
vuint32m4_t vnmsac_vx_u32m4_m (vbool8_t mask, vuint32m4_t vd,
    uint32_t rs1, vuint32m4_t vs2, size_t vl);
vuint32m8_t vnmsac_vv_u32m8_m (vbool4_t mask, vuint32m8_t vd,
    vuint32m8_t vs1, vuint32m8_t vs2, size_t vl);
vuint32m8_t vnmsac_vx_u32m8_m (vbool4_t mask, vuint32m8_t vd,
    uint32_t rs1, vuint32m8_t vs2, size_t vl);
vuint64m1_t vnmsac_vv_u64m1_m (vbool64_t mask, vuint64m1_t vd,
    vuint64m1_t vs1, vuint64m1_t vs2, size_t vl);
vuint64m1_t vnmsac_vx_u64m1_m (vbool64_t mask, vuint64m1_t vd,
    uint64_t rs1, vuint64m1_t vs2, size_t vl);
vuint64m2_t vnmsac_vv_u64m2_m (vbool32_t mask, vuint64m2_t vd,
    vuint64m2_t vs1, vuint64m2_t vs2, size_t vl);
vuint64m2_t vnmsac_vx_u64m2_m (vbool32_t mask, vuint64m2_t vd,
    uint64_t rs1, vuint64m2_t vs2, size_t vl);
vuint64m4_t vnmsac_vv_u64m4_m (vbool16_t mask, vuint64m4_t vd,
    vuint64m4_t vs1, vuint64m4_t vs2, size_t vl);
vuint64m4_t vnmsac_vx_u64m4_m (vbool16_t mask, vuint64m4_t vd,
    uint64_t rs1, vuint64m4_t vs2, size_t vl);
vuint64m8_t vnmsac_vv_u64m8_m (vbool8_t mask, vuint64m8_t vd,
    vuint64m8_t vs1, vuint64m8_t vs2, size_t vl);
vuint64m8_t vnmsac_vx_u64m8_m (vbool8_t mask, vuint64m8_t vd,
    uint64_t rs1, vuint64m8_t vs2, size_t vl);
vint8m1_t vmadd_vv_i8m1_m (vbool8_t mask, vint8m1_t vd,
    vint8m1_t vs1, vint8m1_t vs2, size_t vl);
vint8m1_t vmadd_vx_i8m1_m (vbool8_t mask, vint8m1_t vd, int8_t
    rs1, vint8m1_t vs2, size_t vl);
vint8m2_t vmadd_vv_i8m2_m (vbool4_t mask, vint8m2_t vd,
    vint8m2_t vs1, vint8m2_t vs2, size_t vl);
vint8m2_t vmadd_vx_i8m2_m (vbool4_t mask, vint8m2_t vd, int8_t
    rs1, vint8m2_t vs2, size_t vl);
vint8m4_t vmadd_vv_i8m4_m (vbool2_t mask, vint8m4_t vd,
    vint8m4_t vs1, vint8m4_t vs2, size_t vl);
vint8m4_t vmadd_vx_i8m4_m (vbool2_t mask, vint8m4_t vd, int8_t
    rs1, vint8m4_t vs2, size_t vl);
vint8m8_t vmadd_vv_i8m8_m (vbool1_t mask, vint8m8_t vd,
    vint8m8_t vs1, vint8m8_t vs2, size_t vl);
vint8m8_t vmadd_vx_i8m8_m (vbool1_t mask, vint8m8_t vd, int8_t
    rs1, vint8m8_t vs2, size_t vl);
vint16m1_t vmadd_vv_i16m1_m (vbool16_t mask, vint16m1_t vd,
    vint16m1_t vs1, vint16m1_t vs2, size_t vl);
vint16m1_t vmadd_vx_i16m1_m (vbool16_t mask, vint16m1_t vd,
    int16_t rs1, vint16m1_t vs2, size_t vl);

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vint16m2_t vmadd_vv_i16m2_m (vbool8_t mask, vint16m2_t vd,
    vint16m2_t vs1, vint16m2_t vs2, size_t vl);
vint16m2_t vmadd_vx_i16m2_m (vbool8_t mask, vint16m2_t vd,
    int16_t rs1, vint16m2_t vs2, size_t vl);
vint16m4_t vmadd_vv_i16m4_m (vbool4_t mask, vint16m4_t vd,
    vint16m4_t vs1, vint16m4_t vs2, size_t vl);
vint16m4_t vmadd_vx_i16m4_m (vbool4_t mask, vint16m4_t vd,
    int16_t rs1, vint16m4_t vs2, size_t vl);
vint16m8_t vmadd_vv_i16m8_m (vbool2_t mask, vint16m8_t vd,
    vint16m8_t vs1, vint16m8_t vs2, size_t vl);
vint16m8_t vmadd_vx_i16m8_m (vbool2_t mask, vint16m8_t vd,
    int16_t rs1, vint16m8_t vs2, size_t vl);
vint32m1_t vmadd_vv_i32m1_m (vbool32_t mask, vint32m1_t vd,
    vint32m1_t vs1, vint32m1_t vs2, size_t vl);
vint32m1_t vmadd_vx_i32m1_m (vbool32_t mask, vint32m1_t vd,
    int32_t rs1, vint32m1_t vs2, size_t vl);
vint32m2_t vmadd_vv_i32m2_m (vbool16_t mask, vint32m2_t vd,
    vint32m2_t vs1, vint32m2_t vs2, size_t vl);
vint32m2_t vmadd_vx_i32m2_m (vbool16_t mask, vint32m2_t vd,
    int32_t rs1, vint32m2_t vs2, size_t vl);
vint32m4_t vmadd_vv_i32m4_m (vbool8_t mask, vint32m4_t vd,
    vint32m4_t vs1, vint32m4_t vs2, size_t vl);
vint32m4_t vmadd_vx_i32m4_m (vbool8_t mask, vint32m4_t vd,
    int32_t rs1, vint32m4_t vs2, size_t vl);
vint32m8_t vmadd_vv_i32m8_m (vbool4_t mask, vint32m8_t vd,
    vint32m8_t vs1, vint32m8_t vs2, size_t vl);
vint32m8_t vmadd_vx_i32m8_m (vbool4_t mask, vint32m8_t vd,
    int32_t rs1, vint32m8_t vs2, size_t vl);
vint64m1_t vmadd_vv_i64m1_m (vbool64_t mask, vint64m1_t vd,
    vint64m1_t vs1, vint64m1_t vs2, size_t vl);
vint64m1_t vmadd_vx_i64m1_m (vbool64_t mask, vint64m1_t vd,
    int64_t rs1, vint64m1_t vs2, size_t vl);
vint64m2_t vmadd_vv_i64m2_m (vbool32_t mask, vint64m2_t vd,
    vint64m2_t vs1, vint64m2_t vs2, size_t vl);
vint64m2_t vmadd_vx_i64m2_m (vbool32_t mask, vint64m2_t vd,
    int64_t rs1, vint64m2_t vs2, size_t vl);
vint64m4_t vmadd_vv_i64m4_m (vbool16_t mask, vint64m4_t vd,
    vint64m4_t vs1, vint64m4_t vs2, size_t vl);
vint64m4_t vmadd_vx_i64m4_m (vbool16_t mask, vint64m4_t vd,
    int64_t rs1, vint64m4_t vs2, size_t vl);
vint64m8_t vmadd_vv_i64m8_m (vbool8_t mask, vint64m8_t vd,
    vint64m8_t vs1, vint64m8_t vs2, size_t vl);
vint64m8_t vmadd_vx_i64m8_m (vbool8_t mask, vint64m8_t vd,
    int64_t rs1, vint64m8_t vs2, size_t vl);
vuint8m1_t vmadd_vv_u8m1_m (vbool8_t mask, vuint8m1_t vd,
    vuint8m1_t vs1, vuint8m1_t vs2, size_t vl);

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vuint8m1_t vmadd_vx_u8m1_m (vbool8_t mask, vuint8m1_t vd,
    uint8_t rs1, vuint8m1_t vs2, size_t vl);
vuint8m2_t vmadd_vv_u8m2_m (vbool4_t mask, vuint8m2_t vd,
    vuint8m2_t vs1, vuint8m2_t vs2, size_t vl);
vuint8m2_t vmadd_vx_u8m2_m (vbool4_t mask, vuint8m2_t vd,
    uint8_t rs1, vuint8m2_t vs2, size_t vl);
vuint8m4_t vmadd_vv_u8m4_m (vbool2_t mask, vuint8m4_t vd,
    vuint8m4_t vs1, vuint8m4_t vs2, size_t vl);
vuint8m4_t vmadd_vx_u8m4_m (vbool2_t mask, vuint8m4_t vd,
    uint8_t rs1, vuint8m4_t vs2, size_t vl);
vuint8m8_t vmadd_vv_u8m8_m (vbool1_t mask, vuint8m8_t vd,
    vuint8m8_t vs1, vuint8m8_t vs2, size_t vl);
vuint8m8_t vmadd_vx_u8m8_m (vbool1_t mask, vuint8m8_t vd,
    uint8_t rs1, vuint8m8_t vs2, size_t vl);
vuint16m1_t vmadd_vv_u16m1_m (vbool16_t mask, vuint16m1_t vd,
    vuint16m1_t vs1, vuint16m1_t vs2, size_t vl);
vuint16m1_t vmadd_vx_u16m1_m (vbool16_t mask, vuint16m1_t vd,
    uint16_t rs1, vuint16m1_t vs2, size_t vl);
vuint16m2_t vmadd_vv_u16m2_m (vbool8_t mask, vuint16m2_t vd,
    vuint16m2_t vs1, vuint16m2_t vs2, size_t vl);
vuint16m2_t vmadd_vx_u16m2_m (vbool8_t mask, vuint16m2_t vd,
    uint16_t rs1, vuint16m2_t vs2, size_t vl);
vuint16m4_t vmadd_vv_u16m4_m (vbool4_t mask, vuint16m4_t vd,
    vuint16m4_t vs1, vuint16m4_t vs2, size_t vl);
vuint16m4_t vmadd_vx_u16m4_m (vbool4_t mask, vuint16m4_t vd,
    uint16_t rs1, vuint16m4_t vs2, size_t vl);
vuint16m8_t vmadd_vv_u16m8_m (vbool2_t mask, vuint16m8_t vd,
    vuint16m8_t vs1, vuint16m8_t vs2, size_t vl);
vuint16m8_t vmadd_vx_u16m8_m (vbool2_t mask, vuint16m8_t vd,
    uint16_t rs1, vuint16m8_t vs2, size_t vl);
vuint32m1_t vmadd_vv_u32m1_m (vbool32_t mask, vuint32m1_t vd,
    vuint32m1_t vs1, vuint32m1_t vs2, size_t vl);
vuint32m1_t vmadd_vx_u32m1_m (vbool32_t mask, vuint32m1_t vd,
    uint32_t rs1, vuint32m1_t vs2, size_t vl);
vuint32m2_t vmadd_vv_u32m2_m (vbool16_t mask, vuint32m2_t vd,
    vuint32m2_t vs1, vuint32m2_t vs2, size_t vl);
vuint32m2_t vmadd_vx_u32m2_m (vbool16_t mask, vuint32m2_t vd,
    uint32_t rs1, vuint32m2_t vs2, size_t vl);
vuint32m4_t vmadd_vv_u32m4_m (vbool8_t mask, vuint32m4_t vd,
    vuint32m4_t vs1, vuint32m4_t vs2, size_t vl);
vuint32m4_t vmadd_vx_u32m4_m (vbool8_t mask, vuint32m4_t vd,
    uint32_t rs1, vuint32m4_t vs2, size_t vl);
vuint32m8_t vmadd_vv_u32m8_m (vbool4_t mask, vuint32m8_t vd,
    vuint32m8_t vs1, vuint32m8_t vs2, size_t vl);
vuint32m8_t vmadd_vx_u32m8_m (vbool4_t mask, vuint32m8_t vd,
    uint32_t rs1, vuint32m8_t vs2, size_t vl);

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vuint64m1_t vmadd_vv_u64m1_m (vbool64_t mask, vuint64m1_t vd,
    vuint64m1_t vs1, vuint64m1_t vs2, size_t vl);
vuint64m1_t vmadd_vx_u64m1_m (vbool64_t mask, vuint64m1_t vd,
    uint64_t rs1, vuint64m1_t vs2, size_t vl);
vuint64m2_t vmadd_vv_u64m2_m (vbool32_t mask, vuint64m2_t vd,
    vuint64m2_t vs1, vuint64m2_t vs2, size_t vl);
vuint64m2_t vmadd_vx_u64m2_m (vbool32_t mask, vuint64m2_t vd,
    uint64_t rs1, vuint64m2_t vs2, size_t vl);
vuint64m4_t vmadd_vv_u64m4_m (vbool16_t mask, vuint64m4_t vd,
    vuint64m4_t vs1, vuint64m4_t vs2, size_t vl);
vuint64m4_t vmadd_vx_u64m4_m (vbool16_t mask, vuint64m4_t vd,
    uint64_t rs1, vuint64m4_t vs2, size_t vl);
vuint64m8_t vmadd_vv_u64m8_m (vbool8_t mask, vuint64m8_t vd,
    vuint64m8_t vs1, vuint64m8_t vs2, size_t vl);
vuint64m8_t vmadd_vx_u64m8_m (vbool8_t mask, vuint64m8_t vd,
    uint64_t rs1, vuint64m8_t vs2, size_t vl);
vint8m1_t vnmsub_vv_i8m1_m (vbool8_t mask, vint8m1_t vd,
    vint8m1_t vs1, vint8m1_t vs2, size_t vl);
vint8m1_t vnmsub_vx_i8m1_m (vbool8_t mask, vint8m1_t vd, int8_t
    rs1, vint8m1_t vs2, size_t vl);
vint8m2_t vnmsub_vv_i8m2_m (vbool4_t mask, vint8m2_t vd,
    vint8m2_t vs1, vint8m2_t vs2, size_t vl);
vint8m2_t vnmsub_vx_i8m2_m (vbool4_t mask, vint8m2_t vd, int8_t
    rs1, vint8m2_t vs2, size_t vl);
vint8m4_t vnmsub_vv_i8m4_m (vbool2_t mask, vint8m4_t vd,
    vint8m4_t vs1, vint8m4_t vs2, size_t vl);
vint8m4_t vnmsub_vx_i8m4_m (vbool2_t mask, vint8m4_t vd, int8_t
    rs1, vint8m4_t vs2, size_t vl);
vint8m8_t vnmsub_vv_i8m8_m (vbool1_t mask, vint8m8_t vd,
    vint8m8_t vs1, vint8m8_t vs2, size_t vl);
vint8m8_t vnmsub_vx_i8m8_m (vbool1_t mask, vint8m8_t vd, int8_t
    rs1, vint8m8_t vs2, size_t vl);
vint16m1_t vnmsub_vv_i16m1_m (vbool16_t mask, vint16m1_t vd,
    vint16m1_t vs1, vint16m1_t vs2, size_t vl);
vint16m1_t vnmsub_vx_i16m1_m (vbool16_t mask, vint16m1_t vd,
    int16_t rs1, vint16m1_t vs2, size_t vl);
vint16m2_t vnmsub_vv_i16m2_m (vbool8_t mask, vint16m2_t vd,
    vint16m2_t vs1, vint16m2_t vs2, size_t vl);
vint16m2_t vnmsub_vx_i16m2_m (vbool8_t mask, vint16m2_t vd,
    int16_t rs1, vint16m2_t vs2, size_t vl);
vint16m4_t vnmsub_vv_i16m4_m (vbool4_t mask, vint16m4_t vd,
    vint16m4_t vs1, vint16m4_t vs2, size_t vl);
vint16m4_t vnmsub_vx_i16m4_m (vbool4_t mask, vint16m4_t vd,
    int16_t rs1, vint16m4_t vs2, size_t vl);
vint16m8_t vnmsub_vv_i16m8_m (vbool2_t mask, vint16m8_t vd,
    vint16m8_t vs1, vint16m8_t vs2, size_t vl);

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vint16m8_t vnmsub_vx_i16m8_m (vbool2_t mask, vint16m8_t vd,
    int16_t rs1, vint16m8_t vs2, size_t vl);
vint32m1_t vnmsub_vv_i32m1_m (vbool32_t mask, vint32m1_t vd,
    vint32m1_t vs1, vint32m1_t vs2, size_t vl);
vint32m1_t vnmsub_vx_i32m1_m (vbool32_t mask, vint32m1_t vd,
    int32_t rs1, vint32m1_t vs2, size_t vl);
vint32m2_t vnmsub_vv_i32m2_m (vbool16_t mask, vint32m2_t vd,
    vint32m2_t vs1, vint32m2_t vs2, size_t vl);
vint32m2_t vnmsub_vx_i32m2_m (vbool16_t mask, vint32m2_t vd,
    int32_t rs1, vint32m2_t vs2, size_t vl);
vint32m4_t vnmsub_vv_i32m4_m (vbool8_t mask, vint32m4_t vd,
    vint32m4_t vs1, vint32m4_t vs2, size_t vl);
vint32m4_t vnmsub_vx_i32m4_m (vbool8_t mask, vint32m4_t vd,
    int32_t rs1, vint32m4_t vs2, size_t vl);
vint32m8_t vnmsub_vv_i32m8_m (vbool4_t mask, vint32m8_t vd,
    vint32m8_t vs1, vint32m8_t vs2, size_t vl);
vint32m8_t vnmsub_vx_i32m8_m (vbool4_t mask, vint32m8_t vd,
    int32_t rs1, vint32m8_t vs2, size_t vl);
vint64m1_t vnmsub_vv_i64m1_m (vbool64_t mask, vint64m1_t vd,
    vint64m1_t vs1, vint64m1_t vs2, size_t vl);
vint64m1_t vnmsub_vx_i64m1_m (vbool64_t mask, vint64m1_t vd,
    int64_t rs1, vint64m1_t vs2, size_t vl);
vint64m2_t vnmsub_vv_i64m2_m (vbool32_t mask, vint64m2_t vd,
    vint64m2_t vs1, vint64m2_t vs2, size_t vl);
vint64m2_t vnmsub_vx_i64m2_m (vbool32_t mask, vint64m2_t vd,
    int64_t rs1, vint64m2_t vs2, size_t vl);
vint64m4_t vnmsub_vv_i64m4_m (vbool16_t mask, vint64m4_t vd,
    vint64m4_t vs1, vint64m4_t vs2, size_t vl);
vint64m4_t vnmsub_vx_i64m4_m (vbool16_t mask, vint64m4_t vd,
    int64_t rs1, vint64m4_t vs2, size_t vl);
vint64m8_t vnmsub_vv_i64m8_m (vbool8_t mask, vint64m8_t vd,
    vint64m8_t vs1, vint64m8_t vs2, size_t vl);
vint64m8_t vnmsub_vx_i64m8_m (vbool8_t mask, vint64m8_t vd,
    int64_t rs1, vint64m8_t vs2, size_t vl);
vuint8m1_t vnmsub_vv_u8m1_m (vbool8_t mask, vuint8m1_t vd,
    vuint8m1_t vs1, vuint8m1_t vs2, size_t vl);
vuint8m1_t vnmsub_vx_u8m1_m (vbool8_t mask, vuint8m1_t vd,
    uint8_t rs1, vuint8m1_t vs2, size_t vl);
vuint8m2_t vnmsub_vv_u8m2_m (vbool4_t mask, vuint8m2_t vd,
    vuint8m2_t vs1, vuint8m2_t vs2, size_t vl);
vuint8m2_t vnmsub_vx_u8m2_m (vbool4_t mask, vuint8m2_t vd,
    uint8_t rs1, vuint8m2_t vs2, size_t vl);
vuint8m4_t vnmsub_vv_u8m4_m (vbool2_t mask, vuint8m4_t vd,
    vuint8m4_t vs1, vuint8m4_t vs2, size_t vl);
vuint8m4_t vnmsub_vx_u8m4_m (vbool2_t mask, vuint8m4_t vd,
    uint8_t rs1, vuint8m4_t vs2, size_t vl);

```

```

vuint8m8_t vnmsub_vv_u8m8_m (vbool1_t mask, vuint8m8_t vd,
    vuint8m8_t vs1, vuint8m8_t vs2, size_t vl);
vuint8m8_t vnmsub_vx_u8m8_m (vbool1_t mask, vuint8m8_t vd,
    uint8_t rs1, vuint8m8_t vs2, size_t vl);
vuint16m1_t vnmsub_vv_u16m1_m (vbool16_t mask, vuint16m1_t vd,
    vuint16m1_t vs1, vuint16m1_t vs2, size_t vl);
vuint16m1_t vnmsub_vx_u16m1_m (vbool16_t mask, vuint16m1_t vd,
    uint16_t rs1, vuint16m1_t vs2, size_t vl);
vuint16m2_t vnmsub_vv_u16m2_m (vbool8_t mask, vuint16m2_t vd,
    vuint16m2_t vs1, vuint16m2_t vs2, size_t vl);
vuint16m2_t vnmsub_vx_u16m2_m (vbool8_t mask, vuint16m2_t vd,
    uint16_t rs1, vuint16m2_t vs2, size_t vl);
vuint16m4_t vnmsub_vv_u16m4_m (vbool4_t mask, vuint16m4_t vd,
    vuint16m4_t vs1, vuint16m4_t vs2, size_t vl);
vuint16m4_t vnmsub_vx_u16m4_m (vbool4_t mask, vuint16m4_t vd,
    uint16_t rs1, vuint16m4_t vs2, size_t vl);
vuint16m8_t vnmsub_vv_u16m8_m (vbool2_t mask, vuint16m8_t vd,
    vuint16m8_t vs1, vuint16m8_t vs2, size_t vl);
vuint16m8_t vnmsub_vx_u16m8_m (vbool2_t mask, vuint16m8_t vd,
    uint16_t rs1, vuint16m8_t vs2, size_t vl);
vuint32m1_t vnmsub_vv_u32m1_m (vbool32_t mask, vuint32m1_t vd,
    vuint32m1_t vs1, vuint32m1_t vs2, size_t vl);
vuint32m1_t vnmsub_vx_u32m1_m (vbool32_t mask, vuint32m1_t vd,
    uint32_t rs1, vuint32m1_t vs2, size_t vl);
vuint32m2_t vnmsub_vv_u32m2_m (vbool16_t mask, vuint32m2_t vd,
    vuint32m2_t vs1, vuint32m2_t vs2, size_t vl);
vuint32m2_t vnmsub_vx_u32m2_m (vbool16_t mask, vuint32m2_t vd,
    uint32_t rs1, vuint32m2_t vs2, size_t vl);
vuint32m4_t vnmsub_vv_u32m4_m (vbool8_t mask, vuint32m4_t vd,
    vuint32m4_t vs1, vuint32m4_t vs2, size_t vl);
vuint32m4_t vnmsub_vx_u32m4_m (vbool8_t mask, vuint32m4_t vd,
    uint32_t rs1, vuint32m4_t vs2, size_t vl);
vuint32m8_t vnmsub_vv_u32m8_m (vbool4_t mask, vuint32m8_t vd,
    vuint32m8_t vs1, vuint32m8_t vs2, size_t vl);
vuint32m8_t vnmsub_vx_u32m8_m (vbool4_t mask, vuint32m8_t vd,
    uint32_t rs1, vuint32m8_t vs2, size_t vl);
vuint64m1_t vnmsub_vv_u64m1_m (vbool64_t mask, vuint64m1_t vd,
    vuint64m1_t vs1, vuint64m1_t vs2, size_t vl);
vuint64m1_t vnmsub_vx_u64m1_m (vbool64_t mask, vuint64m1_t vd,
    uint64_t rs1, vuint64m1_t vs2, size_t vl);
vuint64m2_t vnmsub_vv_u64m2_m (vbool32_t mask, vuint64m2_t vd,
    vuint64m2_t vs1, vuint64m2_t vs2, size_t vl);
vuint64m2_t vnmsub_vx_u64m2_m (vbool32_t mask, vuint64m2_t vd,
    uint64_t rs1, vuint64m2_t vs2, size_t vl);
vuint64m4_t vnmsub_vv_u64m4_m (vbool16_t mask, vuint64m4_t vd,
    vuint64m4_t vs1, vuint64m4_t vs2, size_t vl);

```

```

vuint64m4_t vnmsub_vx_u64m4_m (vbool16_t mask, vuint64m4_t vd,
    uint64_t rs1, vuint64m4_t vs2, size_t vl);
vuint64m8_t vnmsub_vv_u64m8_m (vbool18_t mask, vuint64m8_t vd,
    vuint64m8_t vs1, vuint64m8_t vs2, size_t vl);
vuint64m8_t vnmsub_vx_u64m8_m (vbool18_t mask, vuint64m8_t vd,
    uint64_t rs1, vuint64m8_t vs2, size_t vl);

```

## Vector Widening Integer Multiply-Add Functions:

### Prototypes:

```

vint16m2_t vwmacc_vv_i16m2 (vint16m2_t vd, vint8m1_t vs1,
    vint8m1_t vs2, size_t vl);
vint16m2_t vwmacc_vx_i16m2 (vint16m2_t vd, int8_t rs1, vint8m1_t
    vs2, size_t vl);
vint16m4_t vwmacc_vv_i16m4 (vint16m4_t vd, vint8m2_t vs1,
    vint8m2_t vs2, size_t vl);
vint16m4_t vwmacc_vx_i16m4 (vint16m4_t vd, int8_t rs1, vint8m2_t
    vs2, size_t vl);
vint16m8_t vwmacc_vv_i16m8 (vint16m8_t vd, vint8m4_t vs1,
    vint8m4_t vs2, size_t vl);
vint16m8_t vwmacc_vx_i16m8 (vint16m8_t vd, int8_t rs1, vint8m4_t
    vs2, size_t vl);
vint32m2_t vwmacc_vv_i32m2 (vint32m2_t vd, vint16m1_t vs1,
    vint16m1_t vs2, size_t vl);
vint32m2_t vwmacc_vx_i32m2 (vint32m2_t vd, int16_t rs1,
    vint16m1_t vs2, size_t vl);
vint32m4_t vwmacc_vv_i32m4 (vint32m4_t vd, vint16m2_t vs1,
    vint16m2_t vs2, size_t vl);
vint32m4_t vwmacc_vx_i32m4 (vint32m4_t vd, int16_t rs1,
    vint16m2_t vs2, size_t vl);
vint32m8_t vwmacc_vv_i32m8 (vint32m8_t vd, vint16m4_t vs1,
    vint16m4_t vs2, size_t vl);
vint32m8_t vwmacc_vx_i32m8 (vint32m8_t vd, int16_t rs1,
    vint16m4_t vs2, size_t vl);
vint64m2_t vwmacc_vv_i64m2 (vint64m2_t vd, vint32m1_t vs1,
    vint32m1_t vs2, size_t vl);
vint64m2_t vwmacc_vx_i64m2 (vint64m2_t vd, int32_t rs1,
    vint32m1_t vs2, size_t vl);
vint64m4_t vwmacc_vv_i64m4 (vint64m4_t vd, vint32m2_t vs1,
    vint32m2_t vs2, size_t vl);
vint64m4_t vwmacc_vx_i64m4 (vint64m4_t vd, int32_t rs1,
    vint32m2_t vs2, size_t vl);
vint64m8_t vwmacc_vv_i64m8 (vint64m8_t vd, vint32m4_t vs1,
    vint32m4_t vs2, size_t vl);

```



```

vint64m8_t vwmacc_vx_i64m8 (vint64m8_t vd, int32_t rs1,
    vint32m4_t vs2, size_t vl);
vuint16m2_t vwmaccu_vv_u16m2 (vuint16m2_t vd, vuint8m1_t vs1,
    vuint8m1_t vs2, size_t vl);
vuint16m2_t vwmaccu_vx_u16m2 (vuint16m2_t vd, uint8_t rs1,
    vuint8m1_t vs2, size_t vl);
vuint16m4_t vwmaccu_vv_u16m4 (vuint16m4_t vd, vuint8m2_t vs1,
    vuint8m2_t vs2, size_t vl);
vuint16m4_t vwmaccu_vx_u16m4 (vuint16m4_t vd, uint8_t rs1,
    vuint8m2_t vs2, size_t vl);
vuint16m8_t vwmaccu_vv_u16m8 (vuint16m8_t vd, vuint8m4_t vs1,
    vuint8m4_t vs2, size_t vl);
vuint16m8_t vwmaccu_vx_u16m8 (vuint16m8_t vd, uint8_t rs1,
    vuint8m4_t vs2, size_t vl);
vuint32m2_t vwmaccu_vv_u32m2 (vuint32m2_t vd, vuint16m1_t vs1,
    vuint16m1_t vs2, size_t vl);
vuint32m2_t vwmaccu_vx_u32m2 (vuint32m2_t vd, uint16_t rs1,
    vuint16m1_t vs2, size_t vl);
vuint32m4_t vwmaccu_vv_u32m4 (vuint32m4_t vd, vuint16m2_t vs1,
    vuint16m2_t vs2, size_t vl);
vuint32m4_t vwmaccu_vx_u32m4 (vuint32m4_t vd, uint16_t rs1,
    vuint16m2_t vs2, size_t vl);
vuint32m8_t vwmaccu_vv_u32m8 (vuint32m8_t vd, vuint16m4_t vs1,
    vuint16m4_t vs2, size_t vl);
vuint32m8_t vwmaccu_vx_u32m8 (vuint32m8_t vd, uint16_t rs1,
    vuint16m4_t vs2, size_t vl);
vuint64m2_t vwmaccu_vv_u64m2 (vuint64m2_t vd, vuint32m1_t vs1,
    vuint32m1_t vs2, size_t vl);
vuint64m2_t vwmaccu_vx_u64m2 (vuint64m2_t vd, uint32_t rs1,
    vuint32m1_t vs2, size_t vl);
vuint64m4_t vwmaccu_vv_u64m4 (vuint64m4_t vd, vuint32m2_t vs1,
    vuint32m2_t vs2, size_t vl);
vuint64m4_t vwmaccu_vx_u64m4 (vuint64m4_t vd, uint32_t rs1,
    vuint32m2_t vs2, size_t vl);
vuint64m8_t vwmaccu_vv_u64m8 (vuint64m8_t vd, vuint32m4_t vs1,
    vuint32m4_t vs2, size_t vl);
vuint64m8_t vwmaccu_vx_u64m8 (vuint64m8_t vd, uint32_t rs1,
    vuint32m4_t vs2, size_t vl);
vint16m2_t vwmaccsu_vv_i16m2 (vint16m2_t vd, vint8m1_t vs1,
    vuint8m1_t vs2, size_t vl);
vint16m2_t vwmaccsu_vx_i16m2 (vint16m2_t vd, int8_t rs1,
    vuint8m1_t vs2, size_t vl);
vint16m4_t vwmaccsu_vv_i16m4 (vint16m4_t vd, vint8m2_t vs1,
    vuint8m2_t vs2, size_t vl);
vint16m4_t vwmaccsu_vx_i16m4 (vint16m4_t vd, int8_t rs1,
    vuint8m2_t vs2, size_t vl);

```

```

vint16m8_t vwmacccsu_vv_i16m8 (vint16m8_t vd, vint8m4_t vs1,
    vuint8m4_t vs2, size_t vl);
vint16m8_t vwmacccsu_vx_i16m8 (vint16m8_t vd, int8_t rs1,
    vuint8m4_t vs2, size_t vl);
vint32m2_t vwmacccsu_vv_i32m2 (vint32m2_t vd, vint16m1_t vs1,
    vuint16m1_t vs2, size_t vl);
vint32m2_t vwmacccsu_vx_i32m2 (vint32m2_t vd, int16_t rs1,
    vuint16m1_t vs2, size_t vl);
vint32m4_t vwmacccsu_vv_i32m4 (vint32m4_t vd, vint16m2_t vs1,
    vuint16m2_t vs2, size_t vl);
vint32m4_t vwmacccsu_vx_i32m4 (vint32m4_t vd, int16_t rs1,
    vuint16m2_t vs2, size_t vl);
vint32m8_t vwmacccsu_vv_i32m8 (vint32m8_t vd, vint16m4_t vs1,
    vuint16m4_t vs2, size_t vl);
vint32m8_t vwmacccsu_vx_i32m8 (vint32m8_t vd, int16_t rs1,
    vuint16m4_t vs2, size_t vl);
vint64m2_t vwmacccsu_vv_i64m2 (vint64m2_t vd, vint32m1_t vs1,
    vuint32m1_t vs2, size_t vl);
vint64m2_t vwmacccsu_vx_i64m2 (vint64m2_t vd, int32_t rs1,
    vuint32m1_t vs2, size_t vl);
vint64m4_t vwmacccsu_vv_i64m4 (vint64m4_t vd, vint32m2_t vs1,
    vuint32m2_t vs2, size_t vl);
vint64m4_t vwmacccsu_vx_i64m4 (vint64m4_t vd, int32_t rs1,
    vuint32m2_t vs2, size_t vl);
vint64m8_t vwmacccsu_vv_i64m8 (vint64m8_t vd, vint32m4_t vs1,
    vuint32m4_t vs2, size_t vl);
vint64m8_t vwmacccsu_vx_i64m8 (vint64m8_t vd, int32_t rs1,
    vuint32m4_t vs2, size_t vl);
vint16m2_t vwmaccus_vx_i16m2 (vint16m2_t vd, uint8_t rs1,
    vint8m1_t vs2, size_t vl);
vint16m4_t vwmaccus_vx_i16m4 (vint16m4_t vd, uint8_t rs1,
    vint8m2_t vs2, size_t vl);
vint16m8_t vwmaccus_vx_i16m8 (vint16m8_t vd, uint8_t rs1,
    vint8m4_t vs2, size_t vl);
vint32m2_t vwmaccus_vx_i32m2 (vint32m2_t vd, uint16_t rs1,
    vint16m1_t vs2, size_t vl);
vint32m4_t vwmaccus_vx_i32m4 (vint32m4_t vd, uint16_t rs1,
    vint16m2_t vs2, size_t vl);
vint32m8_t vwmaccus_vx_i32m8 (vint32m8_t vd, uint16_t rs1,
    vint16m4_t vs2, size_t vl);
vint64m2_t vwmaccus_vx_i64m2 (vint64m2_t vd, uint32_t rs1,
    vint32m1_t vs2, size_t vl);
vint64m4_t vwmaccus_vx_i64m4 (vint64m4_t vd, uint32_t rs1,
    vint32m2_t vs2, size_t vl);
vint64m8_t vwmaccus_vx_i64m8 (vint64m8_t vd, uint32_t rs1,
    vint32m4_t vs2, size_t vl);

```

```

// masked functions
vint16m2_t vwmacc_vv_i16m2_m (vbool8_t mask, vint16m2_t vd,
    vint8m1_t vs1, vint8m1_t vs2, size_t vl);
vint16m2_t vwmacc_vx_i16m2_m (vbool8_t mask, vint16m2_t vd,
    int8_t rs1, vint8m1_t vs2, size_t vl);
vint16m4_t vwmacc_vv_i16m4_m (vbool4_t mask, vint16m4_t vd,
    vint8m2_t vs1, vint8m2_t vs2, size_t vl);
vint16m4_t vwmacc_vx_i16m4_m (vbool4_t mask, vint16m4_t vd,
    int8_t rs1, vint8m2_t vs2, size_t vl);
vint16m8_t vwmacc_vv_i16m8_m (vbool2_t mask, vint16m8_t vd,
    vint8m4_t vs1, vint8m4_t vs2, size_t vl);
vint16m8_t vwmacc_vx_i16m8_m (vbool2_t mask, vint16m8_t vd,
    int8_t rs1, vint8m4_t vs2, size_t vl);
vint32m2_t vwmacc_vv_i32m2_m (vbool16_t mask, vint32m2_t vd,
    vint16m1_t vs1, vint16m1_t vs2, size_t vl);
vint32m2_t vwmacc_vx_i32m2_m (vbool16_t mask, vint32m2_t vd,
    int16_t rs1, vint16m1_t vs2, size_t vl);
vint32m4_t vwmacc_vv_i32m4_m (vbool8_t mask, vint32m4_t vd,
    vint16m2_t vs1, vint16m2_t vs2, size_t vl);
vint32m4_t vwmacc_vx_i32m4_m (vbool8_t mask, vint32m4_t vd,
    int16_t rs1, vint16m2_t vs2, size_t vl);
vint32m8_t vwmacc_vv_i32m8_m (vbool4_t mask, vint32m8_t vd,
    vint16m4_t vs1, vint16m4_t vs2, size_t vl);
vint32m8_t vwmacc_vx_i32m8_m (vbool4_t mask, vint32m8_t vd,
    int16_t rs1, vint16m4_t vs2, size_t vl);
vint64m2_t vwmacc_vv_i64m2_m (vbool32_t mask, vint64m2_t vd,
    vint32m1_t vs1, vint32m1_t vs2, size_t vl);
vint64m2_t vwmacc_vx_i64m2_m (vbool32_t mask, vint64m2_t vd,
    int32_t rs1, vint32m1_t vs2, size_t vl);
vint64m4_t vwmacc_vv_i64m4_m (vbool16_t mask, vint64m4_t vd,
    vint32m2_t vs1, vint32m2_t vs2, size_t vl);
vint64m4_t vwmacc_vx_i64m4_m (vbool16_t mask, vint64m4_t vd,
    int32_t rs1, vint32m2_t vs2, size_t vl);
vint64m8_t vwmacc_vv_i64m8_m (vbool8_t mask, vint64m8_t vd,
    vint32m4_t vs1, vint32m4_t vs2, size_t vl);
vint64m8_t vwmacc_vx_i64m8_m (vbool8_t mask, vint64m8_t vd,
    int32_t rs1, vint32m4_t vs2, size_t vl);
vuint16m2_t vwmaccu_vv_u16m2_m (vbool8_t mask, vuint16m2_t vd,
    vuint8m1_t vs1, vuint8m1_t vs2, size_t vl);
vuint16m2_t vwmaccu_vx_u16m2_m (vbool8_t mask, vuint16m2_t vd,
    uint8_t rs1, vuint8m1_t vs2, size_t vl);
vuint16m4_t vwmaccu_vv_u16m4_m (vbool4_t mask, vuint16m4_t vd,
    vuint8m2_t vs1, vuint8m2_t vs2, size_t vl);
vuint16m4_t vwmaccu_vx_u16m4_m (vbool4_t mask, vuint16m4_t vd,
    uint8_t rs1, vuint8m2_t vs2, size_t vl);

```

```

vuint16m8_t vwmaccu_vv_u16m8_m (vbool2_t mask, vuint16m8_t vd,
    vuint8m4_t vs1, vuint8m4_t vs2, size_t vl);
vuint16m8_t vwmaccu_vx_u16m8_m (vbool2_t mask, vuint16m8_t vd,
    uint8_t rs1, vuint8m4_t vs2, size_t vl);
vuint32m2_t vwmaccu_vv_u32m2_m (vbool16_t mask, vuint32m2_t vd,
    vuint16m1_t vs1, vuint16m1_t vs2, size_t vl);
vuint32m2_t vwmaccu_vx_u32m2_m (vbool16_t mask, vuint32m2_t vd,
    uint16_t rs1, vuint16m1_t vs2, size_t vl);
vuint32m4_t vwmaccu_vv_u32m4_m (vbool8_t mask, vuint32m4_t vd,
    vuint16m2_t vs1, vuint16m2_t vs2, size_t vl);
vuint32m4_t vwmaccu_vx_u32m4_m (vbool8_t mask, vuint32m4_t vd,
    uint16_t rs1, vuint16m2_t vs2, size_t vl);
vuint32m8_t vwmaccu_vv_u32m8_m (vbool4_t mask, vuint32m8_t vd,
    vuint16m4_t vs1, vuint16m4_t vs2, size_t vl);
vuint32m8_t vwmaccu_vx_u32m8_m (vbool4_t mask, vuint32m8_t vd,
    uint16_t rs1, vuint16m4_t vs2, size_t vl);
vuint64m2_t vwmaccu_vv_u64m2_m (vbool32_t mask, vuint64m2_t vd,
    vuint32m1_t vs1, vuint32m1_t vs2, size_t vl);
vuint64m2_t vwmaccu_vx_u64m2_m (vbool32_t mask, vuint64m2_t vd,
    uint32_t rs1, vuint32m1_t vs2, size_t vl);
vuint64m4_t vwmaccu_vv_u64m4_m (vbool16_t mask, vuint64m4_t vd,
    vuint32m2_t vs1, vuint32m2_t vs2, size_t vl);
vuint64m4_t vwmaccu_vx_u64m4_m (vbool16_t mask, vuint64m4_t vd,
    uint32_t rs1, vuint32m2_t vs2, size_t vl);
vuint64m8_t vwmaccu_vv_u64m8_m (vbool8_t mask, vuint64m8_t vd,
    vuint32m4_t vs1, vuint32m4_t vs2, size_t vl);
vuint64m8_t vwmaccu_vx_u64m8_m (vbool8_t mask, vuint64m8_t vd,
    uint32_t rs1, vuint32m4_t vs2, size_t vl);
vint16m2_t vwmaccsu_vv_i16m2_m (vbool8_t mask, vint16m2_t vd,
    vint8m1_t vs1, vuint8m1_t vs2, size_t vl);
vint16m2_t vwmaccsu_vx_i16m2_m (vbool8_t mask, vint16m2_t vd,
    int8_t rs1, vuint8m1_t vs2, size_t vl);
vint16m4_t vwmaccsu_vv_i16m4_m (vbool4_t mask, vint16m4_t vd,
    vint8m2_t vs1, vuint8m2_t vs2, size_t vl);
vint16m4_t vwmaccsu_vx_i16m4_m (vbool4_t mask, vint16m4_t vd,
    int8_t rs1, vuint8m2_t vs2, size_t vl);
vint16m8_t vwmaccsu_vv_i16m8_m (vbool2_t mask, vint16m8_t vd,
    vint8m4_t vs1, vuint8m4_t vs2, size_t vl);
vint16m8_t vwmaccsu_vx_i16m8_m (vbool2_t mask, vint16m8_t vd,
    int8_t rs1, vuint8m4_t vs2, size_t vl);
vint32m2_t vwmaccsu_vv_i32m2_m (vbool16_t mask, vint32m2_t vd,
    vint16m1_t vs1, vuint16m1_t vs2, size_t vl);
vint32m2_t vwmaccsu_vx_i32m2_m (vbool16_t mask, vint32m2_t vd,
    int16_t rs1, vuint16m1_t vs2, size_t vl);
vint32m4_t vwmaccsu_vv_i32m4_m (vbool8_t mask, vint32m4_t vd,
    vint16m2_t vs1, vuint16m2_t vs2, size_t vl);

```

```

vint32m4_t vwmaccsu_vx_i32m4_m (vbool8_t mask, vint32m4_t vd,
    int16_t rs1, vuint16m2_t vs2, size_t vl);
vint32m8_t vwmaccsu_vv_i32m8_m (vbool4_t mask, vint32m8_t vd,
    vint16m4_t vs1, vuint16m4_t vs2, size_t vl);
vint32m8_t vwmaccsu_vx_i32m8_m (vbool4_t mask, vint32m8_t vd,
    int16_t rs1, vuint16m4_t vs2, size_t vl);
vint64m2_t vwmaccsu_vv_i64m2_m (vbool32_t mask, vint64m2_t vd,
    vint32m1_t vs1, vuint32m1_t vs2, size_t vl);
vint64m2_t vwmaccsu_vx_i64m2_m (vbool32_t mask, vint64m2_t vd,
    int32_t rs1, vuint32m1_t vs2, size_t vl);
vint64m4_t vwmaccsu_vv_i64m4_m (vbool16_t mask, vint64m4_t vd,
    vint32m2_t vs1, vuint32m2_t vs2, size_t vl);
vint64m4_t vwmaccsu_vx_i64m4_m (vbool16_t mask, vint64m4_t vd,
    int32_t rs1, vuint32m2_t vs2, size_t vl);
vint64m8_t vwmaccsu_vv_i64m8_m (vbool8_t mask, vint64m8_t vd,
    vint32m4_t vs1, vuint32m4_t vs2, size_t vl);
vint64m8_t vwmaccsu_vx_i64m8_m (vbool8_t mask, vint64m8_t vd,
    int32_t rs1, vuint32m4_t vs2, size_t vl);
vint16m2_t vwmaccus_vx_i16m2_m (vbool8_t mask, vint16m2_t vd,
    uint8_t rs1, vint8m1_t vs2, size_t vl);
vint16m4_t vwmaccus_vx_i16m4_m (vbool4_t mask, vint16m4_t vd,
    uint8_t rs1, vint8m2_t vs2, size_t vl);
vint16m8_t vwmaccus_vx_i16m8_m (vbool2_t mask, vint16m8_t vd,
    uint8_t rs1, vint8m4_t vs2, size_t vl);
vint32m2_t vwmaccus_vx_i32m2_m (vbool16_t mask, vint32m2_t vd,
    uint16_t rs1, vint16m1_t vs2, size_t vl);
vint32m4_t vwmaccus_vx_i32m4_m (vbool8_t mask, vint32m4_t vd,
    uint16_t rs1, vint16m2_t vs2, size_t vl);
vint32m8_t vwmaccus_vx_i32m8_m (vbool4_t mask, vint32m8_t vd,
    uint16_t rs1, vint16m4_t vs2, size_t vl);
vint64m2_t vwmaccus_vx_i64m2_m (vbool32_t mask, vint64m2_t vd,
    uint32_t rs1, vint32m1_t vs2, size_t vl);
vint64m4_t vwmaccus_vx_i64m4_m (vbool16_t mask, vint64m4_t vd,
    uint32_t rs1, vint32m2_t vs2, size_t vl);
vint64m8_t vwmaccus_vx_i64m8_m (vbool8_t mask, vint64m8_t vd,
    uint32_t rs1, vint32m4_t vs2, size_t vl);

```

## Vector Integer Merge Functions:

### Prototypes:

```

vint8m1_t vmerge_vvm_i8m1 (vbool8_t mask, vint8m1_t op1,
    vint8m1_t op2, size_t vl);
vint8m1_t vmerge_vxm_i8m1 (vbool8_t mask, vint8m1_t op1, int8_t
    op2, size_t vl);

```

```

vint8m2_t vmerge_vvm_i8m2 (vbool4_t mask, vint8m2_t op1,
    vint8m2_t op2, size_t vl);
vint8m2_t vmerge_vxm_i8m2 (vbool4_t mask, vint8m2_t op1, int8_t
    op2, size_t vl);
vint8m4_t vmerge_vvm_i8m4 (vbool2_t mask, vint8m4_t op1,
    vint8m4_t op2, size_t vl);
vint8m4_t vmerge_vxm_i8m4 (vbool2_t mask, vint8m4_t op1, int8_t
    op2, size_t vl);
vint8m8_t vmerge_vvm_i8m8 (vbool1_t mask, vint8m8_t op1,
    vint8m8_t op2, size_t vl);
vint8m8_t vmerge_vxm_i8m8 (vbool1_t mask, vint8m8_t op1, int8_t
    op2, size_t vl);
vint16m1_t vmerge_vvm_i16m1 (vbool16_t mask, vint16m1_t op1,
    vint16m1_t op2, size_t vl);
vint16m1_t vmerge_vxm_i16m1 (vbool16_t mask, vint16m1_t op1,
    int16_t op2, size_t vl);
vint16m2_t vmerge_vvm_i16m2 (vbool8_t mask, vint16m2_t op1,
    vint16m2_t op2, size_t vl);
vint16m2_t vmerge_vxm_i16m2 (vbool8_t mask, vint16m2_t op1,
    int16_t op2, size_t vl);
vint16m4_t vmerge_vvm_i16m4 (vbool4_t mask, vint16m4_t op1,
    vint16m4_t op2, size_t vl);
vint16m4_t vmerge_vxm_i16m4 (vbool4_t mask, vint16m4_t op1,
    int16_t op2, size_t vl);
vint16m8_t vmerge_vvm_i16m8 (vbool2_t mask, vint16m8_t op1,
    vint16m8_t op2, size_t vl);
vint16m8_t vmerge_vxm_i16m8 (vbool2_t mask, vint16m8_t op1,
    int16_t op2, size_t vl);
vint32m1_t vmerge_vvm_i32m1 (vbool32_t mask, vint32m1_t op1,
    vint32m1_t op2, size_t vl);
vint32m1_t vmerge_vxm_i32m1 (vbool32_t mask, vint32m1_t op1,
    int32_t op2, size_t vl);
vint32m2_t vmerge_vvm_i32m2 (vbool16_t mask, vint32m2_t op1,
    vint32m2_t op2, size_t vl);
vint32m2_t vmerge_vxm_i32m2 (vbool16_t mask, vint32m2_t op1,
    int32_t op2, size_t vl);
vint32m4_t vmerge_vvm_i32m4 (vbool8_t mask, vint32m4_t op1,
    vint32m4_t op2, size_t vl);
vint32m4_t vmerge_vxm_i32m4 (vbool8_t mask, vint32m4_t op1,
    int32_t op2, size_t vl);
vint32m8_t vmerge_vvm_i32m8 (vbool4_t mask, vint32m8_t op1,
    vint32m8_t op2, size_t vl);
vint32m8_t vmerge_vxm_i32m8 (vbool4_t mask, vint32m8_t op1,
    int32_t op2, size_t vl);
vint64m1_t vmerge_vvm_i64m1 (vbool64_t mask, vint64m1_t op1,
    vint64m1_t op2, size_t vl);

```

```

vint64m1_t vmerge_vxm_i64m1 (vbool64_t mask, vint64m1_t op1,
    int64_t op2, size_t vl);
vint64m2_t vmerge_vvm_i64m2 (vbool32_t mask, vint64m2_t op1,
    vint64m2_t op2, size_t vl);
vint64m2_t vmerge_vxm_i64m2 (vbool32_t mask, vint64m2_t op1,
    int64_t op2, size_t vl);
vint64m4_t vmerge_vvm_i64m4 (vbool16_t mask, vint64m4_t op1,
    vint64m4_t op2, size_t vl);
vint64m4_t vmerge_vxm_i64m4 (vbool16_t mask, vint64m4_t op1,
    int64_t op2, size_t vl);
vint64m8_t vmerge_vvm_i64m8 (vbool8_t mask, vint64m8_t op1,
    vint64m8_t op2, size_t vl);
vint64m8_t vmerge_vxm_i64m8 (vbool8_t mask, vint64m8_t op1,
    int64_t op2, size_t vl);
vuint8m1_t vmerge_vvm_u8m1 (vbool8_t mask, vuint8m1_t op1,
    vuint8m1_t op2, size_t vl);
vuint8m1_t vmerge_vxm_u8m1 (vbool8_t mask, vuint8m1_t op1,
    uint8_t op2, size_t vl);
vuint8m2_t vmerge_vvm_u8m2 (vbool4_t mask, vuint8m2_t op1,
    vuint8m2_t op2, size_t vl);
vuint8m2_t vmerge_vxm_u8m2 (vbool4_t mask, vuint8m2_t op1,
    uint8_t op2, size_t vl);
vuint8m4_t vmerge_vvm_u8m4 (vbool2_t mask, vuint8m4_t op1,
    vuint8m4_t op2, size_t vl);
vuint8m4_t vmerge_vxm_u8m4 (vbool2_t mask, vuint8m4_t op1,
    uint8_t op2, size_t vl);
vuint8m8_t vmerge_vvm_u8m8 (vbool1_t mask, vuint8m8_t op1,
    vuint8m8_t op2, size_t vl);
vuint8m8_t vmerge_vxm_u8m8 (vbool1_t mask, vuint8m8_t op1,
    uint8_t op2, size_t vl);
vuint16m1_t vmerge_vvm_u16m1 (vbool16_t mask, vuint16m1_t op1,
    vuint16m1_t op2, size_t vl);
vuint16m1_t vmerge_vxm_u16m1 (vbool16_t mask, vuint16m1_t op1,
    uint16_t op2, size_t vl);
vuint16m2_t vmerge_vvm_u16m2 (vbool8_t mask, vuint16m2_t op1,
    vuint16m2_t op2, size_t vl);
vuint16m2_t vmerge_vxm_u16m2 (vbool8_t mask, vuint16m2_t op1,
    uint16_t op2, size_t vl);
vuint16m4_t vmerge_vvm_u16m4 (vbool4_t mask, vuint16m4_t op1,
    vuint16m4_t op2, size_t vl);
vuint16m4_t vmerge_vxm_u16m4 (vbool4_t mask, vuint16m4_t op1,
    uint16_t op2, size_t vl);
vuint16m8_t vmerge_vvm_u16m8 (vbool2_t mask, vuint16m8_t op1,
    vuint16m8_t op2, size_t vl);
vuint16m8_t vmerge_vxm_u16m8 (vbool2_t mask, vuint16m8_t op1,
    uint16_t op2, size_t vl);

```

```

vuint32m1_t vmerge_vvm_u32m1 (vbool32_t mask, vuint32m1_t op1,
    vuint32m1_t op2, size_t vl);
vuint32m1_t vmerge_vxm_u32m1 (vbool32_t mask, vuint32m1_t op1,
    uint32_t op2, size_t vl);
vuint32m2_t vmerge_vvm_u32m2 (vbool16_t mask, vuint32m2_t op1,
    vuint32m2_t op2, size_t vl);
vuint32m2_t vmerge_vxm_u32m2 (vbool16_t mask, vuint32m2_t op1,
    uint32_t op2, size_t vl);
vuint32m4_t vmerge_vvm_u32m4 (vbool8_t mask, vuint32m4_t op1,
    vuint32m4_t op2, size_t vl);
vuint32m4_t vmerge_vxm_u32m4 (vbool8_t mask, vuint32m4_t op1,
    uint32_t op2, size_t vl);
vuint32m8_t vmerge_vvm_u32m8 (vbool4_t mask, vuint32m8_t op1,
    vuint32m8_t op2, size_t vl);
vuint32m8_t vmerge_vxm_u32m8 (vbool4_t mask, vuint32m8_t op1,
    uint32_t op2, size_t vl);
vuint64m1_t vmerge_vvm_u64m1 (vbool64_t mask, vuint64m1_t op1,
    vuint64m1_t op2, size_t vl);
vuint64m1_t vmerge_vxm_u64m1 (vbool64_t mask, vuint64m1_t op1,
    uint64_t op2, size_t vl);
vuint64m2_t vmerge_vvm_u64m2 (vbool32_t mask, vuint64m2_t op1,
    vuint64m2_t op2, size_t vl);
vuint64m2_t vmerge_vxm_u64m2 (vbool32_t mask, vuint64m2_t op1,
    uint64_t op2, size_t vl);
vuint64m4_t vmerge_vvm_u64m4 (vbool16_t mask, vuint64m4_t op1,
    vuint64m4_t op2, size_t vl);
vuint64m4_t vmerge_vxm_u64m4 (vbool16_t mask, vuint64m4_t op1,
    uint64_t op2, size_t vl);
vuint64m8_t vmerge_vvm_u64m8 (vbool8_t mask, vuint64m8_t op1,
    vuint64m8_t op2, size_t vl);
vuint64m8_t vmerge_vxm_u64m8 (vbool8_t mask, vuint64m8_t op1,
    uint64_t op2, size_t vl);

```

## Vector Integer Move Functions:

### Prototypes:

```

vint8m1_t vmv_v_v_i8m1 (vint8m1_t src, size_t vl);
vint8m1_t vmv_v_x_i8m1 (int8_t src, size_t vl);
vint8m2_t vmv_v_v_i8m2 (vint8m2_t src, size_t vl);
vint8m2_t vmv_v_x_i8m2 (int8_t src, size_t vl);
vint8m4_t vmv_v_v_i8m4 (vint8m4_t src, size_t vl);
vint8m4_t vmv_v_x_i8m4 (int8_t src, size_t vl);
vint8m8_t vmv_v_v_i8m8 (vint8m8_t src, size_t vl);
vint8m8_t vmv_v_x_i8m8 (int8_t src, size_t vl);
vint16m1_t vmv_v_v_i16m1 (vint16m1_t src, size_t vl);

```



```

vint16m1_t vmv_v_x_i16m1 (int16_t src, size_t vl);
vint16m2_t vmv_v_v_i16m2 (vint16m2_t src, size_t vl);
vint16m2_t vmv_v_x_i16m2 (int16_t src, size_t vl);
vint16m4_t vmv_v_v_i16m4 (vint16m4_t src, size_t vl);
vint16m4_t vmv_v_x_i16m4 (int16_t src, size_t vl);
vint16m8_t vmv_v_v_i16m8 (vint16m8_t src, size_t vl);
vint16m8_t vmv_v_x_i16m8 (int16_t src, size_t vl);
vint32m1_t vmv_v_v_i32m1 (vint32m1_t src, size_t vl);
vint32m1_t vmv_v_x_i32m1 (int32_t src, size_t vl);
vint32m2_t vmv_v_v_i32m2 (vint32m2_t src, size_t vl);
vint32m2_t vmv_v_x_i32m2 (int32_t src, size_t vl);
vint32m4_t vmv_v_v_i32m4 (vint32m4_t src, size_t vl);
vint32m4_t vmv_v_x_i32m4 (int32_t src, size_t vl);
vint32m8_t vmv_v_v_i32m8 (vint32m8_t src, size_t vl);
vint32m8_t vmv_v_x_i32m8 (int32_t src, size_t vl);
vint64m1_t vmv_v_v_i64m1 (vint64m1_t src, size_t vl);
vint64m1_t vmv_v_x_i64m1 (int64_t src, size_t vl);
vint64m2_t vmv_v_v_i64m2 (vint64m2_t src, size_t vl);
vint64m2_t vmv_v_x_i64m2 (int64_t src, size_t vl);
vint64m4_t vmv_v_v_i64m4 (vint64m4_t src, size_t vl);
vint64m4_t vmv_v_x_i64m4 (int64_t src, size_t vl);
vint64m8_t vmv_v_v_i64m8 (vint64m8_t src, size_t vl);
vint64m8_t vmv_v_x_i64m8 (int64_t src, size_t vl);
vuint8m1_t vmv_v_v_u8m1 (vuint8m1_t src, size_t vl);
vuint8m1_t vmv_v_x_u8m1 (uint8_t src, size_t vl);
vuint8m2_t vmv_v_v_u8m2 (vuint8m2_t src, size_t vl);
vuint8m2_t vmv_v_x_u8m2 (uint8_t src, size_t vl);
vuint8m4_t vmv_v_v_u8m4 (vuint8m4_t src, size_t vl);
vuint8m4_t vmv_v_x_u8m4 (uint8_t src, size_t vl);
vuint8m8_t vmv_v_v_u8m8 (vuint8m8_t src, size_t vl);
vuint8m8_t vmv_v_x_u8m8 (uint8_t src, size_t vl);
vuint16m1_t vmv_v_v_u16m1 (vuint16m1_t src, size_t vl);
vuint16m1_t vmv_v_x_u16m1 (uint16_t src, size_t vl);
vuint16m2_t vmv_v_v_u16m2 (vuint16m2_t src, size_t vl);
vuint16m2_t vmv_v_x_u16m2 (uint16_t src, size_t vl);
vuint16m4_t vmv_v_v_u16m4 (vuint16m4_t src, size_t vl);
vuint16m4_t vmv_v_x_u16m4 (uint16_t src, size_t vl);
vuint16m8_t vmv_v_v_u16m8 (vuint16m8_t src, size_t vl);
vuint16m8_t vmv_v_x_u16m8 (uint16_t src, size_t vl);
vuint32m1_t vmv_v_v_u32m1 (vuint32m1_t src, size_t vl);
vuint32m1_t vmv_v_x_u32m1 (uint32_t src, size_t vl);
vuint32m2_t vmv_v_v_u32m2 (vuint32m2_t src, size_t vl);
vuint32m2_t vmv_v_x_u32m2 (uint32_t src, size_t vl);
vuint32m4_t vmv_v_v_u32m4 (vuint32m4_t src, size_t vl);
vuint32m4_t vmv_v_x_u32m4 (uint32_t src, size_t vl);
vuint32m8_t vmv_v_v_u32m8 (vuint32m8_t src, size_t vl);

```

```

vuint32m8_t vmv_v_x_u32m8 (uint32_t src, size_t vl);
vuint64m1_t vmv_v_v_u64m1 (vuint64m1_t src, size_t vl);
vuint64m1_t vmv_v_x_u64m1 (uint64_t src, size_t vl);
vuint64m2_t vmv_v_v_u64m2 (vuint64m2_t src, size_t vl);
vuint64m2_t vmv_v_x_u64m2 (uint64_t src, size_t vl);
vuint64m4_t vmv_v_v_u64m4 (vuint64m4_t src, size_t vl);
vuint64m4_t vmv_v_x_u64m4 (uint64_t src, size_t vl);
vuint64m8_t vmv_v_v_u64m8 (vuint64m8_t src, size_t vl);
vuint64m8_t vmv_v_x_u64m8 (uint64_t src, size_t vl);

```

## Vector Fixed-Point Arithmetic Functions:

### Vector Single-Width Saturating Add and Subtract Functions:

#### Prototypes:

```

vint8m1_t vsadd_vv_i8m1 (vint8m1_t op1, vint8m1_t op2, size_t
    vl);
vint8m1_t vsadd_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vsadd_vv_i8m2 (vint8m2_t op1, vint8m2_t op2, size_t
    vl);
vint8m2_t vsadd_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vsadd_vv_i8m4 (vint8m4_t op1, vint8m4_t op2, size_t
    vl);
vint8m4_t vsadd_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vsadd_vv_i8m8 (vint8m8_t op1, vint8m8_t op2, size_t
    vl);
vint8m8_t vsadd_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vsadd_vv_i16m1 (vint16m1_t op1, vint16m1_t op2,
    size_t vl);
vint16m1_t vsadd_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint16m2_t vsadd_vv_i16m2 (vint16m2_t op1, vint16m2_t op2,
    size_t vl);
vint16m2_t vsadd_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint16m4_t vsadd_vv_i16m4 (vint16m4_t op1, vint16m4_t op2,
    size_t vl);
vint16m4_t vsadd_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vint16m8_t vsadd_vv_i16m8 (vint16m8_t op1, vint16m8_t op2,
    size_t vl);
vint16m8_t vsadd_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t
    vl);
vint32m1_t vsadd_vv_i32m1 (vint32m1_t op1, vint32m1_t op2,
    size_t vl);

```

```

vint32m1_t vsadd_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint32m2_t vsadd_vv_i32m2 (vint32m2_t op1, vint32m2_t op2,
    size_t vl);
vint32m2_t vsadd_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint32m4_t vsadd_vv_i32m4 (vint32m4_t op1, vint32m4_t op2,
    size_t vl);
vint32m4_t vsadd_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint32m8_t vsadd_vv_i32m8 (vint32m8_t op1, vint32m8_t op2,
    size_t vl);
vint32m8_t vsadd_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t
    vl);
vint64m1_t vsadd_vv_i64m1 (vint64m1_t op1, vint64m1_t op2,
    size_t vl);
vint64m1_t vsadd_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t
    vl);
vint64m2_t vsadd_vv_i64m2 (vint64m2_t op1, vint64m2_t op2,
    size_t vl);
vint64m2_t vsadd_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t
    vl);
vint64m4_t vsadd_vv_i64m4 (vint64m4_t op1, vint64m4_t op2,
    size_t vl);
vint64m4_t vsadd_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t
    vl);
vint64m8_t vsadd_vv_i64m8 (vint64m8_t op1, vint64m8_t op2,
    size_t vl);
vint64m8_t vsadd_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vuint8m1_t vsaddu_vv_u8m1 (vuint8m1_t op1, vuint8m1_t op2,
    size_t vl);
vuint8m1_t vsaddu_vx_u8m1 (vuint8m1_t op1, uint8_t op2, size_t
    vl);
vuint8m2_t vsaddu_vv_u8m2 (vuint8m2_t op1, vuint8m2_t op2,
    size_t vl);
vuint8m2_t vsaddu_vx_u8m2 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vuint8m4_t vsaddu_vv_u8m4 (vuint8m4_t op1, vuint8m4_t op2,
    size_t vl);
vuint8m4_t vsaddu_vx_u8m4 (vuint8m4_t op1, uint8_t op2, size_t
    vl);
vuint8m8_t vsaddu_vv_u8m8 (vuint8m8_t op1, vuint8m8_t op2,
    size_t vl);
vuint8m8_t vsaddu_vx_u8m8 (vuint8m8_t op1, uint8_t op2, size_t
    vl);

```

```

vuint16m1_t vsaddu_vv_u16m1 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vuint16m1_t vsaddu_vx_u16m1 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vuint16m2_t vsaddu_vv_u16m2 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vuint16m2_t vsaddu_vx_u16m2 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vuint16m4_t vsaddu_vv_u16m4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vuint16m4_t vsaddu_vx_u16m4 (vuint16m4_t op1, uint16_t op2,
    size_t vl);
vuint16m8_t vsaddu_vv_u16m8 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);
vuint16m8_t vsaddu_vx_u16m8 (vuint16m8_t op1, uint16_t op2,
    size_t vl);
vuint32m1_t vsaddu_vv_u32m1 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vuint32m1_t vsaddu_vx_u32m1 (vuint32m1_t op1, uint32_t op2,
    size_t vl);
vuint32m2_t vsaddu_vv_u32m2 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vuint32m2_t vsaddu_vx_u32m2 (vuint32m2_t op1, uint32_t op2,
    size_t vl);
vuint32m4_t vsaddu_vv_u32m4 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vuint32m4_t vsaddu_vx_u32m4 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vuint32m8_t vsaddu_vv_u32m8 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vuint32m8_t vsaddu_vx_u32m8 (vuint32m8_t op1, uint32_t op2,
    size_t vl);
vuint64m1_t vsaddu_vv_u64m1 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vuint64m1_t vsaddu_vx_u64m1 (vuint64m1_t op1, uint64_t op2,
    size_t vl);
vuint64m2_t vsaddu_vv_u64m2 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vuint64m2_t vsaddu_vx_u64m2 (vuint64m2_t op1, uint64_t op2,
    size_t vl);
vuint64m4_t vsaddu_vv_u64m4 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vuint64m4_t vsaddu_vx_u64m4 (vuint64m4_t op1, uint64_t op2,
    size_t vl);
vuint64m8_t vsaddu_vv_u64m8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);

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vuint64m8_t vsaddu_vx_u64m8 (vuint64m8_t op1, uint64_t op2,
    size_t vl);
vint8m1_t vssub_vv_i8m1 (vint8m1_t op1, vint8m1_t op2, size_t
    vl);
vint8m1_t vssub_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vssub_vv_i8m2 (vint8m2_t op1, vint8m2_t op2, size_t
    vl);
vint8m2_t vssub_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vssub_vv_i8m4 (vint8m4_t op1, vint8m4_t op2, size_t
    vl);
vint8m4_t vssub_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vssub_vv_i8m8 (vint8m8_t op1, vint8m8_t op2, size_t
    vl);
vint8m8_t vssub_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vssub_vv_i16m1 (vint16m1_t op1, vint16m1_t op2,
    size_t vl);
vint16m1_t vssub_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint16m2_t vssub_vv_i16m2 (vint16m2_t op1, vint16m2_t op2,
    size_t vl);
vint16m2_t vssub_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint16m4_t vssub_vv_i16m4 (vint16m4_t op1, vint16m4_t op2,
    size_t vl);
vint16m4_t vssub_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vint16m8_t vssub_vv_i16m8 (vint16m8_t op1, vint16m8_t op2,
    size_t vl);
vint16m8_t vssub_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t
    vl);
vint32m1_t vssub_vv_i32m1 (vint32m1_t op1, vint32m1_t op2,
    size_t vl);
vint32m1_t vssub_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint32m2_t vssub_vv_i32m2 (vint32m2_t op1, vint32m2_t op2,
    size_t vl);
vint32m2_t vssub_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint32m4_t vssub_vv_i32m4 (vint32m4_t op1, vint32m4_t op2,
    size_t vl);
vint32m4_t vssub_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint32m8_t vssub_vv_i32m8 (vint32m8_t op1, vint32m8_t op2,
    size_t vl);
vint32m8_t vssub_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t
    vl);

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vint64m1_t vssub_vv_i64m1 (vint64m1_t op1, vint64m1_t op2,
    size_t vl);
vint64m1_t vssub_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t
    vl);
vint64m2_t vssub_vv_i64m2 (vint64m2_t op1, vint64m2_t op2,
    size_t vl);
vint64m2_t vssub_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t
    vl);
vint64m4_t vssub_vv_i64m4 (vint64m4_t op1, vint64m4_t op2,
    size_t vl);
vint64m4_t vssub_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t
    vl);
vint64m8_t vssub_vv_i64m8 (vint64m8_t op1, vint64m8_t op2,
    size_t vl);
vint64m8_t vssub_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vuint8m1_t vssubu_vv_u8m1 (vuint8m1_t op1, vuint8m1_t op2,
    size_t vl);
vuint8m1_t vssubu_vx_u8m1 (vuint8m1_t op1, uint8_t op2, size_t
    vl);
vuint8m2_t vssubu_vv_u8m2 (vuint8m2_t op1, vuint8m2_t op2,
    size_t vl);
vuint8m2_t vssubu_vx_u8m2 (vuint8m2_t op1, uint8_t op2, size_t
    vl);
vuint8m4_t vssubu_vv_u8m4 (vuint8m4_t op1, vuint8m4_t op2,
    size_t vl);
vuint8m4_t vssubu_vx_u8m4 (vuint8m4_t op1, uint8_t op2, size_t
    vl);
vuint8m8_t vssubu_vv_u8m8 (vuint8m8_t op1, vuint8m8_t op2,
    size_t vl);
vuint8m8_t vssubu_vx_u8m8 (vuint8m8_t op1, uint8_t op2, size_t
    vl);
vuint16m1_t vssubu_vv_u16m1 (vuint16m1_t op1, vuint16m1_t op2,
    size_t vl);
vuint16m1_t vssubu_vx_u16m1 (vuint16m1_t op1, uint16_t op2,
    size_t vl);
vuint16m2_t vssubu_vv_u16m2 (vuint16m2_t op1, vuint16m2_t op2,
    size_t vl);
vuint16m2_t vssubu_vx_u16m2 (vuint16m2_t op1, uint16_t op2,
    size_t vl);
vuint16m4_t vssubu_vv_u16m4 (vuint16m4_t op1, vuint16m4_t op2,
    size_t vl);
vuint16m4_t vssubu_vx_u16m4 (vuint16m4_t op1, uint16_t op2,
    size_t vl);
vuint16m8_t vssubu_vv_u16m8 (vuint16m8_t op1, vuint16m8_t op2,
    size_t vl);

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vuint16m8_t vssubu_vx_u16m8 (vuint16m8_t op1, uint16_t op2,
    size_t vl);
vuint32m1_t vssubu_vv_u32m1 (vuint32m1_t op1, vuint32m1_t op2,
    size_t vl);
vuint32m1_t vssubu_vx_u32m1 (vuint32m1_t op1, uint32_t op2,
    size_t vl);
vuint32m2_t vssubu_vv_u32m2 (vuint32m2_t op1, vuint32m2_t op2,
    size_t vl);
vuint32m2_t vssubu_vx_u32m2 (vuint32m2_t op1, uint32_t op2,
    size_t vl);
vuint32m4_t vssubu_vv_u32m4 (vuint32m4_t op1, vuint32m4_t op2,
    size_t vl);
vuint32m4_t vssubu_vx_u32m4 (vuint32m4_t op1, uint32_t op2,
    size_t vl);
vuint32m8_t vssubu_vv_u32m8 (vuint32m8_t op1, vuint32m8_t op2,
    size_t vl);
vuint32m8_t vssubu_vx_u32m8 (vuint32m8_t op1, uint32_t op2,
    size_t vl);
vuint64m1_t vssubu_vv_u64m1 (vuint64m1_t op1, vuint64m1_t op2,
    size_t vl);
vuint64m1_t vssubu_vx_u64m1 (vuint64m1_t op1, uint64_t op2,
    size_t vl);
vuint64m2_t vssubu_vv_u64m2 (vuint64m2_t op1, vuint64m2_t op2,
    size_t vl);
vuint64m2_t vssubu_vx_u64m2 (vuint64m2_t op1, uint64_t op2,
    size_t vl);
vuint64m4_t vssubu_vv_u64m4 (vuint64m4_t op1, vuint64m4_t op2,
    size_t vl);
vuint64m4_t vssubu_vx_u64m4 (vuint64m4_t op1, uint64_t op2,
    size_t vl);
vuint64m8_t vssubu_vv_u64m8 (vuint64m8_t op1, vuint64m8_t op2,
    size_t vl);
vuint64m8_t vssubu_vx_u64m8 (vuint64m8_t op1, uint64_t op2,
    size_t vl);
// masked functions
vint8m1_t vsadd_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vsadd_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vsadd_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vsadd_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vsadd_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);

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vint8m4_t vsadd_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vsadd_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vsadd_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vsadd_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint16m1_t vsadd_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vint16m2_t vsadd_vv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, vint16m2_t op2, size_t vl);
vint16m2_t vsadd_vx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vsadd_vv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, vint16m4_t op2, size_t vl);
vint16m4_t vsadd_vx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vsadd_vv_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, vint16m8_t op2, size_t vl);
vint16m8_t vsadd_vx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vsadd_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint32m1_t vsadd_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vsadd_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vint32m2_t vsadd_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint32m4_t vsadd_vv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, vint32m4_t op2, size_t vl);
vint32m4_t vsadd_vx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vsadd_vv_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, vint32m8_t op2, size_t vl);
vint32m8_t vsadd_vx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vsadd_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vint64m1_t vsadd_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vsadd_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vint64m2_t vsadd_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);

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vint64m4_t vsadd_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vint64m4_t vsadd_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vsadd_vv_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, vint64m8_t op2, size_t vl);
vint64m8_t vsadd_vx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, int64_t op2, size_t vl);
vuint8m1_t vsaddu_vv_u8m1_m (vbool8_t mask, vuint8m1_t
    maskedoff, vuint8m1_t op1, vuint8m1_t op2, size_t vl);
vuint8m1_t vsaddu_vx_u8m1_m (vbool8_t mask, vuint8m1_t
    maskedoff, vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vsaddu_vv_u8m2_m (vbool4_t mask, vuint8m2_t
    maskedoff, vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vuint8m2_t vsaddu_vx_u8m2_m (vbool4_t mask, vuint8m2_t
    maskedoff, vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vsaddu_vv_u8m4_m (vbool2_t mask, vuint8m4_t
    maskedoff, vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vuint8m4_t vsaddu_vx_u8m4_m (vbool2_t mask, vuint8m4_t
    maskedoff, vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vsaddu_vv_u8m8_m (vbool1_t mask, vuint8m8_t
    maskedoff, vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vuint8m8_t vsaddu_vx_u8m8_m (vbool1_t mask, vuint8m8_t
    maskedoff, vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vsaddu_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vuint16m1_t vsaddu_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint16m2_t vsaddu_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vuint16m2_t vsaddu_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint16m4_t vsaddu_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vuint16m4_t vsaddu_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vuint16m8_t vsaddu_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vuint16m8_t vsaddu_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vuint32m1_t vsaddu_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vuint32m1_t vsaddu_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vuint32m2_t vsaddu_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);

```

```

vuint32m2_t vsaddu_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint32m4_t vsaddu_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vuint32m4_t vsaddu_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vuint32m8_t vsaddu_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vuint32m8_t vsaddu_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);
vuint64m1_t vsaddu_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vuint64m1_t vsaddu_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vuint64m2_t vsaddu_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vuint64m2_t vsaddu_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vuint64m4_t vsaddu_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);
vuint64m4_t vsaddu_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vuint64m8_t vsaddu_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vuint64m8_t vsaddu_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);
vint8m1_t vssub_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vssub_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vssub_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vssub_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vssub_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vssub_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vssub_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vssub_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vssub_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint16m1_t vssub_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);

```

```

vint16m2_t vssub_vv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, vint16m2_t op2, size_t vl);
vint16m2_t vssub_vx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vssub_vv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, vint16m4_t op2, size_t vl);
vint16m4_t vssub_vx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vssub_vv_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, vint16m8_t op2, size_t vl);
vint16m8_t vssub_vx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vssub_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint32m1_t vssub_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vssub_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vint32m2_t vssub_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint32m4_t vssub_vv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, vint32m4_t op2, size_t vl);
vint32m4_t vssub_vx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vssub_vv_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, vint32m8_t op2, size_t vl);
vint32m8_t vssub_vx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vssub_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vint64m1_t vssub_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vssub_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vint64m2_t vssub_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vint64m4_t vssub_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vint64m4_t vssub_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vssub_vv_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, vint64m8_t op2, size_t vl);
vint64m8_t vssub_vx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, int64_t op2, size_t vl);
vuint8m1_t vssub_vv_u8m1_m (vbool8_t mask, vuint8m1_t
    maskedoff, vuint8m1_t op1, vuint8m1_t op2, size_t vl);

```

```

vuint8m1_t vssubv_vx_u8m1_m (vbool8_t mask, vuint8m1_t
    maskedoff, vuint8m1_t op1, uint8_t op2, size_t vl);
vuint8m2_t vssubv_vv_u8m2_m (vbool4_t mask, vuint8m2_t
    maskedoff, vuint8m2_t op1, vuint8m2_t op2, size_t vl);
vuint8m2_t vssubv_vx_u8m2_m (vbool4_t mask, vuint8m2_t
    maskedoff, vuint8m2_t op1, uint8_t op2, size_t vl);
vuint8m4_t vssubv_vv_u8m4_m (vbool2_t mask, vuint8m4_t
    maskedoff, vuint8m4_t op1, vuint8m4_t op2, size_t vl);
vuint8m4_t vssubv_vx_u8m4_m (vbool2_t mask, vuint8m4_t
    maskedoff, vuint8m4_t op1, uint8_t op2, size_t vl);
vuint8m8_t vssubv_vv_u8m8_m (vbool1_t mask, vuint8m8_t
    maskedoff, vuint8m8_t op1, vuint8m8_t op2, size_t vl);
vuint8m8_t vssubv_vx_u8m8_m (vbool1_t mask, vuint8m8_t
    maskedoff, vuint8m8_t op1, uint8_t op2, size_t vl);
vuint16m1_t vssubv_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t op2, size_t vl);
vuint16m1_t vssubv_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, uint16_t op2, size_t vl);
vuint16m2_t vssubv_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t op2, size_t vl);
vuint16m2_t vssubv_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, uint16_t op2, size_t vl);
vuint16m4_t vssubv_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t op2, size_t vl);
vuint16m4_t vssubv_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, uint16_t op2, size_t vl);
vuint16m8_t vssubv_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t op2, size_t vl);
vuint16m8_t vssubv_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, uint16_t op2, size_t vl);
vuint32m1_t vssubv_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t op2, size_t vl);
vuint32m1_t vssubv_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, uint32_t op2, size_t vl);
vuint32m2_t vssubv_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t op2, size_t vl);
vuint32m2_t vssubv_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, uint32_t op2, size_t vl);
vuint32m4_t vssubv_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t op2, size_t vl);
vuint32m4_t vssubv_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, uint32_t op2, size_t vl);
vuint32m8_t vssubv_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t op2, size_t vl);
vuint32m8_t vssubv_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, uint32_t op2, size_t vl);

```

```

vuint64m1_t vssubv_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t op2, size_t vl);
vuint64m1_t vssubv_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, uint64_t op2, size_t vl);
vuint64m2_t vssubv_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t op2, size_t vl);
vuint64m2_t vssubv_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, uint64_t op2, size_t vl);
vuint64m4_t vssubv_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t op2, size_t vl);
vuint64m4_t vssubv_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, uint64_t op2, size_t vl);
vuint64m8_t vssubv_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t op2, size_t vl);
vuint64m8_t vssubv_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, uint64_t op2, size_t vl);

```

## Vector Single-Width Averaging Add and Subtract Functions:

### Prototypes:

```

vint8m1_t vaadd_vv_i8m1 (vint8m1_t op1, vint8m1_t op2, size_t
    vl);
vint8m1_t vaadd_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vaadd_vv_i8m2 (vint8m2_t op1, vint8m2_t op2, size_t
    vl);
vint8m2_t vaadd_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vaadd_vv_i8m4 (vint8m4_t op1, vint8m4_t op2, size_t
    vl);
vint8m4_t vaadd_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vaadd_vv_i8m8 (vint8m8_t op1, vint8m8_t op2, size_t
    vl);
vint8m8_t vaadd_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vaadd_vv_i16m1 (vint16m1_t op1, vint16m1_t op2,
    size_t vl);
vint16m1_t vaadd_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint16m2_t vaadd_vv_i16m2 (vint16m2_t op1, vint16m2_t op2,
    size_t vl);
vint16m2_t vaadd_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint16m4_t vaadd_vv_i16m4 (vint16m4_t op1, vint16m4_t op2,
    size_t vl);
vint16m4_t vaadd_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t
    vl);

```

```

vint16m8_t vaadd_vv_i16m8 (vint16m8_t op1, vint16m8_t op2,
    size_t vl);
vint16m8_t vaadd_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t
    vl);
vint32m1_t vaadd_vv_i32m1 (vint32m1_t op1, vint32m1_t op2,
    size_t vl);
vint32m1_t vaadd_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint32m2_t vaadd_vv_i32m2 (vint32m2_t op1, vint32m2_t op2,
    size_t vl);
vint32m2_t vaadd_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint32m4_t vaadd_vv_i32m4 (vint32m4_t op1, vint32m4_t op2,
    size_t vl);
vint32m4_t vaadd_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint32m8_t vaadd_vv_i32m8 (vint32m8_t op1, vint32m8_t op2,
    size_t vl);
vint32m8_t vaadd_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t
    vl);
vint64m1_t vaadd_vv_i64m1 (vint64m1_t op1, vint64m1_t op2,
    size_t vl);
vint64m1_t vaadd_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t
    vl);
vint64m2_t vaadd_vv_i64m2 (vint64m2_t op1, vint64m2_t op2,
    size_t vl);
vint64m2_t vaadd_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t
    vl);
vint64m4_t vaadd_vv_i64m4 (vint64m4_t op1, vint64m4_t op2,
    size_t vl);
vint64m4_t vaadd_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t
    vl);
vint64m8_t vaadd_vv_i64m8 (vint64m8_t op1, vint64m8_t op2,
    size_t vl);
vint64m8_t vaadd_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t
    vl);
vint8m1_t vasub_vv_i8m1 (vint8m1_t op1, vint8m1_t op2, size_t
    vl);
vint8m1_t vasub_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vasub_vv_i8m2 (vint8m2_t op1, vint8m2_t op2, size_t
    vl);
vint8m2_t vasub_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vasub_vv_i8m4 (vint8m4_t op1, vint8m4_t op2, size_t
    vl);
vint8m4_t vasub_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);

```

```

vint8m8_t vasub_vv_i8m8 (vint8m8_t op1, vint8m8_t op2, size_t
    vl);
vint8m8_t vasub_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vasub_vv_i16m1 (vint16m1_t op1, vint16m1_t op2,
    size_t vl);
vint16m1_t vasub_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint16m2_t vasub_vv_i16m2 (vint16m2_t op1, vint16m2_t op2,
    size_t vl);
vint16m2_t vasub_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint16m4_t vasub_vv_i16m4 (vint16m4_t op1, vint16m4_t op2,
    size_t vl);
vint16m4_t vasub_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vint16m8_t vasub_vv_i16m8 (vint16m8_t op1, vint16m8_t op2,
    size_t vl);
vint16m8_t vasub_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t
    vl);
vint32m1_t vasub_vv_i32m1 (vint32m1_t op1, vint32m1_t op2,
    size_t vl);
vint32m1_t vasub_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint32m2_t vasub_vv_i32m2 (vint32m2_t op1, vint32m2_t op2,
    size_t vl);
vint32m2_t vasub_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint32m4_t vasub_vv_i32m4 (vint32m4_t op1, vint32m4_t op2,
    size_t vl);
vint32m4_t vasub_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint32m8_t vasub_vv_i32m8 (vint32m8_t op1, vint32m8_t op2,
    size_t vl);
vint32m8_t vasub_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t
    vl);
vint64m1_t vasub_vv_i64m1 (vint64m1_t op1, vint64m1_t op2,
    size_t vl);
vint64m1_t vasub_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t
    vl);
vint64m2_t vasub_vv_i64m2 (vint64m2_t op1, vint64m2_t op2,
    size_t vl);
vint64m2_t vasub_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t
    vl);
vint64m4_t vasub_vv_i64m4 (vint64m4_t op1, vint64m4_t op2,
    size_t vl);

```

```

vint64m4_t vasub_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t
    vl);
vint64m8_t vasub_vv_i64m8 (vint64m8_t op1, vint64m8_t op2,
    size_t vl);
vint64m8_t vasub_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t
    vl);
// masked functions
vint8m1_t vaadd_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vaadd_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vaadd_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vaadd_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vaadd_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vaadd_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vaadd_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vaadd_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vaadd_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint16m1_t vaadd_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vint16m2_t vaadd_vv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, vint16m2_t op2, size_t vl);
vint16m2_t vaadd_vx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vaadd_vv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, vint16m4_t op2, size_t vl);
vint16m4_t vaadd_vx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vaadd_vv_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, vint16m8_t op2, size_t vl);
vint16m8_t vaadd_vx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vaadd_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint32m1_t vaadd_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vaadd_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);

```



```

vint32m2_t vaadd_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint32m4_t vaadd_vv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, vint32m4_t op2, size_t vl);
vint32m4_t vaadd_vx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vaadd_vv_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, vint32m8_t op2, size_t vl);
vint32m8_t vaadd_vx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vaadd_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vint64m1_t vaadd_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vaadd_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vint64m2_t vaadd_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vint64m4_t vaadd_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vint64m4_t vaadd_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vaadd_vv_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, vint64m8_t op2, size_t vl);
vint64m8_t vaadd_vx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, int64_t op2, size_t vl);
vint8m1_t vasub_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vasub_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vasub_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vasub_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vasub_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vasub_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vasub_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vasub_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vasub_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint16m1_t vasub_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);

```

```

vint16m2_t vasub_vv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, vint16m2_t op2, size_t vl);
vint16m2_t vasub_vx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vasub_vv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, vint16m4_t op2, size_t vl);
vint16m4_t vasub_vx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vasub_vv_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, vint16m8_t op2, size_t vl);
vint16m8_t vasub_vx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vasub_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint32m1_t vasub_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vasub_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vint32m2_t vasub_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint32m4_t vasub_vv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, vint32m4_t op2, size_t vl);
vint32m4_t vasub_vx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vasub_vv_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, vint32m8_t op2, size_t vl);
vint32m8_t vasub_vx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vasub_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vint64m1_t vasub_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vasub_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vint64m2_t vasub_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vint64m4_t vasub_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vint64m4_t vasub_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vasub_vv_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, vint64m8_t op2, size_t vl);
vint64m8_t vasub_vx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, int64_t op2, size_t vl);

```

## Vector Single-Width Fractional Multiply with Rounding and Saturation Functions:

### Prototypes:

```
vint8m1_t vsmul_vv_i8m1 (vint8m1_t op1, vint8m1_t op2, size_t
    vl);
vint8m1_t vsmul_vx_i8m1 (vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vsmul_vv_i8m2 (vint8m2_t op1, vint8m2_t op2, size_t
    vl);
vint8m2_t vsmul_vx_i8m2 (vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vsmul_vv_i8m4 (vint8m4_t op1, vint8m4_t op2, size_t
    vl);
vint8m4_t vsmul_vx_i8m4 (vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vsmul_vv_i8m8 (vint8m8_t op1, vint8m8_t op2, size_t
    vl);
vint8m8_t vsmul_vx_i8m8 (vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vsmul_vv_i16m1 (vint16m1_t op1, vint16m1_t op2,
    size_t vl);
vint16m1_t vsmul_vx_i16m1 (vint16m1_t op1, int16_t op2, size_t
    vl);
vint16m2_t vsmul_vv_i16m2 (vint16m2_t op1, vint16m2_t op2,
    size_t vl);
vint16m2_t vsmul_vx_i16m2 (vint16m2_t op1, int16_t op2, size_t
    vl);
vint16m4_t vsmul_vv_i16m4 (vint16m4_t op1, vint16m4_t op2,
    size_t vl);
vint16m4_t vsmul_vx_i16m4 (vint16m4_t op1, int16_t op2, size_t
    vl);
vint16m8_t vsmul_vv_i16m8 (vint16m8_t op1, vint16m8_t op2,
    size_t vl);
vint16m8_t vsmul_vx_i16m8 (vint16m8_t op1, int16_t op2, size_t
    vl);
vint32m1_t vsmul_vv_i32m1 (vint32m1_t op1, vint32m1_t op2,
    size_t vl);
vint32m1_t vsmul_vx_i32m1 (vint32m1_t op1, int32_t op2, size_t
    vl);
vint32m2_t vsmul_vv_i32m2 (vint32m2_t op1, vint32m2_t op2,
    size_t vl);
vint32m2_t vsmul_vx_i32m2 (vint32m2_t op1, int32_t op2, size_t
    vl);
vint32m4_t vsmul_vv_i32m4 (vint32m4_t op1, vint32m4_t op2,
    size_t vl);
vint32m4_t vsmul_vx_i32m4 (vint32m4_t op1, int32_t op2, size_t
    vl);
vint32m8_t vsmul_vv_i32m8 (vint32m8_t op1, vint32m8_t op2,
    size_t vl);
```

```

vint32m8_t vsmul_vx_i32m8 (vint32m8_t op1, int32_t op2, size_t
    vl);
vint64m1_t vsmul_vv_i64m1 (vint64m1_t op1, vint64m1_t op2,
    size_t vl);
vint64m1_t vsmul_vx_i64m1 (vint64m1_t op1, int64_t op2, size_t
    vl);
vint64m2_t vsmul_vv_i64m2 (vint64m2_t op1, vint64m2_t op2,
    size_t vl);
vint64m2_t vsmul_vx_i64m2 (vint64m2_t op1, int64_t op2, size_t
    vl);
vint64m4_t vsmul_vv_i64m4 (vint64m4_t op1, vint64m4_t op2,
    size_t vl);
vint64m4_t vsmul_vx_i64m4 (vint64m4_t op1, int64_t op2, size_t
    vl);
vint64m8_t vsmul_vv_i64m8 (vint64m8_t op1, vint64m8_t op2,
    size_t vl);
vint64m8_t vsmul_vx_i64m8 (vint64m8_t op1, int64_t op2, size_t
    vl);
// masked functions
vint8m1_t vsmul_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vint8m1_t op2, size_t vl);
vint8m1_t vsmul_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, int8_t op2, size_t vl);
vint8m2_t vsmul_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vint8m2_t op2, size_t vl);
vint8m2_t vsmul_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, int8_t op2, size_t vl);
vint8m4_t vsmul_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vint8m4_t op2, size_t vl);
vint8m4_t vsmul_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, int8_t op2, size_t vl);
vint8m8_t vsmul_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vint8m8_t op2, size_t vl);
vint8m8_t vsmul_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, int8_t op2, size_t vl);
vint16m1_t vsmul_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vint16m1_t op2, size_t vl);
vint16m1_t vsmul_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, int16_t op2, size_t vl);
vint16m2_t vsmul_vv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, vint16m2_t op2, size_t vl);
vint16m2_t vsmul_vx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, int16_t op2, size_t vl);
vint16m4_t vsmul_vv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, vint16m4_t op2, size_t vl);

```

```

vint16m4_t vsmul_vx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, int16_t op2, size_t vl);
vint16m8_t vsmul_vv_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, vint16m8_t op2, size_t vl);
vint16m8_t vsmul_vx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, int16_t op2, size_t vl);
vint32m1_t vsmul_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vint32m1_t op2, size_t vl);
vint32m1_t vsmul_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, int32_t op2, size_t vl);
vint32m2_t vsmul_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vint32m2_t op2, size_t vl);
vint32m2_t vsmul_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, int32_t op2, size_t vl);
vint32m4_t vsmul_vv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, vint32m4_t op2, size_t vl);
vint32m4_t vsmul_vx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, int32_t op2, size_t vl);
vint32m8_t vsmul_vv_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, vint32m8_t op2, size_t vl);
vint32m8_t vsmul_vx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, int32_t op2, size_t vl);
vint64m1_t vsmul_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vint64m1_t op2, size_t vl);
vint64m1_t vsmul_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, int64_t op2, size_t vl);
vint64m2_t vsmul_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vint64m2_t op2, size_t vl);
vint64m2_t vsmul_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, int64_t op2, size_t vl);
vint64m4_t vsmul_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vint64m4_t op2, size_t vl);
vint64m4_t vsmul_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, int64_t op2, size_t vl);
vint64m8_t vsmul_vv_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, vint64m8_t op2, size_t vl);
vint64m8_t vsmul_vx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, int64_t op2, size_t vl);

```

### Vector Single-Width Scaling Shift Functions:

#### Prototypes:

```

vuint8m1_t vssrl_vv_u8m1 (vuint8m1_t op1, vuint8m1_t shift,
    size_t vl);

```

```

vuint8m1_t vssrl_vx_u8m1 (vuint8m1_t op1, size_t shift, size_t
    vl);
vuint8m2_t vssrl_vv_u8m2 (vuint8m2_t op1, vuint8m2_t shift,
    size_t vl);
vuint8m2_t vssrl_vx_u8m2 (vuint8m2_t op1, size_t shift, size_t
    vl);
vuint8m4_t vssrl_vv_u8m4 (vuint8m4_t op1, vuint8m4_t shift,
    size_t vl);
vuint8m4_t vssrl_vx_u8m4 (vuint8m4_t op1, size_t shift, size_t
    vl);
vuint8m8_t vssrl_vv_u8m8 (vuint8m8_t op1, vuint8m8_t shift,
    size_t vl);
vuint8m8_t vssrl_vx_u8m8 (vuint8m8_t op1, size_t shift, size_t
    vl);
vuint16m1_t vssrl_vv_u16m1 (vuint16m1_t op1, vuint16m1_t shift,
    size_t vl);
vuint16m1_t vssrl_vx_u16m1 (vuint16m1_t op1, size_t shift,
    size_t vl);
vuint16m2_t vssrl_vv_u16m2 (vuint16m2_t op1, vuint16m2_t shift,
    size_t vl);
vuint16m2_t vssrl_vx_u16m2 (vuint16m2_t op1, size_t shift,
    size_t vl);
vuint16m4_t vssrl_vv_u16m4 (vuint16m4_t op1, vuint16m4_t shift,
    size_t vl);
vuint16m4_t vssrl_vx_u16m4 (vuint16m4_t op1, size_t shift,
    size_t vl);
vuint16m8_t vssrl_vv_u16m8 (vuint16m8_t op1, vuint16m8_t shift,
    size_t vl);
vuint16m8_t vssrl_vx_u16m8 (vuint16m8_t op1, size_t shift,
    size_t vl);
vuint32m1_t vssrl_vv_u32m1 (vuint32m1_t op1, vuint32m1_t shift,
    size_t vl);
vuint32m1_t vssrl_vx_u32m1 (vuint32m1_t op1, size_t shift,
    size_t vl);
vuint32m2_t vssrl_vv_u32m2 (vuint32m2_t op1, vuint32m2_t shift,
    size_t vl);
vuint32m2_t vssrl_vx_u32m2 (vuint32m2_t op1, size_t shift,
    size_t vl);
vuint32m4_t vssrl_vv_u32m4 (vuint32m4_t op1, vuint32m4_t shift,
    size_t vl);
vuint32m4_t vssrl_vx_u32m4 (vuint32m4_t op1, size_t shift,
    size_t vl);
vuint32m8_t vssrl_vv_u32m8 (vuint32m8_t op1, vuint32m8_t shift,
    size_t vl);
vuint32m8_t vssrl_vx_u32m8 (vuint32m8_t op1, size_t shift,
    size_t vl);

```

```

vuint64m1_t vssrl_vv_u64m1 (vuint64m1_t op1, vuint64m1_t shift,
    size_t vl);
vuint64m1_t vssrl_vx_u64m1 (vuint64m1_t op1, size_t shift,
    size_t vl);
vuint64m2_t vssrl_vv_u64m2 (vuint64m2_t op1, vuint64m2_t shift,
    size_t vl);
vuint64m2_t vssrl_vx_u64m2 (vuint64m2_t op1, size_t shift,
    size_t vl);
vuint64m4_t vssrl_vv_u64m4 (vuint64m4_t op1, vuint64m4_t shift,
    size_t vl);
vuint64m4_t vssrl_vx_u64m4 (vuint64m4_t op1, size_t shift,
    size_t vl);
vuint64m8_t vssrl_vv_u64m8 (vuint64m8_t op1, vuint64m8_t shift,
    size_t vl);
vuint64m8_t vssrl_vx_u64m8 (vuint64m8_t op1, size_t shift,
    size_t vl);
vint8m1_t vssra_vv_i8m1 (vint8m1_t op1, vuint8m1_t shift, size_t
    vl);
vint8m1_t vssra_vx_i8m1 (vint8m1_t op1, size_t shift, size_t vl);
vint8m2_t vssra_vv_i8m2 (vint8m2_t op1, vuint8m2_t shift, size_t
    vl);
vint8m2_t vssra_vx_i8m2 (vint8m2_t op1, size_t shift, size_t vl);
vint8m4_t vssra_vv_i8m4 (vint8m4_t op1, vuint8m4_t shift, size_t
    vl);
vint8m4_t vssra_vx_i8m4 (vint8m4_t op1, size_t shift, size_t vl);
vint8m8_t vssra_vv_i8m8 (vint8m8_t op1, vuint8m8_t shift, size_t
    vl);
vint8m8_t vssra_vx_i8m8 (vint8m8_t op1, size_t shift, size_t vl);
vint16m1_t vssra_vv_i16m1 (vint16m1_t op1, vuint16m1_t shift,
    size_t vl);
vint16m1_t vssra_vx_i16m1 (vint16m1_t op1, size_t shift, size_t
    vl);
vint16m2_t vssra_vv_i16m2 (vint16m2_t op1, vuint16m2_t shift,
    size_t vl);
vint16m2_t vssra_vx_i16m2 (vint16m2_t op1, size_t shift, size_t
    vl);
vint16m4_t vssra_vv_i16m4 (vint16m4_t op1, vuint16m4_t shift,
    size_t vl);
vint16m4_t vssra_vx_i16m4 (vint16m4_t op1, size_t shift, size_t
    vl);
vint16m8_t vssra_vv_i16m8 (vint16m8_t op1, vuint16m8_t shift,
    size_t vl);
vint16m8_t vssra_vx_i16m8 (vint16m8_t op1, size_t shift, size_t
    vl);
vint32m1_t vssra_vv_i32m1 (vint32m1_t op1, vuint32m1_t shift,
    size_t vl);

```

```

vint32m1_t vssra_vx_i32m1 (vint32m1_t op1, size_t shift, size_t
    vl);
vint32m2_t vssra_vv_i32m2 (vint32m2_t op1, vuint32m2_t shift,
    size_t vl);
vint32m2_t vssra_vx_i32m2 (vint32m2_t op1, size_t shift, size_t
    vl);
vint32m4_t vssra_vv_i32m4 (vint32m4_t op1, vuint32m4_t shift,
    size_t vl);
vint32m4_t vssra_vx_i32m4 (vint32m4_t op1, size_t shift, size_t
    vl);
vint32m8_t vssra_vv_i32m8 (vint32m8_t op1, vuint32m8_t shift,
    size_t vl);
vint32m8_t vssra_vx_i32m8 (vint32m8_t op1, size_t shift, size_t
    vl);
vint64m1_t vssra_vv_i64m1 (vint64m1_t op1, vuint64m1_t shift,
    size_t vl);
vint64m1_t vssra_vx_i64m1 (vint64m1_t op1, size_t shift, size_t
    vl);
vint64m2_t vssra_vv_i64m2 (vint64m2_t op1, vuint64m2_t shift,
    size_t vl);
vint64m2_t vssra_vx_i64m2 (vint64m2_t op1, size_t shift, size_t
    vl);
vint64m4_t vssra_vv_i64m4 (vint64m4_t op1, vuint64m4_t shift,
    size_t vl);
vint64m4_t vssra_vx_i64m4 (vint64m4_t op1, size_t shift, size_t
    vl);
vint64m8_t vssra_vv_i64m8 (vint64m8_t op1, vuint64m8_t shift,
    size_t vl);
vint64m8_t vssra_vx_i64m8 (vint64m8_t op1, size_t shift, size_t
    vl);
// masked functions
vuint8m1_t vssrl_vv_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, vuint8m1_t shift, size_t vl);
vuint8m1_t vssrl_vx_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vuint8m1_t op1, size_t shift, size_t vl);
vuint8m2_t vssrl_vv_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, vuint8m2_t shift, size_t vl);
vuint8m2_t vssrl_vx_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vuint8m2_t op1, size_t shift, size_t vl);
vuint8m4_t vssrl_vv_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, vuint8m4_t shift, size_t vl);
vuint8m4_t vssrl_vx_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vuint8m4_t op1, size_t shift, size_t vl);
vuint8m8_t vssrl_vv_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, vuint8m8_t shift, size_t vl);

```



```

vuint8m8_t vssrl_vx_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vuint8m8_t op1, size_t shift, size_t vl);
vuint16m1_t vssrl_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t shift, size_t vl);
vuint16m1_t vssrl_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, size_t shift, size_t vl);
vuint16m2_t vssrl_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t shift, size_t vl);
vuint16m2_t vssrl_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, size_t shift, size_t vl);
vuint16m4_t vssrl_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t shift, size_t vl);
vuint16m4_t vssrl_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, size_t shift, size_t vl);
vuint16m8_t vssrl_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t shift, size_t vl);
vuint16m8_t vssrl_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, size_t shift, size_t vl);
vuint32m1_t vssrl_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t shift, size_t vl);
vuint32m1_t vssrl_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, size_t shift, size_t vl);
vuint32m2_t vssrl_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t shift, size_t vl);
vuint32m2_t vssrl_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, size_t shift, size_t vl);
vuint32m4_t vssrl_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t shift, size_t vl);
vuint32m4_t vssrl_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, size_t shift, size_t vl);
vuint32m8_t vssrl_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t shift, size_t vl);
vuint32m8_t vssrl_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, size_t shift, size_t vl);
vuint64m1_t vssrl_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t shift, size_t vl);
vuint64m1_t vssrl_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, size_t shift, size_t vl);
vuint64m2_t vssrl_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t shift, size_t vl);
vuint64m2_t vssrl_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, size_t shift, size_t vl);
vuint64m4_t vssrl_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t shift, size_t vl);
vuint64m4_t vssrl_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, size_t shift, size_t vl);

```

```

vuint64m8_t vssrl_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t shift, size_t vl);
vuint64m8_t vssrl_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, size_t shift, size_t vl);
vint8m1_t vssra_vv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, vuint8m1_t shift, size_t vl);
vint8m1_t vssra_vx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint8m1_t op1, size_t shift, size_t vl);
vint8m2_t vssra_vv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, vuint8m2_t shift, size_t vl);
vint8m2_t vssra_vx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint8m2_t op1, size_t shift, size_t vl);
vint8m4_t vssra_vv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, vuint8m4_t shift, size_t vl);
vint8m4_t vssra_vx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint8m4_t op1, size_t shift, size_t vl);
vint8m8_t vssra_vv_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, vuint8m8_t shift, size_t vl);
vint8m8_t vssra_vx_i8m8_m (vbool1_t mask, vint8m8_t maskedoff,
    vint8m8_t op1, size_t shift, size_t vl);
vint16m1_t vssra_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vuint16m1_t shift, size_t vl);
vint16m1_t vssra_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, size_t shift, size_t vl);
vint16m2_t vssra_vv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, vuint16m2_t shift, size_t vl);
vint16m2_t vssra_vx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, size_t shift, size_t vl);
vint16m4_t vssra_vv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, vuint16m4_t shift, size_t vl);
vint16m4_t vssra_vx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, size_t shift, size_t vl);
vint16m8_t vssra_vv_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, vuint16m8_t shift, size_t vl);
vint16m8_t vssra_vx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, size_t shift, size_t vl);
vint32m1_t vssra_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vuint32m1_t shift, size_t vl);
vint32m1_t vssra_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, size_t shift, size_t vl);
vint32m2_t vssra_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vuint32m2_t shift, size_t vl);
vint32m2_t vssra_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, size_t shift, size_t vl);
vint32m4_t vssra_vv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, vuint32m4_t shift, size_t vl);

```

```

vint32m4_t vssra_vx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, size_t shift, size_t vl);
vint32m8_t vssra_vv_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, vuint32m8_t shift, size_t vl);
vint32m8_t vssra_vx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, size_t shift, size_t vl);
vint64m1_t vssra_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vuint64m1_t shift, size_t vl);
vint64m1_t vssra_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, size_t shift, size_t vl);
vint64m2_t vssra_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vuint64m2_t shift, size_t vl);
vint64m2_t vssra_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, size_t shift, size_t vl);
vint64m4_t vssra_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vuint64m4_t shift, size_t vl);
vint64m4_t vssra_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, size_t shift, size_t vl);
vint64m8_t vssra_vv_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, vuint64m8_t shift, size_t vl);
vint64m8_t vssra_vx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, size_t shift, size_t vl);

```

## Vector Narrowing Fixed-Point Clip Functions:

### Prototypes:

```

vint8m1_t vnclip_wv_i8m1 (vint16m2_t op1, vuint8m1_t shift,
    size_t vl);
vint8m1_t vnclip_wx_i8m1 (vint16m2_t op1, size_t shift, size_t
    vl);
vint8m2_t vnclip_wv_i8m2 (vint16m4_t op1, vuint8m2_t shift,
    size_t vl);
vint8m2_t vnclip_wx_i8m2 (vint16m4_t op1, size_t shift, size_t
    vl);
vint8m4_t vnclip_wv_i8m4 (vint16m8_t op1, vuint8m4_t shift,
    size_t vl);
vint8m4_t vnclip_wx_i8m4 (vint16m8_t op1, size_t shift, size_t
    vl);
vint16m1_t vnclip_wv_i16m1 (vint32m2_t op1, vuint16m1_t shift,
    size_t vl);
vint16m1_t vnclip_wx_i16m1 (vint32m2_t op1, size_t shift, size_t
    vl);
vint16m2_t vnclip_wv_i16m2 (vint32m4_t op1, vuint16m2_t shift,
    size_t vl);

```

```

vint16m2_t vnclip_wx_i16m2 (vint32m4_t op1, size_t shift, size_t
    vl);
vint16m4_t vnclip_wv_i16m4 (vint32m8_t op1, vuint16m4_t shift,
    size_t vl);
vint16m4_t vnclip_wx_i16m4 (vint32m8_t op1, size_t shift, size_t
    vl);
vint32m1_t vnclip_wv_i32m1 (vint64m2_t op1, vuint32m1_t shift,
    size_t vl);
vint32m1_t vnclip_wx_i32m1 (vint64m2_t op1, size_t shift, size_t
    vl);
vint32m2_t vnclip_wv_i32m2 (vint64m4_t op1, vuint32m2_t shift,
    size_t vl);
vint32m2_t vnclip_wx_i32m2 (vint64m4_t op1, size_t shift, size_t
    vl);
vint32m4_t vnclip_wv_i32m4 (vint64m8_t op1, vuint32m4_t shift,
    size_t vl);
vint32m4_t vnclip_wx_i32m4 (vint64m8_t op1, size_t shift, size_t
    vl);
vuint8m1_t vnclipu_wv_u8m1 (vuint16m2_t op1, vuint8m1_t shift,
    size_t vl);
vuint8m1_t vnclipu_wx_u8m1 (vuint16m2_t op1, size_t shift,
    size_t vl);
vuint8m2_t vnclipu_wv_u8m2 (vuint16m4_t op1, vuint8m2_t shift,
    size_t vl);
vuint8m2_t vnclipu_wx_u8m2 (vuint16m4_t op1, size_t shift,
    size_t vl);
vuint8m4_t vnclipu_wv_u8m4 (vuint16m8_t op1, vuint8m4_t shift,
    size_t vl);
vuint8m4_t vnclipu_wx_u8m4 (vuint16m8_t op1, size_t shift,
    size_t vl);
vuint16m1_t vnclipu_wv_u16m1 (vuint32m2_t op1, vuint16m1_t
    shift, size_t vl);
vuint16m1_t vnclipu_wx_u16m1 (vuint32m2_t op1, size_t shift,
    size_t vl);
vuint16m2_t vnclipu_wv_u16m2 (vuint32m4_t op1, vuint16m2_t
    shift, size_t vl);
vuint16m2_t vnclipu_wx_u16m2 (vuint32m4_t op1, size_t shift,
    size_t vl);
vuint16m4_t vnclipu_wv_u16m4 (vuint32m8_t op1, vuint16m4_t
    shift, size_t vl);
vuint16m4_t vnclipu_wx_u16m4 (vuint32m8_t op1, size_t shift,
    size_t vl);
vuint32m1_t vnclipu_wv_u32m1 (vuint64m2_t op1, vuint32m1_t
    shift, size_t vl);
vuint32m1_t vnclipu_wx_u32m1 (vuint64m2_t op1, size_t shift,
    size_t vl);

```

```

vuint32m2_t vnclipu_wv_u32m2 (vuint64m4_t op1, vuint32m2_t
    shift, size_t vl);
vuint32m2_t vnclipu_wx_u32m2 (vuint64m4_t op1, size_t shift,
    size_t vl);
vuint32m4_t vnclipu_wv_u32m4 (vuint64m8_t op1, vuint32m4_t
    shift, size_t vl);
vuint32m4_t vnclipu_wx_u32m4 (vuint64m8_t op1, size_t shift,
    size_t vl);
// masked functions
vint8m1_t vnclip_wv_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint16m2_t op1, vuint8m1_t shift, size_t vl);
vint8m1_t vnclip_wx_i8m1_m (vbool8_t mask, vint8m1_t maskedoff,
    vint16m2_t op1, size_t shift, size_t vl);
vint8m2_t vnclip_wv_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint16m4_t op1, vuint8m2_t shift, size_t vl);
vint8m2_t vnclip_wx_i8m2_m (vbool4_t mask, vint8m2_t maskedoff,
    vint16m4_t op1, size_t shift, size_t vl);
vint8m4_t vnclip_wv_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint16m8_t op1, vuint8m4_t shift, size_t vl);
vint8m4_t vnclip_wx_i8m4_m (vbool2_t mask, vint8m4_t maskedoff,
    vint16m8_t op1, size_t shift, size_t vl);
vint16m1_t vnclip_wv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint32m2_t op1, vuint16m1_t shift, size_t vl);
vint16m1_t vnclip_wx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint32m2_t op1, size_t shift, size_t vl);
vint16m2_t vnclip_wv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint32m4_t op1, vuint16m2_t shift, size_t vl);
vint16m2_t vnclip_wx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint32m4_t op1, size_t shift, size_t vl);
vint16m4_t vnclip_wv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint32m8_t op1, vuint16m4_t shift, size_t vl);
vint16m4_t vnclip_wx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint32m8_t op1, size_t shift, size_t vl);
vint32m1_t vnclip_wv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint64m2_t op1, vuint32m1_t shift, size_t vl);
vint32m1_t vnclip_wx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint64m2_t op1, size_t shift, size_t vl);
vint32m2_t vnclip_wv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint64m4_t op1, vuint32m2_t shift, size_t vl);
vint32m2_t vnclip_wx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint64m4_t op1, size_t shift, size_t vl);
vint32m4_t vnclip_wv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint64m8_t op1, vuint32m4_t shift, size_t vl);
vint32m4_t vnclip_wx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint64m8_t op1, size_t shift, size_t vl);

```

```

vuint8m1_t vnclipu_wv_u8m1_m (vbool8_t mask, vuint8m1_t
    maskedoff, vuint16m2_t op1, vuint8m1_t shift, size_t vl);
vuint8m1_t vnclipu_wx_u8m1_m (vbool8_t mask, vuint8m1_t
    maskedoff, vuint16m2_t op1, size_t shift, size_t vl);
vuint8m2_t vnclipu_wv_u8m2_m (vbool4_t mask, vuint8m2_t
    maskedoff, vuint16m4_t op1, vuint8m2_t shift, size_t vl);
vuint8m2_t vnclipu_wx_u8m2_m (vbool4_t mask, vuint8m2_t
    maskedoff, vuint16m4_t op1, size_t shift, size_t vl);
vuint8m4_t vnclipu_wv_u8m4_m (vbool2_t mask, vuint8m4_t
    maskedoff, vuint16m8_t op1, vuint8m4_t shift, size_t vl);
vuint8m4_t vnclipu_wx_u8m4_m (vbool2_t mask, vuint8m4_t
    maskedoff, vuint16m8_t op1, size_t shift, size_t vl);
vuint16m1_t vnclipu_wv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint32m2_t op1, vuint16m1_t shift, size_t vl);
vuint16m1_t vnclipu_wx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint32m2_t op1, size_t shift, size_t vl);
vuint16m2_t vnclipu_wv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint32m4_t op1, vuint16m2_t shift, size_t vl);
vuint16m2_t vnclipu_wx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint32m4_t op1, size_t shift, size_t vl);
vuint16m4_t vnclipu_wv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint32m8_t op1, vuint16m4_t shift, size_t vl);
vuint16m4_t vnclipu_wx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint32m8_t op1, size_t shift, size_t vl);
vuint32m1_t vnclipu_wv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint64m2_t op1, vuint32m1_t shift, size_t vl);
vuint32m1_t vnclipu_wx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint64m2_t op1, size_t shift, size_t vl);
vuint32m2_t vnclipu_wv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint64m4_t op1, vuint32m2_t shift, size_t vl);
vuint32m2_t vnclipu_wx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint64m4_t op1, size_t shift, size_t vl);
vuint32m4_t vnclipu_wv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint64m8_t op1, vuint32m4_t shift, size_t vl);
vuint32m4_t vnclipu_wx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint64m8_t op1, size_t shift, size_t vl);

```

## Vector Floating-Point Functions:

### Vector Single-Width Floating-Point Add/Subtract Functions:

#### Prototypes:

```

vfloat16m1_t vfadd_vv_f16m1 (vfloat16m1_t op1, vfloat16m1_t op2,
    size_t vl);

```

```

vfloat16m1_t vfadd_vf_f16m1 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vfloat16m2_t vfadd_vv_f16m2 (vfloat16m2_t op1, vfloat16m2_t op2,
    size_t vl);
vfloat16m2_t vfadd_vf_f16m2 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vfloat16m4_t vfadd_vv_f16m4 (vfloat16m4_t op1, vfloat16m4_t op2,
    size_t vl);
vfloat16m4_t vfadd_vf_f16m4 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vfloat16m8_t vfadd_vv_f16m8 (vfloat16m8_t op1, vfloat16m8_t op2,
    size_t vl);
vfloat16m8_t vfadd_vf_f16m8 (vfloat16m8_t op1, float16_t op2,
    size_t vl);
vfloat32m1_t vfadd_vv_f32m1 (vfloat32m1_t op1, vfloat32m1_t op2,
    size_t vl);
vfloat32m1_t vfadd_vf_f32m1 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vfloat32m2_t vfadd_vv_f32m2 (vfloat32m2_t op1, vfloat32m2_t op2,
    size_t vl);
vfloat32m2_t vfadd_vf_f32m2 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vfloat32m4_t vfadd_vv_f32m4 (vfloat32m4_t op1, vfloat32m4_t op2,
    size_t vl);
vfloat32m4_t vfadd_vf_f32m4 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vfloat32m8_t vfadd_vv_f32m8 (vfloat32m8_t op1, vfloat32m8_t op2,
    size_t vl);
vfloat32m8_t vfadd_vf_f32m8 (vfloat32m8_t op1, float32_t op2,
    size_t vl);
vfloat64m1_t vfadd_vv_f64m1 (vfloat64m1_t op1, vfloat64m1_t op2,
    size_t vl);
vfloat64m1_t vfadd_vf_f64m1 (vfloat64m1_t op1, float64_t op2,
    size_t vl);
vfloat64m2_t vfadd_vv_f64m2 (vfloat64m2_t op1, vfloat64m2_t op2,
    size_t vl);
vfloat64m2_t vfadd_vf_f64m2 (vfloat64m2_t op1, float64_t op2,
    size_t vl);
vfloat64m4_t vfadd_vv_f64m4 (vfloat64m4_t op1, vfloat64m4_t op2,
    size_t vl);
vfloat64m4_t vfadd_vf_f64m4 (vfloat64m4_t op1, float64_t op2,
    size_t vl);
vfloat64m8_t vfadd_vv_f64m8 (vfloat64m8_t op1, vfloat64m8_t op2,
    size_t vl);
vfloat64m8_t vfadd_vf_f64m8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);

```

```

vfloat16m1_t vsub_vv_f16m1 (vfloat16m1_t op1, vfloat16m1_t op2,
    size_t vl);
vfloat16m1_t vsub_vf_f16m1 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vfloat16m2_t vsub_vv_f16m2 (vfloat16m2_t op1, vfloat16m2_t op2,
    size_t vl);
vfloat16m2_t vsub_vf_f16m2 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vfloat16m4_t vsub_vv_f16m4 (vfloat16m4_t op1, vfloat16m4_t op2,
    size_t vl);
vfloat16m4_t vsub_vf_f16m4 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vfloat16m8_t vsub_vv_f16m8 (vfloat16m8_t op1, vfloat16m8_t op2,
    size_t vl);
vfloat16m8_t vsub_vf_f16m8 (vfloat16m8_t op1, float16_t op2,
    size_t vl);
vfloat32m1_t vsub_vv_f32m1 (vfloat32m1_t op1, vfloat32m1_t op2,
    size_t vl);
vfloat32m1_t vsub_vf_f32m1 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vfloat32m2_t vsub_vv_f32m2 (vfloat32m2_t op1, vfloat32m2_t op2,
    size_t vl);
vfloat32m2_t vsub_vf_f32m2 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vfloat32m4_t vsub_vv_f32m4 (vfloat32m4_t op1, vfloat32m4_t op2,
    size_t vl);
vfloat32m4_t vsub_vf_f32m4 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vfloat32m8_t vsub_vv_f32m8 (vfloat32m8_t op1, vfloat32m8_t op2,
    size_t vl);
vfloat32m8_t vsub_vf_f32m8 (vfloat32m8_t op1, float32_t op2,
    size_t vl);
vfloat64m1_t vsub_vv_f64m1 (vfloat64m1_t op1, vfloat64m1_t op2,
    size_t vl);
vfloat64m1_t vsub_vf_f64m1 (vfloat64m1_t op1, float64_t op2,
    size_t vl);
vfloat64m2_t vsub_vv_f64m2 (vfloat64m2_t op1, vfloat64m2_t op2,
    size_t vl);
vfloat64m2_t vsub_vf_f64m2 (vfloat64m2_t op1, float64_t op2,
    size_t vl);
vfloat64m4_t vsub_vv_f64m4 (vfloat64m4_t op1, vfloat64m4_t op2,
    size_t vl);
vfloat64m4_t vsub_vf_f64m4 (vfloat64m4_t op1, float64_t op2,
    size_t vl);
vfloat64m8_t vsub_vv_f64m8 (vfloat64m8_t op1, vfloat64m8_t op2,
    size_t vl);

```



```

vfloat64m8_t vfrsub_vf_f64m8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);
vfloat16m1_t vfrsub_vf_f16m1 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vfloat16m2_t vfrsub_vf_f16m2 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vfloat16m4_t vfrsub_vf_f16m4 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vfloat16m8_t vfrsub_vf_f16m8 (vfloat16m8_t op1, float16_t op2,
    size_t vl);
vfloat32m1_t vfrsub_vf_f32m1 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vfloat32m2_t vfrsub_vf_f32m2 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vfloat32m4_t vfrsub_vf_f32m4 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vfloat32m8_t vfrsub_vf_f32m8 (vfloat32m8_t op1, float32_t op2,
    size_t vl);
vfloat64m1_t vfrsub_vf_f64m1 (vfloat64m1_t op1, float64_t op2,
    size_t vl);
vfloat64m2_t vfrsub_vf_f64m2 (vfloat64m2_t op1, float64_t op2,
    size_t vl);
vfloat64m4_t vfrsub_vf_f64m4 (vfloat64m4_t op1, float64_t op2,
    size_t vl);
vfloat64m8_t vfrsub_vf_f64m8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);
vfloat16m1_t vfneg_v_f16m1 (vfloat16m1_t op1, size_t vl);
vfloat16m2_t vfneg_v_f16m2 (vfloat16m2_t op1, size_t vl);
vfloat16m4_t vfneg_v_f16m4 (vfloat16m4_t op1, size_t vl);
vfloat16m8_t vfneg_v_f16m8 (vfloat16m8_t op1, size_t vl);
vfloat32m1_t vfneg_v_f32m1 (vfloat32m1_t op1, size_t vl);
vfloat32m2_t vfneg_v_f32m2 (vfloat32m2_t op1, size_t vl);
vfloat32m4_t vfneg_v_f32m4 (vfloat32m4_t op1, size_t vl);
vfloat32m8_t vfneg_v_f32m8 (vfloat32m8_t op1, size_t vl);
vfloat64m1_t vfneg_v_f64m1 (vfloat64m1_t op1, size_t vl);
vfloat64m2_t vfneg_v_f64m2 (vfloat64m2_t op1, size_t vl);
vfloat64m4_t vfneg_v_f64m4 (vfloat64m4_t op1, size_t vl);
vfloat64m8_t vfneg_v_f64m8 (vfloat64m8_t op1, size_t vl);
// masked functions
vfloat16m1_t vfadd_vv_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vfloat16m1_t vfadd_vf_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vfloat16m2_t vfadd_vv_f16m2_m (vbool16_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);

```

```

vfloat16m2_t vfadd_vf_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, float16_t op2, size_t vl);
vfloat16m4_t vfadd_vv_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vfloat16m4_t vfadd_vf_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, float16_t op2, size_t vl);
vfloat16m8_t vfadd_vv_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, vfloat16m8_t op2, size_t vl);
vfloat16m8_t vfadd_vf_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, float16_t op2, size_t vl);
vfloat32m1_t vfadd_vv_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vfloat32m1_t vfadd_vf_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vfloat32m2_t vfadd_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vfloat32m2_t vfadd_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vfloat32m4_t vfadd_vv_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vfloat32m4_t vfadd_vf_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, float32_t op2, size_t vl);
vfloat32m8_t vfadd_vv_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, vfloat32m8_t op2, size_t vl);
vfloat32m8_t vfadd_vf_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, float32_t op2, size_t vl);
vfloat64m1_t vfadd_vv_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, vfloat64m1_t op2, size_t vl);
vfloat64m1_t vfadd_vf_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);
vfloat64m2_t vfadd_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, vfloat64m2_t op2, size_t vl);
vfloat64m2_t vfadd_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);
vfloat64m4_t vfadd_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, vfloat64m4_t op2, size_t vl);
vfloat64m4_t vfadd_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vfloat64m8_t vfadd_vv_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, vfloat64m8_t op2, size_t vl);
vfloat64m8_t vfadd_vf_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, float64_t op2, size_t vl);
vfloat16m1_t vfloat16m1_t vfsub_vv_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vfloat16m1_t vfloat16m1_t vfsub_vf_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);

```

```

vfloat16m2_t vsub_vv_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vfloat16m2_t vsub_vf_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, float16_t op2, size_t vl);
vfloat16m4_t vsub_vv_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vfloat16m4_t vsub_vf_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, float16_t op2, size_t vl);
vfloat16m8_t vsub_vv_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, vfloat16m8_t op2, size_t vl);
vfloat16m8_t vsub_vf_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, float16_t op2, size_t vl);
vfloat32m1_t vsub_vv_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vfloat32m1_t vsub_vf_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vfloat32m2_t vsub_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vfloat32m2_t vsub_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vfloat32m4_t vsub_vv_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vfloat32m4_t vsub_vf_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, float32_t op2, size_t vl);
vfloat32m8_t vsub_vv_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, vfloat32m8_t op2, size_t vl);
vfloat32m8_t vsub_vf_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, float32_t op2, size_t vl);
vfloat64m1_t vsub_vv_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, vfloat64m1_t op2, size_t vl);
vfloat64m1_t vsub_vf_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);
vfloat64m2_t vsub_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, vfloat64m2_t op2, size_t vl);
vfloat64m2_t vsub_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);
vfloat64m4_t vsub_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, vfloat64m4_t op2, size_t vl);
vfloat64m4_t vsub_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vfloat64m8_t vsub_vv_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, vfloat64m8_t op2, size_t vl);
vfloat64m8_t vsub_vf_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, float64_t op2, size_t vl);
vfloat16m1_t vfrsub_vf_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);

```

```

vfloat16m2_t vfrsub_vf_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, float16_t op2, size_t vl);
vfloat16m4_t vfrsub_vf_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, float16_t op2, size_t vl);
vfloat16m8_t vfrsub_vf_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, float16_t op2, size_t vl);
vfloat32m1_t vfrsub_vf_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vfloat32m2_t vfrsub_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vfloat32m4_t vfrsub_vf_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, float32_t op2, size_t vl);
vfloat32m8_t vfrsub_vf_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, float32_t op2, size_t vl);
vfloat64m1_t vfrsub_vf_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);
vfloat64m2_t vfrsub_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);
vfloat64m4_t vfrsub_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vfloat64m8_t vfrsub_vf_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, float64_t op2, size_t vl);
vfloat16m1_t vfneg_v_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, size_t vl);
vfloat16m2_t vfneg_v_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, size_t vl);
vfloat16m4_t vfneg_v_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, size_t vl);
vfloat16m8_t vfneg_v_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, size_t vl);
vfloat32m1_t vfneg_v_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, size_t vl);
vfloat32m2_t vfneg_v_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, size_t vl);
vfloat32m4_t vfneg_v_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, size_t vl);
vfloat32m8_t vfneg_v_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, size_t vl);
vfloat64m1_t vfneg_v_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, size_t vl);
vfloat64m2_t vfneg_v_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, size_t vl);
vfloat64m4_t vfneg_v_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, size_t vl);
vfloat64m8_t vfneg_v_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, size_t vl);

```

## Vector Widening Floating-Point Add/Subtract Functions:

### Prototypes:

```
vfloat32m2_t vfwadd_vv_f32m2 (vfloat16m1_t op1, vfloat16m1_t
    op2, size_t vl);
vfloat32m2_t vfwadd_vf_f32m2 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vfloat32m2_t vfwadd_wv_f32m2 (vfloat32m2_t op1, vfloat16m1_t
    op2, size_t vl);
vfloat32m2_t vfwadd_wf_f32m2 (vfloat32m2_t op1, float16_t op2,
    size_t vl);
vfloat32m4_t vfwadd_vv_f32m4 (vfloat16m2_t op1, vfloat16m2_t
    op2, size_t vl);
vfloat32m4_t vfwadd_vf_f32m4 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vfloat32m4_t vfwadd_wv_f32m4 (vfloat32m4_t op1, vfloat16m2_t
    op2, size_t vl);
vfloat32m4_t vfwadd_wf_f32m4 (vfloat32m4_t op1, float16_t op2,
    size_t vl);
vfloat32m8_t vfwadd_vv_f32m8 (vfloat16m4_t op1, vfloat16m4_t
    op2, size_t vl);
vfloat32m8_t vfwadd_vf_f32m8 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vfloat32m8_t vfwadd_wv_f32m8 (vfloat32m8_t op1, vfloat16m4_t
    op2, size_t vl);
vfloat32m8_t vfwadd_wf_f32m8 (vfloat32m8_t op1, float16_t op2,
    size_t vl);
vfloat64m2_t vfwadd_vv_f64m2 (vfloat32m1_t op1, vfloat32m1_t
    op2, size_t vl);
vfloat64m2_t vfwadd_vf_f64m2 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vfloat64m2_t vfwadd_wv_f64m2 (vfloat64m2_t op1, vfloat32m1_t
    op2, size_t vl);
vfloat64m2_t vfwadd_wf_f64m2 (vfloat64m2_t op1, float32_t op2,
    size_t vl);
vfloat64m4_t vfwadd_vv_f64m4 (vfloat32m2_t op1, vfloat32m2_t
    op2, size_t vl);
vfloat64m4_t vfwadd_vf_f64m4 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vfloat64m4_t vfwadd_wv_f64m4 (vfloat64m4_t op1, vfloat32m2_t
    op2, size_t vl);
vfloat64m4_t vfwadd_wf_f64m4 (vfloat64m4_t op1, float32_t op2,
    size_t vl);
vfloat64m8_t vfwadd_vv_f64m8 (vfloat32m4_t op1, vfloat32m4_t
    op2, size_t vl);
```

```

vfloat64m8_t vfwadd_vf_f64m8 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vfloat64m8_t vfwadd_wv_f64m8 (vfloat64m8_t op1, vfloat32m4_t
    op2, size_t vl);
vfloat64m8_t vfwadd_wf_f64m8 (vfloat64m8_t op1, float32_t op2,
    size_t vl);
vfloat32m2_t vfwsb_vv_f32m2 (vfloat16m1_t op1, vfloat16m1_t
    op2, size_t vl);
vfloat32m2_t vfwsb_vf_f32m2 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vfloat32m2_t vfwsb_wv_f32m2 (vfloat32m2_t op1, vfloat16m1_t
    op2, size_t vl);
vfloat32m2_t vfwsb_wf_f32m2 (vfloat32m2_t op1, float16_t op2,
    size_t vl);
vfloat32m4_t vfwsb_vv_f32m4 (vfloat16m2_t op1, vfloat16m2_t
    op2, size_t vl);
vfloat32m4_t vfwsb_vf_f32m4 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vfloat32m4_t vfwsb_wv_f32m4 (vfloat32m4_t op1, vfloat16m2_t
    op2, size_t vl);
vfloat32m4_t vfwsb_wf_f32m4 (vfloat32m4_t op1, float16_t op2,
    size_t vl);
vfloat32m8_t vfwsb_vv_f32m8 (vfloat16m4_t op1, vfloat16m4_t
    op2, size_t vl);
vfloat32m8_t vfwsb_vf_f32m8 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vfloat32m8_t vfwsb_wv_f32m8 (vfloat32m8_t op1, vfloat16m4_t
    op2, size_t vl);
vfloat32m8_t vfwsb_wf_f32m8 (vfloat32m8_t op1, float16_t op2,
    size_t vl);
vfloat64m2_t vfwsb_vv_f64m2 (vfloat32m1_t op1, vfloat32m1_t
    op2, size_t vl);
vfloat64m2_t vfwsb_vf_f64m2 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vfloat64m2_t vfwsb_wv_f64m2 (vfloat64m2_t op1, vfloat32m1_t
    op2, size_t vl);
vfloat64m2_t vfwsb_wf_f64m2 (vfloat64m2_t op1, float32_t op2,
    size_t vl);
vfloat64m4_t vfwsb_vv_f64m4 (vfloat32m2_t op1, vfloat32m2_t
    op2, size_t vl);
vfloat64m4_t vfwsb_vf_f64m4 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vfloat64m4_t vfwsb_wv_f64m4 (vfloat64m4_t op1, vfloat32m2_t
    op2, size_t vl);
vfloat64m4_t vfwsb_wf_f64m4 (vfloat64m4_t op1, float32_t op2,
    size_t vl);

```

```

vfloat64m8_t vfwsb_vv_f64m8 (vfloat32m4_t op1, vfloat32m4_t
    op2, size_t vl);
vfloat64m8_t vfwsb_vf_f64m8 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vfloat64m8_t vfwsb_wv_f64m8 (vfloat64m8_t op1, vfloat32m4_t
    op2, size_t vl);
vfloat64m8_t vfwsb_wf_f64m8 (vfloat64m8_t op1, float32_t op2,
    size_t vl);
// masked functions
vfloat32m2_t vfwadd_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vfloat32m2_t vfwadd_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vfloat32m2_t vfwadd_wv_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, vfloat16m1_t op2, size_t vl);
vfloat32m2_t vfwadd_wf_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, float16_t op2, size_t vl);
vfloat32m4_t vfwadd_vv_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vfloat32m4_t vfwadd_vf_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat16m2_t op1, float16_t op2, size_t vl);
vfloat32m4_t vfwadd_wv_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, vfloat16m2_t op2, size_t vl);
vfloat32m4_t vfwadd_wf_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, float16_t op2, size_t vl);
vfloat32m8_t vfwadd_vv_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vfloat32m8_t vfwadd_vf_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat16m4_t op1, float16_t op2, size_t vl);
vfloat32m8_t vfwadd_wv_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, vfloat16m4_t op2, size_t vl);
vfloat32m8_t vfwadd_wf_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, float16_t op2, size_t vl);
vfloat64m2_t vfwadd_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vfloat64m2_t vfwadd_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vfloat64m2_t vfwadd_wv_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, vfloat32m1_t op2, size_t vl);
vfloat64m2_t vfwadd_wf_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, float32_t op2, size_t vl);
vfloat64m4_t vfwadd_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vfloat64m4_t vfwadd_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);

```

```

vfloat64m4_t vfwadd_wv_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, vfloat32m2_t op2, size_t vl);
vfloat64m4_t vfwadd_wf_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, float32_t op2, size_t vl);
vfloat64m8_t vfwadd_vv_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vfloat64m8_t vfwadd_vf_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat32m4_t op1, float32_t op2, size_t vl);
vfloat64m8_t vfwadd_wv_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, vfloat32m4_t op2, size_t vl);
vfloat64m8_t vfwadd_wf_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, float32_t op2, size_t vl);
vfloat32m2_t vfwsb_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vfloat32m2_t vfwsb_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vfloat32m2_t vfwsb_wv_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, vfloat16m1_t op2, size_t vl);
vfloat32m2_t vfwsb_wf_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, float16_t op2, size_t vl);
vfloat32m4_t vfwsb_vv_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vfloat32m4_t vfwsb_vf_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat16m2_t op1, float16_t op2, size_t vl);
vfloat32m4_t vfwsb_wv_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, vfloat16m2_t op2, size_t vl);
vfloat32m4_t vfwsb_wf_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, float16_t op2, size_t vl);
vfloat32m8_t vfwsb_vv_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vfloat32m8_t vfwsb_vf_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat16m4_t op1, float16_t op2, size_t vl);
vfloat32m8_t vfwsb_wv_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, vfloat16m4_t op2, size_t vl);
vfloat32m8_t vfwsb_wf_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, float16_t op2, size_t vl);
vfloat64m2_t vfwsb_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vfloat64m2_t vfwsb_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vfloat64m2_t vfwsb_wv_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, vfloat32m1_t op2, size_t vl);
vfloat64m2_t vfwsb_wf_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, float32_t op2, size_t vl);
vfloat64m4_t vfwsb_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);

```



```

vfloat64m4_t vfwsb_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vfloat64m4_t vfwsb_wv_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, vfloat32m2_t op2, size_t vl);
vfloat64m4_t vfwsb_wf_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, float32_t op2, size_t vl);
vfloat64m8_t vfwsb_vv_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vfloat64m8_t vfwsb_vf_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat32m4_t op1, float32_t op2, size_t vl);
vfloat64m8_t vfwsb_wv_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, vfloat32m4_t op2, size_t vl);
vfloat64m8_t vfwsb_wf_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, float32_t op2, size_t vl);

```

## Vector Single-Width Floating-Point Multiply/Divide Functions:

### Prototypes:

```

vfloat16m1_t vfmul_vv_f16m1 (vfloat16m1_t op1, vfloat16m1_t op2,
    size_t vl);
vfloat16m1_t vfmul_vf_f16m1 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vfloat16m2_t vfmul_vv_f16m2 (vfloat16m2_t op1, vfloat16m2_t op2,
    size_t vl);
vfloat16m2_t vfmul_vf_f16m2 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vfloat16m4_t vfmul_vv_f16m4 (vfloat16m4_t op1, vfloat16m4_t op2,
    size_t vl);
vfloat16m4_t vfmul_vf_f16m4 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vfloat16m8_t vfmul_vv_f16m8 (vfloat16m8_t op1, vfloat16m8_t op2,
    size_t vl);
vfloat16m8_t vfmul_vf_f16m8 (vfloat16m8_t op1, float16_t op2,
    size_t vl);
vfloat32m1_t vfmul_vv_f32m1 (vfloat32m1_t op1, vfloat32m1_t op2,
    size_t vl);
vfloat32m1_t vfmul_vf_f32m1 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vfloat32m2_t vfmul_vv_f32m2 (vfloat32m2_t op1, vfloat32m2_t op2,
    size_t vl);
vfloat32m2_t vfmul_vf_f32m2 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vfloat32m4_t vfmul_vv_f32m4 (vfloat32m4_t op1, vfloat32m4_t op2,
    size_t vl);

```

```

vfloat32m4_t vfmul_vf_f32m4 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vfloat32m8_t vfmul_vv_f32m8 (vfloat32m8_t op1, vfloat32m8_t op2,
    size_t vl);
vfloat32m8_t vfmul_vf_f32m8 (vfloat32m8_t op1, float32_t op2,
    size_t vl);
vfloat64m1_t vfmul_vv_f64m1 (vfloat64m1_t op1, vfloat64m1_t op2,
    size_t vl);
vfloat64m1_t vfmul_vf_f64m1 (vfloat64m1_t op1, float64_t op2,
    size_t vl);
vfloat64m2_t vfmul_vv_f64m2 (vfloat64m2_t op1, vfloat64m2_t op2,
    size_t vl);
vfloat64m2_t vfmul_vf_f64m2 (vfloat64m2_t op1, float64_t op2,
    size_t vl);
vfloat64m4_t vfmul_vv_f64m4 (vfloat64m4_t op1, vfloat64m4_t op2,
    size_t vl);
vfloat64m4_t vfmul_vf_f64m4 (vfloat64m4_t op1, float64_t op2,
    size_t vl);
vfloat64m8_t vfmul_vv_f64m8 (vfloat64m8_t op1, vfloat64m8_t op2,
    size_t vl);
vfloat64m8_t vfmul_vf_f64m8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);
vfloat16m1_t vfdiv_vv_f16m1 (vfloat16m1_t op1, vfloat16m1_t op2,
    size_t vl);
vfloat16m1_t vfdiv_vf_f16m1 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vfloat16m2_t vfdiv_vv_f16m2 (vfloat16m2_t op1, vfloat16m2_t op2,
    size_t vl);
vfloat16m2_t vfdiv_vf_f16m2 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vfloat16m4_t vfdiv_vv_f16m4 (vfloat16m4_t op1, vfloat16m4_t op2,
    size_t vl);
vfloat16m4_t vfdiv_vf_f16m4 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vfloat16m8_t vfdiv_vv_f16m8 (vfloat16m8_t op1, vfloat16m8_t op2,
    size_t vl);
vfloat16m8_t vfdiv_vf_f16m8 (vfloat16m8_t op1, float16_t op2,
    size_t vl);
vfloat32m1_t vfdiv_vv_f32m1 (vfloat32m1_t op1, vfloat32m1_t op2,
    size_t vl);
vfloat32m1_t vfdiv_vf_f32m1 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vfloat32m2_t vfdiv_vv_f32m2 (vfloat32m2_t op1, vfloat32m2_t op2,
    size_t vl);
vfloat32m2_t vfdiv_vf_f32m2 (vfloat32m2_t op1, float32_t op2,
    size_t vl);

```

```

vfloat32m4_t vfddiv_vv_f32m4 (vfloat32m4_t op1, vfloat32m4_t op2,
    size_t vl);
vfloat32m4_t vfddiv_vf_f32m4 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vfloat32m8_t vfddiv_vv_f32m8 (vfloat32m8_t op1, vfloat32m8_t op2,
    size_t vl);
vfloat32m8_t vfddiv_vf_f32m8 (vfloat32m8_t op1, float32_t op2,
    size_t vl);
vfloat64m1_t vfddiv_vv_f64m1 (vfloat64m1_t op1, vfloat64m1_t op2,
    size_t vl);
vfloat64m1_t vfddiv_vf_f64m1 (vfloat64m1_t op1, float64_t op2,
    size_t vl);
vfloat64m2_t vfddiv_vv_f64m2 (vfloat64m2_t op1, vfloat64m2_t op2,
    size_t vl);
vfloat64m2_t vfddiv_vf_f64m2 (vfloat64m2_t op1, float64_t op2,
    size_t vl);
vfloat64m4_t vfddiv_vv_f64m4 (vfloat64m4_t op1, vfloat64m4_t op2,
    size_t vl);
vfloat64m4_t vfddiv_vf_f64m4 (vfloat64m4_t op1, float64_t op2,
    size_t vl);
vfloat64m8_t vfddiv_vv_f64m8 (vfloat64m8_t op1, vfloat64m8_t op2,
    size_t vl);
vfloat64m8_t vfddiv_vf_f64m8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);
vfloat16m1_t vfrdiv_vf_f16m1 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vfloat16m2_t vfrdiv_vf_f16m2 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vfloat16m4_t vfrdiv_vf_f16m4 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vfloat16m8_t vfrdiv_vf_f16m8 (vfloat16m8_t op1, float16_t op2,
    size_t vl);
vfloat32m1_t vfrdiv_vf_f32m1 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vfloat32m2_t vfrdiv_vf_f32m2 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vfloat32m4_t vfrdiv_vf_f32m4 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vfloat32m8_t vfrdiv_vf_f32m8 (vfloat32m8_t op1, float32_t op2,
    size_t vl);
vfloat64m1_t vfrdiv_vf_f64m1 (vfloat64m1_t op1, float64_t op2,
    size_t vl);
vfloat64m2_t vfrdiv_vf_f64m2 (vfloat64m2_t op1, float64_t op2,
    size_t vl);
vfloat64m4_t vfrdiv_vf_f64m4 (vfloat64m4_t op1, float64_t op2,
    size_t vl);

```

```

vfloat64m8_t vfrdiv_vf_f64m8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);
// masked functions
vfloat16m1_t vfmul_vv_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vfloat16m1_t vfmul_vf_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vfloat16m2_t vfmul_vv_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vfloat16m2_t vfmul_vf_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, float16_t op2, size_t vl);
vfloat16m4_t vfmul_vv_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vfloat16m4_t vfmul_vf_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, float16_t op2, size_t vl);
vfloat16m8_t vfmul_vv_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, vfloat16m8_t op2, size_t vl);
vfloat16m8_t vfmul_vf_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, float16_t op2, size_t vl);
vfloat32m1_t vfmul_vv_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vfloat32m1_t vfmul_vf_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vfloat32m2_t vfmul_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vfloat32m2_t vfmul_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vfloat32m4_t vfmul_vv_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vfloat32m4_t vfmul_vf_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, float32_t op2, size_t vl);
vfloat32m8_t vfmul_vv_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, vfloat32m8_t op2, size_t vl);
vfloat32m8_t vfmul_vf_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, float32_t op2, size_t vl);
vfloat64m1_t vfmul_vv_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, vfloat64m1_t op2, size_t vl);
vfloat64m1_t vfmul_vf_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);
vfloat64m2_t vfmul_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, vfloat64m2_t op2, size_t vl);
vfloat64m2_t vfmul_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);
vfloat64m4_t vfmul_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, vfloat64m4_t op2, size_t vl);

```

```

vfloat64m4_t vfmul_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vfloat64m8_t vfmul_vv_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, vfloat64m8_t op2, size_t vl);
vfloat64m8_t vfmul_vf_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, float64_t op2, size_t vl);
vfloat16m1_t vfddiv_vv_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vfloat16m1_t vfddiv_vf_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vfloat16m2_t vfddiv_vv_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vfloat16m2_t vfddiv_vf_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, float16_t op2, size_t vl);
vfloat16m4_t vfddiv_vv_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vfloat16m4_t vfddiv_vf_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, float16_t op2, size_t vl);
vfloat16m8_t vfddiv_vv_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, vfloat16m8_t op2, size_t vl);
vfloat16m8_t vfddiv_vf_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, float16_t op2, size_t vl);
vfloat32m1_t vfddiv_vv_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vfloat32m1_t vfddiv_vf_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vfloat32m2_t vfddiv_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vfloat32m2_t vfddiv_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vfloat32m4_t vfddiv_vv_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vfloat32m4_t vfddiv_vf_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, float32_t op2, size_t vl);
vfloat32m8_t vfddiv_vv_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, vfloat32m8_t op2, size_t vl);
vfloat32m8_t vfddiv_vf_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, float32_t op2, size_t vl);
vfloat64m1_t vfddiv_vv_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, vfloat64m1_t op2, size_t vl);
vfloat64m1_t vfddiv_vf_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);
vfloat64m2_t vfddiv_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, vfloat64m2_t op2, size_t vl);
vfloat64m2_t vfddiv_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);

```

```

vfloat64m4_t vfddiv_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, vfloat64m4_t op2, size_t vl);
vfloat64m4_t vfddiv_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vfloat64m8_t vfddiv_vv_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, vfloat64m8_t op2, size_t vl);
vfloat64m8_t vfddiv_vf_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, float64_t op2, size_t vl);
vfloat16m1_t vfrdiv_vf_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vfloat16m2_t vfrdiv_vf_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, float16_t op2, size_t vl);
vfloat16m4_t vfrdiv_vf_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, float16_t op2, size_t vl);
vfloat16m8_t vfrdiv_vf_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, float16_t op2, size_t vl);
vfloat32m1_t vfrdiv_vf_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vfloat32m2_t vfrdiv_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vfloat32m4_t vfrdiv_vf_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, float32_t op2, size_t vl);
vfloat32m8_t vfrdiv_vf_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, float32_t op2, size_t vl);
vfloat64m1_t vfrdiv_vf_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);
vfloat64m2_t vfrdiv_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);
vfloat64m4_t vfrdiv_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vfloat64m8_t vfrdiv_vf_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, float64_t op2, size_t vl);

```

## Vector Widening Floating-Point Multiply Functions:

### Prototypes:

```

vfloat32m2_t vfwmul_vv_f32m2 (vfloat16m1_t op1, vfloat16m1_t
    op2, size_t vl);
vfloat32m2_t vfwmul_vf_f32m2 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vfloat32m4_t vfwmul_vv_f32m4 (vfloat16m2_t op1, vfloat16m2_t
    op2, size_t vl);
vfloat32m4_t vfwmul_vf_f32m4 (vfloat16m2_t op1, float16_t op2,
    size_t vl);

```

```

vfloat32m8_t vfwmul_vv_f32m8 (vfloat16m4_t op1, vfloat16m4_t
    op2, size_t vl);
vfloat32m8_t vfwmul_vf_f32m8 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vfloat64m2_t vfwmul_vv_f64m2 (vfloat32m1_t op1, vfloat32m1_t
    op2, size_t vl);
vfloat64m2_t vfwmul_vf_f64m2 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vfloat64m4_t vfwmul_vv_f64m4 (vfloat32m2_t op1, vfloat32m2_t
    op2, size_t vl);
vfloat64m4_t vfwmul_vf_f64m4 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vfloat64m8_t vfwmul_vv_f64m8 (vfloat32m4_t op1, vfloat32m4_t
    op2, size_t vl);
vfloat64m8_t vfwmul_vf_f64m8 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
// masked functions
vfloat32m2_t vfwmul_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vfloat32m2_t vfwmul_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vfloat32m4_t vfwmul_vv_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vfloat32m4_t vfwmul_vf_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat16m2_t op1, float16_t op2, size_t vl);
vfloat32m8_t vfwmul_vv_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vfloat32m8_t vfwmul_vf_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat16m4_t op1, float16_t op2, size_t vl);
vfloat64m2_t vfwmul_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vfloat64m2_t vfwmul_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vfloat64m4_t vfwmul_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vfloat64m4_t vfwmul_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vfloat64m8_t vfwmul_vv_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vfloat64m8_t vfwmul_vf_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat32m4_t op1, float32_t op2, size_t vl);

```

## Vector Single-Width Floating-Point Fused Multiply-Add Functions:

### Prototypes:

```

vfloat16m1_t vfmacc_vv_f16m1 (vfloat16m1_t vd, vfloat16m1_t vs1,
    vfloat16m1_t vs2, size_t vl);
vfloat16m1_t vfmacc_vf_f16m1 (vfloat16m1_t vd, float16_t rs1,
    vfloat16m1_t vs2, size_t vl);
vfloat16m2_t vfmacc_vv_f16m2 (vfloat16m2_t vd, vfloat16m2_t vs1,
    vfloat16m2_t vs2, size_t vl);
vfloat16m2_t vfmacc_vf_f16m2 (vfloat16m2_t vd, float16_t rs1,
    vfloat16m2_t vs2, size_t vl);
vfloat16m4_t vfmacc_vv_f16m4 (vfloat16m4_t vd, vfloat16m4_t vs1,
    vfloat16m4_t vs2, size_t vl);
vfloat16m4_t vfmacc_vf_f16m4 (vfloat16m4_t vd, float16_t rs1,
    vfloat16m4_t vs2, size_t vl);
vfloat16m8_t vfmacc_vv_f16m8 (vfloat16m8_t vd, vfloat16m8_t vs1,
    vfloat16m8_t vs2, size_t vl);
vfloat16m8_t vfmacc_vf_f16m8 (vfloat16m8_t vd, float16_t rs1,
    vfloat16m8_t vs2, size_t vl);
vfloat32m1_t vfmacc_vv_f32m1 (vfloat32m1_t vd, vfloat32m1_t vs1,
    vfloat32m1_t vs2, size_t vl);
vfloat32m1_t vfmacc_vf_f32m1 (vfloat32m1_t vd, float32_t rs1,
    vfloat32m1_t vs2, size_t vl);
vfloat32m2_t vfmacc_vv_f32m2 (vfloat32m2_t vd, vfloat32m2_t vs1,
    vfloat32m2_t vs2, size_t vl);
vfloat32m2_t vfmacc_vf_f32m2 (vfloat32m2_t vd, float32_t rs1,
    vfloat32m2_t vs2, size_t vl);
vfloat32m4_t vfmacc_vv_f32m4 (vfloat32m4_t vd, vfloat32m4_t vs1,
    vfloat32m4_t vs2, size_t vl);
vfloat32m4_t vfmacc_vf_f32m4 (vfloat32m4_t vd, float32_t rs1,
    vfloat32m4_t vs2, size_t vl);
vfloat32m8_t vfmacc_vv_f32m8 (vfloat32m8_t vd, vfloat32m8_t vs1,
    vfloat32m8_t vs2, size_t vl);
vfloat32m8_t vfmacc_vf_f32m8 (vfloat32m8_t vd, float32_t rs1,
    vfloat32m8_t vs2, size_t vl);
vfloat64m1_t vfmacc_vv_f64m1 (vfloat64m1_t vd, vfloat64m1_t vs1,
    vfloat64m1_t vs2, size_t vl);
vfloat64m1_t vfmacc_vf_f64m1 (vfloat64m1_t vd, float64_t rs1,
    vfloat64m1_t vs2, size_t vl);
vfloat64m2_t vfmacc_vv_f64m2 (vfloat64m2_t vd, vfloat64m2_t vs1,
    vfloat64m2_t vs2, size_t vl);
vfloat64m2_t vfmacc_vf_f64m2 (vfloat64m2_t vd, float64_t rs1,
    vfloat64m2_t vs2, size_t vl);
vfloat64m4_t vfmacc_vv_f64m4 (vfloat64m4_t vd, vfloat64m4_t vs1,
    vfloat64m4_t vs2, size_t vl);
vfloat64m4_t vfmacc_vf_f64m4 (vfloat64m4_t vd, float64_t rs1,
    vfloat64m4_t vs2, size_t vl);
vfloat64m8_t vfmacc_vv_f64m8 (vfloat64m8_t vd, vfloat64m8_t vs1,
    vfloat64m8_t vs2, size_t vl);

```



```

vfloat64m8_t vfmacc_vf_f64m8 (vfloat64m8_t vd, float64_t rs1,
    vfloat64m8_t vs2, size_t vl);
vfloat16m1_t vfnmacc_vv_f16m1 (vfloat16m1_t vd, vfloat16m1_t
    vs1, vfloat16m1_t vs2, size_t vl);
vfloat16m1_t vfnmacc_vf_f16m1 (vfloat16m1_t vd, float16_t rs1,
    vfloat16m1_t vs2, size_t vl);
vfloat16m2_t vfnmacc_vv_f16m2 (vfloat16m2_t vd, vfloat16m2_t
    vs1, vfloat16m2_t vs2, size_t vl);
vfloat16m2_t vfnmacc_vf_f16m2 (vfloat16m2_t vd, float16_t rs1,
    vfloat16m2_t vs2, size_t vl);
vfloat16m4_t vfnmacc_vv_f16m4 (vfloat16m4_t vd, vfloat16m4_t
    vs1, vfloat16m4_t vs2, size_t vl);
vfloat16m4_t vfnmacc_vf_f16m4 (vfloat16m4_t vd, float16_t rs1,
    vfloat16m4_t vs2, size_t vl);
vfloat16m8_t vfnmacc_vv_f16m8 (vfloat16m8_t vd, vfloat16m8_t
    vs1, vfloat16m8_t vs2, size_t vl);
vfloat16m8_t vfnmacc_vf_f16m8 (vfloat16m8_t vd, float16_t rs1,
    vfloat16m8_t vs2, size_t vl);
vfloat32m1_t vfnmacc_vv_f32m1 (vfloat32m1_t vd, vfloat32m1_t
    vs1, vfloat32m1_t vs2, size_t vl);
vfloat32m1_t vfnmacc_vf_f32m1 (vfloat32m1_t vd, float32_t rs1,
    vfloat32m1_t vs2, size_t vl);
vfloat32m2_t vfnmacc_vv_f32m2 (vfloat32m2_t vd, vfloat32m2_t
    vs1, vfloat32m2_t vs2, size_t vl);
vfloat32m2_t vfnmacc_vf_f32m2 (vfloat32m2_t vd, float32_t rs1,
    vfloat32m2_t vs2, size_t vl);
vfloat32m4_t vfnmacc_vv_f32m4 (vfloat32m4_t vd, vfloat32m4_t
    vs1, vfloat32m4_t vs2, size_t vl);
vfloat32m4_t vfnmacc_vf_f32m4 (vfloat32m4_t vd, float32_t rs1,
    vfloat32m4_t vs2, size_t vl);
vfloat32m8_t vfnmacc_vv_f32m8 (vfloat32m8_t vd, vfloat32m8_t
    vs1, vfloat32m8_t vs2, size_t vl);
vfloat32m8_t vfnmacc_vf_f32m8 (vfloat32m8_t vd, float32_t rs1,
    vfloat32m8_t vs2, size_t vl);
vfloat64m1_t vfnmacc_vv_f64m1 (vfloat64m1_t vd, vfloat64m1_t
    vs1, vfloat64m1_t vs2, size_t vl);
vfloat64m1_t vfnmacc_vf_f64m1 (vfloat64m1_t vd, float64_t rs1,
    vfloat64m1_t vs2, size_t vl);
vfloat64m2_t vfnmacc_vv_f64m2 (vfloat64m2_t vd, vfloat64m2_t
    vs1, vfloat64m2_t vs2, size_t vl);
vfloat64m2_t vfnmacc_vf_f64m2 (vfloat64m2_t vd, float64_t rs1,
    vfloat64m2_t vs2, size_t vl);
vfloat64m4_t vfnmacc_vv_f64m4 (vfloat64m4_t vd, vfloat64m4_t
    vs1, vfloat64m4_t vs2, size_t vl);
vfloat64m4_t vfnmacc_vf_f64m4 (vfloat64m4_t vd, float64_t rs1,
    vfloat64m4_t vs2, size_t vl);

```

```

vfloat64m8_t vfmacc_vv_f64m8 (vfloat64m8_t vd, vfloat64m8_t
    vs1, vfloat64m8_t vs2, size_t vl);
vfloat64m8_t vfmacc_vf_f64m8 (vfloat64m8_t vd, float64_t rs1,
    vfloat64m8_t vs2, size_t vl);
vfloat16m1_t vfmsac_vv_f16m1 (vfloat16m1_t vd, vfloat16m1_t vs1,
    vfloat16m1_t vs2, size_t vl);
vfloat16m1_t vfmsac_vf_f16m1 (vfloat16m1_t vd, float16_t rs1,
    vfloat16m1_t vs2, size_t vl);
vfloat16m2_t vfmsac_vv_f16m2 (vfloat16m2_t vd, vfloat16m2_t vs1,
    vfloat16m2_t vs2, size_t vl);
vfloat16m2_t vfmsac_vf_f16m2 (vfloat16m2_t vd, float16_t rs1,
    vfloat16m2_t vs2, size_t vl);
vfloat16m4_t vfmsac_vv_f16m4 (vfloat16m4_t vd, vfloat16m4_t vs1,
    vfloat16m4_t vs2, size_t vl);
vfloat16m4_t vfmsac_vf_f16m4 (vfloat16m4_t vd, float16_t rs1,
    vfloat16m4_t vs2, size_t vl);
vfloat16m8_t vfmsac_vv_f16m8 (vfloat16m8_t vd, vfloat16m8_t vs1,
    vfloat16m8_t vs2, size_t vl);
vfloat16m8_t vfmsac_vf_f16m8 (vfloat16m8_t vd, float16_t rs1,
    vfloat16m8_t vs2, size_t vl);
vfloat32m1_t vfmsac_vv_f32m1 (vfloat32m1_t vd, vfloat32m1_t vs1,
    vfloat32m1_t vs2, size_t vl);
vfloat32m1_t vfmsac_vf_f32m1 (vfloat32m1_t vd, float32_t rs1,
    vfloat32m1_t vs2, size_t vl);
vfloat32m2_t vfmsac_vv_f32m2 (vfloat32m2_t vd, vfloat32m2_t vs1,
    vfloat32m2_t vs2, size_t vl);
vfloat32m2_t vfmsac_vf_f32m2 (vfloat32m2_t vd, float32_t rs1,
    vfloat32m2_t vs2, size_t vl);
vfloat32m4_t vfmsac_vv_f32m4 (vfloat32m4_t vd, vfloat32m4_t vs1,
    vfloat32m4_t vs2, size_t vl);
vfloat32m4_t vfmsac_vf_f32m4 (vfloat32m4_t vd, float32_t rs1,
    vfloat32m4_t vs2, size_t vl);
vfloat32m8_t vfmsac_vv_f32m8 (vfloat32m8_t vd, vfloat32m8_t vs1,
    vfloat32m8_t vs2, size_t vl);
vfloat32m8_t vfmsac_vf_f32m8 (vfloat32m8_t vd, float32_t rs1,
    vfloat32m8_t vs2, size_t vl);
vfloat64m1_t vfmsac_vv_f64m1 (vfloat64m1_t vd, vfloat64m1_t vs1,
    vfloat64m1_t vs2, size_t vl);
vfloat64m1_t vfmsac_vf_f64m1 (vfloat64m1_t vd, float64_t rs1,
    vfloat64m1_t vs2, size_t vl);
vfloat64m2_t vfmsac_vv_f64m2 (vfloat64m2_t vd, vfloat64m2_t vs1,
    vfloat64m2_t vs2, size_t vl);
vfloat64m2_t vfmsac_vf_f64m2 (vfloat64m2_t vd, float64_t rs1,
    vfloat64m2_t vs2, size_t vl);
vfloat64m4_t vfmsac_vv_f64m4 (vfloat64m4_t vd, vfloat64m4_t vs1,
    vfloat64m4_t vs2, size_t vl);

```

```

vfloat64m4_t vfmsac_vf_f64m4 (vfloat64m4_t vd, float64_t rs1,
    vfloat64m4_t vs2, size_t vl);
vfloat64m8_t vfmsac_vv_f64m8 (vfloat64m8_t vd, vfloat64m8_t vs1,
    vfloat64m8_t vs2, size_t vl);
vfloat64m8_t vfmsac_vf_f64m8 (vfloat64m8_t vd, float64_t rs1,
    vfloat64m8_t vs2, size_t vl);
vfloat16m1_t vfnmsac_vv_f16m1 (vfloat16m1_t vd, vfloat16m1_t
    vs1, vfloat16m1_t vs2, size_t vl);
vfloat16m1_t vfnmsac_vf_f16m1 (vfloat16m1_t vd, float16_t rs1,
    vfloat16m1_t vs2, size_t vl);
vfloat16m2_t vfnmsac_vv_f16m2 (vfloat16m2_t vd, vfloat16m2_t
    vs1, vfloat16m2_t vs2, size_t vl);
vfloat16m2_t vfnmsac_vf_f16m2 (vfloat16m2_t vd, float16_t rs1,
    vfloat16m2_t vs2, size_t vl);
vfloat16m4_t vfnmsac_vv_f16m4 (vfloat16m4_t vd, vfloat16m4_t
    vs1, vfloat16m4_t vs2, size_t vl);
vfloat16m4_t vfnmsac_vf_f16m4 (vfloat16m4_t vd, float16_t rs1,
    vfloat16m4_t vs2, size_t vl);
vfloat16m8_t vfnmsac_vv_f16m8 (vfloat16m8_t vd, vfloat16m8_t
    vs1, vfloat16m8_t vs2, size_t vl);
vfloat16m8_t vfnmsac_vf_f16m8 (vfloat16m8_t vd, float16_t rs1,
    vfloat16m8_t vs2, size_t vl);
vfloat32m1_t vfnmsac_vv_f32m1 (vfloat32m1_t vd, vfloat32m1_t
    vs1, vfloat32m1_t vs2, size_t vl);
vfloat32m1_t vfnmsac_vf_f32m1 (vfloat32m1_t vd, float32_t rs1,
    vfloat32m1_t vs2, size_t vl);
vfloat32m2_t vfnmsac_vv_f32m2 (vfloat32m2_t vd, vfloat32m2_t
    vs1, vfloat32m2_t vs2, size_t vl);
vfloat32m2_t vfnmsac_vf_f32m2 (vfloat32m2_t vd, float32_t rs1,
    vfloat32m2_t vs2, size_t vl);
vfloat32m4_t vfnmsac_vv_f32m4 (vfloat32m4_t vd, vfloat32m4_t
    vs1, vfloat32m4_t vs2, size_t vl);
vfloat32m4_t vfnmsac_vf_f32m4 (vfloat32m4_t vd, float32_t rs1,
    vfloat32m4_t vs2, size_t vl);
vfloat32m8_t vfnmsac_vv_f32m8 (vfloat32m8_t vd, vfloat32m8_t
    vs1, vfloat32m8_t vs2, size_t vl);
vfloat32m8_t vfnmsac_vf_f32m8 (vfloat32m8_t vd, float32_t rs1,
    vfloat32m8_t vs2, size_t vl);
vfloat64m1_t vfnmsac_vv_f64m1 (vfloat64m1_t vd, vfloat64m1_t
    vs1, vfloat64m1_t vs2, size_t vl);
vfloat64m1_t vfnmsac_vf_f64m1 (vfloat64m1_t vd, float64_t rs1,
    vfloat64m1_t vs2, size_t vl);
vfloat64m2_t vfnmsac_vv_f64m2 (vfloat64m2_t vd, vfloat64m2_t
    vs1, vfloat64m2_t vs2, size_t vl);
vfloat64m2_t vfnmsac_vf_f64m2 (vfloat64m2_t vd, float64_t rs1,
    vfloat64m2_t vs2, size_t vl);

```

```

vfloat64m4_t vfnmsac_vv_f64m4 (vfloat64m4_t vd, vfloat64m4_t
    vs1, vfloat64m4_t vs2, size_t vl);
vfloat64m4_t vfnmsac_vf_f64m4 (vfloat64m4_t vd, float64_t rs1,
    vfloat64m4_t vs2, size_t vl);
vfloat64m8_t vfnmsac_vv_f64m8 (vfloat64m8_t vd, vfloat64m8_t
    vs1, vfloat64m8_t vs2, size_t vl);
vfloat64m8_t vfnmsac_vf_f64m8 (vfloat64m8_t vd, float64_t rs1,
    vfloat64m8_t vs2, size_t vl);
vfloat16m1_t vfmadd_vv_f16m1 (vfloat16m1_t vd, vfloat16m1_t vs1,
    vfloat16m1_t vs2, size_t vl);
vfloat16m1_t vfmadd_vf_f16m1 (vfloat16m1_t vd, float16_t rs1,
    vfloat16m1_t vs2, size_t vl);
vfloat16m2_t vfmadd_vv_f16m2 (vfloat16m2_t vd, vfloat16m2_t vs1,
    vfloat16m2_t vs2, size_t vl);
vfloat16m2_t vfmadd_vf_f16m2 (vfloat16m2_t vd, float16_t rs1,
    vfloat16m2_t vs2, size_t vl);
vfloat16m4_t vfmadd_vv_f16m4 (vfloat16m4_t vd, vfloat16m4_t vs1,
    vfloat16m4_t vs2, size_t vl);
vfloat16m4_t vfmadd_vf_f16m4 (vfloat16m4_t vd, float16_t rs1,
    vfloat16m4_t vs2, size_t vl);
vfloat16m8_t vfmadd_vv_f16m8 (vfloat16m8_t vd, vfloat16m8_t vs1,
    vfloat16m8_t vs2, size_t vl);
vfloat16m8_t vfmadd_vf_f16m8 (vfloat16m8_t vd, float16_t rs1,
    vfloat16m8_t vs2, size_t vl);
vfloat32m1_t vfmadd_vv_f32m1 (vfloat32m1_t vd, vfloat32m1_t vs1,
    vfloat32m1_t vs2, size_t vl);
vfloat32m1_t vfmadd_vf_f32m1 (vfloat32m1_t vd, float32_t rs1,
    vfloat32m1_t vs2, size_t vl);
vfloat32m2_t vfmadd_vv_f32m2 (vfloat32m2_t vd, vfloat32m2_t vs1,
    vfloat32m2_t vs2, size_t vl);
vfloat32m2_t vfmadd_vf_f32m2 (vfloat32m2_t vd, float32_t rs1,
    vfloat32m2_t vs2, size_t vl);
vfloat32m4_t vfmadd_vv_f32m4 (vfloat32m4_t vd, vfloat32m4_t vs1,
    vfloat32m4_t vs2, size_t vl);
vfloat32m4_t vfmadd_vf_f32m4 (vfloat32m4_t vd, float32_t rs1,
    vfloat32m4_t vs2, size_t vl);
vfloat32m8_t vfmadd_vv_f32m8 (vfloat32m8_t vd, vfloat32m8_t vs1,
    vfloat32m8_t vs2, size_t vl);
vfloat32m8_t vfmadd_vf_f32m8 (vfloat32m8_t vd, float32_t rs1,
    vfloat32m8_t vs2, size_t vl);
vfloat64m1_t vfmadd_vv_f64m1 (vfloat64m1_t vd, vfloat64m1_t vs1,
    vfloat64m1_t vs2, size_t vl);
vfloat64m1_t vfmadd_vf_f64m1 (vfloat64m1_t vd, float64_t rs1,
    vfloat64m1_t vs2, size_t vl);
vfloat64m2_t vfmadd_vv_f64m2 (vfloat64m2_t vd, vfloat64m2_t vs1,
    vfloat64m2_t vs2, size_t vl);

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vfloat64m2_t vfmadd_vf_f64m2 (vfloat64m2_t vd, float64_t rs1,
    vfloat64m2_t vs2, size_t vl);
vfloat64m4_t vfmadd_vv_f64m4 (vfloat64m4_t vd, vfloat64m4_t vs1,
    vfloat64m4_t vs2, size_t vl);
vfloat64m4_t vfmadd_vf_f64m4 (vfloat64m4_t vd, float64_t rs1,
    vfloat64m4_t vs2, size_t vl);
vfloat64m8_t vfmadd_vv_f64m8 (vfloat64m8_t vd, vfloat64m8_t vs1,
    vfloat64m8_t vs2, size_t vl);
vfloat64m8_t vfmadd_vf_f64m8 (vfloat64m8_t vd, float64_t rs1,
    vfloat64m8_t vs2, size_t vl);
vfloat16m1_t vfnmadd_vv_f16m1 (vfloat16m1_t vd, vfloat16m1_t
    vs1, vfloat16m1_t vs2, size_t vl);
vfloat16m1_t vfnmadd_vf_f16m1 (vfloat16m1_t vd, float16_t rs1,
    vfloat16m1_t vs2, size_t vl);
vfloat16m2_t vfnmadd_vv_f16m2 (vfloat16m2_t vd, vfloat16m2_t
    vs1, vfloat16m2_t vs2, size_t vl);
vfloat16m2_t vfnmadd_vf_f16m2 (vfloat16m2_t vd, float16_t rs1,
    vfloat16m2_t vs2, size_t vl);
vfloat16m4_t vfnmadd_vv_f16m4 (vfloat16m4_t vd, vfloat16m4_t
    vs1, vfloat16m4_t vs2, size_t vl);
vfloat16m4_t vfnmadd_vf_f16m4 (vfloat16m4_t vd, float16_t rs1,
    vfloat16m4_t vs2, size_t vl);
vfloat16m8_t vfnmadd_vv_f16m8 (vfloat16m8_t vd, vfloat16m8_t
    vs1, vfloat16m8_t vs2, size_t vl);
vfloat16m8_t vfnmadd_vf_f16m8 (vfloat16m8_t vd, float16_t rs1,
    vfloat16m8_t vs2, size_t vl);
vfloat32m1_t vfnmadd_vv_f32m1 (vfloat32m1_t vd, vfloat32m1_t
    vs1, vfloat32m1_t vs2, size_t vl);
vfloat32m1_t vfnmadd_vf_f32m1 (vfloat32m1_t vd, float32_t rs1,
    vfloat32m1_t vs2, size_t vl);
vfloat32m2_t vfnmadd_vv_f32m2 (vfloat32m2_t vd, vfloat32m2_t
    vs1, vfloat32m2_t vs2, size_t vl);
vfloat32m2_t vfnmadd_vf_f32m2 (vfloat32m2_t vd, float32_t rs1,
    vfloat32m2_t vs2, size_t vl);
vfloat32m4_t vfnmadd_vv_f32m4 (vfloat32m4_t vd, vfloat32m4_t
    vs1, vfloat32m4_t vs2, size_t vl);
vfloat32m4_t vfnmadd_vf_f32m4 (vfloat32m4_t vd, float32_t rs1,
    vfloat32m4_t vs2, size_t vl);
vfloat32m8_t vfnmadd_vv_f32m8 (vfloat32m8_t vd, vfloat32m8_t
    vs1, vfloat32m8_t vs2, size_t vl);
vfloat32m8_t vfnmadd_vf_f32m8 (vfloat32m8_t vd, float32_t rs1,
    vfloat32m8_t vs2, size_t vl);
vfloat64m1_t vfnmadd_vv_f64m1 (vfloat64m1_t vd, vfloat64m1_t
    vs1, vfloat64m1_t vs2, size_t vl);
vfloat64m1_t vfnmadd_vf_f64m1 (vfloat64m1_t vd, float64_t rs1,
    vfloat64m1_t vs2, size_t vl);

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vfloat64m2_t vfnmadd_vv_f64m2 (vfloat64m2_t vd, vfloat64m2_t
    vs1, vfloat64m2_t vs2, size_t vl);
vfloat64m2_t vfnmadd_vf_f64m2 (vfloat64m2_t vd, float64_t rs1,
    vfloat64m2_t vs2, size_t vl);
vfloat64m4_t vfnmadd_vv_f64m4 (vfloat64m4_t vd, vfloat64m4_t
    vs1, vfloat64m4_t vs2, size_t vl);
vfloat64m4_t vfnmadd_vf_f64m4 (vfloat64m4_t vd, float64_t rs1,
    vfloat64m4_t vs2, size_t vl);
vfloat64m8_t vfnmadd_vv_f64m8 (vfloat64m8_t vd, vfloat64m8_t
    vs1, vfloat64m8_t vs2, size_t vl);
vfloat64m8_t vfnmadd_vf_f64m8 (vfloat64m8_t vd, float64_t rs1,
    vfloat64m8_t vs2, size_t vl);
vfloat16m1_t vfmsub_vv_f16m1 (vfloat16m1_t vd, vfloat16m1_t vs1,
    vfloat16m1_t vs2, size_t vl);
vfloat16m1_t vfmsub_vf_f16m1 (vfloat16m1_t vd, float16_t rs1,
    vfloat16m1_t vs2, size_t vl);
vfloat16m2_t vfmsub_vv_f16m2 (vfloat16m2_t vd, vfloat16m2_t vs1,
    vfloat16m2_t vs2, size_t vl);
vfloat16m2_t vfmsub_vf_f16m2 (vfloat16m2_t vd, float16_t rs1,
    vfloat16m2_t vs2, size_t vl);
vfloat16m4_t vfmsub_vv_f16m4 (vfloat16m4_t vd, vfloat16m4_t vs1,
    vfloat16m4_t vs2, size_t vl);
vfloat16m4_t vfmsub_vf_f16m4 (vfloat16m4_t vd, float16_t rs1,
    vfloat16m4_t vs2, size_t vl);
vfloat16m8_t vfmsub_vv_f16m8 (vfloat16m8_t vd, vfloat16m8_t vs1,
    vfloat16m8_t vs2, size_t vl);
vfloat16m8_t vfmsub_vf_f16m8 (vfloat16m8_t vd, float16_t rs1,
    vfloat16m8_t vs2, size_t vl);
vfloat32m1_t vfmsub_vv_f32m1 (vfloat32m1_t vd, vfloat32m1_t vs1,
    vfloat32m1_t vs2, size_t vl);
vfloat32m1_t vfmsub_vf_f32m1 (vfloat32m1_t vd, float32_t rs1,
    vfloat32m1_t vs2, size_t vl);
vfloat32m2_t vfmsub_vv_f32m2 (vfloat32m2_t vd, vfloat32m2_t vs1,
    vfloat32m2_t vs2, size_t vl);
vfloat32m2_t vfmsub_vf_f32m2 (vfloat32m2_t vd, float32_t rs1,
    vfloat32m2_t vs2, size_t vl);
vfloat32m4_t vfmsub_vv_f32m4 (vfloat32m4_t vd, vfloat32m4_t vs1,
    vfloat32m4_t vs2, size_t vl);
vfloat32m4_t vfmsub_vf_f32m4 (vfloat32m4_t vd, float32_t rs1,
    vfloat32m4_t vs2, size_t vl);
vfloat32m8_t vfmsub_vv_f32m8 (vfloat32m8_t vd, vfloat32m8_t vs1,
    vfloat32m8_t vs2, size_t vl);
vfloat32m8_t vfmsub_vf_f32m8 (vfloat32m8_t vd, float32_t rs1,
    vfloat32m8_t vs2, size_t vl);
vfloat64m1_t vfmsub_vv_f64m1 (vfloat64m1_t vd, vfloat64m1_t vs1,
    vfloat64m1_t vs2, size_t vl);

```

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vfloat64m1_t vfmsub_vf_f64m1 (vfloat64m1_t vd, float64_t rs1,
    vfloat64m1_t vs2, size_t vl);
vfloat64m2_t vfmsub_vv_f64m2 (vfloat64m2_t vd, vfloat64m2_t vs1,
    vfloat64m2_t vs2, size_t vl);
vfloat64m2_t vfmsub_vf_f64m2 (vfloat64m2_t vd, float64_t rs1,
    vfloat64m2_t vs2, size_t vl);
vfloat64m4_t vfmsub_vv_f64m4 (vfloat64m4_t vd, vfloat64m4_t vs1,
    vfloat64m4_t vs2, size_t vl);
vfloat64m4_t vfmsub_vf_f64m4 (vfloat64m4_t vd, float64_t rs1,
    vfloat64m4_t vs2, size_t vl);
vfloat64m8_t vfmsub_vv_f64m8 (vfloat64m8_t vd, vfloat64m8_t vs1,
    vfloat64m8_t vs2, size_t vl);
vfloat64m8_t vfmsub_vf_f64m8 (vfloat64m8_t vd, float64_t rs1,
    vfloat64m8_t vs2, size_t vl);
vfloat16m1_t vfnmsub_vv_f16m1 (vfloat16m1_t vd, vfloat16m1_t
    vs1, vfloat16m1_t vs2, size_t vl);
vfloat16m1_t vfnmsub_vf_f16m1 (vfloat16m1_t vd, float16_t rs1,
    vfloat16m1_t vs2, size_t vl);
vfloat16m2_t vfnmsub_vv_f16m2 (vfloat16m2_t vd, vfloat16m2_t
    vs1, vfloat16m2_t vs2, size_t vl);
vfloat16m2_t vfnmsub_vf_f16m2 (vfloat16m2_t vd, float16_t rs1,
    vfloat16m2_t vs2, size_t vl);
vfloat16m4_t vfnmsub_vv_f16m4 (vfloat16m4_t vd, vfloat16m4_t
    vs1, vfloat16m4_t vs2, size_t vl);
vfloat16m4_t vfnmsub_vf_f16m4 (vfloat16m4_t vd, float16_t rs1,
    vfloat16m4_t vs2, size_t vl);
vfloat16m8_t vfnmsub_vv_f16m8 (vfloat16m8_t vd, vfloat16m8_t
    vs1, vfloat16m8_t vs2, size_t vl);
vfloat16m8_t vfnmsub_vf_f16m8 (vfloat16m8_t vd, float16_t rs1,
    vfloat16m8_t vs2, size_t vl);
vfloat32m1_t vfnmsub_vv_f32m1 (vfloat32m1_t vd, vfloat32m1_t
    vs1, vfloat32m1_t vs2, size_t vl);
vfloat32m1_t vfnmsub_vf_f32m1 (vfloat32m1_t vd, float32_t rs1,
    vfloat32m1_t vs2, size_t vl);
vfloat32m2_t vfnmsub_vv_f32m2 (vfloat32m2_t vd, vfloat32m2_t
    vs1, vfloat32m2_t vs2, size_t vl);
vfloat32m2_t vfnmsub_vf_f32m2 (vfloat32m2_t vd, float32_t rs1,
    vfloat32m2_t vs2, size_t vl);
vfloat32m4_t vfnmsub_vv_f32m4 (vfloat32m4_t vd, vfloat32m4_t
    vs1, vfloat32m4_t vs2, size_t vl);
vfloat32m4_t vfnmsub_vf_f32m4 (vfloat32m4_t vd, float32_t rs1,
    vfloat32m4_t vs2, size_t vl);
vfloat32m8_t vfnmsub_vv_f32m8 (vfloat32m8_t vd, vfloat32m8_t
    vs1, vfloat32m8_t vs2, size_t vl);
vfloat32m8_t vfnmsub_vf_f32m8 (vfloat32m8_t vd, float32_t rs1,
    vfloat32m8_t vs2, size_t vl);

```

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vfloat64m1_t vfnmsub_vv_f64m1 (vfloat64m1_t vd, vfloat64m1_t
    vs1, vfloat64m1_t vs2, size_t vl);
vfloat64m1_t vfnmsub_vf_f64m1 (vfloat64m1_t vd, float64_t rs1,
    vfloat64m1_t vs2, size_t vl);
vfloat64m2_t vfnmsub_vv_f64m2 (vfloat64m2_t vd, vfloat64m2_t
    vs1, vfloat64m2_t vs2, size_t vl);
vfloat64m2_t vfnmsub_vf_f64m2 (vfloat64m2_t vd, float64_t rs1,
    vfloat64m2_t vs2, size_t vl);
vfloat64m4_t vfnmsub_vv_f64m4 (vfloat64m4_t vd, vfloat64m4_t
    vs1, vfloat64m4_t vs2, size_t vl);
vfloat64m4_t vfnmsub_vf_f64m4 (vfloat64m4_t vd, float64_t rs1,
    vfloat64m4_t vs2, size_t vl);
vfloat64m8_t vfnmsub_vv_f64m8 (vfloat64m8_t vd, vfloat64m8_t
    vs1, vfloat64m8_t vs2, size_t vl);
vfloat64m8_t vfnmsub_vf_f64m8 (vfloat64m8_t vd, float64_t rs1,
    vfloat64m8_t vs2, size_t vl);
// masked functions
vfloat16m1_t vfmacc_vv_f16m1_m (vbool16_t mask, vfloat16m1_t vd,
    vfloat16m1_t vs1, vfloat16m1_t vs2, size_t vl);
vfloat16m1_t vfmacc_vf_f16m1_m (vbool16_t mask, vfloat16m1_t vd,
    float16_t rs1, vfloat16m1_t vs2, size_t vl);
vfloat16m2_t vfmacc_vv_f16m2_m (vbool8_t mask, vfloat16m2_t vd,
    vfloat16m2_t vs1, vfloat16m2_t vs2, size_t vl);
vfloat16m2_t vfmacc_vf_f16m2_m (vbool8_t mask, vfloat16m2_t vd,
    float16_t rs1, vfloat16m2_t vs2, size_t vl);
vfloat16m4_t vfmacc_vv_f16m4_m (vbool4_t mask, vfloat16m4_t vd,
    vfloat16m4_t vs1, vfloat16m4_t vs2, size_t vl);
vfloat16m4_t vfmacc_vf_f16m4_m (vbool4_t mask, vfloat16m4_t vd,
    float16_t rs1, vfloat16m4_t vs2, size_t vl);
vfloat16m8_t vfmacc_vv_f16m8_m (vbool2_t mask, vfloat16m8_t vd,
    vfloat16m8_t vs1, vfloat16m8_t vs2, size_t vl);
vfloat16m8_t vfmacc_vf_f16m8_m (vbool2_t mask, vfloat16m8_t vd,
    float16_t rs1, vfloat16m8_t vs2, size_t vl);
vfloat32m1_t vfmacc_vv_f32m1_m (vbool32_t mask, vfloat32m1_t vd,
    vfloat32m1_t vs1, vfloat32m1_t vs2, size_t vl);
vfloat32m1_t vfmacc_vf_f32m1_m (vbool32_t mask, vfloat32m1_t vd,
    float32_t rs1, vfloat32m1_t vs2, size_t vl);
vfloat32m2_t vfmacc_vv_f32m2_m (vbool16_t mask, vfloat32m2_t vd,
    vfloat32m2_t vs1, vfloat32m2_t vs2, size_t vl);
vfloat32m2_t vfmacc_vf_f32m2_m (vbool16_t mask, vfloat32m2_t vd,
    float32_t rs1, vfloat32m2_t vs2, size_t vl);
vfloat32m4_t vfmacc_vv_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    vfloat32m4_t vs1, vfloat32m4_t vs2, size_t vl);
vfloat32m4_t vfmacc_vf_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    float32_t rs1, vfloat32m4_t vs2, size_t vl);

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vfloat32m8_t vfmacc_vv_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    vfloat32m8_t vs1, vfloat32m8_t vs2, size_t vl);
vfloat32m8_t vfmacc_vf_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    float32_t rs1, vfloat32m8_t vs2, size_t vl);
vfloat64m1_t vfmacc_vv_f64m1_m (vbool64_t mask, vfloat64m1_t vd,
    vfloat64m1_t vs1, vfloat64m1_t vs2, size_t vl);
vfloat64m1_t vfmacc_vf_f64m1_m (vbool64_t mask, vfloat64m1_t vd,
    float64_t rs1, vfloat64m1_t vs2, size_t vl);
vfloat64m2_t vfmacc_vv_f64m2_m (vbool32_t mask, vfloat64m2_t vd,
    vfloat64m2_t vs1, vfloat64m2_t vs2, size_t vl);
vfloat64m2_t vfmacc_vf_f64m2_m (vbool32_t mask, vfloat64m2_t vd,
    float64_t rs1, vfloat64m2_t vs2, size_t vl);
vfloat64m4_t vfmacc_vv_f64m4_m (vbool16_t mask, vfloat64m4_t vd,
    vfloat64m4_t vs1, vfloat64m4_t vs2, size_t vl);
vfloat64m4_t vfmacc_vf_f64m4_m (vbool16_t mask, vfloat64m4_t vd,
    float64_t rs1, vfloat64m4_t vs2, size_t vl);
vfloat64m8_t vfmacc_vv_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    vfloat64m8_t vs1, vfloat64m8_t vs2, size_t vl);
vfloat64m8_t vfmacc_vf_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    float64_t rs1, vfloat64m8_t vs2, size_t vl);
vfloat16m1_t vfnmacc_vv_f16m1_m (vbool16_t mask, vfloat16m1_t
    vd, vfloat16m1_t vs1, vfloat16m1_t vs2, size_t vl);
vfloat16m1_t vfnmacc_vf_f16m1_m (vbool16_t mask, vfloat16m1_t
    vd, float16_t rs1, vfloat16m1_t vs2, size_t vl);
vfloat16m2_t vfnmacc_vv_f16m2_m (vbool8_t mask, vfloat16m2_t vd,
    vfloat16m2_t vs1, vfloat16m2_t vs2, size_t vl);
vfloat16m2_t vfnmacc_vf_f16m2_m (vbool8_t mask, vfloat16m2_t vd,
    float16_t rs1, vfloat16m2_t vs2, size_t vl);
vfloat16m4_t vfnmacc_vv_f16m4_m (vbool4_t mask, vfloat16m4_t vd,
    vfloat16m4_t vs1, vfloat16m4_t vs2, size_t vl);
vfloat16m4_t vfnmacc_vf_f16m4_m (vbool4_t mask, vfloat16m4_t vd,
    float16_t rs1, vfloat16m4_t vs2, size_t vl);
vfloat16m8_t vfnmacc_vv_f16m8_m (vbool2_t mask, vfloat16m8_t vd,
    vfloat16m8_t vs1, vfloat16m8_t vs2, size_t vl);
vfloat16m8_t vfnmacc_vf_f16m8_m (vbool2_t mask, vfloat16m8_t vd,
    float16_t rs1, vfloat16m8_t vs2, size_t vl);
vfloat32m1_t vfnmacc_vv_f32m1_m (vbool32_t mask, vfloat32m1_t
    vd, vfloat32m1_t vs1, vfloat32m1_t vs2, size_t vl);
vfloat32m1_t vfnmacc_vf_f32m1_m (vbool32_t mask, vfloat32m1_t
    vd, float32_t rs1, vfloat32m1_t vs2, size_t vl);
vfloat32m2_t vfnmacc_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    vd, vfloat32m2_t vs1, vfloat32m2_t vs2, size_t vl);
vfloat32m2_t vfnmacc_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    vd, float32_t rs1, vfloat32m2_t vs2, size_t vl);
vfloat32m4_t vfnmacc_vv_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    vfloat32m4_t vs1, vfloat32m4_t vs2, size_t vl);

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vfloat32m4_t vfnmacc_vf_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    float32_t rs1, vfloat32m4_t vs2, size_t vl);
vfloat32m8_t vfnmacc_vv_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    vfloat32m8_t vs1, vfloat32m8_t vs2, size_t vl);
vfloat32m8_t vfnmacc_vf_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    float32_t rs1, vfloat32m8_t vs2, size_t vl);
vfloat64m1_t vfnmacc_vv_f64m1_m (vbool64_t mask, vfloat64m1_t
    vd, vfloat64m1_t vs1, vfloat64m1_t vs2, size_t vl);
vfloat64m1_t vfnmacc_vf_f64m1_m (vbool64_t mask, vfloat64m1_t
    vd, float64_t rs1, vfloat64m1_t vs2, size_t vl);
vfloat64m2_t vfnmacc_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    vd, vfloat64m2_t vs1, vfloat64m2_t vs2, size_t vl);
vfloat64m2_t vfnmacc_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    vd, float64_t rs1, vfloat64m2_t vs2, size_t vl);
vfloat64m4_t vfnmacc_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    vd, vfloat64m4_t vs1, vfloat64m4_t vs2, size_t vl);
vfloat64m4_t vfnmacc_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    vd, float64_t rs1, vfloat64m4_t vs2, size_t vl);
vfloat64m8_t vfnmacc_vv_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    vfloat64m8_t vs1, vfloat64m8_t vs2, size_t vl);
vfloat64m8_t vfnmacc_vf_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    float64_t rs1, vfloat64m8_t vs2, size_t vl);
vfloat16m1_t vfmsac_vv_f16m1_m (vbool16_t mask, vfloat16m1_t vd,
    vfloat16m1_t vs1, vfloat16m1_t vs2, size_t vl);
vfloat16m1_t vfmsac_vf_f16m1_m (vbool16_t mask, vfloat16m1_t vd,
    float16_t rs1, vfloat16m1_t vs2, size_t vl);
vfloat16m2_t vfmsac_vv_f16m2_m (vbool8_t mask, vfloat16m2_t vd,
    vfloat16m2_t vs1, vfloat16m2_t vs2, size_t vl);
vfloat16m2_t vfmsac_vf_f16m2_m (vbool8_t mask, vfloat16m2_t vd,
    float16_t rs1, vfloat16m2_t vs2, size_t vl);
vfloat16m4_t vfmsac_vv_f16m4_m (vbool4_t mask, vfloat16m4_t vd,
    vfloat16m4_t vs1, vfloat16m4_t vs2, size_t vl);
vfloat16m4_t vfmsac_vf_f16m4_m (vbool4_t mask, vfloat16m4_t vd,
    float16_t rs1, vfloat16m4_t vs2, size_t vl);
vfloat16m8_t vfmsac_vv_f16m8_m (vbool2_t mask, vfloat16m8_t vd,
    vfloat16m8_t vs1, vfloat16m8_t vs2, size_t vl);
vfloat16m8_t vfmsac_vf_f16m8_m (vbool2_t mask, vfloat16m8_t vd,
    float16_t rs1, vfloat16m8_t vs2, size_t vl);
vfloat32m1_t vfmsac_vv_f32m1_m (vbool32_t mask, vfloat32m1_t vd,
    vfloat32m1_t vs1, vfloat32m1_t vs2, size_t vl);
vfloat32m1_t vfmsac_vf_f32m1_m (vbool32_t mask, vfloat32m1_t vd,
    float32_t rs1, vfloat32m1_t vs2, size_t vl);
vfloat32m2_t vfmsac_vv_f32m2_m (vbool16_t mask, vfloat32m2_t vd,
    vfloat32m2_t vs1, vfloat32m2_t vs2, size_t vl);
vfloat32m2_t vfmsac_vf_f32m2_m (vbool16_t mask, vfloat32m2_t vd,
    float32_t rs1, vfloat32m2_t vs2, size_t vl);

```

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vfloat32m4_t vfmsac_vv_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    vfloat32m4_t vs1, vfloat32m4_t vs2, size_t vl);
vfloat32m4_t vfmsac_vf_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    float32_t rs1, vfloat32m4_t vs2, size_t vl);
vfloat32m8_t vfmsac_vv_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    vfloat32m8_t vs1, vfloat32m8_t vs2, size_t vl);
vfloat32m8_t vfmsac_vf_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    float32_t rs1, vfloat32m8_t vs2, size_t vl);
vfloat64m1_t vfmsac_vv_f64m1_m (vbool64_t mask, vfloat64m1_t vd,
    vfloat64m1_t vs1, vfloat64m1_t vs2, size_t vl);
vfloat64m1_t vfmsac_vf_f64m1_m (vbool64_t mask, vfloat64m1_t vd,
    float64_t rs1, vfloat64m1_t vs2, size_t vl);
vfloat64m2_t vfmsac_vv_f64m2_m (vbool32_t mask, vfloat64m2_t vd,
    vfloat64m2_t vs1, vfloat64m2_t vs2, size_t vl);
vfloat64m2_t vfmsac_vf_f64m2_m (vbool32_t mask, vfloat64m2_t vd,
    float64_t rs1, vfloat64m2_t vs2, size_t vl);
vfloat64m4_t vfmsac_vv_f64m4_m (vbool16_t mask, vfloat64m4_t vd,
    vfloat64m4_t vs1, vfloat64m4_t vs2, size_t vl);
vfloat64m4_t vfmsac_vf_f64m4_m (vbool16_t mask, vfloat64m4_t vd,
    float64_t rs1, vfloat64m4_t vs2, size_t vl);
vfloat64m8_t vfmsac_vv_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    vfloat64m8_t vs1, vfloat64m8_t vs2, size_t vl);
vfloat64m8_t vfmsac_vf_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    float64_t rs1, vfloat64m8_t vs2, size_t vl);
vfloat16m1_t vfnmsac_vv_f16m1_m (vbool16_t mask, vfloat16m1_t
    vd, vfloat16m1_t vs1, vfloat16m1_t vs2, size_t vl);
vfloat16m1_t vfnmsac_vf_f16m1_m (vbool16_t mask, vfloat16m1_t
    vd, float16_t rs1, vfloat16m1_t vs2, size_t vl);
vfloat16m2_t vfnmsac_vv_f16m2_m (vbool8_t mask, vfloat16m2_t vd,
    vfloat16m2_t vs1, vfloat16m2_t vs2, size_t vl);
vfloat16m2_t vfnmsac_vf_f16m2_m (vbool8_t mask, vfloat16m2_t vd,
    float16_t rs1, vfloat16m2_t vs2, size_t vl);
vfloat16m4_t vfnmsac_vv_f16m4_m (vbool4_t mask, vfloat16m4_t vd,
    vfloat16m4_t vs1, vfloat16m4_t vs2, size_t vl);
vfloat16m4_t vfnmsac_vf_f16m4_m (vbool4_t mask, vfloat16m4_t vd,
    float16_t rs1, vfloat16m4_t vs2, size_t vl);
vfloat16m8_t vfnmsac_vv_f16m8_m (vbool2_t mask, vfloat16m8_t vd,
    vfloat16m8_t vs1, vfloat16m8_t vs2, size_t vl);
vfloat16m8_t vfnmsac_vf_f16m8_m (vbool2_t mask, vfloat16m8_t vd,
    float16_t rs1, vfloat16m8_t vs2, size_t vl);
vfloat32m1_t vfnmsac_vv_f32m1_m (vbool32_t mask, vfloat32m1_t
    vd, vfloat32m1_t vs1, vfloat32m1_t vs2, size_t vl);
vfloat32m1_t vfnmsac_vf_f32m1_m (vbool32_t mask, vfloat32m1_t
    vd, float32_t rs1, vfloat32m1_t vs2, size_t vl);
vfloat32m2_t vfnmsac_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    vd, vfloat32m2_t vs1, vfloat32m2_t vs2, size_t vl);

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vfloat32m2_t vfnmsac_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    vd, float32_t rs1, vfloat32m2_t vs2, size_t vl);
vfloat32m4_t vfnmsac_vv_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    vfloat32m4_t vs1, vfloat32m4_t vs2, size_t vl);
vfloat32m4_t vfnmsac_vf_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    float32_t rs1, vfloat32m4_t vs2, size_t vl);
vfloat32m8_t vfnmsac_vv_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    vfloat32m8_t vs1, vfloat32m8_t vs2, size_t vl);
vfloat32m8_t vfnmsac_vf_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    float32_t rs1, vfloat32m8_t vs2, size_t vl);
vfloat64m1_t vfnmsac_vv_f64m1_m (vbool64_t mask, vfloat64m1_t
    vd, vfloat64m1_t vs1, vfloat64m1_t vs2, size_t vl);
vfloat64m1_t vfnmsac_vf_f64m1_m (vbool64_t mask, vfloat64m1_t
    vd, float64_t rs1, vfloat64m1_t vs2, size_t vl);
vfloat64m2_t vfnmsac_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    vd, vfloat64m2_t vs1, vfloat64m2_t vs2, size_t vl);
vfloat64m2_t vfnmsac_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    vd, float64_t rs1, vfloat64m2_t vs2, size_t vl);
vfloat64m4_t vfnmsac_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    vd, vfloat64m4_t vs1, vfloat64m4_t vs2, size_t vl);
vfloat64m4_t vfnmsac_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    vd, float64_t rs1, vfloat64m4_t vs2, size_t vl);
vfloat64m8_t vfnmsac_vv_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    vfloat64m8_t vs1, vfloat64m8_t vs2, size_t vl);
vfloat64m8_t vfnmsac_vf_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    float64_t rs1, vfloat64m8_t vs2, size_t vl);
vfloat16m1_t vfmadd_vv_f16m1_m (vbool16_t mask, vfloat16m1_t vd,
    vfloat16m1_t vs1, vfloat16m1_t vs2, size_t vl);
vfloat16m1_t vfmadd_vf_f16m1_m (vbool16_t mask, vfloat16m1_t vd,
    float16_t rs1, vfloat16m1_t vs2, size_t vl);
vfloat16m2_t vfmadd_vv_f16m2_m (vbool8_t mask, vfloat16m2_t vd,
    vfloat16m2_t vs1, vfloat16m2_t vs2, size_t vl);
vfloat16m2_t vfmadd_vf_f16m2_m (vbool8_t mask, vfloat16m2_t vd,
    float16_t rs1, vfloat16m2_t vs2, size_t vl);
vfloat16m4_t vfmadd_vv_f16m4_m (vbool4_t mask, vfloat16m4_t vd,
    vfloat16m4_t vs1, vfloat16m4_t vs2, size_t vl);
vfloat16m4_t vfmadd_vf_f16m4_m (vbool4_t mask, vfloat16m4_t vd,
    float16_t rs1, vfloat16m4_t vs2, size_t vl);
vfloat16m8_t vfmadd_vv_f16m8_m (vbool2_t mask, vfloat16m8_t vd,
    vfloat16m8_t vs1, vfloat16m8_t vs2, size_t vl);
vfloat16m8_t vfmadd_vf_f16m8_m (vbool2_t mask, vfloat16m8_t vd,
    float16_t rs1, vfloat16m8_t vs2, size_t vl);
vfloat32m1_t vfmadd_vv_f32m1_m (vbool32_t mask, vfloat32m1_t vd,
    vfloat32m1_t vs1, vfloat32m1_t vs2, size_t vl);
vfloat32m1_t vfmadd_vf_f32m1_m (vbool32_t mask, vfloat32m1_t vd,
    float32_t rs1, vfloat32m1_t vs2, size_t vl);

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vfloat32m2_t vfmadd_vv_f32m2_m (vbool16_t mask, vfloat32m2_t vd,
    vfloat32m2_t vs1, vfloat32m2_t vs2, size_t vl);
vfloat32m2_t vfmadd_vf_f32m2_m (vbool16_t mask, vfloat32m2_t vd,
    float32_t rs1, vfloat32m2_t vs2, size_t vl);
vfloat32m4_t vfmadd_vv_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    vfloat32m4_t vs1, vfloat32m4_t vs2, size_t vl);
vfloat32m4_t vfmadd_vf_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    float32_t rs1, vfloat32m4_t vs2, size_t vl);
vfloat32m8_t vfmadd_vv_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    vfloat32m8_t vs1, vfloat32m8_t vs2, size_t vl);
vfloat32m8_t vfmadd_vf_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    float32_t rs1, vfloat32m8_t vs2, size_t vl);
vfloat64m1_t vfmadd_vv_f64m1_m (vbool64_t mask, vfloat64m1_t vd,
    vfloat64m1_t vs1, vfloat64m1_t vs2, size_t vl);
vfloat64m1_t vfmadd_vf_f64m1_m (vbool64_t mask, vfloat64m1_t vd,
    float64_t rs1, vfloat64m1_t vs2, size_t vl);
vfloat64m2_t vfmadd_vv_f64m2_m (vbool32_t mask, vfloat64m2_t vd,
    vfloat64m2_t vs1, vfloat64m2_t vs2, size_t vl);
vfloat64m2_t vfmadd_vf_f64m2_m (vbool32_t mask, vfloat64m2_t vd,
    float64_t rs1, vfloat64m2_t vs2, size_t vl);
vfloat64m4_t vfmadd_vv_f64m4_m (vbool16_t mask, vfloat64m4_t vd,
    vfloat64m4_t vs1, vfloat64m4_t vs2, size_t vl);
vfloat64m4_t vfmadd_vf_f64m4_m (vbool16_t mask, vfloat64m4_t vd,
    float64_t rs1, vfloat64m4_t vs2, size_t vl);
vfloat64m8_t vfmadd_vv_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    vfloat64m8_t vs1, vfloat64m8_t vs2, size_t vl);
vfloat64m8_t vfmadd_vf_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    float64_t rs1, vfloat64m8_t vs2, size_t vl);
vfloat16m1_t vfnmadd_vv_f16m1_m (vbool16_t mask, vfloat16m1_t
    vd, vfloat16m1_t vs1, vfloat16m1_t vs2, size_t vl);
vfloat16m1_t vfnmadd_vf_f16m1_m (vbool16_t mask, vfloat16m1_t
    vd, float16_t rs1, vfloat16m1_t vs2, size_t vl);
vfloat16m2_t vfnmadd_vv_f16m2_m (vbool8_t mask, vfloat16m2_t vd,
    vfloat16m2_t vs1, vfloat16m2_t vs2, size_t vl);
vfloat16m2_t vfnmadd_vf_f16m2_m (vbool8_t mask, vfloat16m2_t vd,
    float16_t rs1, vfloat16m2_t vs2, size_t vl);
vfloat16m4_t vfnmadd_vv_f16m4_m (vbool4_t mask, vfloat16m4_t vd,
    vfloat16m4_t vs1, vfloat16m4_t vs2, size_t vl);
vfloat16m4_t vfnmadd_vf_f16m4_m (vbool4_t mask, vfloat16m4_t vd,
    float16_t rs1, vfloat16m4_t vs2, size_t vl);
vfloat16m8_t vfnmadd_vv_f16m8_m (vbool2_t mask, vfloat16m8_t vd,
    vfloat16m8_t vs1, vfloat16m8_t vs2, size_t vl);
vfloat16m8_t vfnmadd_vf_f16m8_m (vbool2_t mask, vfloat16m8_t vd,
    float16_t rs1, vfloat16m8_t vs2, size_t vl);
vfloat32m1_t vfnmadd_vv_f32m1_m (vbool32_t mask, vfloat32m1_t
    vd, vfloat32m1_t vs1, vfloat32m1_t vs2, size_t vl);

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vfloat32m1_t vfnmadd_vf_f32m1_m (vbool32_t mask, vfloat32m1_t
    vd, float32_t rs1, vfloat32m1_t vs2, size_t vl);
vfloat32m2_t vfnmadd_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    vd, vfloat32m2_t vs1, vfloat32m2_t vs2, size_t vl);
vfloat32m2_t vfnmadd_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    vd, float32_t rs1, vfloat32m2_t vs2, size_t vl);
vfloat32m4_t vfnmadd_vv_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    vfloat32m4_t vs1, vfloat32m4_t vs2, size_t vl);
vfloat32m4_t vfnmadd_vf_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    float32_t rs1, vfloat32m4_t vs2, size_t vl);
vfloat32m8_t vfnmadd_vv_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    vfloat32m8_t vs1, vfloat32m8_t vs2, size_t vl);
vfloat32m8_t vfnmadd_vf_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    float32_t rs1, vfloat32m8_t vs2, size_t vl);
vfloat64m1_t vfnmadd_vv_f64m1_m (vbool64_t mask, vfloat64m1_t
    vd, vfloat64m1_t vs1, vfloat64m1_t vs2, size_t vl);
vfloat64m1_t vfnmadd_vf_f64m1_m (vbool64_t mask, vfloat64m1_t
    vd, float64_t rs1, vfloat64m1_t vs2, size_t vl);
vfloat64m2_t vfnmadd_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    vd, vfloat64m2_t vs1, vfloat64m2_t vs2, size_t vl);
vfloat64m2_t vfnmadd_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    vd, float64_t rs1, vfloat64m2_t vs2, size_t vl);
vfloat64m4_t vfnmadd_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    vd, vfloat64m4_t vs1, vfloat64m4_t vs2, size_t vl);
vfloat64m4_t vfnmadd_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    vd, float64_t rs1, vfloat64m4_t vs2, size_t vl);
vfloat64m8_t vfnmadd_vv_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    vfloat64m8_t vs1, vfloat64m8_t vs2, size_t vl);
vfloat64m8_t vfnmadd_vf_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    float64_t rs1, vfloat64m8_t vs2, size_t vl);
vfloat16m1_t vfmsub_vv_f16m1_m (vbool16_t mask, vfloat16m1_t vd,
    vfloat16m1_t vs1, vfloat16m1_t vs2, size_t vl);
vfloat16m1_t vfmsub_vf_f16m1_m (vbool16_t mask, vfloat16m1_t vd,
    float16_t rs1, vfloat16m1_t vs2, size_t vl);
vfloat16m2_t vfmsub_vv_f16m2_m (vbool8_t mask, vfloat16m2_t vd,
    vfloat16m2_t vs1, vfloat16m2_t vs2, size_t vl);
vfloat16m2_t vfmsub_vf_f16m2_m (vbool8_t mask, vfloat16m2_t vd,
    float16_t rs1, vfloat16m2_t vs2, size_t vl);
vfloat16m4_t vfmsub_vv_f16m4_m (vbool4_t mask, vfloat16m4_t vd,
    vfloat16m4_t vs1, vfloat16m4_t vs2, size_t vl);
vfloat16m4_t vfmsub_vf_f16m4_m (vbool4_t mask, vfloat16m4_t vd,
    float16_t rs1, vfloat16m4_t vs2, size_t vl);
vfloat16m8_t vfmsub_vv_f16m8_m (vbool2_t mask, vfloat16m8_t vd,
    vfloat16m8_t vs1, vfloat16m8_t vs2, size_t vl);
vfloat16m8_t vfmsub_vf_f16m8_m (vbool2_t mask, vfloat16m8_t vd,
    float16_t rs1, vfloat16m8_t vs2, size_t vl);

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vfloat32m1_t vfmsub_vv_f32m1_m (vbool32_t mask, vfloat32m1_t vd,
    vfloat32m1_t vs1, vfloat32m1_t vs2, size_t vl);
vfloat32m1_t vfmsub_vf_f32m1_m (vbool32_t mask, vfloat32m1_t vd,
    float32_t rs1, vfloat32m1_t vs2, size_t vl);
vfloat32m2_t vfmsub_vv_f32m2_m (vbool16_t mask, vfloat32m2_t vd,
    vfloat32m2_t vs1, vfloat32m2_t vs2, size_t vl);
vfloat32m2_t vfmsub_vf_f32m2_m (vbool16_t mask, vfloat32m2_t vd,
    float32_t rs1, vfloat32m2_t vs2, size_t vl);
vfloat32m4_t vfmsub_vv_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    vfloat32m4_t vs1, vfloat32m4_t vs2, size_t vl);
vfloat32m4_t vfmsub_vf_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    float32_t rs1, vfloat32m4_t vs2, size_t vl);
vfloat32m8_t vfmsub_vv_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    vfloat32m8_t vs1, vfloat32m8_t vs2, size_t vl);
vfloat32m8_t vfmsub_vf_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    float32_t rs1, vfloat32m8_t vs2, size_t vl);
vfloat64m1_t vfmsub_vv_f64m1_m (vbool64_t mask, vfloat64m1_t vd,
    vfloat64m1_t vs1, vfloat64m1_t vs2, size_t vl);
vfloat64m1_t vfmsub_vf_f64m1_m (vbool64_t mask, vfloat64m1_t vd,
    float64_t rs1, vfloat64m1_t vs2, size_t vl);
vfloat64m2_t vfmsub_vv_f64m2_m (vbool32_t mask, vfloat64m2_t vd,
    vfloat64m2_t vs1, vfloat64m2_t vs2, size_t vl);
vfloat64m2_t vfmsub_vf_f64m2_m (vbool32_t mask, vfloat64m2_t vd,
    float64_t rs1, vfloat64m2_t vs2, size_t vl);
vfloat64m4_t vfmsub_vv_f64m4_m (vbool16_t mask, vfloat64m4_t vd,
    vfloat64m4_t vs1, vfloat64m4_t vs2, size_t vl);
vfloat64m4_t vfmsub_vf_f64m4_m (vbool16_t mask, vfloat64m4_t vd,
    float64_t rs1, vfloat64m4_t vs2, size_t vl);
vfloat64m8_t vfmsub_vv_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    vfloat64m8_t vs1, vfloat64m8_t vs2, size_t vl);
vfloat64m8_t vfmsub_vf_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    float64_t rs1, vfloat64m8_t vs2, size_t vl);
vfloat16m1_t vfnmsub_vv_f16m1_m (vbool16_t mask, vfloat16m1_t
    vd, vfloat16m1_t vs1, vfloat16m1_t vs2, size_t vl);
vfloat16m1_t vfnmsub_vf_f16m1_m (vbool16_t mask, vfloat16m1_t
    vd, float16_t rs1, vfloat16m1_t vs2, size_t vl);
vfloat16m2_t vfnmsub_vv_f16m2_m (vbool8_t mask, vfloat16m2_t vd,
    vfloat16m2_t vs1, vfloat16m2_t vs2, size_t vl);
vfloat16m2_t vfnmsub_vf_f16m2_m (vbool8_t mask, vfloat16m2_t vd,
    float16_t rs1, vfloat16m2_t vs2, size_t vl);
vfloat16m4_t vfnmsub_vv_f16m4_m (vbool4_t mask, vfloat16m4_t vd,
    vfloat16m4_t vs1, vfloat16m4_t vs2, size_t vl);
vfloat16m4_t vfnmsub_vf_f16m4_m (vbool4_t mask, vfloat16m4_t vd,
    float16_t rs1, vfloat16m4_t vs2, size_t vl);
vfloat16m8_t vfnmsub_vv_f16m8_m (vbool2_t mask, vfloat16m8_t vd,
    vfloat16m8_t vs1, vfloat16m8_t vs2, size_t vl);

```

```

vfloat16m8_t vfnmsub_vf_f16m8_m (vbool2_t mask, vfloat16m8_t vd,
    float16_t rs1, vfloat16m8_t vs2, size_t vl);
vfloat32m1_t vfnmsub_vv_f32m1_m (vbool32_t mask, vfloat32m1_t
    vd, vfloat32m1_t vs1, vfloat32m1_t vs2, size_t vl);
vfloat32m1_t vfnmsub_vf_f32m1_m (vbool32_t mask, vfloat32m1_t
    vd, float32_t rs1, vfloat32m1_t vs2, size_t vl);
vfloat32m2_t vfnmsub_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    vd, vfloat32m2_t vs1, vfloat32m2_t vs2, size_t vl);
vfloat32m2_t vfnmsub_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    vd, float32_t rs1, vfloat32m2_t vs2, size_t vl);
vfloat32m4_t vfnmsub_vv_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    vfloat32m4_t vs1, vfloat32m4_t vs2, size_t vl);
vfloat32m4_t vfnmsub_vf_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    float32_t rs1, vfloat32m4_t vs2, size_t vl);
vfloat32m8_t vfnmsub_vv_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    vfloat32m8_t vs1, vfloat32m8_t vs2, size_t vl);
vfloat32m8_t vfnmsub_vf_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    float32_t rs1, vfloat32m8_t vs2, size_t vl);
vfloat64m1_t vfnmsub_vv_f64m1_m (vbool64_t mask, vfloat64m1_t
    vd, vfloat64m1_t vs1, vfloat64m1_t vs2, size_t vl);
vfloat64m1_t vfnmsub_vf_f64m1_m (vbool64_t mask, vfloat64m1_t
    vd, float64_t rs1, vfloat64m1_t vs2, size_t vl);
vfloat64m2_t vfnmsub_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    vd, vfloat64m2_t vs1, vfloat64m2_t vs2, size_t vl);
vfloat64m2_t vfnmsub_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    vd, float64_t rs1, vfloat64m2_t vs2, size_t vl);
vfloat64m4_t vfnmsub_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    vd, vfloat64m4_t vs1, vfloat64m4_t vs2, size_t vl);
vfloat64m4_t vfnmsub_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    vd, float64_t rs1, vfloat64m4_t vs2, size_t vl);
vfloat64m8_t vfnmsub_vv_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    vfloat64m8_t vs1, vfloat64m8_t vs2, size_t vl);
vfloat64m8_t vfnmsub_vf_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    float64_t rs1, vfloat64m8_t vs2, size_t vl);

```

## Vector Widening Floating-Point Fused Multiply-Add Functions:

### Prototypes:

```

vfloat32m2_t vfwfmac_vv_f32m2 (vfloat32m2_t vd, vfloat16m1_t
    vs1, vfloat16m1_t vs2, size_t vl);
vfloat32m2_t vfwfmac_vf_f32m2 (vfloat32m2_t vd, float16_t vs1,
    vfloat16m1_t vs2, size_t vl);
vfloat32m4_t vfwfmac_vv_f32m4 (vfloat32m4_t vd, vfloat16m2_t
    vs1, vfloat16m2_t vs2, size_t vl);

```



```

vfloat32m4_t vfwmac_vf_f32m4 (vfloat32m4_t vd, float16_t vs1,
    vfloat16m2_t vs2, size_t vl);
vfloat32m8_t vfwmac_vv_f32m8 (vfloat32m8_t vd, vfloat16m4_t
    vs1, vfloat16m4_t vs2, size_t vl);
vfloat32m8_t vfwmac_vf_f32m8 (vfloat32m8_t vd, float16_t vs1,
    vfloat16m4_t vs2, size_t vl);
vfloat64m2_t vfwmac_vv_f64m2 (vfloat64m2_t vd, vfloat32m1_t
    vs1, vfloat32m1_t vs2, size_t vl);
vfloat64m2_t vfwmac_vf_f64m2 (vfloat64m2_t vd, float32_t vs1,
    vfloat32m1_t vs2, size_t vl);
vfloat64m4_t vfwmac_vv_f64m4 (vfloat64m4_t vd, vfloat32m2_t
    vs1, vfloat32m2_t vs2, size_t vl);
vfloat64m4_t vfwmac_vf_f64m4 (vfloat64m4_t vd, float32_t vs1,
    vfloat32m2_t vs2, size_t vl);
vfloat64m8_t vfwmac_vv_f64m8 (vfloat64m8_t vd, vfloat32m4_t
    vs1, vfloat32m4_t vs2, size_t vl);
vfloat64m8_t vfwmac_vf_f64m8 (vfloat64m8_t vd, float32_t vs1,
    vfloat32m4_t vs2, size_t vl);
vfloat32m2_t vfwmac_vv_f32m2 (vfloat32m2_t vd, vfloat16m1_t
    vs1, vfloat16m1_t vs2, size_t vl);
vfloat32m2_t vfwmac_vf_f32m2 (vfloat32m2_t vd, float16_t vs1,
    vfloat16m1_t vs2, size_t vl);
vfloat32m4_t vfwmac_vv_f32m4 (vfloat32m4_t vd, vfloat16m2_t
    vs1, vfloat16m2_t vs2, size_t vl);
vfloat32m4_t vfwmac_vf_f32m4 (vfloat32m4_t vd, float16_t vs1,
    vfloat16m2_t vs2, size_t vl);
vfloat32m8_t vfwmac_vv_f32m8 (vfloat32m8_t vd, vfloat16m4_t
    vs1, vfloat16m4_t vs2, size_t vl);
vfloat32m8_t vfwmac_vf_f32m8 (vfloat32m8_t vd, float16_t vs1,
    vfloat16m4_t vs2, size_t vl);
vfloat64m2_t vfwmac_vv_f64m2 (vfloat64m2_t vd, vfloat32m1_t
    vs1, vfloat32m1_t vs2, size_t vl);
vfloat64m2_t vfwmac_vf_f64m2 (vfloat64m2_t vd, float32_t vs1,
    vfloat32m1_t vs2, size_t vl);
vfloat64m4_t vfwmac_vv_f64m4 (vfloat64m4_t vd, vfloat32m2_t
    vs1, vfloat32m2_t vs2, size_t vl);
vfloat64m4_t vfwmac_vf_f64m4 (vfloat64m4_t vd, float32_t vs1,
    vfloat32m2_t vs2, size_t vl);
vfloat64m8_t vfwmac_vv_f64m8 (vfloat64m8_t vd, vfloat32m4_t
    vs1, vfloat32m4_t vs2, size_t vl);
vfloat64m8_t vfwmac_vf_f64m8 (vfloat64m8_t vd, float32_t vs1,
    vfloat32m4_t vs2, size_t vl);
vfloat32m2_t vfwmsac_vv_f32m2 (vfloat32m2_t vd, vfloat16m1_t
    vs1, vfloat16m1_t vs2, size_t vl);
vfloat32m2_t vfwmsac_vf_f32m2 (vfloat32m2_t vd, float16_t vs1,
    vfloat16m1_t vs2, size_t vl);

```

```

vfloat32m4_t vfwmsac_vv_f32m4 (vfloat32m4_t vd, vfloat16m2_t
    vs1, vfloat16m2_t vs2, size_t vl);
vfloat32m4_t vfwmsac_vf_f32m4 (vfloat32m4_t vd, float16_t vs1,
    vfloat16m2_t vs2, size_t vl);
vfloat32m8_t vfwmsac_vv_f32m8 (vfloat32m8_t vd, vfloat16m4_t
    vs1, vfloat16m4_t vs2, size_t vl);
vfloat32m8_t vfwmsac_vf_f32m8 (vfloat32m8_t vd, float16_t vs1,
    vfloat16m4_t vs2, size_t vl);
vfloat64m2_t vfwmsac_vv_f64m2 (vfloat64m2_t vd, vfloat32m1_t
    vs1, vfloat32m1_t vs2, size_t vl);
vfloat64m2_t vfwmsac_vf_f64m2 (vfloat64m2_t vd, float32_t vs1,
    vfloat32m1_t vs2, size_t vl);
vfloat64m4_t vfwmsac_vv_f64m4 (vfloat64m4_t vd, vfloat32m2_t
    vs1, vfloat32m2_t vs2, size_t vl);
vfloat64m4_t vfwmsac_vf_f64m4 (vfloat64m4_t vd, float32_t vs1,
    vfloat32m2_t vs2, size_t vl);
vfloat64m8_t vfwmsac_vv_f64m8 (vfloat64m8_t vd, vfloat32m4_t
    vs1, vfloat32m4_t vs2, size_t vl);
vfloat64m8_t vfwmsac_vf_f64m8 (vfloat64m8_t vd, float32_t vs1,
    vfloat32m4_t vs2, size_t vl);
vfloat32m2_t vfwmsac_vv_f32m2 (vfloat32m2_t vd, vfloat16m1_t
    vs1, vfloat16m1_t vs2, size_t vl);
vfloat32m2_t vfwmsac_vf_f32m2 (vfloat32m2_t vd, float16_t vs1,
    vfloat16m1_t vs2, size_t vl);
vfloat32m4_t vfwmsac_vv_f32m4 (vfloat32m4_t vd, vfloat16m2_t
    vs1, vfloat16m2_t vs2, size_t vl);
vfloat32m4_t vfwmsac_vf_f32m4 (vfloat32m4_t vd, float16_t vs1,
    vfloat16m2_t vs2, size_t vl);
vfloat32m8_t vfwmsac_vv_f32m8 (vfloat32m8_t vd, vfloat16m4_t
    vs1, vfloat16m4_t vs2, size_t vl);
vfloat32m8_t vfwmsac_vf_f32m8 (vfloat32m8_t vd, float16_t vs1,
    vfloat16m4_t vs2, size_t vl);
vfloat64m2_t vfwmsac_vv_f64m2 (vfloat64m2_t vd, vfloat32m1_t
    vs1, vfloat32m1_t vs2, size_t vl);
vfloat64m2_t vfwmsac_vf_f64m2 (vfloat64m2_t vd, float32_t vs1,
    vfloat32m1_t vs2, size_t vl);
vfloat64m4_t vfwmsac_vv_f64m4 (vfloat64m4_t vd, vfloat32m2_t
    vs1, vfloat32m2_t vs2, size_t vl);
vfloat64m4_t vfwmsac_vf_f64m4 (vfloat64m4_t vd, float32_t vs1,
    vfloat32m2_t vs2, size_t vl);
vfloat64m8_t vfwmsac_vv_f64m8 (vfloat64m8_t vd, vfloat32m4_t
    vs1, vfloat32m4_t vs2, size_t vl);
vfloat64m8_t vfwmsac_vf_f64m8 (vfloat64m8_t vd, float32_t vs1,
    vfloat32m4_t vs2, size_t vl);

```

*// masked functions*

```

vfloat32m2_t vfwmaccc_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    vd, vfloat16m1_t vs1, vfloat16m1_t vs2, size_t vl);
vfloat32m2_t vfwmaccc_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    vd, float16_t vs1, vfloat16m1_t vs2, size_t vl);
vfloat32m4_t vfwmaccc_vv_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    vfloat16m2_t vs1, vfloat16m2_t vs2, size_t vl);
vfloat32m4_t vfwmaccc_vf_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    float16_t vs1, vfloat16m2_t vs2, size_t vl);
vfloat32m8_t vfwmaccc_vv_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    vfloat16m4_t vs1, vfloat16m4_t vs2, size_t vl);
vfloat32m8_t vfwmaccc_vf_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    float16_t vs1, vfloat16m4_t vs2, size_t vl);
vfloat64m2_t vfwmaccc_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    vd, vfloat32m1_t vs1, vfloat32m1_t vs2, size_t vl);
vfloat64m2_t vfwmaccc_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    vd, float32_t vs1, vfloat32m1_t vs2, size_t vl);
vfloat64m4_t vfwmaccc_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    vd, vfloat32m2_t vs1, vfloat32m2_t vs2, size_t vl);
vfloat64m4_t vfwmaccc_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    vd, float32_t vs1, vfloat32m2_t vs2, size_t vl);
vfloat64m8_t vfwmaccc_vv_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    vfloat32m4_t vs1, vfloat32m4_t vs2, size_t vl);
vfloat64m8_t vfwmaccc_vf_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    float32_t vs1, vfloat32m4_t vs2, size_t vl);
vfloat32m2_t vfwnmacc_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    vd, vfloat16m1_t vs1, vfloat16m1_t vs2, size_t vl);
vfloat32m2_t vfwnmacc_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    vd, float16_t vs1, vfloat16m1_t vs2, size_t vl);
vfloat32m4_t vfwnmacc_vv_f32m4_m (vbool8_t mask, vfloat32m4_t
    vd, vfloat16m2_t vs1, vfloat16m2_t vs2, size_t vl);
vfloat32m4_t vfwnmacc_vf_f32m4_m (vbool8_t mask, vfloat32m4_t
    vd, float16_t vs1, vfloat16m2_t vs2, size_t vl);
vfloat32m8_t vfwnmacc_vv_f32m8_m (vbool4_t mask, vfloat32m8_t
    vd, vfloat16m4_t vs1, vfloat16m4_t vs2, size_t vl);
vfloat32m8_t vfwnmacc_vf_f32m8_m (vbool4_t mask, vfloat32m8_t
    vd, float16_t vs1, vfloat16m4_t vs2, size_t vl);
vfloat64m2_t vfwnmacc_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    vd, vfloat32m1_t vs1, vfloat32m1_t vs2, size_t vl);
vfloat64m2_t vfwnmacc_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    vd, float32_t vs1, vfloat32m1_t vs2, size_t vl);
vfloat64m4_t vfwnmacc_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    vd, vfloat32m2_t vs1, vfloat32m2_t vs2, size_t vl);
vfloat64m4_t vfwnmacc_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    vd, float32_t vs1, vfloat32m2_t vs2, size_t vl);
vfloat64m8_t vfwnmacc_vv_f64m8_m (vbool8_t mask, vfloat64m8_t
    vd, vfloat32m4_t vs1, vfloat32m4_t vs2, size_t vl);

```

```

vfloat64m8_t vfwnmacc_vf_f64m8_m (vbool8_t mask, vfloat64m8_t
    vd, float32_t vs1, vfloat32m4_t vs2, size_t vl);
vfloat32m2_t vfwmsac_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    vd, vfloat16m1_t vs1, vfloat16m1_t vs2, size_t vl);
vfloat32m2_t vfwmsac_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    vd, float16_t vs1, vfloat16m1_t vs2, size_t vl);
vfloat32m4_t vfwmsac_vv_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    vfloat16m2_t vs1, vfloat16m2_t vs2, size_t vl);
vfloat32m4_t vfwmsac_vf_f32m4_m (vbool8_t mask, vfloat32m4_t vd,
    float16_t vs1, vfloat16m2_t vs2, size_t vl);
vfloat32m8_t vfwmsac_vv_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    vfloat16m4_t vs1, vfloat16m4_t vs2, size_t vl);
vfloat32m8_t vfwmsac_vf_f32m8_m (vbool4_t mask, vfloat32m8_t vd,
    float16_t vs1, vfloat16m4_t vs2, size_t vl);
vfloat64m2_t vfwmsac_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    vd, vfloat32m1_t vs1, vfloat32m1_t vs2, size_t vl);
vfloat64m2_t vfwmsac_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    vd, float32_t vs1, vfloat32m1_t vs2, size_t vl);
vfloat64m4_t vfwmsac_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    vd, vfloat32m2_t vs1, vfloat32m2_t vs2, size_t vl);
vfloat64m4_t vfwmsac_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    vd, float32_t vs1, vfloat32m2_t vs2, size_t vl);
vfloat64m8_t vfwmsac_vv_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    vfloat32m4_t vs1, vfloat32m4_t vs2, size_t vl);
vfloat64m8_t vfwmsac_vf_f64m8_m (vbool8_t mask, vfloat64m8_t vd,
    float32_t vs1, vfloat32m4_t vs2, size_t vl);
vfloat32m2_t vfwmsac_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    vd, vfloat16m1_t vs1, vfloat16m1_t vs2, size_t vl);
vfloat32m2_t vfwmsac_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    vd, float16_t vs1, vfloat16m1_t vs2, size_t vl);
vfloat32m4_t vfwmsac_vv_f32m4_m (vbool8_t mask, vfloat32m4_t
    vd, vfloat16m2_t vs1, vfloat16m2_t vs2, size_t vl);
vfloat32m4_t vfwmsac_vf_f32m4_m (vbool8_t mask, vfloat32m4_t
    vd, float16_t vs1, vfloat16m2_t vs2, size_t vl);
vfloat32m8_t vfwmsac_vv_f32m8_m (vbool4_t mask, vfloat32m8_t
    vd, vfloat16m4_t vs1, vfloat16m4_t vs2, size_t vl);
vfloat32m8_t vfwmsac_vf_f32m8_m (vbool4_t mask, vfloat32m8_t
    vd, float16_t vs1, vfloat16m4_t vs2, size_t vl);
vfloat64m2_t vfwmsac_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    vd, vfloat32m1_t vs1, vfloat32m1_t vs2, size_t vl);
vfloat64m2_t vfwmsac_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    vd, float32_t vs1, vfloat32m1_t vs2, size_t vl);
vfloat64m4_t vfwmsac_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    vd, vfloat32m2_t vs1, vfloat32m2_t vs2, size_t vl);
vfloat64m4_t vfwmsac_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    vd, float32_t vs1, vfloat32m2_t vs2, size_t vl);

```

```

vfloat64m8_t vfwmsac_vv_f64m8_m (vbool8_t mask, vfloat64m8_t
    vd, vfloat32m4_t vs1, vfloat32m4_t vs2, size_t vl);
vfloat64m8_t vfwmsac_vf_f64m8_m (vbool8_t mask, vfloat64m8_t
    vd, float32_t vs1, vfloat32m4_t vs2, size_t vl);

```

## Vector Floating-Point Square-Root Functions:

### Prototypes:

```

vfloat16m1_t vfsqrt_v_f16m1 (vfloat16m1_t op1, size_t vl);
vfloat16m2_t vfsqrt_v_f16m2 (vfloat16m2_t op1, size_t vl);
vfloat16m4_t vfsqrt_v_f16m4 (vfloat16m4_t op1, size_t vl);
vfloat16m8_t vfsqrt_v_f16m8 (vfloat16m8_t op1, size_t vl);
vfloat32m1_t vfsqrt_v_f32m1 (vfloat32m1_t op1, size_t vl);
vfloat32m2_t vfsqrt_v_f32m2 (vfloat32m2_t op1, size_t vl);
vfloat32m4_t vfsqrt_v_f32m4 (vfloat32m4_t op1, size_t vl);
vfloat32m8_t vfsqrt_v_f32m8 (vfloat32m8_t op1, size_t vl);
vfloat64m1_t vfsqrt_v_f64m1 (vfloat64m1_t op1, size_t vl);
vfloat64m2_t vfsqrt_v_f64m2 (vfloat64m2_t op1, size_t vl);
vfloat64m4_t vfsqrt_v_f64m4 (vfloat64m4_t op1, size_t vl);
vfloat64m8_t vfsqrt_v_f64m8 (vfloat64m8_t op1, size_t vl);
// masked functions
vfloat16m1_t vfsqrt_v_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, size_t vl);
vfloat16m2_t vfsqrt_v_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, size_t vl);
vfloat16m4_t vfsqrt_v_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, size_t vl);
vfloat16m8_t vfsqrt_v_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, size_t vl);
vfloat32m1_t vfsqrt_v_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, size_t vl);
vfloat32m2_t vfsqrt_v_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, size_t vl);
vfloat32m4_t vfsqrt_v_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, size_t vl);
vfloat32m8_t vfsqrt_v_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, size_t vl);
vfloat64m1_t vfsqrt_v_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, size_t vl);
vfloat64m2_t vfsqrt_v_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, size_t vl);
vfloat64m4_t vfsqrt_v_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, size_t vl);
vfloat64m8_t vfsqrt_v_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, size_t vl);

```

## Vector Floating-Point MIN/MAX Functions:

### Prototypes:

```
vfloat16m1_t vfmmin_vv_f16m1 (vfloat16m1_t op1, vfloat16m1_t op2,  
    size_t vl);  
vfloat16m1_t vfmmin_vf_f16m1 (vfloat16m1_t op1, float16_t op2,  
    size_t vl);  
vfloat16m2_t vfmmin_vv_f16m2 (vfloat16m2_t op1, vfloat16m2_t op2,  
    size_t vl);  
vfloat16m2_t vfmmin_vf_f16m2 (vfloat16m2_t op1, float16_t op2,  
    size_t vl);  
vfloat16m4_t vfmmin_vv_f16m4 (vfloat16m4_t op1, vfloat16m4_t op2,  
    size_t vl);  
vfloat16m4_t vfmmin_vf_f16m4 (vfloat16m4_t op1, float16_t op2,  
    size_t vl);  
vfloat16m8_t vfmmin_vv_f16m8 (vfloat16m8_t op1, vfloat16m8_t op2,  
    size_t vl);  
vfloat16m8_t vfmmin_vf_f16m8 (vfloat16m8_t op1, float16_t op2,  
    size_t vl);  
vfloat32m1_t vfmmin_vv_f32m1 (vfloat32m1_t op1, vfloat32m1_t op2,  
    size_t vl);  
vfloat32m1_t vfmmin_vf_f32m1 (vfloat32m1_t op1, float32_t op2,  
    size_t vl);  
vfloat32m2_t vfmmin_vv_f32m2 (vfloat32m2_t op1, vfloat32m2_t op2,  
    size_t vl);  
vfloat32m2_t vfmmin_vf_f32m2 (vfloat32m2_t op1, float32_t op2,  
    size_t vl);  
vfloat32m4_t vfmmin_vv_f32m4 (vfloat32m4_t op1, vfloat32m4_t op2,  
    size_t vl);  
vfloat32m4_t vfmmin_vf_f32m4 (vfloat32m4_t op1, float32_t op2,  
    size_t vl);  
vfloat32m8_t vfmmin_vv_f32m8 (vfloat32m8_t op1, vfloat32m8_t op2,  
    size_t vl);  
vfloat32m8_t vfmmin_vf_f32m8 (vfloat32m8_t op1, float32_t op2,  
    size_t vl);  
vfloat64m1_t vfmmin_vv_f64m1 (vfloat64m1_t op1, vfloat64m1_t op2,  
    size_t vl);  
vfloat64m1_t vfmmin_vf_f64m1 (vfloat64m1_t op1, float64_t op2,  
    size_t vl);  
vfloat64m2_t vfmmin_vv_f64m2 (vfloat64m2_t op1, vfloat64m2_t op2,  
    size_t vl);  
vfloat64m2_t vfmmin_vf_f64m2 (vfloat64m2_t op1, float64_t op2,  
    size_t vl);  
vfloat64m4_t vfmmin_vv_f64m4 (vfloat64m4_t op1, vfloat64m4_t op2,  
    size_t vl);
```

```

vfloat64m4_t vfmmin_vf_f64m4 (vfloat64m4_t op1, float64_t op2,
    size_t vl);
vfloat64m8_t vfmmin_vv_f64m8 (vfloat64m8_t op1, vfloat64m8_t op2,
    size_t vl);
vfloat64m8_t vfmmin_vf_f64m8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);
vfloat16m1_t vfmax_vv_f16m1 (vfloat16m1_t op1, vfloat16m1_t op2,
    size_t vl);
vfloat16m1_t vfmax_vf_f16m1 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vfloat16m2_t vfmax_vv_f16m2 (vfloat16m2_t op1, vfloat16m2_t op2,
    size_t vl);
vfloat16m2_t vfmax_vf_f16m2 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vfloat16m4_t vfmax_vv_f16m4 (vfloat16m4_t op1, vfloat16m4_t op2,
    size_t vl);
vfloat16m4_t vfmax_vf_f16m4 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vfloat16m8_t vfmax_vv_f16m8 (vfloat16m8_t op1, vfloat16m8_t op2,
    size_t vl);
vfloat16m8_t vfmax_vf_f16m8 (vfloat16m8_t op1, float16_t op2,
    size_t vl);
vfloat32m1_t vfmax_vv_f32m1 (vfloat32m1_t op1, vfloat32m1_t op2,
    size_t vl);
vfloat32m1_t vfmax_vf_f32m1 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vfloat32m2_t vfmax_vv_f32m2 (vfloat32m2_t op1, vfloat32m2_t op2,
    size_t vl);
vfloat32m2_t vfmax_vf_f32m2 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vfloat32m4_t vfmax_vv_f32m4 (vfloat32m4_t op1, vfloat32m4_t op2,
    size_t vl);
vfloat32m4_t vfmax_vf_f32m4 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vfloat32m8_t vfmax_vv_f32m8 (vfloat32m8_t op1, vfloat32m8_t op2,
    size_t vl);
vfloat32m8_t vfmax_vf_f32m8 (vfloat32m8_t op1, float32_t op2,
    size_t vl);
vfloat64m1_t vfmax_vv_f64m1 (vfloat64m1_t op1, vfloat64m1_t op2,
    size_t vl);
vfloat64m1_t vfmax_vf_f64m1 (vfloat64m1_t op1, float64_t op2,
    size_t vl);
vfloat64m2_t vfmax_vv_f64m2 (vfloat64m2_t op1, vfloat64m2_t op2,
    size_t vl);
vfloat64m2_t vfmax_vf_f64m2 (vfloat64m2_t op1, float64_t op2,
    size_t vl);

```

```

vfloat64m4_t vfmmax_vv_f64m4 (vfloat64m4_t op1, vfloat64m4_t op2,
    size_t vl);
vfloat64m4_t vfmmax_vf_f64m4 (vfloat64m4_t op1, float64_t op2,
    size_t vl);
vfloat64m8_t vfmmax_vv_f64m8 (vfloat64m8_t op1, vfloat64m8_t op2,
    size_t vl);
vfloat64m8_t vfmmax_vf_f64m8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);
// masked functions
vfloat16m1_t vfmmin_vv_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vfloat16m1_t vfmmin_vf_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vfloat16m2_t vfmmin_vv_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vfloat16m2_t vfmmin_vf_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, float16_t op2, size_t vl);
vfloat16m4_t vfmmin_vv_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vfloat16m4_t vfmmin_vf_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, float16_t op2, size_t vl);
vfloat16m8_t vfmmin_vv_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, vfloat16m8_t op2, size_t vl);
vfloat16m8_t vfmmin_vf_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, float16_t op2, size_t vl);
vfloat32m1_t vfmmin_vv_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vfloat32m1_t vfmmin_vf_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vfloat32m2_t vfmmin_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vfloat32m2_t vfmmin_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vfloat32m4_t vfmmin_vv_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vfloat32m4_t vfmmin_vf_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, float32_t op2, size_t vl);
vfloat32m8_t vfmmin_vv_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, vfloat32m8_t op2, size_t vl);
vfloat32m8_t vfmmin_vf_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, float32_t op2, size_t vl);
vfloat64m1_t vfmmin_vv_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, vfloat64m1_t op2, size_t vl);
vfloat64m1_t vfmmin_vf_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);

```



```

vfloat64m2_t vfmmin_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, vfloat64m2_t op2, size_t vl);
vfloat64m2_t vfmmin_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);
vfloat64m4_t vfmmin_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, vfloat64m4_t op2, size_t vl);
vfloat64m4_t vfmmin_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vfloat64m8_t vfmmin_vv_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, vfloat64m8_t op2, size_t vl);
vfloat64m8_t vfmmin_vf_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, float64_t op2, size_t vl);
vfloat16m1_t vfmmax_vv_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vfloat16m1_t vfmmax_vf_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vfloat16m2_t vfmmax_vv_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vfloat16m2_t vfmmax_vf_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, float16_t op2, size_t vl);
vfloat16m4_t vfmmax_vv_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vfloat16m4_t vfmmax_vf_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, float16_t op2, size_t vl);
vfloat16m8_t vfmmax_vv_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, vfloat16m8_t op2, size_t vl);
vfloat16m8_t vfmmax_vf_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, float16_t op2, size_t vl);
vfloat32m1_t vfmmax_vv_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vfloat32m1_t vfmmax_vf_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vfloat32m2_t vfmmax_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vfloat32m2_t vfmmax_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vfloat32m4_t vfmmax_vv_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vfloat32m4_t vfmmax_vf_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, float32_t op2, size_t vl);
vfloat32m8_t vfmmax_vv_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, vfloat32m8_t op2, size_t vl);
vfloat32m8_t vfmmax_vf_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, float32_t op2, size_t vl);
vfloat64m1_t vfmmax_vv_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, vfloat64m1_t op2, size_t vl);

```

```

vfloat64m1_t vfmmax_vf_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);
vfloat64m2_t vfmmax_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, vfloat64m2_t op2, size_t vl);
vfloat64m2_t vfmmax_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);
vfloat64m4_t vfmmax_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, vfloat64m4_t op2, size_t vl);
vfloat64m4_t vfmmax_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vfloat64m8_t vfmmax_vv_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, vfloat64m8_t op2, size_t vl);
vfloat64m8_t vfmmax_vf_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, float64_t op2, size_t vl);

```

### Vector Floating-Point Sign-Injection Functions:

#### Prototypes:

```

vfloat16m1_t vfsgnj_vv_f16m1 (vfloat16m1_t op1, vfloat16m1_t
    op2, size_t vl);
vfloat16m1_t vfsgnj_vf_f16m1 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vfloat16m2_t vfsgnj_vv_f16m2 (vfloat16m2_t op1, vfloat16m2_t
    op2, size_t vl);
vfloat16m2_t vfsgnj_vf_f16m2 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vfloat16m4_t vfsgnj_vv_f16m4 (vfloat16m4_t op1, vfloat16m4_t
    op2, size_t vl);
vfloat16m4_t vfsgnj_vf_f16m4 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vfloat16m8_t vfsgnj_vv_f16m8 (vfloat16m8_t op1, vfloat16m8_t
    op2, size_t vl);
vfloat16m8_t vfsgnj_vf_f16m8 (vfloat16m8_t op1, float16_t op2,
    size_t vl);
vfloat32m1_t vfsgnj_vv_f32m1 (vfloat32m1_t op1, vfloat32m1_t
    op2, size_t vl);
vfloat32m1_t vfsgnj_vf_f32m1 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vfloat32m2_t vfsgnj_vv_f32m2 (vfloat32m2_t op1, vfloat32m2_t
    op2, size_t vl);
vfloat32m2_t vfsgnj_vf_f32m2 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vfloat32m4_t vfsgnj_vv_f32m4 (vfloat32m4_t op1, vfloat32m4_t
    op2, size_t vl);

```

```

vfloat32m4_t vfsgnj_vf_f32m4 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vfloat32m8_t vfsgnj_vv_f32m8 (vfloat32m8_t op1, vfloat32m8_t
    op2, size_t vl);
vfloat32m8_t vfsgnj_vf_f32m8 (vfloat32m8_t op1, float32_t op2,
    size_t vl);
vfloat64m1_t vfsgnj_vv_f64m1 (vfloat64m1_t op1, vfloat64m1_t
    op2, size_t vl);
vfloat64m1_t vfsgnj_vf_f64m1 (vfloat64m1_t op1, float64_t op2,
    size_t vl);
vfloat64m2_t vfsgnj_vv_f64m2 (vfloat64m2_t op1, vfloat64m2_t
    op2, size_t vl);
vfloat64m2_t vfsgnj_vf_f64m2 (vfloat64m2_t op1, float64_t op2,
    size_t vl);
vfloat64m4_t vfsgnj_vv_f64m4 (vfloat64m4_t op1, vfloat64m4_t
    op2, size_t vl);
vfloat64m4_t vfsgnj_vf_f64m4 (vfloat64m4_t op1, float64_t op2,
    size_t vl);
vfloat64m8_t vfsgnj_vv_f64m8 (vfloat64m8_t op1, vfloat64m8_t
    op2, size_t vl);
vfloat64m8_t vfsgnj_vf_f64m8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);
vfloat16m1_t vfsgnjn_vv_f16m1 (vfloat16m1_t op1, vfloat16m1_t
    op2, size_t vl);
vfloat16m1_t vfsgnjn_vf_f16m1 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vfloat16m2_t vfsgnjn_vv_f16m2 (vfloat16m2_t op1, vfloat16m2_t
    op2, size_t vl);
vfloat16m2_t vfsgnjn_vf_f16m2 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vfloat16m4_t vfsgnjn_vv_f16m4 (vfloat16m4_t op1, vfloat16m4_t
    op2, size_t vl);
vfloat16m4_t vfsgnjn_vf_f16m4 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vfloat16m8_t vfsgnjn_vv_f16m8 (vfloat16m8_t op1, vfloat16m8_t
    op2, size_t vl);
vfloat16m8_t vfsgnjn_vf_f16m8 (vfloat16m8_t op1, float16_t op2,
    size_t vl);
vfloat32m1_t vfsgnjn_vv_f32m1 (vfloat32m1_t op1, vfloat32m1_t
    op2, size_t vl);
vfloat32m1_t vfsgnjn_vf_f32m1 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vfloat32m2_t vfsgnjn_vv_f32m2 (vfloat32m2_t op1, vfloat32m2_t
    op2, size_t vl);
vfloat32m2_t vfsgnjn_vf_f32m2 (vfloat32m2_t op1, float32_t op2,
    size_t vl);

```

```

vfloat32m4_t vfsgnvn_vv_f32m4 (vfloat32m4_t op1, vfloat32m4_t
    op2, size_t vl);
vfloat32m4_t vfsgnvn_vf_f32m4 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vfloat32m8_t vfsgnvn_vv_f32m8 (vfloat32m8_t op1, vfloat32m8_t
    op2, size_t vl);
vfloat32m8_t vfsgnvn_vf_f32m8 (vfloat32m8_t op1, float32_t op2,
    size_t vl);
vfloat64m1_t vfsgnvn_vv_f64m1 (vfloat64m1_t op1, vfloat64m1_t
    op2, size_t vl);
vfloat64m1_t vfsgnvn_vf_f64m1 (vfloat64m1_t op1, float64_t op2,
    size_t vl);
vfloat64m2_t vfsgnvn_vv_f64m2 (vfloat64m2_t op1, vfloat64m2_t
    op2, size_t vl);
vfloat64m2_t vfsgnvn_vf_f64m2 (vfloat64m2_t op1, float64_t op2,
    size_t vl);
vfloat64m4_t vfsgnvn_vv_f64m4 (vfloat64m4_t op1, vfloat64m4_t
    op2, size_t vl);
vfloat64m4_t vfsgnvn_vf_f64m4 (vfloat64m4_t op1, float64_t op2,
    size_t vl);
vfloat64m8_t vfsgnvn_vv_f64m8 (vfloat64m8_t op1, vfloat64m8_t
    op2, size_t vl);
vfloat64m8_t vfsgnvn_vf_f64m8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);
vfloat16m1_t vfsgnvn_vv_f16m1 (vfloat16m1_t op1, vfloat16m1_t
    op2, size_t vl);
vfloat16m1_t vfsgnvn_vf_f16m1 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vfloat16m2_t vfsgnvn_vv_f16m2 (vfloat16m2_t op1, vfloat16m2_t
    op2, size_t vl);
vfloat16m2_t vfsgnvn_vf_f16m2 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vfloat16m4_t vfsgnvn_vv_f16m4 (vfloat16m4_t op1, vfloat16m4_t
    op2, size_t vl);
vfloat16m4_t vfsgnvn_vf_f16m4 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vfloat16m8_t vfsgnvn_vv_f16m8 (vfloat16m8_t op1, vfloat16m8_t
    op2, size_t vl);
vfloat16m8_t vfsgnvn_vf_f16m8 (vfloat16m8_t op1, float16_t op2,
    size_t vl);
vfloat32m1_t vfsgnvn_vv_f32m1 (vfloat32m1_t op1, vfloat32m1_t
    op2, size_t vl);
vfloat32m1_t vfsgnvn_vf_f32m1 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vfloat32m2_t vfsgnvn_vv_f32m2 (vfloat32m2_t op1, vfloat32m2_t
    op2, size_t vl);

```

```

vfloat32m2_t vfsgnjx_vf_f32m2 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vfloat32m4_t vfsgnjx_vv_f32m4 (vfloat32m4_t op1, vfloat32m4_t
    op2, size_t vl);
vfloat32m4_t vfsgnjx_vf_f32m4 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vfloat32m8_t vfsgnjx_vv_f32m8 (vfloat32m8_t op1, vfloat32m8_t
    op2, size_t vl);
vfloat32m8_t vfsgnjx_vf_f32m8 (vfloat32m8_t op1, float32_t op2,
    size_t vl);
vfloat64m1_t vfsgnjx_vv_f64m1 (vfloat64m1_t op1, vfloat64m1_t
    op2, size_t vl);
vfloat64m1_t vfsgnjx_vf_f64m1 (vfloat64m1_t op1, float64_t op2,
    size_t vl);
vfloat64m2_t vfsgnjx_vv_f64m2 (vfloat64m2_t op1, vfloat64m2_t
    op2, size_t vl);
vfloat64m2_t vfsgnjx_vf_f64m2 (vfloat64m2_t op1, float64_t op2,
    size_t vl);
vfloat64m4_t vfsgnjx_vv_f64m4 (vfloat64m4_t op1, vfloat64m4_t
    op2, size_t vl);
vfloat64m4_t vfsgnjx_vf_f64m4 (vfloat64m4_t op1, float64_t op2,
    size_t vl);
vfloat64m8_t vfsgnjx_vv_f64m8 (vfloat64m8_t op1, vfloat64m8_t
    op2, size_t vl);
vfloat64m8_t vfsgnjx_vf_f64m8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);
// masked functions
vfloat16m1_t vfsgnj_vv_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vfloat16m1_t vfsgnj_vf_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vfloat16m2_t vfsgnj_vv_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vfloat16m2_t vfsgnj_vf_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, float16_t op2, size_t vl);
vfloat16m4_t vfsgnj_vv_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vfloat16m4_t vfsgnj_vf_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, float16_t op2, size_t vl);
vfloat16m8_t vfsgnj_vv_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, vfloat16m8_t op2, size_t vl);
vfloat16m8_t vfsgnj_vf_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, float16_t op2, size_t vl);
vfloat32m1_t vfsgnj_vv_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);

```

```

vfloat32m1_t vfsgnj_vf_f32m1_m (vbool32_t mask, vfloat32m1_t
maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vfloat32m2_t vfsgnj_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vfloat32m2_t vfsgnj_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vfloat32m4_t vfsgnj_vv_f32m4_m (vbool8_t mask, vfloat32m4_t
maskedoff, vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vfloat32m4_t vfsgnj_vf_f32m4_m (vbool8_t mask, vfloat32m4_t
maskedoff, vfloat32m4_t op1, float32_t op2, size_t vl);
vfloat32m8_t vfsgnj_vv_f32m8_m (vbool4_t mask, vfloat32m8_t
maskedoff, vfloat32m8_t op1, vfloat32m8_t op2, size_t vl);
vfloat32m8_t vfsgnj_vf_f32m8_m (vbool4_t mask, vfloat32m8_t
maskedoff, vfloat32m8_t op1, float32_t op2, size_t vl);
vfloat64m1_t vfsgnj_vv_f64m1_m (vbool64_t mask, vfloat64m1_t
maskedoff, vfloat64m1_t op1, vfloat64m1_t op2, size_t vl);
vfloat64m1_t vfsgnj_vf_f64m1_m (vbool64_t mask, vfloat64m1_t
maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);
vfloat64m2_t vfsgnj_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
maskedoff, vfloat64m2_t op1, vfloat64m2_t op2, size_t vl);
vfloat64m2_t vfsgnj_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);
vfloat64m4_t vfsgnj_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
maskedoff, vfloat64m4_t op1, vfloat64m4_t op2, size_t vl);
vfloat64m4_t vfsgnj_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vfloat64m8_t vfsgnj_vv_f64m8_m (vbool8_t mask, vfloat64m8_t
maskedoff, vfloat64m8_t op1, vfloat64m8_t op2, size_t vl);
vfloat64m8_t vfsgnj_vf_f64m8_m (vbool8_t mask, vfloat64m8_t
maskedoff, vfloat64m8_t op1, float64_t op2, size_t vl);
vfloat16m1_t vfsgnjn_vv_f16m1_m (vbool16_t mask, vfloat16m1_t
maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vfloat16m1_t vfsgnjn_vf_f16m1_m (vbool16_t mask, vfloat16m1_t
maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vfloat16m2_t vfsgnjn_vv_f16m2_m (vbool8_t mask, vfloat16m2_t
maskedoff, vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vfloat16m2_t vfsgnjn_vf_f16m2_m (vbool8_t mask, vfloat16m2_t
maskedoff, vfloat16m2_t op1, float16_t op2, size_t vl);
vfloat16m4_t vfsgnjn_vv_f16m4_m (vbool4_t mask, vfloat16m4_t
maskedoff, vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vfloat16m4_t vfsgnjn_vf_f16m4_m (vbool4_t mask, vfloat16m4_t
maskedoff, vfloat16m4_t op1, float16_t op2, size_t vl);
vfloat16m8_t vfsgnjn_vv_f16m8_m (vbool2_t mask, vfloat16m8_t
maskedoff, vfloat16m8_t op1, vfloat16m8_t op2, size_t vl);
vfloat16m8_t vfsgnjn_vf_f16m8_m (vbool2_t mask, vfloat16m8_t
maskedoff, vfloat16m8_t op1, float16_t op2, size_t vl);

```

```

vfloat32m1_t vfsgnvn_vv_f32m1_m (vbool32_t mask, vfloat32m1_t
maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vfloat32m1_t vfsgnvn_vf_f32m1_m (vbool32_t mask, vfloat32m1_t
maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vfloat32m2_t vfsgnvn_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vfloat32m2_t vfsgnvn_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vfloat32m4_t vfsgnvn_vv_f32m4_m (vbool8_t mask, vfloat32m4_t
maskedoff, vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vfloat32m4_t vfsgnvn_vf_f32m4_m (vbool8_t mask, vfloat32m4_t
maskedoff, vfloat32m4_t op1, float32_t op2, size_t vl);
vfloat32m8_t vfsgnvn_vv_f32m8_m (vbool4_t mask, vfloat32m8_t
maskedoff, vfloat32m8_t op1, vfloat32m8_t op2, size_t vl);
vfloat32m8_t vfsgnvn_vf_f32m8_m (vbool4_t mask, vfloat32m8_t
maskedoff, vfloat32m8_t op1, float32_t op2, size_t vl);
vfloat64m1_t vfsgnvn_vv_f64m1_m (vbool64_t mask, vfloat64m1_t
maskedoff, vfloat64m1_t op1, vfloat64m1_t op2, size_t vl);
vfloat64m1_t vfsgnvn_vf_f64m1_m (vbool64_t mask, vfloat64m1_t
maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);
vfloat64m2_t vfsgnvn_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
maskedoff, vfloat64m2_t op1, vfloat64m2_t op2, size_t vl);
vfloat64m2_t vfsgnvn_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);
vfloat64m4_t vfsgnvn_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
maskedoff, vfloat64m4_t op1, vfloat64m4_t op2, size_t vl);
vfloat64m4_t vfsgnvn_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vfloat64m8_t vfsgnvn_vv_f64m8_m (vbool8_t mask, vfloat64m8_t
maskedoff, vfloat64m8_t op1, vfloat64m8_t op2, size_t vl);
vfloat64m8_t vfsgnvn_vf_f64m8_m (vbool8_t mask, vfloat64m8_t
maskedoff, vfloat64m8_t op1, float64_t op2, size_t vl);
vfloat16m1_t vfsgnvn_vv_f16m1_m (vbool16_t mask, vfloat16m1_t
maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vfloat16m1_t vfsgnvn_vf_f16m1_m (vbool16_t mask, vfloat16m1_t
maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vfloat16m2_t vfsgnvn_vv_f16m2_m (vbool8_t mask, vfloat16m2_t
maskedoff, vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vfloat16m2_t vfsgnvn_vf_f16m2_m (vbool8_t mask, vfloat16m2_t
maskedoff, vfloat16m2_t op1, float16_t op2, size_t vl);
vfloat16m4_t vfsgnvn_vv_f16m4_m (vbool4_t mask, vfloat16m4_t
maskedoff, vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vfloat16m4_t vfsgnvn_vf_f16m4_m (vbool4_t mask, vfloat16m4_t
maskedoff, vfloat16m4_t op1, float16_t op2, size_t vl);
vfloat16m8_t vfsgnvn_vv_f16m8_m (vbool2_t mask, vfloat16m8_t
maskedoff, vfloat16m8_t op1, vfloat16m8_t op2, size_t vl);

```

```

vfloat16m8_t vfsgnjx_vf_f16m8_m (vbool2_t mask, vfloat16m8_t
maskedoff, vfloat16m8_t op1, float16_t op2, size_t vl);
vfloat32m1_t vfsgnjx_vv_f32m1_m (vbool32_t mask, vfloat32m1_t
maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vfloat32m1_t vfsgnjx_vf_f32m1_m (vbool32_t mask, vfloat32m1_t
maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vfloat32m2_t vfsgnjx_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vfloat32m2_t vfsgnjx_vf_f32m2_m (vbool16_t mask, vfloat32m2_t
maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vfloat32m4_t vfsgnjx_vv_f32m4_m (vbool8_t mask, vfloat32m4_t
maskedoff, vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vfloat32m4_t vfsgnjx_vf_f32m4_m (vbool8_t mask, vfloat32m4_t
maskedoff, vfloat32m4_t op1, float32_t op2, size_t vl);
vfloat32m8_t vfsgnjx_vv_f32m8_m (vbool4_t mask, vfloat32m8_t
maskedoff, vfloat32m8_t op1, vfloat32m8_t op2, size_t vl);
vfloat32m8_t vfsgnjx_vf_f32m8_m (vbool4_t mask, vfloat32m8_t
maskedoff, vfloat32m8_t op1, float32_t op2, size_t vl);
vfloat64m1_t vfsgnjx_vv_f64m1_m (vbool64_t mask, vfloat64m1_t
maskedoff, vfloat64m1_t op1, vfloat64m1_t op2, size_t vl);
vfloat64m1_t vfsgnjx_vf_f64m1_m (vbool64_t mask, vfloat64m1_t
maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);
vfloat64m2_t vfsgnjx_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
maskedoff, vfloat64m2_t op1, vfloat64m2_t op2, size_t vl);
vfloat64m2_t vfsgnjx_vf_f64m2_m (vbool32_t mask, vfloat64m2_t
maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);
vfloat64m4_t vfsgnjx_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
maskedoff, vfloat64m4_t op1, vfloat64m4_t op2, size_t vl);
vfloat64m4_t vfsgnjx_vf_f64m4_m (vbool16_t mask, vfloat64m4_t
maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vfloat64m8_t vfsgnjx_vv_f64m8_m (vbool8_t mask, vfloat64m8_t
maskedoff, vfloat64m8_t op1, vfloat64m8_t op2, size_t vl);
vfloat64m8_t vfsgnjx_vf_f64m8_m (vbool8_t mask, vfloat64m8_t
maskedoff, vfloat64m8_t op1, float64_t op2, size_t vl);

```

## Vector Floating-Point Absolute Value Functions:

### Prototypes:

```

vfloat16m1_t vfabs_v_f16m1 (vfloat16m1_t op1, size_t vl);
vfloat16m2_t vfabs_v_f16m2 (vfloat16m2_t op1, size_t vl);
vfloat16m4_t vfabs_v_f16m4 (vfloat16m4_t op1, size_t vl);
vfloat16m8_t vfabs_v_f16m8 (vfloat16m8_t op1, size_t vl);
vfloat32m1_t vfabs_v_f32m1 (vfloat32m1_t op1, size_t vl);
vfloat32m2_t vfabs_v_f32m2 (vfloat32m2_t op1, size_t vl);
vfloat32m4_t vfabs_v_f32m4 (vfloat32m4_t op1, size_t vl);

```



```

vfloat32m8_t vfabs_v_f32m8 (vfloat32m8_t op1, size_t vl);
vfloat64m1_t vfabs_v_f64m1 (vfloat64m1_t op1, size_t vl);
vfloat64m2_t vfabs_v_f64m2 (vfloat64m2_t op1, size_t vl);
vfloat64m4_t vfabs_v_f64m4 (vfloat64m4_t op1, size_t vl);
vfloat64m8_t vfabs_v_f64m8 (vfloat64m8_t op1, size_t vl);
// masked functions
vfloat16m1_t vfabs_v_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, size_t vl);
vfloat16m2_t vfabs_v_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, size_t vl);
vfloat16m4_t vfabs_v_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, size_t vl);
vfloat16m8_t vfabs_v_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, size_t vl);
vfloat32m1_t vfabs_v_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, size_t vl);
vfloat32m2_t vfabs_v_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, size_t vl);
vfloat32m4_t vfabs_v_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, size_t vl);
vfloat32m8_t vfabs_v_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, size_t vl);
vfloat64m1_t vfabs_v_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, size_t vl);
vfloat64m2_t vfabs_v_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, size_t vl);
vfloat64m4_t vfabs_v_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, size_t vl);
vfloat64m8_t vfabs_v_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, size_t vl);

```

## Vector Floating-Point Compare Functions:

### Prototypes:

```

vbool16_t vmfeq_vv_f16m1_b16 (vfloat16m1_t op1, vfloat16m1_t
    op2, size_t vl);
vbool16_t vmfeq_vf_f16m1_b16 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vbool8_t vmfeq_vv_f16m2_b8 (vfloat16m2_t op1, vfloat16m2_t op2,
    size_t vl);
vbool8_t vmfeq_vf_f16m2_b8 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vbool4_t vmfeq_vv_f16m4_b4 (vfloat16m4_t op1, vfloat16m4_t op2,
    size_t vl);

```

```

vbool4_t vmfeq_vf_f16m4_b4 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vbool2_t vmfeq_vv_f16m8_b2 (vfloat16m8_t op1, vfloat16m8_t op2,
    size_t vl);
vbool2_t vmfeq_vf_f16m8_b2 (vfloat16m8_t op1, float16_t op2,
    size_t vl);
vbool32_t vmfeq_vv_f32m1_b32 (vfloat32m1_t op1, vfloat32m1_t
    op2, size_t vl);
vbool32_t vmfeq_vf_f32m1_b32 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vbool16_t vmfeq_vv_f32m2_b16 (vfloat32m2_t op1, vfloat32m2_t
    op2, size_t vl);
vbool16_t vmfeq_vf_f32m2_b16 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vbool8_t vmfeq_vv_f32m4_b8 (vfloat32m4_t op1, vfloat32m4_t op2,
    size_t vl);
vbool8_t vmfeq_vf_f32m4_b8 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vbool4_t vmfeq_vv_f32m8_b4 (vfloat32m8_t op1, vfloat32m8_t op2,
    size_t vl);
vbool4_t vmfeq_vf_f32m8_b4 (vfloat32m8_t op1, float32_t op2,
    size_t vl);
vbool64_t vmfeq_vv_f64m1_b64 (vfloat64m1_t op1, vfloat64m1_t
    op2, size_t vl);
vbool64_t vmfeq_vf_f64m1_b64 (vfloat64m1_t op1, float64_t op2,
    size_t vl);
vbool32_t vmfeq_vv_f64m2_b32 (vfloat64m2_t op1, vfloat64m2_t
    op2, size_t vl);
vbool32_t vmfeq_vf_f64m2_b32 (vfloat64m2_t op1, float64_t op2,
    size_t vl);
vbool16_t vmfeq_vv_f64m4_b16 (vfloat64m4_t op1, vfloat64m4_t
    op2, size_t vl);
vbool16_t vmfeq_vf_f64m4_b16 (vfloat64m4_t op1, float64_t op2,
    size_t vl);
vbool8_t vmfeq_vv_f64m8_b8 (vfloat64m8_t op1, vfloat64m8_t op2,
    size_t vl);
vbool8_t vmfeq_vf_f64m8_b8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);
vbool16_t vmfne_vv_f16m1_b16 (vfloat16m1_t op1, vfloat16m1_t
    op2, size_t vl);
vbool16_t vmfne_vf_f16m1_b16 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vbool8_t vmfne_vv_f16m2_b8 (vfloat16m2_t op1, vfloat16m2_t op2,
    size_t vl);
vbool8_t vmfne_vf_f16m2_b8 (vfloat16m2_t op1, float16_t op2,
    size_t vl);

```

```

vbool4_t vmfne_vv_f16m4_b4 (vfloat16m4_t op1, vfloat16m4_t op2,
    size_t vl);
vbool4_t vmfne_vf_f16m4_b4 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vbool2_t vmfne_vv_f16m8_b2 (vfloat16m8_t op1, vfloat16m8_t op2,
    size_t vl);
vbool2_t vmfne_vf_f16m8_b2 (vfloat16m8_t op1, float16_t op2,
    size_t vl);
vbool32_t vmfne_vv_f32m1_b32 (vfloat32m1_t op1, vfloat32m1_t
    op2, size_t vl);
vbool32_t vmfne_vf_f32m1_b32 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vbool16_t vmfne_vv_f32m2_b16 (vfloat32m2_t op1, vfloat32m2_t
    op2, size_t vl);
vbool16_t vmfne_vf_f32m2_b16 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vbool8_t vmfne_vv_f32m4_b8 (vfloat32m4_t op1, vfloat32m4_t op2,
    size_t vl);
vbool8_t vmfne_vf_f32m4_b8 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vbool4_t vmfne_vv_f32m8_b4 (vfloat32m8_t op1, vfloat32m8_t op2,
    size_t vl);
vbool4_t vmfne_vf_f32m8_b4 (vfloat32m8_t op1, float32_t op2,
    size_t vl);
vbool64_t vmfne_vv_f64m1_b64 (vfloat64m1_t op1, vfloat64m1_t
    op2, size_t vl);
vbool64_t vmfne_vf_f64m1_b64 (vfloat64m1_t op1, float64_t op2,
    size_t vl);
vbool32_t vmfne_vv_f64m2_b32 (vfloat64m2_t op1, vfloat64m2_t
    op2, size_t vl);
vbool32_t vmfne_vf_f64m2_b32 (vfloat64m2_t op1, float64_t op2,
    size_t vl);
vbool16_t vmfne_vv_f64m4_b16 (vfloat64m4_t op1, vfloat64m4_t
    op2, size_t vl);
vbool16_t vmfne_vf_f64m4_b16 (vfloat64m4_t op1, float64_t op2,
    size_t vl);
vbool8_t vmfne_vv_f64m8_b8 (vfloat64m8_t op1, vfloat64m8_t op2,
    size_t vl);
vbool8_t vmfne_vf_f64m8_b8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);
vbool16_t vmflt_vv_f16m1_b16 (vfloat16m1_t op1, vfloat16m1_t
    op2, size_t vl);
vbool16_t vmflt_vf_f16m1_b16 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vbool8_t vmflt_vv_f16m2_b8 (vfloat16m2_t op1, vfloat16m2_t op2,
    size_t vl);

```

```

vbool8_t vmflt_vf_f16m2_b8 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vbool4_t vmflt_vv_f16m4_b4 (vfloat16m4_t op1, vfloat16m4_t op2,
    size_t vl);
vbool4_t vmflt_vf_f16m4_b4 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vbool2_t vmflt_vv_f16m8_b2 (vfloat16m8_t op1, vfloat16m8_t op2,
    size_t vl);
vbool2_t vmflt_vf_f16m8_b2 (vfloat16m8_t op1, float16_t op2,
    size_t vl);
vbool32_t vmflt_vv_f32m1_b32 (vfloat32m1_t op1, vfloat32m1_t
    op2, size_t vl);
vbool32_t vmflt_vf_f32m1_b32 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vbool16_t vmflt_vv_f32m2_b16 (vfloat32m2_t op1, vfloat32m2_t
    op2, size_t vl);
vbool16_t vmflt_vf_f32m2_b16 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vbool8_t vmflt_vv_f32m4_b8 (vfloat32m4_t op1, vfloat32m4_t op2,
    size_t vl);
vbool8_t vmflt_vf_f32m4_b8 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vbool4_t vmflt_vv_f32m8_b4 (vfloat32m8_t op1, vfloat32m8_t op2,
    size_t vl);
vbool4_t vmflt_vf_f32m8_b4 (vfloat32m8_t op1, float32_t op2,
    size_t vl);
vbool64_t vmflt_vv_f64m1_b64 (vfloat64m1_t op1, vfloat64m1_t
    op2, size_t vl);
vbool64_t vmflt_vf_f64m1_b64 (vfloat64m1_t op1, float64_t op2,
    size_t vl);
vbool32_t vmflt_vv_f64m2_b32 (vfloat64m2_t op1, vfloat64m2_t
    op2, size_t vl);
vbool32_t vmflt_vf_f64m2_b32 (vfloat64m2_t op1, float64_t op2,
    size_t vl);
vbool16_t vmflt_vv_f64m4_b16 (vfloat64m4_t op1, vfloat64m4_t
    op2, size_t vl);
vbool16_t vmflt_vf_f64m4_b16 (vfloat64m4_t op1, float64_t op2,
    size_t vl);
vbool8_t vmflt_vv_f64m8_b8 (vfloat64m8_t op1, vfloat64m8_t op2,
    size_t vl);
vbool8_t vmflt_vf_f64m8_b8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);
vbool16_t vmfle_vv_f16m1_b16 (vfloat16m1_t op1, vfloat16m1_t
    op2, size_t vl);
vbool16_t vmfle_vf_f16m1_b16 (vfloat16m1_t op1, float16_t op2,
    size_t vl);

```

```

vbool8_t vmfle_vv_f16m2_b8 (vfloat16m2_t op1, vfloat16m2_t op2,
    size_t vl);
vbool8_t vmfle_vf_f16m2_b8 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vbool4_t vmfle_vv_f16m4_b4 (vfloat16m4_t op1, vfloat16m4_t op2,
    size_t vl);
vbool4_t vmfle_vf_f16m4_b4 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vbool2_t vmfle_vv_f16m8_b2 (vfloat16m8_t op1, vfloat16m8_t op2,
    size_t vl);
vbool2_t vmfle_vf_f16m8_b2 (vfloat16m8_t op1, float16_t op2,
    size_t vl);
vbool32_t vmfle_vv_f32m1_b32 (vfloat32m1_t op1, vfloat32m1_t
    op2, size_t vl);
vbool32_t vmfle_vf_f32m1_b32 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vbool16_t vmfle_vv_f32m2_b16 (vfloat32m2_t op1, vfloat32m2_t
    op2, size_t vl);
vbool16_t vmfle_vf_f32m2_b16 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vbool8_t vmfle_vv_f32m4_b8 (vfloat32m4_t op1, vfloat32m4_t op2,
    size_t vl);
vbool8_t vmfle_vf_f32m4_b8 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vbool4_t vmfle_vv_f32m8_b4 (vfloat32m8_t op1, vfloat32m8_t op2,
    size_t vl);
vbool4_t vmfle_vf_f32m8_b4 (vfloat32m8_t op1, float32_t op2,
    size_t vl);
vbool64_t vmfle_vv_f64m1_b64 (vfloat64m1_t op1, vfloat64m1_t
    op2, size_t vl);
vbool64_t vmfle_vf_f64m1_b64 (vfloat64m1_t op1, float64_t op2,
    size_t vl);
vbool32_t vmfle_vv_f64m2_b32 (vfloat64m2_t op1, vfloat64m2_t
    op2, size_t vl);
vbool32_t vmfle_vf_f64m2_b32 (vfloat64m2_t op1, float64_t op2,
    size_t vl);
vbool16_t vmfle_vv_f64m4_b16 (vfloat64m4_t op1, vfloat64m4_t
    op2, size_t vl);
vbool16_t vmfle_vf_f64m4_b16 (vfloat64m4_t op1, float64_t op2,
    size_t vl);
vbool8_t vmfle_vv_f64m8_b8 (vfloat64m8_t op1, vfloat64m8_t op2,
    size_t vl);
vbool8_t vmfle_vf_f64m8_b8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);
vbool16_t vmfgt_vv_f16m1_b16 (vfloat16m1_t op1, vfloat16m1_t
    op2, size_t vl);

```

```

vbool16_t vmfgt_vf_f16m1_b16 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vbool8_t vmfgt_vv_f16m2_b8 (vfloat16m2_t op1, vfloat16m2_t op2,
    size_t vl);
vbool8_t vmfgt_vf_f16m2_b8 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vbool4_t vmfgt_vv_f16m4_b4 (vfloat16m4_t op1, vfloat16m4_t op2,
    size_t vl);
vbool4_t vmfgt_vf_f16m4_b4 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vbool2_t vmfgt_vv_f16m8_b2 (vfloat16m8_t op1, vfloat16m8_t op2,
    size_t vl);
vbool2_t vmfgt_vf_f16m8_b2 (vfloat16m8_t op1, float16_t op2,
    size_t vl);
vbool32_t vmfgt_vv_f32m1_b32 (vfloat32m1_t op1, vfloat32m1_t
    op2, size_t vl);
vbool32_t vmfgt_vf_f32m1_b32 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vbool16_t vmfgt_vv_f32m2_b16 (vfloat32m2_t op1, vfloat32m2_t
    op2, size_t vl);
vbool16_t vmfgt_vf_f32m2_b16 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vbool8_t vmfgt_vv_f32m4_b8 (vfloat32m4_t op1, vfloat32m4_t op2,
    size_t vl);
vbool8_t vmfgt_vf_f32m4_b8 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vbool4_t vmfgt_vv_f32m8_b4 (vfloat32m8_t op1, vfloat32m8_t op2,
    size_t vl);
vbool4_t vmfgt_vf_f32m8_b4 (vfloat32m8_t op1, float32_t op2,
    size_t vl);
vbool64_t vmfgt_vv_f64m1_b64 (vfloat64m1_t op1, vfloat64m1_t
    op2, size_t vl);
vbool64_t vmfgt_vf_f64m1_b64 (vfloat64m1_t op1, float64_t op2,
    size_t vl);
vbool32_t vmfgt_vv_f64m2_b32 (vfloat64m2_t op1, vfloat64m2_t
    op2, size_t vl);
vbool32_t vmfgt_vf_f64m2_b32 (vfloat64m2_t op1, float64_t op2,
    size_t vl);
vbool16_t vmfgt_vv_f64m4_b16 (vfloat64m4_t op1, vfloat64m4_t
    op2, size_t vl);
vbool16_t vmfgt_vf_f64m4_b16 (vfloat64m4_t op1, float64_t op2,
    size_t vl);
vbool8_t vmfgt_vv_f64m8_b8 (vfloat64m8_t op1, vfloat64m8_t op2,
    size_t vl);
vbool8_t vmfgt_vf_f64m8_b8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);

```

```

vbool16_t vmfge_vv_f16m1_b16 (vfloat16m1_t op1, vfloat16m1_t
    op2, size_t vl);
vbool16_t vmfge_vf_f16m1_b16 (vfloat16m1_t op1, float16_t op2,
    size_t vl);
vbool8_t vmfge_vv_f16m2_b8 (vfloat16m2_t op1, vfloat16m2_t op2,
    size_t vl);
vbool8_t vmfge_vf_f16m2_b8 (vfloat16m2_t op1, float16_t op2,
    size_t vl);
vbool4_t vmfge_vv_f16m4_b4 (vfloat16m4_t op1, vfloat16m4_t op2,
    size_t vl);
vbool4_t vmfge_vf_f16m4_b4 (vfloat16m4_t op1, float16_t op2,
    size_t vl);
vbool2_t vmfge_vv_f16m8_b2 (vfloat16m8_t op1, vfloat16m8_t op2,
    size_t vl);
vbool2_t vmfge_vf_f16m8_b2 (vfloat16m8_t op1, float16_t op2,
    size_t vl);
vbool32_t vmfge_vv_f32m1_b32 (vfloat32m1_t op1, vfloat32m1_t
    op2, size_t vl);
vbool32_t vmfge_vf_f32m1_b32 (vfloat32m1_t op1, float32_t op2,
    size_t vl);
vbool16_t vmfge_vv_f32m2_b16 (vfloat32m2_t op1, vfloat32m2_t
    op2, size_t vl);
vbool16_t vmfge_vf_f32m2_b16 (vfloat32m2_t op1, float32_t op2,
    size_t vl);
vbool8_t vmfge_vv_f32m4_b8 (vfloat32m4_t op1, vfloat32m4_t op2,
    size_t vl);
vbool8_t vmfge_vf_f32m4_b8 (vfloat32m4_t op1, float32_t op2,
    size_t vl);
vbool4_t vmfge_vv_f32m8_b4 (vfloat32m8_t op1, vfloat32m8_t op2,
    size_t vl);
vbool4_t vmfge_vf_f32m8_b4 (vfloat32m8_t op1, float32_t op2,
    size_t vl);
vbool64_t vmfge_vv_f64m1_b64 (vfloat64m1_t op1, vfloat64m1_t
    op2, size_t vl);
vbool64_t vmfge_vf_f64m1_b64 (vfloat64m1_t op1, float64_t op2,
    size_t vl);
vbool32_t vmfge_vv_f64m2_b32 (vfloat64m2_t op1, vfloat64m2_t
    op2, size_t vl);
vbool32_t vmfge_vf_f64m2_b32 (vfloat64m2_t op1, float64_t op2,
    size_t vl);
vbool16_t vmfge_vv_f64m4_b16 (vfloat64m4_t op1, vfloat64m4_t
    op2, size_t vl);
vbool16_t vmfge_vf_f64m4_b16 (vfloat64m4_t op1, float64_t op2,
    size_t vl);
vbool8_t vmfge_vv_f64m8_b8 (vfloat64m8_t op1, vfloat64m8_t op2,
    size_t vl);

```

```

vbool8_t vmfge_vf_f64m8_b8 (vfloat64m8_t op1, float64_t op2,
    size_t vl);
// masked functions
vbool16_t vmfeq_vv_f16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vbool16_t vmfeq_vf_f16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vbool8_t vmfeq_vv_f16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vbool8_t vmfeq_vf_f16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vfloat16m2_t op1, float16_t op2, size_t vl);
vbool4_t vmfeq_vv_f16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vbool4_t vmfeq_vf_f16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vfloat16m4_t op1, float16_t op2, size_t vl);
vbool2_t vmfeq_vv_f16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vfloat16m8_t op1, vfloat16m8_t op2, size_t vl);
vbool2_t vmfeq_vf_f16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vfloat16m8_t op1, float16_t op2, size_t vl);
vbool32_t vmfeq_vv_f32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vbool32_t vmfeq_vf_f32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vbool16_t vmfeq_vv_f32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vbool16_t vmfeq_vf_f32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vbool8_t vmfeq_vv_f32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vbool8_t vmfeq_vf_f32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vfloat32m4_t op1, float32_t op2, size_t vl);
vbool4_t vmfeq_vv_f32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vfloat32m8_t op1, vfloat32m8_t op2, size_t vl);
vbool4_t vmfeq_vf_f32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vfloat32m8_t op1, float32_t op2, size_t vl);
vbool64_t vmfeq_vv_f64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vfloat64m1_t op1, vfloat64m1_t op2, size_t vl);
vbool64_t vmfeq_vf_f64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);
vbool32_t vmfeq_vv_f64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vfloat64m2_t op1, vfloat64m2_t op2, size_t vl);
vbool32_t vmfeq_vf_f64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);
vbool16_t vmfeq_vv_f64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vfloat64m4_t op1, vfloat64m4_t op2, size_t vl);

```



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vbool16_t vmfeq_vf_f64m4_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vbool8_t vmfeq_vv_f64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat64m8_t op1, vfloat64m8_t op2, size_t vl);
vbool8_t vmfeq_vf_f64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat64m8_t op1, float64_t op2, size_t vl);
vbool16_t vmfne_vv_f16m1_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vbool16_t vmfne_vf_f16m1_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vbool8_t vmfne_vv_f16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vbool8_t vmfne_vf_f16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat16m2_t op1, float16_t op2, size_t vl);
vbool4_t vmfne_vv_f16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vbool4_t vmfne_vf_f16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
vfloat16m4_t op1, float16_t op2, size_t vl);
vbool2_t vmfne_vv_f16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
vfloat16m8_t op1, vfloat16m8_t op2, size_t vl);
vbool2_t vmfne_vf_f16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
vfloat16m8_t op1, float16_t op2, size_t vl);
vbool32_t vmfne_vv_f32m1_b32_m (vbool32_t mask, vbool32_t
maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vbool32_t vmfne_vf_f32m1_b32_m (vbool32_t mask, vbool32_t
maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vbool16_t vmfne_vv_f32m2_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vbool16_t vmfne_vf_f32m2_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vbool8_t vmfne_vv_f32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vbool8_t vmfne_vf_f32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat32m4_t op1, float32_t op2, size_t vl);
vbool4_t vmfne_vv_f32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
vfloat32m8_t op1, vfloat32m8_t op2, size_t vl);
vbool4_t vmfne_vf_f32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
vfloat32m8_t op1, float32_t op2, size_t vl);
vbool64_t vmfne_vv_f64m1_b64_m (vbool64_t mask, vbool64_t
maskedoff, vfloat64m1_t op1, vfloat64m1_t op2, size_t vl);
vbool64_t vmfne_vf_f64m1_b64_m (vbool64_t mask, vbool64_t
maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);
vbool32_t vmfne_vv_f64m2_b32_m (vbool32_t mask, vbool32_t
maskedoff, vfloat64m2_t op1, vfloat64m2_t op2, size_t vl);
vbool32_t vmfne_vf_f64m2_b32_m (vbool32_t mask, vbool32_t
maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);

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vbool16_t vmfne_vv_f64m4_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat64m4_t op1, vfloat64m4_t op2, size_t vl);
vbool16_t vmfne_vf_f64m4_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vbool8_t vmfne_vv_f64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat64m8_t op1, vfloat64m8_t op2, size_t vl);
vbool8_t vmfne_vf_f64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat64m8_t op1, float64_t op2, size_t vl);
vbool16_t vmflt_vv_f16m1_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vbool16_t vmflt_vf_f16m1_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vbool8_t vmflt_vv_f16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vbool8_t vmflt_vf_f16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat16m2_t op1, float16_t op2, size_t vl);
vbool4_t vmflt_vv_f16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vbool4_t vmflt_vf_f16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
vfloat16m4_t op1, float16_t op2, size_t vl);
vbool2_t vmflt_vv_f16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
vfloat16m8_t op1, vfloat16m8_t op2, size_t vl);
vbool2_t vmflt_vf_f16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
vfloat16m8_t op1, float16_t op2, size_t vl);
vbool32_t vmflt_vv_f32m1_b32_m (vbool32_t mask, vbool32_t
maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vbool32_t vmflt_vf_f32m1_b32_m (vbool32_t mask, vbool32_t
maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vbool16_t vmflt_vv_f32m2_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vbool16_t vmflt_vf_f32m2_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vbool8_t vmflt_vv_f32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vbool8_t vmflt_vf_f32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat32m4_t op1, float32_t op2, size_t vl);
vbool4_t vmflt_vv_f32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
vfloat32m8_t op1, vfloat32m8_t op2, size_t vl);
vbool4_t vmflt_vf_f32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
vfloat32m8_t op1, float32_t op2, size_t vl);
vbool64_t vmflt_vv_f64m1_b64_m (vbool64_t mask, vbool64_t
maskedoff, vfloat64m1_t op1, vfloat64m1_t op2, size_t vl);
vbool64_t vmflt_vf_f64m1_b64_m (vbool64_t mask, vbool64_t
maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);
vbool32_t vmflt_vv_f64m2_b32_m (vbool32_t mask, vbool32_t
maskedoff, vfloat64m2_t op1, vfloat64m2_t op2, size_t vl);

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vbool32_t vmflt_vf_f64m2_b32_m (vbool32_t mask, vbool32_t
maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);
vbool16_t vmflt_vv_f64m4_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat64m4_t op1, vfloat64m4_t op2, size_t vl);
vbool16_t vmflt_vf_f64m4_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vbool8_t vmflt_vv_f64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat64m8_t op1, vfloat64m8_t op2, size_t vl);
vbool8_t vmflt_vf_f64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat64m8_t op1, float64_t op2, size_t vl);
vbool16_t vmfle_vv_f16m1_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vbool16_t vmfle_vf_f16m1_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vbool8_t vmfle_vv_f16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vbool8_t vmfle_vf_f16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat16m2_t op1, float16_t op2, size_t vl);
vbool4_t vmfle_vv_f16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vbool4_t vmfle_vf_f16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
vfloat16m4_t op1, float16_t op2, size_t vl);
vbool2_t vmfle_vv_f16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
vfloat16m8_t op1, vfloat16m8_t op2, size_t vl);
vbool2_t vmfle_vf_f16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
vfloat16m8_t op1, float16_t op2, size_t vl);
vbool32_t vmfle_vv_f32m1_b32_m (vbool32_t mask, vbool32_t
maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vbool32_t vmfle_vf_f32m1_b32_m (vbool32_t mask, vbool32_t
maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vbool16_t vmfle_vv_f32m2_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vbool16_t vmfle_vf_f32m2_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vbool8_t vmfle_vv_f32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vbool8_t vmfle_vf_f32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat32m4_t op1, float32_t op2, size_t vl);
vbool4_t vmfle_vv_f32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
vfloat32m8_t op1, vfloat32m8_t op2, size_t vl);
vbool4_t vmfle_vf_f32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
vfloat32m8_t op1, float32_t op2, size_t vl);
vbool64_t vmfle_vv_f64m1_b64_m (vbool64_t mask, vbool64_t
maskedoff, vfloat64m1_t op1, vfloat64m1_t op2, size_t vl);
vbool64_t vmfle_vf_f64m1_b64_m (vbool64_t mask, vbool64_t
maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);

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vbool32_t vmfle_vv_f64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vfloat64m2_t op1, vfloat64m2_t op2, size_t vl);
vbool32_t vmfle_vf_f64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);
vbool16_t vmfle_vv_f64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vfloat64m4_t op1, vfloat64m4_t op2, size_t vl);
vbool16_t vmfle_vf_f64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vbool8_t vmfle_vv_f64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vfloat64m8_t op1, vfloat64m8_t op2, size_t vl);
vbool8_t vmfle_vf_f64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vfloat64m8_t op1, float64_t op2, size_t vl);
vbool16_t vmfgt_vv_f16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vbool16_t vmfgt_vf_f16m1_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vbool8_t vmfgt_vv_f16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vbool8_t vmfgt_vf_f16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vfloat16m2_t op1, float16_t op2, size_t vl);
vbool4_t vmfgt_vv_f16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vbool4_t vmfgt_vf_f16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vfloat16m4_t op1, float16_t op2, size_t vl);
vbool2_t vmfgt_vv_f16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vfloat16m8_t op1, vfloat16m8_t op2, size_t vl);
vbool2_t vmfgt_vf_f16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vfloat16m8_t op1, float16_t op2, size_t vl);
vbool32_t vmfgt_vv_f32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vbool32_t vmfgt_vf_f32m1_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vbool16_t vmfgt_vv_f32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vbool16_t vmfgt_vf_f32m2_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vbool8_t vmfgt_vv_f32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vbool8_t vmfgt_vf_f32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vfloat32m4_t op1, float32_t op2, size_t vl);
vbool4_t vmfgt_vv_f32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vfloat32m8_t op1, vfloat32m8_t op2, size_t vl);
vbool4_t vmfgt_vf_f32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vfloat32m8_t op1, float32_t op2, size_t vl);
vbool64_t vmfgt_vv_f64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vfloat64m1_t op1, vfloat64m1_t op2, size_t vl);

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vbool64_t vmfgt_vf_f64m1_b64_m (vbool64_t mask, vbool64_t
maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);
vbool32_t vmfgt_vv_f64m2_b32_m (vbool32_t mask, vbool32_t
maskedoff, vfloat64m2_t op1, vfloat64m2_t op2, size_t vl);
vbool32_t vmfgt_vf_f64m2_b32_m (vbool32_t mask, vbool32_t
maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);
vbool16_t vmfgt_vv_f64m4_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat64m4_t op1, vfloat64m4_t op2, size_t vl);
vbool16_t vmfgt_vf_f64m4_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vbool8_t vmfgt_vv_f64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat64m8_t op1, vfloat64m8_t op2, size_t vl);
vbool8_t vmfgt_vf_f64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat64m8_t op1, float64_t op2, size_t vl);
vbool16_t vmfge_vv_f16m1_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat16m1_t op1, vfloat16m1_t op2, size_t vl);
vbool16_t vmfge_vf_f16m1_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat16m1_t op1, float16_t op2, size_t vl);
vbool8_t vmfge_vv_f16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat16m2_t op1, vfloat16m2_t op2, size_t vl);
vbool8_t vmfge_vf_f16m2_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat16m2_t op1, float16_t op2, size_t vl);
vbool4_t vmfge_vv_f16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
vfloat16m4_t op1, vfloat16m4_t op2, size_t vl);
vbool4_t vmfge_vf_f16m4_b4_m (vbool4_t mask, vbool4_t maskedoff,
vfloat16m4_t op1, float16_t op2, size_t vl);
vbool2_t vmfge_vv_f16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
vfloat16m8_t op1, vfloat16m8_t op2, size_t vl);
vbool2_t vmfge_vf_f16m8_b2_m (vbool2_t mask, vbool2_t maskedoff,
vfloat16m8_t op1, float16_t op2, size_t vl);
vbool32_t vmfge_vv_f32m1_b32_m (vbool32_t mask, vbool32_t
maskedoff, vfloat32m1_t op1, vfloat32m1_t op2, size_t vl);
vbool32_t vmfge_vf_f32m1_b32_m (vbool32_t mask, vbool32_t
maskedoff, vfloat32m1_t op1, float32_t op2, size_t vl);
vbool16_t vmfge_vv_f32m2_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat32m2_t op1, vfloat32m2_t op2, size_t vl);
vbool16_t vmfge_vf_f32m2_b16_m (vbool16_t mask, vbool16_t
maskedoff, vfloat32m2_t op1, float32_t op2, size_t vl);
vbool8_t vmfge_vv_f32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat32m4_t op1, vfloat32m4_t op2, size_t vl);
vbool8_t vmfge_vf_f32m4_b8_m (vbool8_t mask, vbool8_t maskedoff,
vfloat32m4_t op1, float32_t op2, size_t vl);
vbool4_t vmfge_vv_f32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
vfloat32m8_t op1, vfloat32m8_t op2, size_t vl);
vbool4_t vmfge_vf_f32m8_b4_m (vbool4_t mask, vbool4_t maskedoff,
vfloat32m8_t op1, float32_t op2, size_t vl);

```

```

vbool64_t vmfge_vv_f64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vfloat64m1_t op1, vfloat64m1_t op2, size_t vl);
vbool64_t vmfge_vf_f64m1_b64_m (vbool64_t mask, vbool64_t
    maskedoff, vfloat64m1_t op1, float64_t op2, size_t vl);
vbool32_t vmfge_vv_f64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vfloat64m2_t op1, vfloat64m2_t op2, size_t vl);
vbool32_t vmfge_vf_f64m2_b32_m (vbool32_t mask, vbool32_t
    maskedoff, vfloat64m2_t op1, float64_t op2, size_t vl);
vbool16_t vmfge_vv_f64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vfloat64m4_t op1, vfloat64m4_t op2, size_t vl);
vbool16_t vmfge_vf_f64m4_b16_m (vbool16_t mask, vbool16_t
    maskedoff, vfloat64m4_t op1, float64_t op2, size_t vl);
vbool8_t vmfge_vv_f64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vfloat64m8_t op1, vfloat64m8_t op2, size_t vl);
vbool8_t vmfge_vf_f64m8_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vfloat64m8_t op1, float64_t op2, size_t vl);

```

## Vector Floating-Point Classify Functions:

### Prototypes:

```

vuint16m1_t vfclass_v_u16m1 (vfloat16m1_t op1, size_t vl);
vuint16m2_t vfclass_v_u16m2 (vfloat16m2_t op1, size_t vl);
vuint16m4_t vfclass_v_u16m4 (vfloat16m4_t op1, size_t vl);
vuint16m8_t vfclass_v_u16m8 (vfloat16m8_t op1, size_t vl);
vuint32m1_t vfclass_v_u32m1 (vfloat32m1_t op1, size_t vl);
vuint32m2_t vfclass_v_u32m2 (vfloat32m2_t op1, size_t vl);
vuint32m4_t vfclass_v_u32m4 (vfloat32m4_t op1, size_t vl);
vuint32m8_t vfclass_v_u32m8 (vfloat32m8_t op1, size_t vl);
vuint64m1_t vfclass_v_u64m1 (vfloat64m1_t op1, size_t vl);
vuint64m2_t vfclass_v_u64m2 (vfloat64m2_t op1, size_t vl);
vuint64m4_t vfclass_v_u64m4 (vfloat64m4_t op1, size_t vl);
vuint64m8_t vfclass_v_u64m8 (vfloat64m8_t op1, size_t vl);
// masked functions
vuint16m1_t vfclass_v_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vfloat16m1_t op1, size_t vl);
vuint16m2_t vfclass_v_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vfloat16m2_t op1, size_t vl);
vuint16m4_t vfclass_v_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vfloat16m4_t op1, size_t vl);
vuint16m8_t vfclass_v_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vfloat16m8_t op1, size_t vl);
vuint32m1_t vfclass_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vfloat32m1_t op1, size_t vl);
vuint32m2_t vfclass_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vfloat32m2_t op1, size_t vl);

```

```

vuint32m4_t vfclass_v_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vfloat32m4_t op1, size_t vl);
vuint32m8_t vfclass_v_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vfloat32m8_t op1, size_t vl);
vuint64m1_t vfclass_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vfloat64m1_t op1, size_t vl);
vuint64m2_t vfclass_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vfloat64m2_t op1, size_t vl);
vuint64m4_t vfclass_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vfloat64m4_t op1, size_t vl);
vuint64m8_t vfclass_v_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vfloat64m8_t op1, size_t vl);

```

## Vector Floating-Point Merge Functions:

### Prototypes:

```

vfloat16m1_t vmerge_vvm_f16m1 (vbool16_t mask, vfloat16m1_t op1,
    vfloat16m1_t op2, size_t vl);
vfloat16m1_t vfmerge_vfm_f16m1 (vbool16_t mask, vfloat16m1_t
    op1, float16_t op2, size_t vl);
vfloat16m2_t vmerge_vvm_f16m2 (vbool8_t mask, vfloat16m2_t op1,
    vfloat16m2_t op2, size_t vl);
vfloat16m2_t vfmerge_vfm_f16m2 (vbool8_t mask, vfloat16m2_t op1,
    float16_t op2, size_t vl);
vfloat16m4_t vmerge_vvm_f16m4 (vbool4_t mask, vfloat16m4_t op1,
    vfloat16m4_t op2, size_t vl);
vfloat16m4_t vfmerge_vfm_f16m4 (vbool4_t mask, vfloat16m4_t op1,
    float16_t op2, size_t vl);
vfloat16m8_t vmerge_vvm_f16m8 (vbool2_t mask, vfloat16m8_t op1,
    vfloat16m8_t op2, size_t vl);
vfloat16m8_t vfmerge_vfm_f16m8 (vbool2_t mask, vfloat16m8_t op1,
    float16_t op2, size_t vl);
vfloat32m1_t vmerge_vvm_f32m1 (vbool32_t mask, vfloat32m1_t op1,
    vfloat32m1_t op2, size_t vl);
vfloat32m1_t vfmerge_vfm_f32m1 (vbool32_t mask, vfloat32m1_t
    op1, float32_t op2, size_t vl);
vfloat32m2_t vmerge_vvm_f32m2 (vbool16_t mask, vfloat32m2_t op1,
    vfloat32m2_t op2, size_t vl);
vfloat32m2_t vfmerge_vfm_f32m2 (vbool16_t mask, vfloat32m2_t
    op1, float32_t op2, size_t vl);
vfloat32m4_t vmerge_vvm_f32m4 (vbool8_t mask, vfloat32m4_t op1,
    vfloat32m4_t op2, size_t vl);
vfloat32m4_t vfmerge_vfm_f32m4 (vbool8_t mask, vfloat32m4_t op1,
    float32_t op2, size_t vl);

```

```

vfloat32m8_t vmerge_vvm_f32m8 (vbool4_t mask, vfloat32m8_t op1,
    vfloat32m8_t op2, size_t vl);
vfloat32m8_t vfmerge_vfm_f32m8 (vbool4_t mask, vfloat32m8_t op1,
    float32_t op2, size_t vl);
vfloat64m1_t vmerge_vvm_f64m1 (vbool64_t mask, vfloat64m1_t op1,
    vfloat64m1_t op2, size_t vl);
vfloat64m1_t vfmerge_vfm_f64m1 (vbool64_t mask, vfloat64m1_t
    op1, float64_t op2, size_t vl);
vfloat64m2_t vmerge_vvm_f64m2 (vbool32_t mask, vfloat64m2_t op1,
    vfloat64m2_t op2, size_t vl);
vfloat64m2_t vfmerge_vfm_f64m2 (vbool32_t mask, vfloat64m2_t
    op1, float64_t op2, size_t vl);
vfloat64m4_t vmerge_vvm_f64m4 (vbool16_t mask, vfloat64m4_t op1,
    vfloat64m4_t op2, size_t vl);
vfloat64m4_t vfmerge_vfm_f64m4 (vbool16_t mask, vfloat64m4_t
    op1, float64_t op2, size_t vl);
vfloat64m8_t vmerge_vvm_f64m8 (vbool8_t mask, vfloat64m8_t op1,
    vfloat64m8_t op2, size_t vl);
vfloat64m8_t vfmerge_vfm_f64m8 (vbool8_t mask, vfloat64m8_t op1,
    float64_t op2, size_t vl);

```

## Vector Floating-Point Move Functions:

### Prototypes:

```

vfloat16m1_t vmv_v_v_f16m1 (vfloat16m1_t src, size_t vl);
vfloat16m1_t vfmv_v_f_f16m1 (float16_t src, size_t vl);
vfloat16m2_t vmv_v_v_f16m2 (vfloat16m2_t src, size_t vl);
vfloat16m2_t vfmv_v_f_f16m2 (float16_t src, size_t vl);
vfloat16m4_t vmv_v_v_f16m4 (vfloat16m4_t src, size_t vl);
vfloat16m4_t vfmv_v_f_f16m4 (float16_t src, size_t vl);
vfloat16m8_t vmv_v_v_f16m8 (vfloat16m8_t src, size_t vl);
vfloat16m8_t vfmv_v_f_f16m8 (float16_t src, size_t vl);
vfloat32m1_t vmv_v_v_f32m1 (vfloat32m1_t src, size_t vl);
vfloat32m1_t vfmv_v_f_f32m1 (float32_t src, size_t vl);
vfloat32m2_t vmv_v_v_f32m2 (vfloat32m2_t src, size_t vl);
vfloat32m2_t vfmv_v_f_f32m2 (float32_t src, size_t vl);
vfloat32m4_t vmv_v_v_f32m4 (vfloat32m4_t src, size_t vl);
vfloat32m4_t vfmv_v_f_f32m4 (float32_t src, size_t vl);
vfloat32m8_t vmv_v_v_f32m8 (vfloat32m8_t src, size_t vl);
vfloat32m8_t vfmv_v_f_f32m8 (float32_t src, size_t vl);
vfloat64m1_t vmv_v_v_f64m1 (vfloat64m1_t src, size_t vl);
vfloat64m1_t vfmv_v_f_f64m1 (float64_t src, size_t vl);
vfloat64m2_t vmv_v_v_f64m2 (vfloat64m2_t src, size_t vl);
vfloat64m2_t vfmv_v_f_f64m2 (float64_t src, size_t vl);
vfloat64m4_t vmv_v_v_f64m4 (vfloat64m4_t src, size_t vl);

```



```

vfloat64m4_t vfmv_v_f_f64m4 (float64_t src, size_t vl);
vfloat64m8_t vmv_v_v_f64m8 (vfloat64m8_t src, size_t vl);
vfloat64m8_t vfmv_v_f_f64m8 (float64_t src, size_t vl);

```

## Single-Width Floating-Point/Integer Type-Convert Functions:

### Prototypes:

```

vint16m1_t vfcvt_x_f_v_i16m1 (vfloat16m1_t src, size_t vl);
vint16m2_t vfcvt_x_f_v_i16m2 (vfloat16m2_t src, size_t vl);
vint16m4_t vfcvt_x_f_v_i16m4 (vfloat16m4_t src, size_t vl);
vint16m8_t vfcvt_x_f_v_i16m8 (vfloat16m8_t src, size_t vl);
vuint16m1_t vfcvt_xu_f_v_u16m1 (vfloat16m1_t src, size_t vl);
vuint16m2_t vfcvt_xu_f_v_u16m2 (vfloat16m2_t src, size_t vl);
vuint16m4_t vfcvt_xu_f_v_u16m4 (vfloat16m4_t src, size_t vl);
vuint16m8_t vfcvt_xu_f_v_u16m8 (vfloat16m8_t src, size_t vl);
vfloat16m1_t vfcvt_f_x_v_f16m1 (vint16m1_t src, size_t vl);
vfloat16m2_t vfcvt_f_x_v_f16m2 (vint16m2_t src, size_t vl);
vfloat16m4_t vfcvt_f_x_v_f16m4 (vint16m4_t src, size_t vl);
vfloat16m8_t vfcvt_f_x_v_f16m8 (vint16m8_t src, size_t vl);
vfloat16m1_t vfcvt_f_xu_v_f16m1 (vuint16m1_t src, size_t vl);
vfloat16m2_t vfcvt_f_xu_v_f16m2 (vuint16m2_t src, size_t vl);
vfloat16m4_t vfcvt_f_xu_v_f16m4 (vuint16m4_t src, size_t vl);
vfloat16m8_t vfcvt_f_xu_v_f16m8 (vuint16m8_t src, size_t vl);
vint32m1_t vfcvt_x_f_v_i32m1 (vfloat32m1_t src, size_t vl);
vint32m2_t vfcvt_x_f_v_i32m2 (vfloat32m2_t src, size_t vl);
vint32m4_t vfcvt_x_f_v_i32m4 (vfloat32m4_t src, size_t vl);
vint32m8_t vfcvt_x_f_v_i32m8 (vfloat32m8_t src, size_t vl);
vuint32m1_t vfcvt_xu_f_v_u32m1 (vfloat32m1_t src, size_t vl);
vuint32m2_t vfcvt_xu_f_v_u32m2 (vfloat32m2_t src, size_t vl);
vuint32m4_t vfcvt_xu_f_v_u32m4 (vfloat32m4_t src, size_t vl);
vuint32m8_t vfcvt_xu_f_v_u32m8 (vfloat32m8_t src, size_t vl);
vfloat32m1_t vfcvt_f_x_v_f32m1 (vint32m1_t src, size_t vl);
vfloat32m2_t vfcvt_f_x_v_f32m2 (vint32m2_t src, size_t vl);
vfloat32m4_t vfcvt_f_x_v_f32m4 (vint32m4_t src, size_t vl);
vfloat32m8_t vfcvt_f_x_v_f32m8 (vint32m8_t src, size_t vl);
vfloat32m1_t vfcvt_f_xu_v_f32m1 (vuint32m1_t src, size_t vl);
vfloat32m2_t vfcvt_f_xu_v_f32m2 (vuint32m2_t src, size_t vl);
vfloat32m4_t vfcvt_f_xu_v_f32m4 (vuint32m4_t src, size_t vl);
vfloat32m8_t vfcvt_f_xu_v_f32m8 (vuint32m8_t src, size_t vl);
vint64m1_t vfcvt_x_f_v_i64m1 (vfloat64m1_t src, size_t vl);
vint64m2_t vfcvt_x_f_v_i64m2 (vfloat64m2_t src, size_t vl);
vint64m4_t vfcvt_x_f_v_i64m4 (vfloat64m4_t src, size_t vl);
vint64m8_t vfcvt_x_f_v_i64m8 (vfloat64m8_t src, size_t vl);
vuint64m1_t vfcvt_xu_f_v_u64m1 (vfloat64m1_t src, size_t vl);
vuint64m2_t vfcvt_xu_f_v_u64m2 (vfloat64m2_t src, size_t vl);

```

```

vuint64m4_t vfcvt_xu_f_v_u64m4 (vfloat64m4_t src, size_t vl);
vuint64m8_t vfcvt_xu_f_v_u64m8 (vfloat64m8_t src, size_t vl);
vfloat64m1_t vfcvt_f_x_v_f64m1 (vint64m1_t src, size_t vl);
vfloat64m2_t vfcvt_f_x_v_f64m2 (vint64m2_t src, size_t vl);
vfloat64m4_t vfcvt_f_x_v_f64m4 (vint64m4_t src, size_t vl);
vfloat64m8_t vfcvt_f_x_v_f64m8 (vint64m8_t src, size_t vl);
vfloat64m1_t vfcvt_f_xu_v_f64m1 (vuint64m1_t src, size_t vl);
vfloat64m2_t vfcvt_f_xu_v_f64m2 (vuint64m2_t src, size_t vl);
vfloat64m4_t vfcvt_f_xu_v_f64m4 (vuint64m4_t src, size_t vl);
vfloat64m8_t vfcvt_f_xu_v_f64m8 (vuint64m8_t src, size_t vl);
// masked functions
vint16m1_t vfcvt_x_f_v_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vfloat16m1_t src, size_t vl);
vint16m2_t vfcvt_x_f_v_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vfloat16m2_t src, size_t vl);
vint16m4_t vfcvt_x_f_v_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vfloat16m4_t src, size_t vl);
vint16m8_t vfcvt_x_f_v_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vfloat16m8_t src, size_t vl);
vuint16m1_t vfcvt_xu_f_v_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vfloat16m1_t src, size_t vl);
vuint16m2_t vfcvt_xu_f_v_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vfloat16m2_t src, size_t vl);
vuint16m4_t vfcvt_xu_f_v_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vfloat16m4_t src, size_t vl);
vuint16m8_t vfcvt_xu_f_v_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vfloat16m8_t src, size_t vl);
vfloat16m1_t vfcvt_f_x_v_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vint16m1_t src, size_t vl);
vfloat16m2_t vfcvt_f_x_v_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vint16m2_t src, size_t vl);
vfloat16m4_t vfcvt_f_x_v_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vint16m4_t src, size_t vl);
vfloat16m8_t vfcvt_f_x_v_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vint16m8_t src, size_t vl);
vfloat16m1_t vfcvt_f_xu_v_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vuint16m1_t src, size_t vl);
vfloat16m2_t vfcvt_f_xu_v_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vuint16m2_t src, size_t vl);
vfloat16m4_t vfcvt_f_xu_v_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vuint16m4_t src, size_t vl);
vfloat16m8_t vfcvt_f_xu_v_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vuint16m8_t src, size_t vl);
vint32m1_t vfcvt_x_f_v_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vfloat32m1_t src, size_t vl);

```

```

vint32m2_t vfcvt_x_f_v_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vfloat32m2_t src, size_t vl);
vint32m4_t vfcvt_x_f_v_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vfloat32m4_t src, size_t vl);
vint32m8_t vfcvt_x_f_v_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vfloat32m8_t src, size_t vl);
vuint32m1_t vfcvt_xu_f_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vfloat32m1_t src, size_t vl);
vuint32m2_t vfcvt_xu_f_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vfloat32m2_t src, size_t vl);
vuint32m4_t vfcvt_xu_f_v_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vfloat32m4_t src, size_t vl);
vuint32m8_t vfcvt_xu_f_v_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vfloat32m8_t src, size_t vl);
vfloat32m1_t vfcvt_f_x_v_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vint32m1_t src, size_t vl);
vfloat32m2_t vfcvt_f_x_v_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vint32m2_t src, size_t vl);
vfloat32m4_t vfcvt_f_x_v_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vint32m4_t src, size_t vl);
vfloat32m8_t vfcvt_f_x_v_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vint32m8_t src, size_t vl);
vfloat32m1_t vfcvt_f_xu_v_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vuint32m1_t src, size_t vl);
vfloat32m2_t vfcvt_f_xu_v_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vuint32m2_t src, size_t vl);
vfloat32m4_t vfcvt_f_xu_v_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vuint32m4_t src, size_t vl);
vfloat32m8_t vfcvt_f_xu_v_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vuint32m8_t src, size_t vl);
vint64m1_t vfcvt_x_f_v_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vfloat64m1_t src, size_t vl);
vint64m2_t vfcvt_x_f_v_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vfloat64m2_t src, size_t vl);
vint64m4_t vfcvt_x_f_v_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vfloat64m4_t src, size_t vl);
vint64m8_t vfcvt_x_f_v_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vfloat64m8_t src, size_t vl);
vuint64m1_t vfcvt_xu_f_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vfloat64m1_t src, size_t vl);
vuint64m2_t vfcvt_xu_f_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vfloat64m2_t src, size_t vl);
vuint64m4_t vfcvt_xu_f_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vfloat64m4_t src, size_t vl);
vuint64m8_t vfcvt_xu_f_v_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vfloat64m8_t src, size_t vl);

```

```

vfloat64m1_t vfcvt_f_x_v_f64m1_m (vbool64_t mask, vfloat64m1_t
maskedoff, vint64m1_t src, size_t vl);
vfloat64m2_t vfcvt_f_x_v_f64m2_m (vbool32_t mask, vfloat64m2_t
maskedoff, vint64m2_t src, size_t vl);
vfloat64m4_t vfcvt_f_x_v_f64m4_m (vbool16_t mask, vfloat64m4_t
maskedoff, vint64m4_t src, size_t vl);
vfloat64m8_t vfcvt_f_x_v_f64m8_m (vbool8_t mask, vfloat64m8_t
maskedoff, vint64m8_t src, size_t vl);
vfloat64m1_t vfcvt_f_xu_v_f64m1_m (vbool64_t mask, vfloat64m1_t
maskedoff, vuint64m1_t src, size_t vl);
vfloat64m2_t vfcvt_f_xu_v_f64m2_m (vbool32_t mask, vfloat64m2_t
maskedoff, vuint64m2_t src, size_t vl);
vfloat64m4_t vfcvt_f_xu_v_f64m4_m (vbool16_t mask, vfloat64m4_t
maskedoff, vuint64m4_t src, size_t vl);
vfloat64m8_t vfcvt_f_xu_v_f64m8_m (vbool8_t mask, vfloat64m8_t
maskedoff, vuint64m8_t src, size_t vl);

```

## Widening Floating-Point/Integer Type-Convert Functions:

### Prototypes:

```

vint16m2_t vwcvt_x_x_v_i16m2 (vint8m1_t src, size_t vl);
vint16m4_t vwcvt_x_x_v_i16m4 (vint8m2_t src, size_t vl);
vint16m8_t vwcvt_x_x_v_i16m8 (vint8m4_t src, size_t vl);
vuint16m2_t vwcvtu_x_x_v_u16m2 (vuint8m1_t src, size_t vl);
vuint16m4_t vwcvtu_x_x_v_u16m4 (vuint8m2_t src, size_t vl);
vuint16m8_t vwcvtu_x_x_v_u16m8 (vuint8m4_t src, size_t vl);
vfloat16m2_t vfwcvt_f_x_v_f16m2 (vint8m1_t src, size_t vl);
vfloat16m4_t vfwcvt_f_x_v_f16m4 (vint8m2_t src, size_t vl);
vfloat16m8_t vfwcvt_f_x_v_f16m8 (vint8m4_t src, size_t vl);
vfloat16m2_t vfwcvt_f_xu_v_f16m2 (vuint8m1_t src, size_t vl);
vfloat16m4_t vfwcvt_f_xu_v_f16m4 (vuint8m2_t src, size_t vl);
vfloat16m8_t vfwcvt_f_xu_v_f16m8 (vuint8m4_t src, size_t vl);
vint32m2_t vfwcvt_x_f_v_i32m2 (vfloat16m1_t src, size_t vl);
vint32m4_t vfwcvt_x_f_v_i32m4 (vfloat16m2_t src, size_t vl);
vint32m8_t vfwcvt_x_f_v_i32m8 (vfloat16m4_t src, size_t vl);
vint32m2_t vwcvt_x_x_v_i32m2 (vint16m1_t src, size_t vl);
vint32m4_t vwcvt_x_x_v_i32m4 (vint16m2_t src, size_t vl);
vint32m8_t vwcvt_x_x_v_i32m8 (vint16m4_t src, size_t vl);
vuint32m2_t vwcvtu_x_x_v_u32m2 (vuint16m1_t src, size_t vl);
vuint32m4_t vwcvtu_x_x_v_u32m4 (vuint16m2_t src, size_t vl);
vuint32m8_t vwcvtu_x_x_v_u32m8 (vuint16m4_t src, size_t vl);
vuint32m2_t vfwcvt_xu_f_v_u32m2 (vfloat16m1_t src, size_t vl);
vuint32m4_t vfwcvt_xu_f_v_u32m4 (vfloat16m2_t src, size_t vl);
vuint32m8_t vfwcvt_xu_f_v_u32m8 (vfloat16m4_t src, size_t vl);
vfloat32m2_t vfwcvt_f_x_v_f32m2 (vint16m1_t src, size_t vl);

```

```

vfloat32m4_t vfwcvt_f_x_v_f32m4 (vint16m2_t src, size_t vl);
vfloat32m8_t vfwcvt_f_x_v_f32m8 (vint16m4_t src, size_t vl);
vfloat32m2_t vfwcvt_f_xu_v_f32m2 (vuint16m1_t src, size_t vl);
vfloat32m4_t vfwcvt_f_xu_v_f32m4 (vuint16m2_t src, size_t vl);
vfloat32m8_t vfwcvt_f_xu_v_f32m8 (vuint16m4_t src, size_t vl);
vfloat32m2_t vfwcvt_f_f_v_f32m2 (vfloat16m1_t src, size_t vl);
vfloat32m4_t vfwcvt_f_f_v_f32m4 (vfloat16m2_t src, size_t vl);
vfloat32m8_t vfwcvt_f_f_v_f32m8 (vfloat16m4_t src, size_t vl);
vint64m2_t vfwcvt_x_f_v_i64m2 (vfloat32m1_t src, size_t vl);
vint64m4_t vfwcvt_x_f_v_i64m4 (vfloat32m2_t src, size_t vl);
vint64m8_t vfwcvt_x_f_v_i64m8 (vfloat32m4_t src, size_t vl);
vint64m2_t vwcvt_x_x_v_i64m2 (vint32m1_t src, size_t vl);
vint64m4_t vwcvt_x_x_v_i64m4 (vint32m2_t src, size_t vl);
vint64m8_t vwcvt_x_x_v_i64m8 (vint32m4_t src, size_t vl);
vuint64m2_t vwcvtu_x_x_v_u64m2 (vuint32m1_t src, size_t vl);
vuint64m4_t vwcvtu_x_x_v_u64m4 (vuint32m2_t src, size_t vl);
vuint64m8_t vwcvtu_x_x_v_u64m8 (vuint32m4_t src, size_t vl);
vuint64m2_t vfwcvt_xu_f_v_u64m2 (vfloat32m1_t src, size_t vl);
vuint64m4_t vfwcvt_xu_f_v_u64m4 (vfloat32m2_t src, size_t vl);
vuint64m8_t vfwcvt_xu_f_v_u64m8 (vfloat32m4_t src, size_t vl);
vfloat64m2_t vfwcvt_f_x_v_f64m2 (vint32m1_t src, size_t vl);
vfloat64m4_t vfwcvt_f_x_v_f64m4 (vint32m2_t src, size_t vl);
vfloat64m8_t vfwcvt_f_x_v_f64m8 (vint32m4_t src, size_t vl);
vfloat64m2_t vfwcvt_f_xu_v_f64m2 (vuint32m1_t src, size_t vl);
vfloat64m4_t vfwcvt_f_xu_v_f64m4 (vuint32m2_t src, size_t vl);
vfloat64m8_t vfwcvt_f_xu_v_f64m8 (vuint32m4_t src, size_t vl);
vfloat64m2_t vfwcvt_f_f_v_f64m2 (vfloat32m1_t src, size_t vl);
vfloat64m4_t vfwcvt_f_f_v_f64m4 (vfloat32m2_t src, size_t vl);
vfloat64m8_t vfwcvt_f_f_v_f64m8 (vfloat32m4_t src, size_t vl);
// masked functions
vint16m2_t vwcvt_x_x_v_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint8m1_t src, size_t vl);
vint16m4_t vwcvt_x_x_v_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint8m2_t src, size_t vl);
vint16m8_t vwcvt_x_x_v_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint8m4_t src, size_t vl);
vuint16m2_t vwcvtu_x_x_v_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint8m1_t src, size_t vl);
vuint16m4_t vwcvtu_x_x_v_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint8m2_t src, size_t vl);
vuint16m8_t vwcvtu_x_x_v_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint8m4_t src, size_t vl);
vfloat16m2_t vfwcvt_f_x_v_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vint8m1_t src, size_t vl);
vfloat16m4_t vfwcvt_f_x_v_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vint8m2_t src, size_t vl);

```

```

vfloat16m8_t vfwcvt_f_x_v_f16m8_m (vbool12_t mask, vfloat16m8_t
maskedoff, vint8m4_t src, size_t vl);
vfloat16m2_t vfwcvt_f_xu_v_f16m2_m (vbool8_t mask, vfloat16m2_t
maskedoff, vuint8m1_t src, size_t vl);
vfloat16m4_t vfwcvt_f_xu_v_f16m4_m (vbool4_t mask, vfloat16m4_t
maskedoff, vuint8m2_t src, size_t vl);
vfloat16m8_t vfwcvt_f_xu_v_f16m8_m (vbool12_t mask, vfloat16m8_t
maskedoff, vuint8m4_t src, size_t vl);
vint32m2_t vfwcvt_x_f_v_i32m2_m (vbool16_t mask, vint32m2_t
maskedoff, vfloat16m1_t src, size_t vl);
vint32m4_t vfwcvt_x_f_v_i32m4_m (vbool8_t mask, vint32m4_t
maskedoff, vfloat16m2_t src, size_t vl);
vint32m8_t vfwcvt_x_f_v_i32m8_m (vbool4_t mask, vint32m8_t
maskedoff, vfloat16m4_t src, size_t vl);
vint32m2_t vwcvt_x_x_v_i32m2_m (vbool16_t mask, vint32m2_t
maskedoff, vint16m1_t src, size_t vl);
vint32m4_t vwcvt_x_x_v_i32m4_m (vbool8_t mask, vint32m4_t
maskedoff, vint16m2_t src, size_t vl);
vint32m8_t vwcvt_x_x_v_i32m8_m (vbool4_t mask, vint32m8_t
maskedoff, vint16m4_t src, size_t vl);
vuint32m2_t vwcvtu_x_x_v_u32m2_m (vbool16_t mask, vuint32m2_t
maskedoff, vuint16m1_t src, size_t vl);
vuint32m4_t vwcvtu_x_x_v_u32m4_m (vbool8_t mask, vuint32m4_t
maskedoff, vuint16m2_t src, size_t vl);
vuint32m8_t vwcvtu_x_x_v_u32m8_m (vbool4_t mask, vuint32m8_t
maskedoff, vuint16m4_t src, size_t vl);
vuint32m2_t vfwcvt_xu_f_v_u32m2_m (vbool16_t mask, vuint32m2_t
maskedoff, vfloat16m1_t src, size_t vl);
vuint32m4_t vfwcvt_xu_f_v_u32m4_m (vbool8_t mask, vuint32m4_t
maskedoff, vfloat16m2_t src, size_t vl);
vuint32m8_t vfwcvt_xu_f_v_u32m8_m (vbool4_t mask, vuint32m8_t
maskedoff, vfloat16m4_t src, size_t vl);
vfloat32m2_t vfwcvt_f_x_v_f32m2_m (vbool16_t mask, vfloat32m2_t
maskedoff, vint16m1_t src, size_t vl);
vfloat32m4_t vfwcvt_f_x_v_f32m4_m (vbool8_t mask, vfloat32m4_t
maskedoff, vint16m2_t src, size_t vl);
vfloat32m8_t vfwcvt_f_x_v_f32m8_m (vbool4_t mask, vfloat32m8_t
maskedoff, vint16m4_t src, size_t vl);
vfloat32m2_t vfwcvt_f_xu_v_f32m2_m (vbool16_t mask, vfloat32m2_t
maskedoff, vuint16m1_t src, size_t vl);
vfloat32m4_t vfwcvt_f_xu_v_f32m4_m (vbool8_t mask, vfloat32m4_t
maskedoff, vuint16m2_t src, size_t vl);
vfloat32m8_t vfwcvt_f_xu_v_f32m8_m (vbool4_t mask, vfloat32m8_t
maskedoff, vuint16m4_t src, size_t vl);
vfloat32m2_t vfwcvt_f_f_v_f32m2_m (vbool16_t mask, vfloat32m2_t
maskedoff, vfloat16m1_t src, size_t vl);

```

```

vfloat32m4_t vfwcvt_f_f_v_f32m4_m (vbool8_t mask, vfloat32m4_t
maskedoff, vfloat16m2_t src, size_t vl);
vfloat32m8_t vfwcvt_f_f_v_f32m8_m (vbool4_t mask, vfloat32m8_t
maskedoff, vfloat16m4_t src, size_t vl);
vint64m2_t vfwcvt_x_f_v_i64m2_m (vbool32_t mask, vint64m2_t
maskedoff, vfloat32m1_t src, size_t vl);
vint64m4_t vfwcvt_x_f_v_i64m4_m (vbool16_t mask, vint64m4_t
maskedoff, vfloat32m2_t src, size_t vl);
vint64m8_t vfwcvt_x_f_v_i64m8_m (vbool8_t mask, vint64m8_t
maskedoff, vfloat32m4_t src, size_t vl);
vint64m2_t vwcvt_x_x_v_i64m2_m (vbool32_t mask, vint64m2_t
maskedoff, vint32m1_t src, size_t vl);
vint64m4_t vwcvt_x_x_v_i64m4_m (vbool16_t mask, vint64m4_t
maskedoff, vint32m2_t src, size_t vl);
vint64m8_t vwcvt_x_x_v_i64m8_m (vbool8_t mask, vint64m8_t
maskedoff, vint32m4_t src, size_t vl);
vuint64m2_t vwcvtu_x_x_v_u64m2_m (vbool32_t mask, vuint64m2_t
maskedoff, vuint32m1_t src, size_t vl);
vuint64m4_t vwcvtu_x_x_v_u64m4_m (vbool16_t mask, vuint64m4_t
maskedoff, vuint32m2_t src, size_t vl);
vuint64m8_t vwcvtu_x_x_v_u64m8_m (vbool8_t mask, vuint64m8_t
maskedoff, vuint32m4_t src, size_t vl);
vuint64m2_t vfwcvt_xu_f_v_u64m2_m (vbool32_t mask, vuint64m2_t
maskedoff, vfloat32m1_t src, size_t vl);
vuint64m4_t vfwcvt_xu_f_v_u64m4_m (vbool16_t mask, vuint64m4_t
maskedoff, vfloat32m2_t src, size_t vl);
vuint64m8_t vfwcvt_xu_f_v_u64m8_m (vbool8_t mask, vuint64m8_t
maskedoff, vfloat32m4_t src, size_t vl);
vfloat64m2_t vfwcvt_f_x_v_f64m2_m (vbool32_t mask, vfloat64m2_t
maskedoff, vint32m1_t src, size_t vl);
vfloat64m4_t vfwcvt_f_x_v_f64m4_m (vbool16_t mask, vfloat64m4_t
maskedoff, vint32m2_t src, size_t vl);
vfloat64m8_t vfwcvt_f_x_v_f64m8_m (vbool8_t mask, vfloat64m8_t
maskedoff, vint32m4_t src, size_t vl);
vfloat64m2_t vfwcvt_f_xu_v_f64m2_m (vbool32_t mask, vfloat64m2_t
maskedoff, vuint32m1_t src, size_t vl);
vfloat64m4_t vfwcvt_f_xu_v_f64m4_m (vbool16_t mask, vfloat64m4_t
maskedoff, vuint32m2_t src, size_t vl);
vfloat64m8_t vfwcvt_f_xu_v_f64m8_m (vbool8_t mask, vfloat64m8_t
maskedoff, vuint32m4_t src, size_t vl);
vfloat64m2_t vfwcvt_f_f_v_f64m2_m (vbool32_t mask, vfloat64m2_t
maskedoff, vfloat32m1_t src, size_t vl);
vfloat64m4_t vfwcvt_f_f_v_f64m4_m (vbool16_t mask, vfloat64m4_t
maskedoff, vfloat32m2_t src, size_t vl);
vfloat64m8_t vfwcvt_f_f_v_f64m8_m (vbool8_t mask, vfloat64m8_t
maskedoff, vfloat32m4_t src, size_t vl);

```

## Narrowing Floating-Point/Integer Type-Convert Functions:

### Prototypes:

```
vint8m1_t vfncvt_x_f_w_i8m1 (vfloat16m2_t src, size_t vl);
vint8m2_t vfncvt_x_f_w_i8m2 (vfloat16m4_t src, size_t vl);
vint8m4_t vfncvt_x_f_w_i8m4 (vfloat16m8_t src, size_t vl);
vint8m1_t vncvt_x_x_w_i8m1 (vint16m2_t src, size_t vl);
vint8m2_t vncvt_x_x_w_i8m2 (vint16m4_t src, size_t vl);
vint8m4_t vncvt_x_x_w_i8m4 (vint16m8_t src, size_t vl);
vuint8m1_t vncvt_x_x_w_u8m1 (vuint16m2_t src, size_t vl);
vuint8m2_t vncvt_x_x_w_u8m2 (vuint16m4_t src, size_t vl);
vuint8m4_t vncvt_x_x_w_u8m4 (vuint16m8_t src, size_t vl);
vuint8m1_t vfncvt_xu_f_w_u8m1 (vfloat16m2_t src, size_t vl);
vuint8m2_t vfncvt_xu_f_w_u8m2 (vfloat16m4_t src, size_t vl);
vuint8m4_t vfncvt_xu_f_w_u8m4 (vfloat16m8_t src, size_t vl);
vint16m1_t vfncvt_x_f_w_i16m1 (vfloat32m2_t src, size_t vl);
vint16m2_t vfncvt_x_f_w_i16m2 (vfloat32m4_t src, size_t vl);
vint16m4_t vfncvt_x_f_w_i16m4 (vfloat32m8_t src, size_t vl);
vint16m1_t vncvt_x_x_w_i16m1 (vint32m2_t src, size_t vl);
vint16m2_t vncvt_x_x_w_i16m2 (vint32m4_t src, size_t vl);
vint16m4_t vncvt_x_x_w_i16m4 (vint32m8_t src, size_t vl);
vuint16m1_t vncvt_x_x_w_u16m1 (vuint32m2_t src, size_t vl);
vuint16m2_t vncvt_x_x_w_u16m2 (vuint32m4_t src, size_t vl);
vuint16m4_t vncvt_x_x_w_u16m4 (vuint32m8_t src, size_t vl);
vuint16m1_t vfncvt_xu_f_w_u16m1 (vfloat32m2_t src, size_t vl);
vuint16m2_t vfncvt_xu_f_w_u16m2 (vfloat32m4_t src, size_t vl);
vuint16m4_t vfncvt_xu_f_w_u16m4 (vfloat32m8_t src, size_t vl);
vfloat16m1_t vfncvt_f_x_w_f16m1 (vint32m2_t src, size_t vl);
vfloat16m2_t vfncvt_f_x_w_f16m2 (vint32m4_t src, size_t vl);
vfloat16m4_t vfncvt_f_x_w_f16m4 (vint32m8_t src, size_t vl);
vfloat16m1_t vfncvt_f_xu_w_f16m1 (vuint32m2_t src, size_t vl);
vfloat16m2_t vfncvt_f_xu_w_f16m2 (vuint32m4_t src, size_t vl);
vfloat16m4_t vfncvt_f_xu_w_f16m4 (vuint32m8_t src, size_t vl);
vfloat16m1_t vfncvt_f_f_w_f16m1 (vfloat32m2_t src, size_t vl);
vfloat16m2_t vfncvt_f_f_w_f16m2 (vfloat32m4_t src, size_t vl);
vfloat16m4_t vfncvt_f_f_w_f16m4 (vfloat32m8_t src, size_t vl);
vint32m1_t vfncvt_x_f_w_i32m1 (vfloat64m2_t src, size_t vl);
vint32m2_t vfncvt_x_f_w_i32m2 (vfloat64m4_t src, size_t vl);
vint32m4_t vfncvt_x_f_w_i32m4 (vfloat64m8_t src, size_t vl);
vint32m1_t vncvt_x_x_w_i32m1 (vint64m2_t src, size_t vl);
vint32m2_t vncvt_x_x_w_i32m2 (vint64m4_t src, size_t vl);
vint32m4_t vncvt_x_x_w_i32m4 (vint64m8_t src, size_t vl);
vuint32m1_t vncvt_x_x_w_u32m1 (vuint64m2_t src, size_t vl);
vuint32m2_t vncvt_x_x_w_u32m2 (vuint64m4_t src, size_t vl);
vuint32m4_t vncvt_x_x_w_u32m4 (vuint64m8_t src, size_t vl);
vuint32m1_t vfncvt_xu_f_w_u32m1 (vfloat64m2_t src, size_t vl);
```



```

vuint32m2_t vfncvt_xu_f_w_u32m2 (vfloat64m4_t src, size_t vl);
vuint32m4_t vfncvt_xu_f_w_u32m4 (vfloat64m8_t src, size_t vl);
vfloat32m1_t vfncvt_f_x_w_f32m1 (vint64m2_t src, size_t vl);
vfloat32m2_t vfncvt_f_x_w_f32m2 (vint64m4_t src, size_t vl);
vfloat32m4_t vfncvt_f_x_w_f32m4 (vint64m8_t src, size_t vl);
vfloat32m1_t vfncvt_f_xu_w_f32m1 (vuint64m2_t src, size_t vl);
vfloat32m2_t vfncvt_f_xu_w_f32m2 (vuint64m4_t src, size_t vl);
vfloat32m4_t vfncvt_f_xu_w_f32m4 (vuint64m8_t src, size_t vl);
vfloat32m1_t vfncvt_f_f_w_f32m1 (vfloat64m2_t src, size_t vl);
vfloat32m2_t vfncvt_f_f_w_f32m2 (vfloat64m4_t src, size_t vl);
vfloat32m4_t vfncvt_f_f_w_f32m4 (vfloat64m8_t src, size_t vl);
// masked functions
vint8m1_t vfncvt_x_f_w_i8m1_m (vbool8_t mask, vint8m1_t
    maskedoff, vfloat16m2_t src, size_t vl);
vint8m2_t vfncvt_x_f_w_i8m2_m (vbool4_t mask, vint8m2_t
    maskedoff, vfloat16m4_t src, size_t vl);
vint8m4_t vfncvt_x_f_w_i8m4_m (vbool2_t mask, vint8m4_t
    maskedoff, vfloat16m8_t src, size_t vl);
vint8m1_t vncvt_x_x_w_i8m1_m (vbool8_t mask, vint8m1_t
    maskedoff, vint16m2_t src, size_t vl);
vint8m2_t vncvt_x_x_w_i8m2_m (vbool4_t mask, vint8m2_t
    maskedoff, vint16m4_t src, size_t vl);
vint8m4_t vncvt_x_x_w_i8m4_m (vbool2_t mask, vint8m4_t
    maskedoff, vint16m8_t src, size_t vl);
vuint8m1_t vncvt_x_x_w_u8m1_m (vbool8_t mask, vuint8m1_t
    maskedoff, vuint16m2_t src, size_t vl);
vuint8m2_t vncvt_x_x_w_u8m2_m (vbool4_t mask, vuint8m2_t
    maskedoff, vuint16m4_t src, size_t vl);
vuint8m4_t vncvt_x_x_w_u8m4_m (vbool2_t mask, vuint8m4_t
    maskedoff, vuint16m8_t src, size_t vl);
vuint8m1_t vfncvt_xu_f_w_u8m1_m (vbool8_t mask, vuint8m1_t
    maskedoff, vfloat16m2_t src, size_t vl);
vuint8m2_t vfncvt_xu_f_w_u8m2_m (vbool4_t mask, vuint8m2_t
    maskedoff, vfloat16m4_t src, size_t vl);
vuint8m4_t vfncvt_xu_f_w_u8m4_m (vbool2_t mask, vuint8m4_t
    maskedoff, vfloat16m8_t src, size_t vl);
vint16m1_t vfncvt_x_f_w_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vfloat32m2_t src, size_t vl);
vint16m2_t vfncvt_x_f_w_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vfloat32m4_t src, size_t vl);
vint16m4_t vfncvt_x_f_w_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vfloat32m8_t src, size_t vl);
vint16m1_t vncvt_x_x_w_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint32m2_t src, size_t vl);
vint16m2_t vncvt_x_x_w_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint32m4_t src, size_t vl);

```

```

vint16m4_t vncvt_x_x_w_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint32m8_t src, size_t vl);
vuint16m1_t vncvt_x_x_w_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint32m2_t src, size_t vl);
vuint16m2_t vncvt_x_x_w_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint32m4_t src, size_t vl);
vuint16m4_t vncvt_x_x_w_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint32m8_t src, size_t vl);
vuint16m1_t vfncvt_xu_f_w_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vfloat32m2_t src, size_t vl);
vuint16m2_t vfncvt_xu_f_w_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vfloat32m4_t src, size_t vl);
vuint16m4_t vfncvt_xu_f_w_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vfloat32m8_t src, size_t vl);
vfloat16m1_t vfncvt_f_x_w_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vint32m2_t src, size_t vl);
vfloat16m2_t vfncvt_f_x_w_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vint32m4_t src, size_t vl);
vfloat16m4_t vfncvt_f_x_w_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vint32m8_t src, size_t vl);
vfloat16m1_t vfncvt_f_xu_w_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vuint32m2_t src, size_t vl);
vfloat16m2_t vfncvt_f_xu_w_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vuint32m4_t src, size_t vl);
vfloat16m4_t vfncvt_f_xu_w_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vuint32m8_t src, size_t vl);
vfloat16m1_t vfncvt_f_f_w_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat32m2_t src, size_t vl);
vfloat16m2_t vfncvt_f_f_w_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat32m4_t src, size_t vl);
vfloat16m4_t vfncvt_f_f_w_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat32m8_t src, size_t vl);
vint32m1_t vfncvt_x_f_w_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vfloat64m2_t src, size_t vl);
vint32m2_t vfncvt_x_f_w_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vfloat64m4_t src, size_t vl);
vint32m4_t vfncvt_x_f_w_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vfloat64m8_t src, size_t vl);
vint32m1_t vncvt_x_x_w_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint64m2_t src, size_t vl);
vint32m2_t vncvt_x_x_w_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint64m4_t src, size_t vl);
vint32m4_t vncvt_x_x_w_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint64m8_t src, size_t vl);
vuint32m1_t vncvt_x_x_w_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint64m2_t src, size_t vl);

```

```

vuint32m2_t vncvt_x_x_w_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint64m4_t src, size_t vl);
vuint32m4_t vncvt_x_x_w_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint64m8_t src, size_t vl);
vuint32m1_t vfncvt_xu_f_w_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vfloat64m2_t src, size_t vl);
vuint32m2_t vfncvt_xu_f_w_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vfloat64m4_t src, size_t vl);
vuint32m4_t vfncvt_xu_f_w_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vfloat64m8_t src, size_t vl);
vfloat32m1_t vfncvt_f_x_w_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vint64m2_t src, size_t vl);
vfloat32m2_t vfncvt_f_x_w_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vint64m4_t src, size_t vl);
vfloat32m4_t vfncvt_f_x_w_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vint64m8_t src, size_t vl);
vfloat32m1_t vfncvt_f_xu_w_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vuint64m2_t src, size_t vl);
vfloat32m2_t vfncvt_f_xu_w_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vuint64m4_t src, size_t vl);
vfloat32m4_t vfncvt_f_xu_w_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vuint64m8_t src, size_t vl);
vfloat32m1_t vfncvt_f_f_w_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat64m2_t src, size_t vl);
vfloat32m2_t vfncvt_f_f_w_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat64m4_t src, size_t vl);
vfloat32m4_t vfncvt_f_f_w_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat64m8_t src, size_t vl);

```

## Vector Reduction Functions:

### Vector Single-Width Integer Reduction Functions:

#### Prototypes:

```

vint8m1_t vredsum_vs_i8m1_i8m1 (vint8m1_t dest, vint8m1_t
    vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredsum_vs_i8m2_i8m1 (vint8m1_t dest, vint8m2_t
    vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredsum_vs_i8m4_i8m1 (vint8m1_t dest, vint8m4_t
    vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredsum_vs_i8m8_i8m1 (vint8m1_t dest, vint8m8_t
    vector, vint8m1_t scalar, size_t vl);
vint16m1_t vredsum_vs_i16m1_i16m1 (vint16m1_t dest, vint16m1_t
    vector, vint16m1_t scalar, size_t vl);

```

```

vint16m1_t vredsum_vs_i16m2_i16m1 (vint16m1_t dest, vint16m2_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredsum_vs_i16m4_i16m1 (vint16m1_t dest, vint16m4_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredsum_vs_i16m8_i16m1 (vint16m1_t dest, vint16m8_t
    vector, vint16m1_t scalar, size_t vl);
vint32m1_t vredsum_vs_i32m1_i32m1 (vint32m1_t dest, vint32m1_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredsum_vs_i32m2_i32m1 (vint32m1_t dest, vint32m2_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredsum_vs_i32m4_i32m1 (vint32m1_t dest, vint32m4_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredsum_vs_i32m8_i32m1 (vint32m1_t dest, vint32m8_t
    vector, vint32m1_t scalar, size_t vl);
vint64m1_t vredsum_vs_i64m1_i64m1 (vint64m1_t dest, vint64m1_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredsum_vs_i64m2_i64m1 (vint64m1_t dest, vint64m2_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredsum_vs_i64m4_i64m1 (vint64m1_t dest, vint64m4_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredsum_vs_i64m8_i64m1 (vint64m1_t dest, vint64m8_t
    vector, vint64m1_t scalar, size_t vl);
vuint8m1_t vredsum_vs_u8m1_u8m1 (vuint8m1_t dest, vuint8m1_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredsum_vs_u8m2_u8m1 (vuint8m1_t dest, vuint8m2_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredsum_vs_u8m4_u8m1 (vuint8m1_t dest, vuint8m4_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredsum_vs_u8m8_u8m1 (vuint8m1_t dest, vuint8m8_t
    vector, vuint8m1_t scalar, size_t vl);
vuint16m1_t vredsum_vs_u16m1_u16m1 (vuint16m1_t dest,
    vuint16m1_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredsum_vs_u16m2_u16m1 (vuint16m1_t dest,
    vuint16m2_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredsum_vs_u16m4_u16m1 (vuint16m1_t dest,
    vuint16m4_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredsum_vs_u16m8_u16m1 (vuint16m1_t dest,
    vuint16m8_t vector, vuint16m1_t scalar, size_t vl);
vuint32m1_t vredsum_vs_u32m1_u32m1 (vuint32m1_t dest,
    vuint32m1_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredsum_vs_u32m2_u32m1 (vuint32m1_t dest,
    vuint32m2_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredsum_vs_u32m4_u32m1 (vuint32m1_t dest,
    vuint32m4_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredsum_vs_u32m8_u32m1 (vuint32m1_t dest,
    vuint32m8_t vector, vuint32m1_t scalar, size_t vl);

```

```

vuint64m1_t vredsum_vs_u64m1_u64m1 (vuint64m1_t dest,
    vuint64m1_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredsum_vs_u64m2_u64m1 (vuint64m1_t dest,
    vuint64m2_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredsum_vs_u64m4_u64m1 (vuint64m1_t dest,
    vuint64m4_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredsum_vs_u64m8_u64m1 (vuint64m1_t dest,
    vuint64m8_t vector, vuint64m1_t scalar, size_t vl);
vint8m1_t vredmax_vs_i8m1_i8m1 (vint8m1_t dest, vint8m1_t
    vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredmax_vs_i8m2_i8m1 (vint8m1_t dest, vint8m2_t
    vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredmax_vs_i8m4_i8m1 (vint8m1_t dest, vint8m4_t
    vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredmax_vs_i8m8_i8m1 (vint8m1_t dest, vint8m8_t
    vector, vint8m1_t scalar, size_t vl);
vint16m1_t vredmax_vs_i16m1_i16m1 (vint16m1_t dest, vint16m1_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredmax_vs_i16m2_i16m1 (vint16m1_t dest, vint16m2_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredmax_vs_i16m4_i16m1 (vint16m1_t dest, vint16m4_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredmax_vs_i16m8_i16m1 (vint16m1_t dest, vint16m8_t
    vector, vint16m1_t scalar, size_t vl);
vint32m1_t vredmax_vs_i32m1_i32m1 (vint32m1_t dest, vint32m1_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredmax_vs_i32m2_i32m1 (vint32m1_t dest, vint32m2_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredmax_vs_i32m4_i32m1 (vint32m1_t dest, vint32m4_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredmax_vs_i32m8_i32m1 (vint32m1_t dest, vint32m8_t
    vector, vint32m1_t scalar, size_t vl);
vint64m1_t vredmax_vs_i64m1_i64m1 (vint64m1_t dest, vint64m1_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredmax_vs_i64m2_i64m1 (vint64m1_t dest, vint64m2_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredmax_vs_i64m4_i64m1 (vint64m1_t dest, vint64m4_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredmax_vs_i64m8_i64m1 (vint64m1_t dest, vint64m8_t
    vector, vint64m1_t scalar, size_t vl);
vuint8m1_t vredmaxu_vs_u8m1_u8m1 (vuint8m1_t dest, vuint8m1_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredmaxu_vs_u8m2_u8m1 (vuint8m1_t dest, vuint8m2_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredmaxu_vs_u8m4_u8m1 (vuint8m1_t dest, vuint8m4_t
    vector, vuint8m1_t scalar, size_t vl);

```

```

vuint8m1_t vredmaxu_vs_u8m8_u8m1 (vuint8m1_t dest, vuint8m8_t
    vector, vuint8m1_t scalar, size_t vl);
vuint16m1_t vredmaxu_vs_u16m1_u16m1 (vuint16m1_t dest,
    vuint16m1_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredmaxu_vs_u16m2_u16m1 (vuint16m1_t dest,
    vuint16m2_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredmaxu_vs_u16m4_u16m1 (vuint16m1_t dest,
    vuint16m4_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredmaxu_vs_u16m8_u16m1 (vuint16m1_t dest,
    vuint16m8_t vector, vuint16m1_t scalar, size_t vl);
vuint32m1_t vredmaxu_vs_u32m1_u32m1 (vuint32m1_t dest,
    vuint32m1_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredmaxu_vs_u32m2_u32m1 (vuint32m1_t dest,
    vuint32m2_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredmaxu_vs_u32m4_u32m1 (vuint32m1_t dest,
    vuint32m4_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredmaxu_vs_u32m8_u32m1 (vuint32m1_t dest,
    vuint32m8_t vector, vuint32m1_t scalar, size_t vl);
vuint64m1_t vredmaxu_vs_u64m1_u64m1 (vuint64m1_t dest,
    vuint64m1_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredmaxu_vs_u64m2_u64m1 (vuint64m1_t dest,
    vuint64m2_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredmaxu_vs_u64m4_u64m1 (vuint64m1_t dest,
    vuint64m4_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredmaxu_vs_u64m8_u64m1 (vuint64m1_t dest,
    vuint64m8_t vector, vuint64m1_t scalar, size_t vl);
vint8m1_t vredmin_vs_i8m1_i8m1 (vint8m1_t dest, vint8m1_t
    vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredmin_vs_i8m2_i8m1 (vint8m1_t dest, vint8m2_t
    vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredmin_vs_i8m4_i8m1 (vint8m1_t dest, vint8m4_t
    vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredmin_vs_i8m8_i8m1 (vint8m1_t dest, vint8m8_t
    vector, vint8m1_t scalar, size_t vl);
vint16m1_t vredmin_vs_i16m1_i16m1 (vint16m1_t dest, vint16m1_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredmin_vs_i16m2_i16m1 (vint16m1_t dest, vint16m2_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredmin_vs_i16m4_i16m1 (vint16m1_t dest, vint16m4_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredmin_vs_i16m8_i16m1 (vint16m1_t dest, vint16m8_t
    vector, vint16m1_t scalar, size_t vl);
vint32m1_t vredmin_vs_i32m1_i32m1 (vint32m1_t dest, vint32m1_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredmin_vs_i32m2_i32m1 (vint32m1_t dest, vint32m2_t
    vector, vint32m1_t scalar, size_t vl);

```

```

vint32m1_t vredmin_vs_i32m4_i32m1 (vint32m1_t dest, vint32m4_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredmin_vs_i32m8_i32m1 (vint32m1_t dest, vint32m8_t
    vector, vint32m1_t scalar, size_t vl);
vint64m1_t vredmin_vs_i64m1_i64m1 (vint64m1_t dest, vint64m1_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredmin_vs_i64m2_i64m1 (vint64m1_t dest, vint64m2_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredmin_vs_i64m4_i64m1 (vint64m1_t dest, vint64m4_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredmin_vs_i64m8_i64m1 (vint64m1_t dest, vint64m8_t
    vector, vint64m1_t scalar, size_t vl);
vuint8m1_t vredminu_vs_u8m1_u8m1 (vuint8m1_t dest, vuint8m1_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredminu_vs_u8m2_u8m1 (vuint8m1_t dest, vuint8m2_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredminu_vs_u8m4_u8m1 (vuint8m1_t dest, vuint8m4_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredminu_vs_u8m8_u8m1 (vuint8m1_t dest, vuint8m8_t
    vector, vuint8m1_t scalar, size_t vl);
vuint16m1_t vredminu_vs_u16m1_u16m1 (vuint16m1_t dest,
    vuint16m1_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredminu_vs_u16m2_u16m1 (vuint16m1_t dest,
    vuint16m2_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredminu_vs_u16m4_u16m1 (vuint16m1_t dest,
    vuint16m4_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredminu_vs_u16m8_u16m1 (vuint16m1_t dest,
    vuint16m8_t vector, vuint16m1_t scalar, size_t vl);
vuint32m1_t vredminu_vs_u32m1_u32m1 (vuint32m1_t dest,
    vuint32m1_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredminu_vs_u32m2_u32m1 (vuint32m1_t dest,
    vuint32m2_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredminu_vs_u32m4_u32m1 (vuint32m1_t dest,
    vuint32m4_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredminu_vs_u32m8_u32m1 (vuint32m1_t dest,
    vuint32m8_t vector, vuint32m1_t scalar, size_t vl);
vuint64m1_t vredminu_vs_u64m1_u64m1 (vuint64m1_t dest,
    vuint64m1_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredminu_vs_u64m2_u64m1 (vuint64m1_t dest,
    vuint64m2_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredminu_vs_u64m4_u64m1 (vuint64m1_t dest,
    vuint64m4_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredminu_vs_u64m8_u64m1 (vuint64m1_t dest,
    vuint64m8_t vector, vuint64m1_t scalar, size_t vl);
vint8m1_t vredand_vs_i8m1_i8m1 (vint8m1_t dest, vint8m1_t
    vector, vint8m1_t scalar, size_t vl);

```

```

vint8m1_t vredand_vs_i8m2_i8m1 (vint8m1_t dest, vint8m2_t
    vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredand_vs_i8m4_i8m1 (vint8m1_t dest, vint8m4_t
    vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredand_vs_i8m8_i8m1 (vint8m1_t dest, vint8m8_t
    vector, vint8m1_t scalar, size_t vl);
vint16m1_t vredand_vs_i16m1_i16m1 (vint16m1_t dest, vint16m1_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredand_vs_i16m2_i16m1 (vint16m1_t dest, vint16m2_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredand_vs_i16m4_i16m1 (vint16m1_t dest, vint16m4_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredand_vs_i16m8_i16m1 (vint16m1_t dest, vint16m8_t
    vector, vint16m1_t scalar, size_t vl);
vint32m1_t vredand_vs_i32m1_i32m1 (vint32m1_t dest, vint32m1_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredand_vs_i32m2_i32m1 (vint32m1_t dest, vint32m2_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredand_vs_i32m4_i32m1 (vint32m1_t dest, vint32m4_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredand_vs_i32m8_i32m1 (vint32m1_t dest, vint32m8_t
    vector, vint32m1_t scalar, size_t vl);
vint64m1_t vredand_vs_i64m1_i64m1 (vint64m1_t dest, vint64m1_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredand_vs_i64m2_i64m1 (vint64m1_t dest, vint64m2_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredand_vs_i64m4_i64m1 (vint64m1_t dest, vint64m4_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredand_vs_i64m8_i64m1 (vint64m1_t dest, vint64m8_t
    vector, vint64m1_t scalar, size_t vl);
vuint8m1_t vredand_vs_u8m1_u8m1 (vuint8m1_t dest, vuint8m1_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredand_vs_u8m2_u8m1 (vuint8m1_t dest, vuint8m2_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredand_vs_u8m4_u8m1 (vuint8m1_t dest, vuint8m4_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredand_vs_u8m8_u8m1 (vuint8m1_t dest, vuint8m8_t
    vector, vuint8m1_t scalar, size_t vl);
vuint16m1_t vredand_vs_u16m1_u16m1 (vuint16m1_t dest,
    vuint16m1_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredand_vs_u16m2_u16m1 (vuint16m1_t dest,
    vuint16m2_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredand_vs_u16m4_u16m1 (vuint16m1_t dest,
    vuint16m4_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredand_vs_u16m8_u16m1 (vuint16m1_t dest,
    vuint16m8_t vector, vuint16m1_t scalar, size_t vl);

```



```

vuint32m1_t vredand_vs_u32m1_u32m1 (vuint32m1_t dest,
    vuint32m1_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredand_vs_u32m2_u32m1 (vuint32m1_t dest,
    vuint32m2_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredand_vs_u32m4_u32m1 (vuint32m1_t dest,
    vuint32m4_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredand_vs_u32m8_u32m1 (vuint32m1_t dest,
    vuint32m8_t vector, vuint32m1_t scalar, size_t vl);
vuint64m1_t vredand_vs_u64m1_u64m1 (vuint64m1_t dest,
    vuint64m1_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredand_vs_u64m2_u64m1 (vuint64m1_t dest,
    vuint64m2_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredand_vs_u64m4_u64m1 (vuint64m1_t dest,
    vuint64m4_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredand_vs_u64m8_u64m1 (vuint64m1_t dest,
    vuint64m8_t vector, vuint64m1_t scalar, size_t vl);
vint8m1_t vredor_vs_i8m1_i8m1 (vint8m1_t dest, vint8m1_t vector,
    vint8m1_t scalar, size_t vl);
vint8m1_t vredor_vs_i8m2_i8m1 (vint8m1_t dest, vint8m2_t vector,
    vint8m1_t scalar, size_t vl);
vint8m1_t vredor_vs_i8m4_i8m1 (vint8m1_t dest, vint8m4_t vector,
    vint8m1_t scalar, size_t vl);
vint8m1_t vredor_vs_i8m8_i8m1 (vint8m1_t dest, vint8m8_t vector,
    vint8m1_t scalar, size_t vl);
vint16m1_t vredor_vs_i16m1_i16m1 (vint16m1_t dest, vint16m1_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredor_vs_i16m2_i16m1 (vint16m1_t dest, vint16m2_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredor_vs_i16m4_i16m1 (vint16m1_t dest, vint16m4_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredor_vs_i16m8_i16m1 (vint16m1_t dest, vint16m8_t
    vector, vint16m1_t scalar, size_t vl);
vint32m1_t vredor_vs_i32m1_i32m1 (vint32m1_t dest, vint32m1_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredor_vs_i32m2_i32m1 (vint32m1_t dest, vint32m2_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredor_vs_i32m4_i32m1 (vint32m1_t dest, vint32m4_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredor_vs_i32m8_i32m1 (vint32m1_t dest, vint32m8_t
    vector, vint32m1_t scalar, size_t vl);
vint64m1_t vredor_vs_i64m1_i64m1 (vint64m1_t dest, vint64m1_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredor_vs_i64m2_i64m1 (vint64m1_t dest, vint64m2_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredor_vs_i64m4_i64m1 (vint64m1_t dest, vint64m4_t
    vector, vint64m1_t scalar, size_t vl);

```

```

vint64m1_t vredor_vs_i64m8_i64m1 (vint64m1_t dest, vint64m8_t
    vector, vint64m1_t scalar, size_t vl);
vuint8m1_t vredor_vs_u8m1_u8m1 (vuint8m1_t dest, vuint8m1_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredor_vs_u8m2_u8m1 (vuint8m1_t dest, vuint8m2_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredor_vs_u8m4_u8m1 (vuint8m1_t dest, vuint8m4_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredor_vs_u8m8_u8m1 (vuint8m1_t dest, vuint8m8_t
    vector, vuint8m1_t scalar, size_t vl);
vuint16m1_t vredor_vs_u16m1_u16m1 (vuint16m1_t dest, vuint16m1_t
    vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredor_vs_u16m2_u16m1 (vuint16m1_t dest, vuint16m2_t
    vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredor_vs_u16m4_u16m1 (vuint16m1_t dest, vuint16m4_t
    vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredor_vs_u16m8_u16m1 (vuint16m1_t dest, vuint16m8_t
    vector, vuint16m1_t scalar, size_t vl);
vuint32m1_t vredor_vs_u32m1_u32m1 (vuint32m1_t dest, vuint32m1_t
    vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredor_vs_u32m2_u32m1 (vuint32m1_t dest, vuint32m2_t
    vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredor_vs_u32m4_u32m1 (vuint32m1_t dest, vuint32m4_t
    vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredor_vs_u32m8_u32m1 (vuint32m1_t dest, vuint32m8_t
    vector, vuint32m1_t scalar, size_t vl);
vuint64m1_t vredor_vs_u64m1_u64m1 (vuint64m1_t dest, vuint64m1_t
    vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredor_vs_u64m2_u64m1 (vuint64m1_t dest, vuint64m2_t
    vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredor_vs_u64m4_u64m1 (vuint64m1_t dest, vuint64m4_t
    vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredor_vs_u64m8_u64m1 (vuint64m1_t dest, vuint64m8_t
    vector, vuint64m1_t scalar, size_t vl);
vint8m1_t vredxor_vs_i8m1_i8m1 (vint8m1_t dest, vint8m1_t
    vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredxor_vs_i8m2_i8m1 (vint8m1_t dest, vint8m2_t
    vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredxor_vs_i8m4_i8m1 (vint8m1_t dest, vint8m4_t
    vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredxor_vs_i8m8_i8m1 (vint8m1_t dest, vint8m8_t
    vector, vint8m1_t scalar, size_t vl);
vint16m1_t vredxor_vs_i16m1_i16m1 (vint16m1_t dest, vint16m1_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredxor_vs_i16m2_i16m1 (vint16m1_t dest, vint16m2_t
    vector, vint16m1_t scalar, size_t vl);

```

```

vint16m1_t vredxor_vs_i16m4_i16m1 (vint16m1_t dest, vint16m4_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredxor_vs_i16m8_i16m1 (vint16m1_t dest, vint16m8_t
    vector, vint16m1_t scalar, size_t vl);
vint32m1_t vredxor_vs_i32m1_i32m1 (vint32m1_t dest, vint32m1_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredxor_vs_i32m2_i32m1 (vint32m1_t dest, vint32m2_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredxor_vs_i32m4_i32m1 (vint32m1_t dest, vint32m4_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredxor_vs_i32m8_i32m1 (vint32m1_t dest, vint32m8_t
    vector, vint32m1_t scalar, size_t vl);
vint64m1_t vredxor_vs_i64m1_i64m1 (vint64m1_t dest, vint64m1_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredxor_vs_i64m2_i64m1 (vint64m1_t dest, vint64m2_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredxor_vs_i64m4_i64m1 (vint64m1_t dest, vint64m4_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredxor_vs_i64m8_i64m1 (vint64m1_t dest, vint64m8_t
    vector, vint64m1_t scalar, size_t vl);
vuint8m1_t vredxor_vs_u8m1_u8m1 (vuint8m1_t dest, vuint8m1_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredxor_vs_u8m2_u8m1 (vuint8m1_t dest, vuint8m2_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredxor_vs_u8m4_u8m1 (vuint8m1_t dest, vuint8m4_t
    vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredxor_vs_u8m8_u8m1 (vuint8m1_t dest, vuint8m8_t
    vector, vuint8m1_t scalar, size_t vl);
vuint16m1_t vredxor_vs_u16m1_u16m1 (vuint16m1_t dest,
    vuint16m1_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredxor_vs_u16m2_u16m1 (vuint16m1_t dest,
    vuint16m2_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredxor_vs_u16m4_u16m1 (vuint16m1_t dest,
    vuint16m4_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredxor_vs_u16m8_u16m1 (vuint16m1_t dest,
    vuint16m8_t vector, vuint16m1_t scalar, size_t vl);
vuint32m1_t vredxor_vs_u32m1_u32m1 (vuint32m1_t dest,
    vuint32m1_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredxor_vs_u32m2_u32m1 (vuint32m1_t dest,
    vuint32m2_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredxor_vs_u32m4_u32m1 (vuint32m1_t dest,
    vuint32m4_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredxor_vs_u32m8_u32m1 (vuint32m1_t dest,
    vuint32m8_t vector, vuint32m1_t scalar, size_t vl);
vuint64m1_t vredxor_vs_u64m1_u64m1 (vuint64m1_t dest,
    vuint64m1_t vector, vuint64m1_t scalar, size_t vl);

```

```

vuint64m1_t vredxor_vs_u64m2_u64m1 (vuint64m1_t dest,
    vuint64m2_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredxor_vs_u64m4_u64m1 (vuint64m1_t dest,
    vuint64m4_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredxor_vs_u64m8_u64m1 (vuint64m1_t dest,
    vuint64m8_t vector, vuint64m1_t scalar, size_t vl);
// masked functions
vint8m1_t vredsum_vs_i8m1_i8m1_m (vbool8_t mask, vint8m1_t dest,
    vint8m1_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredsum_vs_i8m2_i8m1_m (vbool4_t mask, vint8m1_t dest,
    vint8m2_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredsum_vs_i8m4_i8m1_m (vbool2_t mask, vint8m1_t dest,
    vint8m4_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredsum_vs_i8m8_i8m1_m (vbool1_t mask, vint8m1_t dest,
    vint8m8_t vector, vint8m1_t scalar, size_t vl);
vint16m1_t vredsum_vs_i16m1_i16m1_m (vbool16_t mask, vint16m1_t
    dest, vint16m1_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredsum_vs_i16m2_i16m1_m (vbool8_t mask, vint16m1_t
    dest, vint16m2_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredsum_vs_i16m4_i16m1_m (vbool4_t mask, vint16m1_t
    dest, vint16m4_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredsum_vs_i16m8_i16m1_m (vbool2_t mask, vint16m1_t
    dest, vint16m8_t vector, vint16m1_t scalar, size_t vl);
vint32m1_t vredsum_vs_i32m1_i32m1_m (vbool32_t mask, vint32m1_t
    dest, vint32m1_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredsum_vs_i32m2_i32m1_m (vbool16_t mask, vint32m1_t
    dest, vint32m2_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredsum_vs_i32m4_i32m1_m (vbool8_t mask, vint32m1_t
    dest, vint32m4_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredsum_vs_i32m8_i32m1_m (vbool4_t mask, vint32m1_t
    dest, vint32m8_t vector, vint32m1_t scalar, size_t vl);
vint64m1_t vredsum_vs_i64m1_i64m1_m (vbool64_t mask, vint64m1_t
    dest, vint64m1_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredsum_vs_i64m2_i64m1_m (vbool32_t mask, vint64m1_t
    dest, vint64m2_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredsum_vs_i64m4_i64m1_m (vbool16_t mask, vint64m1_t
    dest, vint64m4_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredsum_vs_i64m8_i64m1_m (vbool8_t mask, vint64m1_t
    dest, vint64m8_t vector, vint64m1_t scalar, size_t vl);
vuint8m1_t vredsum_vs_u8m1_u8m1_m (vbool8_t mask, vuint8m1_t
    dest, vuint8m1_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredsum_vs_u8m2_u8m1_m (vbool4_t mask, vuint8m1_t
    dest, vuint8m2_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredsum_vs_u8m4_u8m1_m (vbool2_t mask, vuint8m1_t
    dest, vuint8m4_t vector, vuint8m1_t scalar, size_t vl);

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vuint8m1_t vredsum_vs_u8m8_u8m1_m (vbool1_t mask, vuint8m1_t
    dest, vuint8m8_t vector, vuint8m1_t scalar, size_t vl);
vuint16m1_t vredsum_vs_u16m1_u16m1_m (vbool16_t mask,
    vuint16m1_t dest, vuint16m1_t vector, vuint16m1_t scalar,
    size_t vl);
vuint16m1_t vredsum_vs_u16m2_u16m1_m (vbool8_t mask, vuint16m1_t
    dest, vuint16m2_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredsum_vs_u16m4_u16m1_m (vbool4_t mask, vuint16m1_t
    dest, vuint16m4_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredsum_vs_u16m8_u16m1_m (vbool2_t mask, vuint16m1_t
    dest, vuint16m8_t vector, vuint16m1_t scalar, size_t vl);
vuint32m1_t vredsum_vs_u32m1_u32m1_m (vbool32_t mask,
    vuint32m1_t dest, vuint32m1_t vector, vuint32m1_t scalar,
    size_t vl);
vuint32m1_t vredsum_vs_u32m2_u32m1_m (vbool16_t mask,
    vuint32m1_t dest, vuint32m2_t vector, vuint32m1_t scalar,
    size_t vl);
vuint32m1_t vredsum_vs_u32m4_u32m1_m (vbool8_t mask, vuint32m1_t
    dest, vuint32m4_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredsum_vs_u32m8_u32m1_m (vbool4_t mask, vuint32m1_t
    dest, vuint32m8_t vector, vuint32m1_t scalar, size_t vl);
vuint64m1_t vredsum_vs_u64m1_u64m1_m (vbool64_t mask,
    vuint64m1_t dest, vuint64m1_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vredsum_vs_u64m2_u64m1_m (vbool32_t mask,
    vuint64m1_t dest, vuint64m2_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vredsum_vs_u64m4_u64m1_m (vbool16_t mask,
    vuint64m1_t dest, vuint64m4_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vredsum_vs_u64m8_u64m1_m (vbool8_t mask, vuint64m1_t
    dest, vuint64m8_t vector, vuint64m1_t scalar, size_t vl);
vint8m1_t vredmax_vs_i8m1_i8m1_m (vbool8_t mask, vint8m1_t dest,
    vint8m1_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredmax_vs_i8m2_i8m1_m (vbool4_t mask, vint8m1_t dest,
    vint8m2_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredmax_vs_i8m4_i8m1_m (vbool2_t mask, vint8m1_t dest,
    vint8m4_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredmax_vs_i8m8_i8m1_m (vbool1_t mask, vint8m1_t dest,
    vint8m8_t vector, vint8m1_t scalar, size_t vl);
vint16m1_t vredmax_vs_i16m1_i16m1_m (vbool16_t mask, vint16m1_t
    dest, vint16m1_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredmax_vs_i16m2_i16m1_m (vbool8_t mask, vint16m1_t
    dest, vint16m2_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredmax_vs_i16m4_i16m1_m (vbool4_t mask, vint16m1_t
    dest, vint16m4_t vector, vint16m1_t scalar, size_t vl);

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vint16m1_t vredmax_vs_i16m8_i16m1_m (vbool2_t mask, vint16m1_t
    dest, vint16m8_t vector, vint16m1_t scalar, size_t vl);
vint32m1_t vredmax_vs_i32m1_i32m1_m (vbool32_t mask, vint32m1_t
    dest, vint32m1_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredmax_vs_i32m2_i32m1_m (vbool16_t mask, vint32m1_t
    dest, vint32m2_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredmax_vs_i32m4_i32m1_m (vbool8_t mask, vint32m1_t
    dest, vint32m4_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredmax_vs_i32m8_i32m1_m (vbool4_t mask, vint32m1_t
    dest, vint32m8_t vector, vint32m1_t scalar, size_t vl);
vint64m1_t vredmax_vs_i64m1_i64m1_m (vbool64_t mask, vint64m1_t
    dest, vint64m1_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredmax_vs_i64m2_i64m1_m (vbool32_t mask, vint64m1_t
    dest, vint64m2_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredmax_vs_i64m4_i64m1_m (vbool16_t mask, vint64m1_t
    dest, vint64m4_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredmax_vs_i64m8_i64m1_m (vbool8_t mask, vint64m1_t
    dest, vint64m8_t vector, vint64m1_t scalar, size_t vl);
vuint8m1_t vredmaxu_vs_u8m1_u8m1_m (vbool8_t mask, vuint8m1_t
    dest, vuint8m1_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredmaxu_vs_u8m2_u8m1_m (vbool4_t mask, vuint8m1_t
    dest, vuint8m2_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredmaxu_vs_u8m4_u8m1_m (vbool2_t mask, vuint8m1_t
    dest, vuint8m4_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredmaxu_vs_u8m8_u8m1_m (vbool1_t mask, vuint8m1_t
    dest, vuint8m8_t vector, vuint8m1_t scalar, size_t vl);
vuint16m1_t vredmaxu_vs_u16m1_u16m1_m (vbool16_t mask,
    vuint16m1_t dest, vuint16m1_t vector, vuint16m1_t scalar,
    size_t vl);
vuint16m1_t vredmaxu_vs_u16m2_u16m1_m (vbool8_t mask,
    vuint16m1_t dest, vuint16m2_t vector, vuint16m1_t scalar,
    size_t vl);
vuint16m1_t vredmaxu_vs_u16m4_u16m1_m (vbool4_t mask,
    vuint16m1_t dest, vuint16m4_t vector, vuint16m1_t scalar,
    size_t vl);
vuint16m1_t vredmaxu_vs_u16m8_u16m1_m (vbool2_t mask,
    vuint16m1_t dest, vuint16m8_t vector, vuint16m1_t scalar,
    size_t vl);
vuint32m1_t vredmaxu_vs_u32m1_u32m1_m (vbool32_t mask,
    vuint32m1_t dest, vuint32m1_t vector, vuint32m1_t scalar,
    size_t vl);
vuint32m1_t vredmaxu_vs_u32m2_u32m1_m (vbool16_t mask,
    vuint32m1_t dest, vuint32m2_t vector, vuint32m1_t scalar,
    size_t vl);
vuint32m1_t vredmaxu_vs_u32m4_u32m1_m (vbool8_t mask,
    vuint32m1_t dest, vuint32m4_t vector, vuint32m1_t scalar,

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    size_t vl);
vuint32m1_t vredmaxu_vs_u32m8_u32m1_m (vbool4_t mask,
    vuint32m1_t dest, vuint32m8_t vector, vuint32m1_t scalar,
    size_t vl);
vuint64m1_t vredmaxu_vs_u64m1_u64m1_m (vbool64_t mask,
    vuint64m1_t dest, vuint64m1_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vredmaxu_vs_u64m2_u64m1_m (vbool32_t mask,
    vuint64m1_t dest, vuint64m2_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vredmaxu_vs_u64m4_u64m1_m (vbool16_t mask,
    vuint64m1_t dest, vuint64m4_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vredmaxu_vs_u64m8_u64m1_m (vbool8_t mask,
    vuint64m1_t dest, vuint64m8_t vector, vuint64m1_t scalar,
    size_t vl);
vint8m1_t vredmin_vs_i8m1_i8m1_m (vbool8_t mask, vint8m1_t dest,
    vint8m1_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredmin_vs_i8m2_i8m1_m (vbool4_t mask, vint8m1_t dest,
    vint8m2_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredmin_vs_i8m4_i8m1_m (vbool2_t mask, vint8m1_t dest,
    vint8m4_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredmin_vs_i8m8_i8m1_m (vbool1_t mask, vint8m1_t dest,
    vint8m8_t vector, vint8m1_t scalar, size_t vl);
vint16m1_t vredmin_vs_i16m1_i16m1_m (vbool16_t mask, vint16m1_t
    dest, vint16m1_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredmin_vs_i16m2_i16m1_m (vbool8_t mask, vint16m1_t
    dest, vint16m2_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredmin_vs_i16m4_i16m1_m (vbool4_t mask, vint16m1_t
    dest, vint16m4_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredmin_vs_i16m8_i16m1_m (vbool2_t mask, vint16m1_t
    dest, vint16m8_t vector, vint16m1_t scalar, size_t vl);
vint32m1_t vredmin_vs_i32m1_i32m1_m (vbool32_t mask, vint32m1_t
    dest, vint32m1_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredmin_vs_i32m2_i32m1_m (vbool16_t mask, vint32m1_t
    dest, vint32m2_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredmin_vs_i32m4_i32m1_m (vbool8_t mask, vint32m1_t
    dest, vint32m4_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredmin_vs_i32m8_i32m1_m (vbool4_t mask, vint32m1_t
    dest, vint32m8_t vector, vint32m1_t scalar, size_t vl);
vint64m1_t vredmin_vs_i64m1_i64m1_m (vbool64_t mask, vint64m1_t
    dest, vint64m1_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredmin_vs_i64m2_i64m1_m (vbool32_t mask, vint64m1_t
    dest, vint64m2_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredmin_vs_i64m4_i64m1_m (vbool16_t mask, vint64m1_t
    dest, vint64m4_t vector, vint64m1_t scalar, size_t vl);

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vint64m1_t vredmin_vs_i64m8_i64m1_m (vbool8_t mask, vint64m1_t
    dest, vint64m8_t vector, vint64m1_t scalar, size_t vl);
vuint8m1_t vredminu_vs_u8m1_u8m1_m (vbool8_t mask, vuint8m1_t
    dest, vuint8m1_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredminu_vs_u8m2_u8m1_m (vbool4_t mask, vuint8m1_t
    dest, vuint8m2_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredminu_vs_u8m4_u8m1_m (vbool2_t mask, vuint8m1_t
    dest, vuint8m4_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredminu_vs_u8m8_u8m1_m (vbool1_t mask, vuint8m1_t
    dest, vuint8m8_t vector, vuint8m1_t scalar, size_t vl);
vuint16m1_t vredminu_vs_u16m1_u16m1_m (vbool16_t mask,
    vuint16m1_t dest, vuint16m1_t vector, vuint16m1_t scalar,
    size_t vl);
vuint16m1_t vredminu_vs_u16m2_u16m1_m (vbool8_t mask,
    vuint16m1_t dest, vuint16m2_t vector, vuint16m1_t scalar,
    size_t vl);
vuint16m1_t vredminu_vs_u16m4_u16m1_m (vbool4_t mask,
    vuint16m1_t dest, vuint16m4_t vector, vuint16m1_t scalar,
    size_t vl);
vuint16m1_t vredminu_vs_u16m8_u16m1_m (vbool2_t mask,
    vuint16m1_t dest, vuint16m8_t vector, vuint16m1_t scalar,
    size_t vl);
vuint32m1_t vredminu_vs_u32m1_u32m1_m (vbool32_t mask,
    vuint32m1_t dest, vuint32m1_t vector, vuint32m1_t scalar,
    size_t vl);
vuint32m1_t vredminu_vs_u32m2_u32m1_m (vbool16_t mask,
    vuint32m1_t dest, vuint32m2_t vector, vuint32m1_t scalar,
    size_t vl);
vuint32m1_t vredminu_vs_u32m4_u32m1_m (vbool8_t mask,
    vuint32m1_t dest, vuint32m4_t vector, vuint32m1_t scalar,
    size_t vl);
vuint32m1_t vredminu_vs_u32m8_u32m1_m (vbool4_t mask,
    vuint32m1_t dest, vuint32m8_t vector, vuint32m1_t scalar,
    size_t vl);
vuint64m1_t vredminu_vs_u64m1_u64m1_m (vbool64_t mask,
    vuint64m1_t dest, vuint64m1_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vredminu_vs_u64m2_u64m1_m (vbool32_t mask,
    vuint64m1_t dest, vuint64m2_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vredminu_vs_u64m4_u64m1_m (vbool16_t mask,
    vuint64m1_t dest, vuint64m4_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vredminu_vs_u64m8_u64m1_m (vbool8_t mask,
    vuint64m1_t dest, vuint64m8_t vector, vuint64m1_t scalar,
    size_t vl);

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vint8m1_t vredand_vs_i8m1_i8m1_m (vbool8_t mask, vint8m1_t dest,
    vint8m1_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredand_vs_i8m2_i8m1_m (vbool4_t mask, vint8m1_t dest,
    vint8m2_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredand_vs_i8m4_i8m1_m (vbool2_t mask, vint8m1_t dest,
    vint8m4_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredand_vs_i8m8_i8m1_m (vbool1_t mask, vint8m1_t dest,
    vint8m8_t vector, vint8m1_t scalar, size_t vl);
vint16m1_t vredand_vs_i16m1_i16m1_m (vbool16_t mask, vint16m1_t
    dest, vint16m1_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredand_vs_i16m2_i16m1_m (vbool8_t mask, vint16m1_t
    dest, vint16m2_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredand_vs_i16m4_i16m1_m (vbool4_t mask, vint16m1_t
    dest, vint16m4_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredand_vs_i16m8_i16m1_m (vbool2_t mask, vint16m1_t
    dest, vint16m8_t vector, vint16m1_t scalar, size_t vl);
vint32m1_t vredand_vs_i32m1_i32m1_m (vbool32_t mask, vint32m1_t
    dest, vint32m1_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredand_vs_i32m2_i32m1_m (vbool16_t mask, vint32m1_t
    dest, vint32m2_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredand_vs_i32m4_i32m1_m (vbool8_t mask, vint32m1_t
    dest, vint32m4_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredand_vs_i32m8_i32m1_m (vbool4_t mask, vint32m1_t
    dest, vint32m8_t vector, vint32m1_t scalar, size_t vl);
vint64m1_t vredand_vs_i64m1_i64m1_m (vbool64_t mask, vint64m1_t
    dest, vint64m1_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredand_vs_i64m2_i64m1_m (vbool32_t mask, vint64m1_t
    dest, vint64m2_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredand_vs_i64m4_i64m1_m (vbool16_t mask, vint64m1_t
    dest, vint64m4_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredand_vs_i64m8_i64m1_m (vbool8_t mask, vint64m1_t
    dest, vint64m8_t vector, vint64m1_t scalar, size_t vl);
vuint8m1_t vredand_vs_u8m1_u8m1_m (vbool8_t mask, vuint8m1_t
    dest, vuint8m1_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredand_vs_u8m2_u8m1_m (vbool4_t mask, vuint8m1_t
    dest, vuint8m2_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredand_vs_u8m4_u8m1_m (vbool2_t mask, vuint8m1_t
    dest, vuint8m4_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredand_vs_u8m8_u8m1_m (vbool1_t mask, vuint8m1_t
    dest, vuint8m8_t vector, vuint8m1_t scalar, size_t vl);
vuint16m1_t vredand_vs_u16m1_u16m1_m (vbool16_t mask,
    vuint16m1_t dest, vuint16m1_t vector, vuint16m1_t scalar,
    size_t vl);
vuint16m1_t vredand_vs_u16m2_u16m1_m (vbool8_t mask, vuint16m1_t
    dest, vuint16m2_t vector, vuint16m1_t scalar, size_t vl);

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vuint16m1_t vredand_vs_u16m4_u16m1_m (vbool4_t mask, vuint16m1_t
    dest, vuint16m4_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredand_vs_u16m8_u16m1_m (vbool2_t mask, vuint16m1_t
    dest, vuint16m8_t vector, vuint16m1_t scalar, size_t vl);
vuint32m1_t vredand_vs_u32m1_u32m1_m (vbool32_t mask,
    vuint32m1_t dest, vuint32m1_t vector, vuint32m1_t scalar,
    size_t vl);
vuint32m1_t vredand_vs_u32m2_u32m1_m (vbool16_t mask,
    vuint32m1_t dest, vuint32m2_t vector, vuint32m1_t scalar,
    size_t vl);
vuint32m1_t vredand_vs_u32m4_u32m1_m (vbool8_t mask, vuint32m1_t
    dest, vuint32m4_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredand_vs_u32m8_u32m1_m (vbool4_t mask, vuint32m1_t
    dest, vuint32m8_t vector, vuint32m1_t scalar, size_t vl);
vuint64m1_t vredand_vs_u64m1_u64m1_m (vbool64_t mask,
    vuint64m1_t dest, vuint64m1_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vredand_vs_u64m2_u64m1_m (vbool32_t mask,
    vuint64m1_t dest, vuint64m2_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vredand_vs_u64m4_u64m1_m (vbool16_t mask,
    vuint64m1_t dest, vuint64m4_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vredand_vs_u64m8_u64m1_m (vbool8_t mask, vuint64m1_t
    dest, vuint64m8_t vector, vuint64m1_t scalar, size_t vl);
vint8m1_t vredor_vs_i8m1_i8m1_m (vbool8_t mask, vint8m1_t dest,
    vint8m1_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredor_vs_i8m2_i8m1_m (vbool4_t mask, vint8m1_t dest,
    vint8m2_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredor_vs_i8m4_i8m1_m (vbool2_t mask, vint8m1_t dest,
    vint8m4_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredor_vs_i8m8_i8m1_m (vbool1_t mask, vint8m1_t dest,
    vint8m8_t vector, vint8m1_t scalar, size_t vl);
vint16m1_t vredor_vs_i16m1_i16m1_m (vbool16_t mask, vint16m1_t
    dest, vint16m1_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredor_vs_i16m2_i16m1_m (vbool8_t mask, vint16m1_t
    dest, vint16m2_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredor_vs_i16m4_i16m1_m (vbool4_t mask, vint16m1_t
    dest, vint16m4_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredor_vs_i16m8_i16m1_m (vbool2_t mask, vint16m1_t
    dest, vint16m8_t vector, vint16m1_t scalar, size_t vl);
vint32m1_t vredor_vs_i32m1_i32m1_m (vbool32_t mask, vint32m1_t
    dest, vint32m1_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredor_vs_i32m2_i32m1_m (vbool16_t mask, vint32m1_t
    dest, vint32m2_t vector, vint32m1_t scalar, size_t vl);

```

```

vint32m1_t vredor_vs_i32m4_i32m1_m (vbool8_t mask, vint32m1_t
    dest, vint32m4_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredor_vs_i32m8_i32m1_m (vbool4_t mask, vint32m1_t
    dest, vint32m8_t vector, vint32m1_t scalar, size_t vl);
vint64m1_t vredor_vs_i64m1_i64m1_m (vbool64_t mask, vint64m1_t
    dest, vint64m1_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredor_vs_i64m2_i64m1_m (vbool32_t mask, vint64m1_t
    dest, vint64m2_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredor_vs_i64m4_i64m1_m (vbool16_t mask, vint64m1_t
    dest, vint64m4_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredor_vs_i64m8_i64m1_m (vbool8_t mask, vint64m1_t
    dest, vint64m8_t vector, vint64m1_t scalar, size_t vl);
vuint8m1_t vredor_vs_u8m1_u8m1_m (vbool8_t mask, vuint8m1_t
    dest, vuint8m1_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredor_vs_u8m2_u8m1_m (vbool4_t mask, vuint8m1_t
    dest, vuint8m2_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredor_vs_u8m4_u8m1_m (vbool2_t mask, vuint8m1_t
    dest, vuint8m4_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredor_vs_u8m8_u8m1_m (vbool1_t mask, vuint8m1_t
    dest, vuint8m8_t vector, vuint8m1_t scalar, size_t vl);
vuint16m1_t vredor_vs_u16m1_u16m1_m (vbool16_t mask, vuint16m1_t
    dest, vuint16m1_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredor_vs_u16m2_u16m1_m (vbool8_t mask, vuint16m1_t
    dest, vuint16m2_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredor_vs_u16m4_u16m1_m (vbool4_t mask, vuint16m1_t
    dest, vuint16m4_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredor_vs_u16m8_u16m1_m (vbool2_t mask, vuint16m1_t
    dest, vuint16m8_t vector, vuint16m1_t scalar, size_t vl);
vuint32m1_t vredor_vs_u32m1_u32m1_m (vbool32_t mask, vuint32m1_t
    dest, vuint32m1_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredor_vs_u32m2_u32m1_m (vbool16_t mask, vuint32m1_t
    dest, vuint32m2_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredor_vs_u32m4_u32m1_m (vbool8_t mask, vuint32m1_t
    dest, vuint32m4_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredor_vs_u32m8_u32m1_m (vbool4_t mask, vuint32m1_t
    dest, vuint32m8_t vector, vuint32m1_t scalar, size_t vl);
vuint64m1_t vredor_vs_u64m1_u64m1_m (vbool64_t mask, vuint64m1_t
    dest, vuint64m1_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredor_vs_u64m2_u64m1_m (vbool32_t mask, vuint64m1_t
    dest, vuint64m2_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredor_vs_u64m4_u64m1_m (vbool16_t mask, vuint64m1_t
    dest, vuint64m4_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vredor_vs_u64m8_u64m1_m (vbool8_t mask, vuint64m1_t
    dest, vuint64m8_t vector, vuint64m1_t scalar, size_t vl);
vint8m1_t vredxor_vs_i8m1_i8m1_m (vbool8_t mask, vint8m1_t dest,
    vint8m1_t vector, vint8m1_t scalar, size_t vl);

```

```

vint8m1_t vredxor_vs_i8m2_i8m1_m (vbool4_t mask, vint8m1_t dest,
    vint8m2_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredxor_vs_i8m4_i8m1_m (vbool2_t mask, vint8m1_t dest,
    vint8m4_t vector, vint8m1_t scalar, size_t vl);
vint8m1_t vredxor_vs_i8m8_i8m1_m (vbool1_t mask, vint8m1_t dest,
    vint8m8_t vector, vint8m1_t scalar, size_t vl);
vint16m1_t vredxor_vs_i16m1_i16m1_m (vbool16_t mask, vint16m1_t
    dest, vint16m1_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredxor_vs_i16m2_i16m1_m (vbool8_t mask, vint16m1_t
    dest, vint16m2_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredxor_vs_i16m4_i16m1_m (vbool4_t mask, vint16m1_t
    dest, vint16m4_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vredxor_vs_i16m8_i16m1_m (vbool2_t mask, vint16m1_t
    dest, vint16m8_t vector, vint16m1_t scalar, size_t vl);
vint32m1_t vredxor_vs_i32m1_i32m1_m (vbool32_t mask, vint32m1_t
    dest, vint32m1_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredxor_vs_i32m2_i32m1_m (vbool16_t mask, vint32m1_t
    dest, vint32m2_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredxor_vs_i32m4_i32m1_m (vbool8_t mask, vint32m1_t
    dest, vint32m4_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vredxor_vs_i32m8_i32m1_m (vbool4_t mask, vint32m1_t
    dest, vint32m8_t vector, vint32m1_t scalar, size_t vl);
vint64m1_t vredxor_vs_i64m1_i64m1_m (vbool64_t mask, vint64m1_t
    dest, vint64m1_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredxor_vs_i64m2_i64m1_m (vbool32_t mask, vint64m1_t
    dest, vint64m2_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredxor_vs_i64m4_i64m1_m (vbool16_t mask, vint64m1_t
    dest, vint64m4_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vredxor_vs_i64m8_i64m1_m (vbool8_t mask, vint64m1_t
    dest, vint64m8_t vector, vint64m1_t scalar, size_t vl);
vuint8m1_t vredxor_vs_u8m1_u8m1_m (vbool8_t mask, vuint8m1_t
    dest, vuint8m1_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredxor_vs_u8m2_u8m1_m (vbool4_t mask, vuint8m1_t
    dest, vuint8m2_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredxor_vs_u8m4_u8m1_m (vbool2_t mask, vuint8m1_t
    dest, vuint8m4_t vector, vuint8m1_t scalar, size_t vl);
vuint8m1_t vredxor_vs_u8m8_u8m1_m (vbool1_t mask, vuint8m1_t
    dest, vuint8m8_t vector, vuint8m1_t scalar, size_t vl);
vuint16m1_t vredxor_vs_u16m1_u16m1_m (vbool16_t mask,
    vuint16m1_t dest, vuint16m1_t vector, vuint16m1_t scalar,
    size_t vl);
vuint16m1_t vredxor_vs_u16m2_u16m1_m (vbool8_t mask, vuint16m1_t
    dest, vuint16m2_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vredxor_vs_u16m4_u16m1_m (vbool4_t mask, vuint16m1_t
    dest, vuint16m4_t vector, vuint16m1_t scalar, size_t vl);

```

```

vuint16m1_t vredxor_vs_u16m8_u16m1_m (vbool2_t mask, vuint16m1_t
    dest, vuint16m8_t vector, vuint16m1_t scalar, size_t vl);
vuint32m1_t vredxor_vs_u32m1_u32m1_m (vbool32_t mask,
    vuint32m1_t dest, vuint32m1_t vector, vuint32m1_t scalar,
    size_t vl);
vuint32m1_t vredxor_vs_u32m2_u32m1_m (vbool16_t mask,
    vuint32m1_t dest, vuint32m2_t vector, vuint32m1_t scalar,
    size_t vl);
vuint32m1_t vredxor_vs_u32m4_u32m1_m (vbool8_t mask, vuint32m1_t
    dest, vuint32m4_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vredxor_vs_u32m8_u32m1_m (vbool4_t mask, vuint32m1_t
    dest, vuint32m8_t vector, vuint32m1_t scalar, size_t vl);
vuint64m1_t vredxor_vs_u64m1_u64m1_m (vbool64_t mask,
    vuint64m1_t dest, vuint64m1_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vredxor_vs_u64m2_u64m1_m (vbool32_t mask,
    vuint64m1_t dest, vuint64m2_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vredxor_vs_u64m4_u64m1_m (vbool16_t mask,
    vuint64m1_t dest, vuint64m4_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vredxor_vs_u64m8_u64m1_m (vbool8_t mask, vuint64m1_t
    dest, vuint64m8_t vector, vuint64m1_t scalar, size_t vl);

```

## Vector Widening Integer Reduction Functions:

### Prototypes:

```

vint16m1_t vwredsum_vs_i8m1_i16m1 (vint16m1_t dest, vint8m1_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vwredsum_vs_i8m2_i16m1 (vint16m1_t dest, vint8m2_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vwredsum_vs_i8m4_i16m1 (vint16m1_t dest, vint8m4_t
    vector, vint16m1_t scalar, size_t vl);
vint16m1_t vwredsum_vs_i8m8_i16m1 (vint16m1_t dest, vint8m8_t
    vector, vint16m1_t scalar, size_t vl);
vint32m1_t vwredsum_vs_i16m1_i32m1 (vint32m1_t dest, vint16m1_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vwredsum_vs_i16m2_i32m1 (vint32m1_t dest, vint16m2_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vwredsum_vs_i16m4_i32m1 (vint32m1_t dest, vint16m4_t
    vector, vint32m1_t scalar, size_t vl);
vint32m1_t vwredsum_vs_i16m8_i32m1 (vint32m1_t dest, vint16m8_t
    vector, vint32m1_t scalar, size_t vl);
vint64m1_t vwredsum_vs_i32m1_i64m1 (vint64m1_t dest, vint32m1_t
    vector, vint64m1_t scalar, size_t vl);

```

```

vint64m1_t vwredsum_vs_i32m2_i64m1 (vint64m1_t dest, vint32m2_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vwredsum_vs_i32m4_i64m1 (vint64m1_t dest, vint32m4_t
    vector, vint64m1_t scalar, size_t vl);
vint64m1_t vwredsum_vs_i32m8_i64m1 (vint64m1_t dest, vint32m8_t
    vector, vint64m1_t scalar, size_t vl);
vuint16m1_t vwredsumu_vs_u8m1_u16m1 (vuint16m1_t dest,
    vuint8m1_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vwredsumu_vs_u8m2_u16m1 (vuint16m1_t dest,
    vuint8m2_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vwredsumu_vs_u8m4_u16m1 (vuint16m1_t dest,
    vuint8m4_t vector, vuint16m1_t scalar, size_t vl);
vuint16m1_t vwredsumu_vs_u8m8_u16m1 (vuint16m1_t dest,
    vuint8m8_t vector, vuint16m1_t scalar, size_t vl);
vuint32m1_t vwredsumu_vs_u16m1_u32m1 (vuint32m1_t dest,
    vuint16m1_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vwredsumu_vs_u16m2_u32m1 (vuint32m1_t dest,
    vuint16m2_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vwredsumu_vs_u16m4_u32m1 (vuint32m1_t dest,
    vuint16m4_t vector, vuint32m1_t scalar, size_t vl);
vuint32m1_t vwredsumu_vs_u16m8_u32m1 (vuint32m1_t dest,
    vuint16m8_t vector, vuint32m1_t scalar, size_t vl);
vuint64m1_t vwredsumu_vs_u32m1_u64m1 (vuint64m1_t dest,
    vuint32m1_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vwredsumu_vs_u32m2_u64m1 (vuint64m1_t dest,
    vuint32m2_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vwredsumu_vs_u32m4_u64m1 (vuint64m1_t dest,
    vuint32m4_t vector, vuint64m1_t scalar, size_t vl);
vuint64m1_t vwredsumu_vs_u32m8_u64m1 (vuint64m1_t dest,
    vuint32m8_t vector, vuint64m1_t scalar, size_t vl);
// masked functions
vint16m1_t vwredsum_vs_i8m1_i16m1_m (vbool8_t mask, vint16m1_t
    dest, vint8m1_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vwredsum_vs_i8m2_i16m1_m (vbool4_t mask, vint16m1_t
    dest, vint8m2_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vwredsum_vs_i8m4_i16m1_m (vbool2_t mask, vint16m1_t
    dest, vint8m4_t vector, vint16m1_t scalar, size_t vl);
vint16m1_t vwredsum_vs_i8m8_i16m1_m (vbool1_t mask, vint16m1_t
    dest, vint8m8_t vector, vint16m1_t scalar, size_t vl);
vint32m1_t vwredsum_vs_i16m1_i32m1_m (vbool16_t mask, vint32m1_t
    dest, vint16m1_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vwredsum_vs_i16m2_i32m1_m (vbool8_t mask, vint32m1_t
    dest, vint16m2_t vector, vint32m1_t scalar, size_t vl);
vint32m1_t vwredsum_vs_i16m4_i32m1_m (vbool4_t mask, vint32m1_t
    dest, vint16m4_t vector, vint32m1_t scalar, size_t vl);

```

```

vint32m1_t vwredsum_vs_i16m8_i32m1_m (vbool2_t mask, vint32m1_t
    dest, vint16m8_t vector, vint32m1_t scalar, size_t vl);
vint64m1_t vwredsum_vs_i32m1_i64m1_m (vbool32_t mask, vint64m1_t
    dest, vint32m1_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vwredsum_vs_i32m2_i64m1_m (vbool16_t mask, vint64m1_t
    dest, vint32m2_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vwredsum_vs_i32m4_i64m1_m (vbool8_t mask, vint64m1_t
    dest, vint32m4_t vector, vint64m1_t scalar, size_t vl);
vint64m1_t vwredsum_vs_i32m8_i64m1_m (vbool4_t mask, vint64m1_t
    dest, vint32m8_t vector, vint64m1_t scalar, size_t vl);
vuint16m1_t vwredsumu_vs_u8m1_u16m1_m (vbool8_t mask,
    vuint16m1_t dest, vuint8m1_t vector, vuint16m1_t scalar,
    size_t vl);
vuint16m1_t vwredsumu_vs_u8m2_u16m1_m (vbool4_t mask,
    vuint16m1_t dest, vuint8m2_t vector, vuint16m1_t scalar,
    size_t vl);
vuint16m1_t vwredsumu_vs_u8m4_u16m1_m (vbool2_t mask,
    vuint16m1_t dest, vuint8m4_t vector, vuint16m1_t scalar,
    size_t vl);
vuint16m1_t vwredsumu_vs_u8m8_u16m1_m (vbool1_t mask,
    vuint16m1_t dest, vuint8m8_t vector, vuint16m1_t scalar,
    size_t vl);
vuint32m1_t vwredsumu_vs_u16m1_u32m1_m (vbool16_t mask,
    vuint32m1_t dest, vuint16m1_t vector, vuint32m1_t scalar,
    size_t vl);
vuint32m1_t vwredsumu_vs_u16m2_u32m1_m (vbool8_t mask,
    vuint32m1_t dest, vuint16m2_t vector, vuint32m1_t scalar,
    size_t vl);
vuint32m1_t vwredsumu_vs_u16m4_u32m1_m (vbool4_t mask,
    vuint32m1_t dest, vuint16m4_t vector, vuint32m1_t scalar,
    size_t vl);
vuint32m1_t vwredsumu_vs_u16m8_u32m1_m (vbool2_t mask,
    vuint32m1_t dest, vuint16m8_t vector, vuint32m1_t scalar,
    size_t vl);
vuint64m1_t vwredsumu_vs_u32m1_u64m1_m (vbool32_t mask,
    vuint64m1_t dest, vuint32m1_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vwredsumu_vs_u32m2_u64m1_m (vbool16_t mask,
    vuint64m1_t dest, vuint32m2_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vwredsumu_vs_u32m4_u64m1_m (vbool8_t mask,
    vuint64m1_t dest, vuint32m4_t vector, vuint64m1_t scalar,
    size_t vl);
vuint64m1_t vwredsumu_vs_u32m8_u64m1_m (vbool4_t mask,
    vuint64m1_t dest, vuint32m8_t vector, vuint64m1_t scalar,
    size_t vl);

```

## Vector Single-Width Floating-Point Reduction Functions:

### Prototypes:

```
vfloat16m1_t vfredosum_vs_f16m1_f16m1 (vfloat16m1_t dest,
    vfloat16m1_t vector, vfloat16m1_t scalar, size_t vl);
vfloat16m1_t vfredosum_vs_f16m2_f16m1 (vfloat16m1_t dest,
    vfloat16m2_t vector, vfloat16m1_t scalar, size_t vl);
vfloat16m1_t vfredosum_vs_f16m4_f16m1 (vfloat16m1_t dest,
    vfloat16m4_t vector, vfloat16m1_t scalar, size_t vl);
vfloat16m1_t vfredosum_vs_f16m8_f16m1 (vfloat16m1_t dest,
    vfloat16m8_t vector, vfloat16m1_t scalar, size_t vl);
vfloat32m1_t vfredosum_vs_f32m1_f32m1 (vfloat32m1_t dest,
    vfloat32m1_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfredosum_vs_f32m2_f32m1 (vfloat32m1_t dest,
    vfloat32m2_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfredosum_vs_f32m4_f32m1 (vfloat32m1_t dest,
    vfloat32m4_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfredosum_vs_f32m8_f32m1 (vfloat32m1_t dest,
    vfloat32m8_t vector, vfloat32m1_t scalar, size_t vl);
vfloat64m1_t vfredosum_vs_f64m1_f64m1 (vfloat64m1_t dest,
    vfloat64m1_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfredosum_vs_f64m2_f64m1 (vfloat64m1_t dest,
    vfloat64m2_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfredosum_vs_f64m4_f64m1 (vfloat64m1_t dest,
    vfloat64m4_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfredosum_vs_f64m8_f64m1 (vfloat64m1_t dest,
    vfloat64m8_t vector, vfloat64m1_t scalar, size_t vl);
vfloat16m1_t vfredusum_vs_f16m1_f16m1 (vfloat16m1_t dest,
    vfloat16m1_t vector, vfloat16m1_t scalar, size_t vl);
vfloat16m1_t vfredusum_vs_f16m2_f16m1 (vfloat16m1_t dest,
    vfloat16m2_t vector, vfloat16m1_t scalar, size_t vl);
vfloat16m1_t vfredusum_vs_f16m4_f16m1 (vfloat16m1_t dest,
    vfloat16m4_t vector, vfloat16m1_t scalar, size_t vl);
vfloat16m1_t vfredusum_vs_f16m8_f16m1 (vfloat16m1_t dest,
    vfloat16m8_t vector, vfloat16m1_t scalar, size_t vl);
vfloat32m1_t vfredusum_vs_f32m1_f32m1 (vfloat32m1_t dest,
    vfloat32m1_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfredusum_vs_f32m2_f32m1 (vfloat32m1_t dest,
    vfloat32m2_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfredusum_vs_f32m4_f32m1 (vfloat32m1_t dest,
    vfloat32m4_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfredusum_vs_f32m8_f32m1 (vfloat32m1_t dest,
    vfloat32m8_t vector, vfloat32m1_t scalar, size_t vl);
vfloat64m1_t vfredusum_vs_f64m1_f64m1 (vfloat64m1_t dest,
    vfloat64m1_t vector, vfloat64m1_t scalar, size_t vl);
```



```

vfloat64m1_t vfredusum_vs_f64m2_f64m1 (vfloat64m1_t dest,
    vfloat64m2_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfredusum_vs_f64m4_f64m1 (vfloat64m1_t dest,
    vfloat64m4_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfredusum_vs_f64m8_f64m1 (vfloat64m1_t dest,
    vfloat64m8_t vector, vfloat64m1_t scalar, size_t vl);
vfloat16m1_t vfredmax_vs_f16m1_f16m1 (vfloat16m1_t dest,
    vfloat16m1_t vector, vfloat16m1_t scalar, size_t vl);
vfloat16m1_t vfredmax_vs_f16m2_f16m1 (vfloat16m1_t dest,
    vfloat16m2_t vector, vfloat16m1_t scalar, size_t vl);
vfloat16m1_t vfredmax_vs_f16m4_f16m1 (vfloat16m1_t dest,
    vfloat16m4_t vector, vfloat16m1_t scalar, size_t vl);
vfloat16m1_t vfredmax_vs_f16m8_f16m1 (vfloat16m1_t dest,
    vfloat16m8_t vector, vfloat16m1_t scalar, size_t vl);
vfloat32m1_t vfredmax_vs_f32m1_f32m1 (vfloat32m1_t dest,
    vfloat32m1_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfredmax_vs_f32m2_f32m1 (vfloat32m1_t dest,
    vfloat32m2_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfredmax_vs_f32m4_f32m1 (vfloat32m1_t dest,
    vfloat32m4_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfredmax_vs_f32m8_f32m1 (vfloat32m1_t dest,
    vfloat32m8_t vector, vfloat32m1_t scalar, size_t vl);
vfloat64m1_t vfredmax_vs_f64m1_f64m1 (vfloat64m1_t dest,
    vfloat64m1_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfredmax_vs_f64m2_f64m1 (vfloat64m1_t dest,
    vfloat64m2_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfredmax_vs_f64m4_f64m1 (vfloat64m1_t dest,
    vfloat64m4_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfredmax_vs_f64m8_f64m1 (vfloat64m1_t dest,
    vfloat64m8_t vector, vfloat64m1_t scalar, size_t vl);
vfloat16m1_t vfredmin_vs_f16m1_f16m1 (vfloat16m1_t dest,
    vfloat16m1_t vector, vfloat16m1_t scalar, size_t vl);
vfloat16m1_t vfredmin_vs_f16m2_f16m1 (vfloat16m1_t dest,
    vfloat16m2_t vector, vfloat16m1_t scalar, size_t vl);
vfloat16m1_t vfredmin_vs_f16m4_f16m1 (vfloat16m1_t dest,
    vfloat16m4_t vector, vfloat16m1_t scalar, size_t vl);
vfloat16m1_t vfredmin_vs_f16m8_f16m1 (vfloat16m1_t dest,
    vfloat16m8_t vector, vfloat16m1_t scalar, size_t vl);
vfloat32m1_t vfredmin_vs_f32m1_f32m1 (vfloat32m1_t dest,
    vfloat32m1_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfredmin_vs_f32m2_f32m1 (vfloat32m1_t dest,
    vfloat32m2_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfredmin_vs_f32m4_f32m1 (vfloat32m1_t dest,
    vfloat32m4_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfredmin_vs_f32m8_f32m1 (vfloat32m1_t dest,
    vfloat32m8_t vector, vfloat32m1_t scalar, size_t vl);

```

```

vfloat64m1_t vfredmin_vs_f64m1_f64m1 (vfloat64m1_t dest,
    vfloat64m1_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfredmin_vs_f64m2_f64m1 (vfloat64m1_t dest,
    vfloat64m2_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfredmin_vs_f64m4_f64m1 (vfloat64m1_t dest,
    vfloat64m4_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfredmin_vs_f64m8_f64m1 (vfloat64m1_t dest,
    vfloat64m8_t vector, vfloat64m1_t scalar, size_t vl);
// masked functions
vfloat16m1_t vfredosum_vs_f16m1_f16m1_m (vbool16_t mask,
    vfloat16m1_t dest, vfloat16m1_t vector, vfloat16m1_t scalar,
    size_t vl);
vfloat16m1_t vfredosum_vs_f16m2_f16m1_m (vbool8_t mask,
    vfloat16m1_t dest, vfloat16m2_t vector, vfloat16m1_t scalar,
    size_t vl);
vfloat16m1_t vfredosum_vs_f16m4_f16m1_m (vbool4_t mask,
    vfloat16m1_t dest, vfloat16m4_t vector, vfloat16m1_t scalar,
    size_t vl);
vfloat16m1_t vfredosum_vs_f16m8_f16m1_m (vbool2_t mask,
    vfloat16m1_t dest, vfloat16m8_t vector, vfloat16m1_t scalar,
    size_t vl);
vfloat32m1_t vfredosum_vs_f32m1_f32m1_m (vbool32_t mask,
    vfloat32m1_t dest, vfloat32m1_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfredosum_vs_f32m2_f32m1_m (vbool16_t mask,
    vfloat32m1_t dest, vfloat32m2_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfredosum_vs_f32m4_f32m1_m (vbool8_t mask,
    vfloat32m1_t dest, vfloat32m4_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfredosum_vs_f32m8_f32m1_m (vbool4_t mask,
    vfloat32m1_t dest, vfloat32m8_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat64m1_t vfredosum_vs_f64m1_f64m1_m (vbool64_t mask,
    vfloat64m1_t dest, vfloat64m1_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfredosum_vs_f64m2_f64m1_m (vbool32_t mask,
    vfloat64m1_t dest, vfloat64m2_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfredosum_vs_f64m4_f64m1_m (vbool16_t mask,
    vfloat64m1_t dest, vfloat64m4_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfredosum_vs_f64m8_f64m1_m (vbool8_t mask,
    vfloat64m1_t dest, vfloat64m8_t vector, vfloat64m1_t scalar,
    size_t vl);

```

```

vfloat16m1_t vfredusum_vs_f16m1_f16m1_m (vbool16_t mask,
    vfloat16m1_t dest, vfloat16m1_t vector, vfloat16m1_t scalar,
    size_t vl);
vfloat16m1_t vfredusum_vs_f16m2_f16m1_m (vbool8_t mask,
    vfloat16m1_t dest, vfloat16m2_t vector, vfloat16m1_t scalar,
    size_t vl);
vfloat16m1_t vfredusum_vs_f16m4_f16m1_m (vbool4_t mask,
    vfloat16m1_t dest, vfloat16m4_t vector, vfloat16m1_t scalar,
    size_t vl);
vfloat16m1_t vfredusum_vs_f16m8_f16m1_m (vbool2_t mask,
    vfloat16m1_t dest, vfloat16m8_t vector, vfloat16m1_t scalar,
    size_t vl);
vfloat32m1_t vfredusum_vs_f32m1_f32m1_m (vbool32_t mask,
    vfloat32m1_t dest, vfloat32m1_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfredusum_vs_f32m2_f32m1_m (vbool16_t mask,
    vfloat32m1_t dest, vfloat32m2_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfredusum_vs_f32m4_f32m1_m (vbool8_t mask,
    vfloat32m1_t dest, vfloat32m4_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfredusum_vs_f32m8_f32m1_m (vbool4_t mask,
    vfloat32m1_t dest, vfloat32m8_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat64m1_t vfredusum_vs_f64m1_f64m1_m (vbool64_t mask,
    vfloat64m1_t dest, vfloat64m1_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfredusum_vs_f64m2_f64m1_m (vbool32_t mask,
    vfloat64m1_t dest, vfloat64m2_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfredusum_vs_f64m4_f64m1_m (vbool16_t mask,
    vfloat64m1_t dest, vfloat64m4_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfredusum_vs_f64m8_f64m1_m (vbool8_t mask,
    vfloat64m1_t dest, vfloat64m8_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat16m1_t vfredmax_vs_f16m1_f16m1_m (vbool16_t mask,
    vfloat16m1_t dest, vfloat16m1_t vector, vfloat16m1_t scalar,
    size_t vl);
vfloat16m1_t vfredmax_vs_f16m2_f16m1_m (vbool8_t mask,
    vfloat16m1_t dest, vfloat16m2_t vector, vfloat16m1_t scalar,
    size_t vl);
vfloat16m1_t vfredmax_vs_f16m4_f16m1_m (vbool4_t mask,
    vfloat16m1_t dest, vfloat16m4_t vector, vfloat16m1_t scalar,
    size_t vl);

```

```

vfloat16m1_t vfredmax_vs_f16m8_f16m1_m (vbool2_t mask,
    vfloat16m1_t dest, vfloat16m8_t vector, vfloat16m1_t scalar,
    size_t vl);
vfloat32m1_t vfredmax_vs_f32m1_f32m1_m (vbool32_t mask,
    vfloat32m1_t dest, vfloat32m1_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfredmax_vs_f32m2_f32m1_m (vbool16_t mask,
    vfloat32m1_t dest, vfloat32m2_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfredmax_vs_f32m4_f32m1_m (vbool8_t mask,
    vfloat32m1_t dest, vfloat32m4_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfredmax_vs_f32m8_f32m1_m (vbool4_t mask,
    vfloat32m1_t dest, vfloat32m8_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat64m1_t vfredmax_vs_f64m1_f64m1_m (vbool64_t mask,
    vfloat64m1_t dest, vfloat64m1_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfredmax_vs_f64m2_f64m1_m (vbool32_t mask,
    vfloat64m1_t dest, vfloat64m2_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfredmax_vs_f64m4_f64m1_m (vbool16_t mask,
    vfloat64m1_t dest, vfloat64m4_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfredmax_vs_f64m8_f64m1_m (vbool8_t mask,
    vfloat64m1_t dest, vfloat64m8_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat16m1_t vfredmin_vs_f16m1_f16m1_m (vbool16_t mask,
    vfloat16m1_t dest, vfloat16m1_t vector, vfloat16m1_t scalar,
    size_t vl);
vfloat16m1_t vfredmin_vs_f16m2_f16m1_m (vbool8_t mask,
    vfloat16m1_t dest, vfloat16m2_t vector, vfloat16m1_t scalar,
    size_t vl);
vfloat16m1_t vfredmin_vs_f16m4_f16m1_m (vbool4_t mask,
    vfloat16m1_t dest, vfloat16m4_t vector, vfloat16m1_t scalar,
    size_t vl);
vfloat16m1_t vfredmin_vs_f16m8_f16m1_m (vbool2_t mask,
    vfloat16m1_t dest, vfloat16m8_t vector, vfloat16m1_t scalar,
    size_t vl);
vfloat32m1_t vfredmin_vs_f32m1_f32m1_m (vbool32_t mask,
    vfloat32m1_t dest, vfloat32m1_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfredmin_vs_f32m2_f32m1_m (vbool16_t mask,
    vfloat32m1_t dest, vfloat32m2_t vector, vfloat32m1_t scalar,
    size_t vl);

```

```

vfloat32m1_t vfredmin_vs_f32m4_f32m1_m (vbool8_t mask,
    vfloat32m1_t dest, vfloat32m4_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfredmin_vs_f32m8_f32m1_m (vbool4_t mask,
    vfloat32m1_t dest, vfloat32m8_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat64m1_t vfredmin_vs_f64m1_f64m1_m (vbool64_t mask,
    vfloat64m1_t dest, vfloat64m1_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfredmin_vs_f64m2_f64m1_m (vbool32_t mask,
    vfloat64m1_t dest, vfloat64m2_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfredmin_vs_f64m4_f64m1_m (vbool16_t mask,
    vfloat64m1_t dest, vfloat64m4_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfredmin_vs_f64m8_f64m1_m (vbool8_t mask,
    vfloat64m1_t dest, vfloat64m8_t vector, vfloat64m1_t scalar,
    size_t vl);

```

## Vector Widening Floating-Point Reduction Functions:

### Prototypes:

```

vfloat32m1_t vfwredosum_vs_f16m1_f32m1 (vfloat32m1_t dest,
    vfloat16m1_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfwredosum_vs_f16m2_f32m1 (vfloat32m1_t dest,
    vfloat16m2_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfwredosum_vs_f16m4_f32m1 (vfloat32m1_t dest,
    vfloat16m4_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfwredosum_vs_f16m8_f32m1 (vfloat32m1_t dest,
    vfloat16m8_t vector, vfloat32m1_t scalar, size_t vl);
vfloat64m1_t vfwredosum_vs_f32m1_f64m1 (vfloat64m1_t dest,
    vfloat32m1_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfwredosum_vs_f32m2_f64m1 (vfloat64m1_t dest,
    vfloat32m2_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfwredosum_vs_f32m4_f64m1 (vfloat64m1_t dest,
    vfloat32m4_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfwredosum_vs_f32m8_f64m1 (vfloat64m1_t dest,
    vfloat32m8_t vector, vfloat64m1_t scalar, size_t vl);
vfloat32m1_t vfwredusum_vs_f16m1_f32m1 (vfloat32m1_t dest,
    vfloat16m1_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfwredusum_vs_f16m2_f32m1 (vfloat32m1_t dest,
    vfloat16m2_t vector, vfloat32m1_t scalar, size_t vl);
vfloat32m1_t vfwredusum_vs_f16m4_f32m1 (vfloat32m1_t dest,
    vfloat16m4_t vector, vfloat32m1_t scalar, size_t vl);

```

```

vfloat32m1_t vfwredusum_vs_f16m8_f32m1 (vfloat32m1_t dest,
    vfloat16m8_t vector, vfloat32m1_t scalar, size_t vl);
vfloat64m1_t vfwredusum_vs_f32m1_f64m1 (vfloat64m1_t dest,
    vfloat32m1_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfwredusum_vs_f32m2_f64m1 (vfloat64m1_t dest,
    vfloat32m2_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfwredusum_vs_f32m4_f64m1 (vfloat64m1_t dest,
    vfloat32m4_t vector, vfloat64m1_t scalar, size_t vl);
vfloat64m1_t vfwredusum_vs_f32m8_f64m1 (vfloat64m1_t dest,
    vfloat32m8_t vector, vfloat64m1_t scalar, size_t vl);
// masked functions
vfloat32m1_t vfwredosum_vs_f16m1_f32m1_m (vbool16_t mask,
    vfloat32m1_t dest, vfloat16m1_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfwredosum_vs_f16m2_f32m1_m (vbool8_t mask,
    vfloat32m1_t dest, vfloat16m2_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfwredosum_vs_f16m4_f32m1_m (vbool4_t mask,
    vfloat32m1_t dest, vfloat16m4_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfwredosum_vs_f16m8_f32m1_m (vbool2_t mask,
    vfloat32m1_t dest, vfloat16m8_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat64m1_t vfwredosum_vs_f32m1_f64m1_m (vbool32_t mask,
    vfloat64m1_t dest, vfloat32m1_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfwredosum_vs_f32m2_f64m1_m (vbool16_t mask,
    vfloat64m1_t dest, vfloat32m2_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfwredosum_vs_f32m4_f64m1_m (vbool8_t mask,
    vfloat64m1_t dest, vfloat32m4_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfwredosum_vs_f32m8_f64m1_m (vbool4_t mask,
    vfloat64m1_t dest, vfloat32m8_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat32m1_t vfwredusum_vs_f16m1_f32m1_m (vbool16_t mask,
    vfloat32m1_t dest, vfloat16m1_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfwredusum_vs_f16m2_f32m1_m (vbool8_t mask,
    vfloat32m1_t dest, vfloat16m2_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfwredusum_vs_f16m4_f32m1_m (vbool4_t mask,
    vfloat32m1_t dest, vfloat16m4_t vector, vfloat32m1_t scalar,
    size_t vl);
vfloat32m1_t vfwredusum_vs_f16m8_f32m1_m (vbool2_t mask,
    vfloat32m1_t dest, vfloat16m8_t vector, vfloat32m1_t scalar,

```

```

    size_t vl);
vfloat64m1_t vfwredusum_vs_f32m1_f64m1_m (vbool32_t mask,
    vfloat64m1_t dest, vfloat32m1_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfwredusum_vs_f32m2_f64m1_m (vbool16_t mask,
    vfloat64m1_t dest, vfloat32m2_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfwredusum_vs_f32m4_f64m1_m (vbool8_t mask,
    vfloat64m1_t dest, vfloat32m4_t vector, vfloat64m1_t scalar,
    size_t vl);
vfloat64m1_t vfwredusum_vs_f32m8_f64m1_m (vbool4_t mask,
    vfloat64m1_t dest, vfloat32m8_t vector, vfloat64m1_t scalar,
    size_t vl);

```

## Vector Mask Functions:

### Vector Mask-Register Logical Functions:

#### Prototypes:

```

vbool1_t vmand_mm_b1 (vbool1_t op1, vbool1_t op2, size_t vl);
vbool2_t vmand_mm_b2 (vbool2_t op1, vbool2_t op2, size_t vl);
vbool4_t vmand_mm_b4 (vbool4_t op1, vbool4_t op2, size_t vl);
vbool8_t vmand_mm_b8 (vbool8_t op1, vbool8_t op2, size_t vl);
vbool16_t vmand_mm_b16 (vbool16_t op1, vbool16_t op2, size_t vl);
vbool32_t vmand_mm_b32 (vbool32_t op1, vbool32_t op2, size_t vl);
vbool64_t vmand_mm_b64 (vbool64_t op1, vbool64_t op2, size_t vl);
vbool1_t vmnand_mm_b1 (vbool1_t op1, vbool1_t op2, size_t vl);
vbool2_t vmnand_mm_b2 (vbool2_t op1, vbool2_t op2, size_t vl);
vbool4_t vmnand_mm_b4 (vbool4_t op1, vbool4_t op2, size_t vl);
vbool8_t vmnand_mm_b8 (vbool8_t op1, vbool8_t op2, size_t vl);
vbool16_t vmnand_mm_b16 (vbool16_t op1, vbool16_t op2, size_t
    vl);
vbool32_t vmnand_mm_b32 (vbool32_t op1, vbool32_t op2, size_t
    vl);
vbool64_t vmnand_mm_b64 (vbool64_t op1, vbool64_t op2, size_t
    vl);
vbool1_t vmandn_mm_b1 (vbool1_t op1, vbool1_t op2, size_t vl);
vbool2_t vmandn_mm_b2 (vbool2_t op1, vbool2_t op2, size_t vl);
vbool4_t vmandn_mm_b4 (vbool4_t op1, vbool4_t op2, size_t vl);
vbool8_t vmandn_mm_b8 (vbool8_t op1, vbool8_t op2, size_t vl);
vbool16_t vmandn_mm_b16 (vbool16_t op1, vbool16_t op2, size_t
    vl);
vbool32_t vmandn_mm_b32 (vbool32_t op1, vbool32_t op2, size_t
    vl);

```

```

vbool64_t vmandn_mm_b64 (vbool64_t op1, vbool64_t op2, size_t
    vl);
vbool1_t vmxor_mm_b1 (vbool1_t op1, vbool1_t op2, size_t vl);
vbool2_t vmxor_mm_b2 (vbool2_t op1, vbool2_t op2, size_t vl);
vbool4_t vmxor_mm_b4 (vbool4_t op1, vbool4_t op2, size_t vl);
vbool8_t vmxor_mm_b8 (vbool8_t op1, vbool8_t op2, size_t vl);
vbool16_t vmxor_mm_b16 (vbool16_t op1, vbool16_t op2, size_t vl);
vbool32_t vmxor_mm_b32 (vbool32_t op1, vbool32_t op2, size_t vl);
vbool64_t vmxor_mm_b64 (vbool64_t op1, vbool64_t op2, size_t vl);
vbool1_t vmor_mm_b1 (vbool1_t op1, vbool1_t op2, size_t vl);
vbool2_t vmor_mm_b2 (vbool2_t op1, vbool2_t op2, size_t vl);
vbool4_t vmor_mm_b4 (vbool4_t op1, vbool4_t op2, size_t vl);
vbool8_t vmor_mm_b8 (vbool8_t op1, vbool8_t op2, size_t vl);
vbool16_t vmor_mm_b16 (vbool16_t op1, vbool16_t op2, size_t vl);
vbool32_t vmor_mm_b32 (vbool32_t op1, vbool32_t op2, size_t vl);
vbool64_t vmor_mm_b64 (vbool64_t op1, vbool64_t op2, size_t vl);
vbool1_t vmnor_mm_b1 (vbool1_t op1, vbool1_t op2, size_t vl);
vbool2_t vmnor_mm_b2 (vbool2_t op1, vbool2_t op2, size_t vl);
vbool4_t vmnor_mm_b4 (vbool4_t op1, vbool4_t op2, size_t vl);
vbool8_t vmnor_mm_b8 (vbool8_t op1, vbool8_t op2, size_t vl);
vbool16_t vmnor_mm_b16 (vbool16_t op1, vbool16_t op2, size_t vl);
vbool32_t vmnor_mm_b32 (vbool32_t op1, vbool32_t op2, size_t vl);
vbool64_t vmnor_mm_b64 (vbool64_t op1, vbool64_t op2, size_t vl);
vbool1_t vmorn_mm_b1 (vbool1_t op1, vbool1_t op2, size_t vl);
vbool2_t vmorn_mm_b2 (vbool2_t op1, vbool2_t op2, size_t vl);
vbool4_t vmorn_mm_b4 (vbool4_t op1, vbool4_t op2, size_t vl);
vbool8_t vmorn_mm_b8 (vbool8_t op1, vbool8_t op2, size_t vl);
vbool16_t vmorn_mm_b16 (vbool16_t op1, vbool16_t op2, size_t vl);
vbool32_t vmorn_mm_b32 (vbool32_t op1, vbool32_t op2, size_t vl);
vbool64_t vmorn_mm_b64 (vbool64_t op1, vbool64_t op2, size_t vl);
vbool1_t vmxnor_mm_b1 (vbool1_t op1, vbool1_t op2, size_t vl);
vbool2_t vmxnor_mm_b2 (vbool2_t op1, vbool2_t op2, size_t vl);
vbool4_t vmxnor_mm_b4 (vbool4_t op1, vbool4_t op2, size_t vl);
vbool8_t vmxnor_mm_b8 (vbool8_t op1, vbool8_t op2, size_t vl);
vbool16_t vmxnor_mm_b16 (vbool16_t op1, vbool16_t op2, size_t
    vl);
vbool32_t vmxnor_mm_b32 (vbool32_t op1, vbool32_t op2, size_t
    vl);
vbool64_t vmxnor_mm_b64 (vbool64_t op1, vbool64_t op2, size_t
    vl);
vbool1_t vmmv_m_b1 (vbool1_t op1, size_t vl);
vbool2_t vmmv_m_b2 (vbool2_t op1, size_t vl);
vbool4_t vmmv_m_b4 (vbool4_t op1, size_t vl);
vbool8_t vmmv_m_b8 (vbool8_t op1, size_t vl);
vbool16_t vmmv_m_b16 (vbool16_t op1, size_t vl);
vbool32_t vmmv_m_b32 (vbool32_t op1, size_t vl);

```



```

vbool64_t vmmv_m_b64 (vbool64_t op1, size_t vl);
vbool1_t vmclr_m_b1 (size_t vl);
vbool2_t vmclr_m_b2 (size_t vl);
vbool4_t vmclr_m_b4 (size_t vl);
vbool8_t vmclr_m_b8 (size_t vl);
vbool16_t vmclr_m_b16 (size_t vl);
vbool32_t vmclr_m_b32 (size_t vl);
vbool64_t vmclr_m_b64 (size_t vl);
vbool1_t vmset_m_b1 (size_t vl);
vbool2_t vmset_m_b2 (size_t vl);
vbool4_t vmset_m_b4 (size_t vl);
vbool8_t vmset_m_b8 (size_t vl);
vbool16_t vmset_m_b16 (size_t vl);
vbool32_t vmset_m_b32 (size_t vl);
vbool64_t vmset_m_b64 (size_t vl);
vbool1_t vmnot_m_b1 (vbool1_t op1, size_t vl);
vbool2_t vmnot_m_b2 (vbool2_t op1, size_t vl);
vbool4_t vmnot_m_b4 (vbool4_t op1, size_t vl);
vbool8_t vmnot_m_b8 (vbool8_t op1, size_t vl);
vbool16_t vmnot_m_b16 (vbool16_t op1, size_t vl);
vbool32_t vmnot_m_b32 (vbool32_t op1, size_t vl);
vbool64_t vmnot_m_b64 (vbool64_t op1, size_t vl);

```

## Vector mask population count Functions:

### Prototypes:

```

unsigned long vcpop_m_b1 (vbool1_t op1, size_t vl);
unsigned long vcpop_m_b2 (vbool2_t op1, size_t vl);
unsigned long vcpop_m_b4 (vbool4_t op1, size_t vl);
unsigned long vcpop_m_b8 (vbool8_t op1, size_t vl);
unsigned long vcpop_m_b16 (vbool16_t op1, size_t vl);
unsigned long vcpop_m_b32 (vbool32_t op1, size_t vl);
unsigned long vcpop_m_b64 (vbool64_t op1, size_t vl);
// masked functions
unsigned long vcpop_m_b1_m (vbool1_t mask, vbool1_t op1, size_t
    vl);
unsigned long vcpop_m_b2_m (vbool2_t mask, vbool2_t op1, size_t
    vl);
unsigned long vcpop_m_b4_m (vbool4_t mask, vbool4_t op1, size_t
    vl);
unsigned long vcpop_m_b8_m (vbool8_t mask, vbool8_t op1, size_t
    vl);
unsigned long vcpop_m_b16_m (vbool16_t mask, vbool16_t op1,
    size_t vl);

```

```

unsigned long vcpop_m_b32_m (vbool32_t mask, vbool32_t op1,
    size_t vl);
unsigned long vcpop_m_b64_m (vbool64_t mask, vbool64_t op1,
    size_t vl);

```

### Find-first-set mask bit Functions:

#### Prototypes:

```

long vfirst_m_b1 (vbool1_t op1, size_t vl);
long vfirst_m_b2 (vbool2_t op1, size_t vl);
long vfirst_m_b4 (vbool4_t op1, size_t vl);
long vfirst_m_b8 (vbool8_t op1, size_t vl);
long vfirst_m_b16 (vbool16_t op1, size_t vl);
long vfirst_m_b32 (vbool32_t op1, size_t vl);
long vfirst_m_b64 (vbool64_t op1, size_t vl);
// masked functions
long vfirst_m_b1_m (vbool1_t mask, vbool1_t op1, size_t vl);
long vfirst_m_b2_m (vbool2_t mask, vbool2_t op1, size_t vl);
long vfirst_m_b4_m (vbool4_t mask, vbool4_t op1, size_t vl);
long vfirst_m_b8_m (vbool8_t mask, vbool8_t op1, size_t vl);
long vfirst_m_b16_m (vbool16_t mask, vbool16_t op1, size_t vl);
long vfirst_m_b32_m (vbool32_t mask, vbool32_t op1, size_t vl);
long vfirst_m_b64_m (vbool64_t mask, vbool64_t op1, size_t vl);

```

### Set-before-first mask bit Functions:

#### Prototypes:

```

vbool1_t vmsbf_m_b1 (vbool1_t op1, size_t vl);
vbool2_t vmsbf_m_b2 (vbool2_t op1, size_t vl);
vbool4_t vmsbf_m_b4 (vbool4_t op1, size_t vl);
vbool8_t vmsbf_m_b8 (vbool8_t op1, size_t vl);
vbool16_t vmsbf_m_b16 (vbool16_t op1, size_t vl);
vbool32_t vmsbf_m_b32 (vbool32_t op1, size_t vl);
vbool64_t vmsbf_m_b64 (vbool64_t op1, size_t vl);
// masked functions
vbool1_t vmsbf_m_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vbool1_t op1, size_t vl);
vbool2_t vmsbf_m_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vbool2_t op1, size_t vl);
vbool4_t vmsbf_m_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vbool4_t op1, size_t vl);
vbool8_t vmsbf_m_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vbool8_t op1, size_t vl);
vbool16_t vmsbf_m_b16_m (vbool16_t mask, vbool16_t maskedoff,
    vbool16_t op1, size_t vl);

```

```
vbool32_t vmsbf_m_b32_m (vbool32_t mask, vbool32_t maskedoff,
    vbool32_t op1, size_t vl);
vbool64_t vmsbf_m_b64_m (vbool64_t mask, vbool64_t maskedoff,
    vbool64_t op1, size_t vl);
```

### Set-including-first mask bit Functions:

#### Prototypes:

```
vbool1_t vmsif_m_b1 (vbool1_t op1, size_t vl);
vbool2_t vmsif_m_b2 (vbool2_t op1, size_t vl);
vbool4_t vmsif_m_b4 (vbool4_t op1, size_t vl);
vbool8_t vmsif_m_b8 (vbool8_t op1, size_t vl);
vbool16_t vmsif_m_b16 (vbool16_t op1, size_t vl);
vbool32_t vmsif_m_b32 (vbool32_t op1, size_t vl);
vbool64_t vmsif_m_b64 (vbool64_t op1, size_t vl);
// masked functions
vbool1_t vmsif_m_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vbool1_t op1, size_t vl);
vbool2_t vmsif_m_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vbool2_t op1, size_t vl);
vbool4_t vmsif_m_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vbool4_t op1, size_t vl);
vbool8_t vmsif_m_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vbool8_t op1, size_t vl);
vbool16_t vmsif_m_b16_m (vbool16_t mask, vbool16_t maskedoff,
    vbool16_t op1, size_t vl);
vbool32_t vmsif_m_b32_m (vbool32_t mask, vbool32_t maskedoff,
    vbool32_t op1, size_t vl);
vbool64_t vmsif_m_b64_m (vbool64_t mask, vbool64_t maskedoff,
    vbool64_t op1, size_t vl);
```

### Set-only-first mask bit Functions:

#### Prototypes:

```
vbool1_t vmsof_m_b1 (vbool1_t op1, size_t vl);
vbool2_t vmsof_m_b2 (vbool2_t op1, size_t vl);
vbool4_t vmsof_m_b4 (vbool4_t op1, size_t vl);
vbool8_t vmsof_m_b8 (vbool8_t op1, size_t vl);
vbool16_t vmsof_m_b16 (vbool16_t op1, size_t vl);
vbool32_t vmsof_m_b32 (vbool32_t op1, size_t vl);
vbool64_t vmsof_m_b64 (vbool64_t op1, size_t vl);
// masked functions
vbool1_t vmsof_m_b1_m (vbool1_t mask, vbool1_t maskedoff,
    vbool1_t op1, size_t vl);
```

```

vbool2_t vmsof_m_b2_m (vbool2_t mask, vbool2_t maskedoff,
    vbool2_t op1, size_t vl);
vbool4_t vmsof_m_b4_m (vbool4_t mask, vbool4_t maskedoff,
    vbool4_t op1, size_t vl);
vbool8_t vmsof_m_b8_m (vbool8_t mask, vbool8_t maskedoff,
    vbool8_t op1, size_t vl);
vbool16_t vmsof_m_b16_m (vbool16_t mask, vbool16_t maskedoff,
    vbool16_t op1, size_t vl);
vbool32_t vmsof_m_b32_m (vbool32_t mask, vbool32_t maskedoff,
    vbool32_t op1, size_t vl);
vbool64_t vmsof_m_b64_m (vbool64_t mask, vbool64_t maskedoff,
    vbool64_t op1, size_t vl);

```

## Vector Iota Functions:

### Prototypes:

```

vuint8m1_t viota_m_u8m1 (vbool8_t op1, size_t vl);
vuint8m2_t viota_m_u8m2 (vbool4_t op1, size_t vl);
vuint8m4_t viota_m_u8m4 (vbool2_t op1, size_t vl);
vuint8m8_t viota_m_u8m8 (vbool1_t op1, size_t vl);
vuint16m1_t viota_m_u16m1 (vbool16_t op1, size_t vl);
vuint16m2_t viota_m_u16m2 (vbool8_t op1, size_t vl);
vuint16m4_t viota_m_u16m4 (vbool4_t op1, size_t vl);
vuint16m8_t viota_m_u16m8 (vbool2_t op1, size_t vl);
vuint32m1_t viota_m_u32m1 (vbool32_t op1, size_t vl);
vuint32m2_t viota_m_u32m2 (vbool16_t op1, size_t vl);
vuint32m4_t viota_m_u32m4 (vbool8_t op1, size_t vl);
vuint32m8_t viota_m_u32m8 (vbool4_t op1, size_t vl);
vuint64m1_t viota_m_u64m1 (vbool64_t op1, size_t vl);
vuint64m2_t viota_m_u64m2 (vbool32_t op1, size_t vl);
vuint64m4_t viota_m_u64m4 (vbool16_t op1, size_t vl);
vuint64m8_t viota_m_u64m8 (vbool8_t op1, size_t vl);
// masked functions
vuint8m1_t viota_m_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    vbool8_t op1, size_t vl);
vuint8m2_t viota_m_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    vbool4_t op1, size_t vl);
vuint8m4_t viota_m_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    vbool2_t op1, size_t vl);
vuint8m8_t viota_m_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    vbool1_t op1, size_t vl);
vuint16m1_t viota_m_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vbool16_t op1, size_t vl);
vuint16m2_t viota_m_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vbool8_t op1, size_t vl);

```

```

vuint16m4_t viota_m_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vbool4_t op1, size_t vl);
vuint16m8_t viota_m_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vbool2_t op1, size_t vl);
vuint32m1_t viota_m_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vbool32_t op1, size_t vl);
vuint32m2_t viota_m_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vbool16_t op1, size_t vl);
vuint32m4_t viota_m_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vbool8_t op1, size_t vl);
vuint32m8_t viota_m_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vbool4_t op1, size_t vl);
vuint64m1_t viota_m_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vbool64_t op1, size_t vl);
vuint64m2_t viota_m_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vbool32_t op1, size_t vl);
vuint64m4_t viota_m_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vbool16_t op1, size_t vl);
vuint64m8_t viota_m_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vbool8_t op1, size_t vl);

```

## Vector Element Index Functions:

### Prototypes:

```

vuint8m1_t vid_v_u8m1 (size_t vl);
vuint8m2_t vid_v_u8m2 (size_t vl);
vuint8m4_t vid_v_u8m4 (size_t vl);
vuint8m8_t vid_v_u8m8 (size_t vl);
vuint16m1_t vid_v_u16m1 (size_t vl);
vuint16m2_t vid_v_u16m2 (size_t vl);
vuint16m4_t vid_v_u16m4 (size_t vl);
vuint16m8_t vid_v_u16m8 (size_t vl);
vuint32m1_t vid_v_u32m1 (size_t vl);
vuint32m2_t vid_v_u32m2 (size_t vl);
vuint32m4_t vid_v_u32m4 (size_t vl);
vuint32m8_t vid_v_u32m8 (size_t vl);
vuint64m1_t vid_v_u64m1 (size_t vl);
vuint64m2_t vid_v_u64m2 (size_t vl);
vuint64m4_t vid_v_u64m4 (size_t vl);
vuint64m8_t vid_v_u64m8 (size_t vl);
// masked functions
vuint8m1_t vid_v_u8m1_m (vbool8_t mask, vuint8m1_t maskedoff,
    size_t vl);
vuint8m2_t vid_v_u8m2_m (vbool4_t mask, vuint8m2_t maskedoff,
    size_t vl);

```

```

vuint8m4_t vid_v_u8m4_m (vbool2_t mask, vuint8m4_t maskedoff,
    size_t vl);
vuint8m8_t vid_v_u8m8_m (vbool1_t mask, vuint8m8_t maskedoff,
    size_t vl);
vuint16m1_t vid_v_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, size_t vl);
vuint16m2_t vid_v_u16m2_m (vbool8_t mask, vuint16m2_t maskedoff,
    size_t vl);
vuint16m4_t vid_v_u16m4_m (vbool4_t mask, vuint16m4_t maskedoff,
    size_t vl);
vuint16m8_t vid_v_u16m8_m (vbool2_t mask, vuint16m8_t maskedoff,
    size_t vl);
vuint32m1_t vid_v_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, size_t vl);
vuint32m2_t vid_v_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, size_t vl);
vuint32m4_t vid_v_u32m4_m (vbool8_t mask, vuint32m4_t maskedoff,
    size_t vl);
vuint32m8_t vid_v_u32m8_m (vbool4_t mask, vuint32m8_t maskedoff,
    size_t vl);
vuint64m1_t vid_v_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, size_t vl);
vuint64m2_t vid_v_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, size_t vl);
vuint64m4_t vid_v_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, size_t vl);
vuint64m8_t vid_v_u64m8_m (vbool8_t mask, vuint64m8_t maskedoff,
    size_t vl);

```

## Vector Permutation Functions:

### Integer Extract Functions:

```

int8_t vext_x_v_i8m1_i8 (vint8m1_t a, unsigned idx, size_t vl);
int8_t vext_x_v_i8m2_i8 (vint8m2_t a, unsigned idx, size_t vl);
int8_t vext_x_v_i8m4_i8 (vint8m4_t a, unsigned idx, size_t vl);
int8_t vext_x_v_i8m8_i8 (vint8m8_t a, unsigned idx, size_t vl);
int16_t vext_x_v_i16m1_i16 (vint16m1_t a, unsigned idx, size_t
    vl);
int16_t vext_x_v_i16m2_i16 (vint16m2_t a, unsigned idx, size_t
    vl);
int16_t vext_x_v_i16m4_i16 (vint16m4_t a, unsigned idx, size_t
    vl);
int16_t vext_x_v_i16m8_i16 (vint16m8_t a, unsigned idx, size_t
    vl);
int32_t vext_x_v_i32m1_i32 (vint32m1_t a, unsigned idx, size_t

```

```

    vl);
int32_t vext_x_v_i32m2_i32 (vint32m2_t a, unsigned idx, size_t
    vl);
int32_t vext_x_v_i32m4_i32 (vint32m4_t a, unsigned idx, size_t
    vl);
int32_t vext_x_v_i32m8_i32 (vint32m8_t a, unsigned idx, size_t
    vl);
int64_t vext_x_v_i64m1_i64 (vint64m1_t a, unsigned idx, size_t
    vl);
int64_t vext_x_v_i64m2_i64 (vint64m2_t a, unsigned idx, size_t
    vl);
int64_t vext_x_v_i64m4_i64 (vint64m4_t a, unsigned idx, size_t
    vl);
int64_t vext_x_v_i64m8_i64 (vint64m8_t a, unsigned idx, size_t
    vl);
uint8_t vext_x_v_u8m1_u8 (vuint8m1_t a, unsigned idx, size_t vl);
uint8_t vext_x_v_u8m2_u8 (vuint8m2_t a, unsigned idx, size_t vl);
uint8_t vext_x_v_u8m4_u8 (vuint8m4_t a, unsigned idx, size_t vl);
uint8_t vext_x_v_u8m8_u8 (vuint8m8_t a, unsigned idx, size_t vl);
uint16_t vext_x_v_u16m1_u16 (vuint16m1_t a, unsigned idx, size_t
    vl);
uint16_t vext_x_v_u16m2_u16 (vuint16m2_t a, unsigned idx, size_t
    vl);
uint16_t vext_x_v_u16m4_u16 (vuint16m4_t a, unsigned idx, size_t
    vl);
uint16_t vext_x_v_u16m8_u16 (vuint16m8_t a, unsigned idx, size_t
    vl);
uint32_t vext_x_v_u32m1_u32 (vuint32m1_t a, unsigned idx, size_t
    vl);
uint32_t vext_x_v_u32m2_u32 (vuint32m2_t a, unsigned idx, size_t
    vl);
uint32_t vext_x_v_u32m4_u32 (vuint32m4_t a, unsigned idx, size_t
    vl);
uint32_t vext_x_v_u32m8_u32 (vuint32m8_t a, unsigned idx, size_t
    vl);
uint64_t vext_x_v_u64m1_u64 (vuint64m1_t a, unsigned idx, size_t
    vl);
uint64_t vext_x_v_u64m2_u64 (vuint64m2_t a, unsigned idx, size_t
    vl);
uint64_t vext_x_v_u64m4_u64 (vuint64m4_t a, unsigned idx, size_t
    vl);
uint64_t vext_x_v_u64m8_u64 (vuint64m8_t a, unsigned idx, size_t
    vl);

```

## Integer and Floating-Point Scalar Move Functions:

### Prototypes:

```
int8_t vmv_x_s_i8m1_i8 (vint8m1_t src);
vint8m1_t vmv_s_x_i8m1 (vint8m1_t dest, int8_t src, size_t vl);
int8_t vmv_x_s_i8m2_i8 (vint8m2_t src);
vint8m2_t vmv_s_x_i8m2 (vint8m2_t dest, int8_t src, size_t vl);
int8_t vmv_x_s_i8m4_i8 (vint8m4_t src);
vint8m4_t vmv_s_x_i8m4 (vint8m4_t dest, int8_t src, size_t vl);
int8_t vmv_x_s_i8m8_i8 (vint8m8_t src);
vint8m8_t vmv_s_x_i8m8 (vint8m8_t dest, int8_t src, size_t vl);
int16_t vmv_x_s_i16m1_i16 (vint16m1_t src);
vint16m1_t vmv_s_x_i16m1 (vint16m1_t dest, int16_t src, size_t
vl);
int16_t vmv_x_s_i16m2_i16 (vint16m2_t src);
vint16m2_t vmv_s_x_i16m2 (vint16m2_t dest, int16_t src, size_t
vl);
int16_t vmv_x_s_i16m4_i16 (vint16m4_t src);
vint16m4_t vmv_s_x_i16m4 (vint16m4_t dest, int16_t src, size_t
vl);
int16_t vmv_x_s_i16m8_i16 (vint16m8_t src);
vint16m8_t vmv_s_x_i16m8 (vint16m8_t dest, int16_t src, size_t
vl);
int32_t vmv_x_s_i32m1_i32 (vint32m1_t src);
vint32m1_t vmv_s_x_i32m1 (vint32m1_t dest, int32_t src, size_t
vl);
int32_t vmv_x_s_i32m2_i32 (vint32m2_t src);
vint32m2_t vmv_s_x_i32m2 (vint32m2_t dest, int32_t src, size_t
vl);
int32_t vmv_x_s_i32m4_i32 (vint32m4_t src);
vint32m4_t vmv_s_x_i32m4 (vint32m4_t dest, int32_t src, size_t
vl);
int32_t vmv_x_s_i32m8_i32 (vint32m8_t src);
vint32m8_t vmv_s_x_i32m8 (vint32m8_t dest, int32_t src, size_t
vl);
int64_t vmv_x_s_i64m1_i64 (vint64m1_t src);
vint64m1_t vmv_s_x_i64m1 (vint64m1_t dest, int64_t src, size_t
vl);
int64_t vmv_x_s_i64m2_i64 (vint64m2_t src);
vint64m2_t vmv_s_x_i64m2 (vint64m2_t dest, int64_t src, size_t
vl);
int64_t vmv_x_s_i64m4_i64 (vint64m4_t src);
vint64m4_t vmv_s_x_i64m4 (vint64m4_t dest, int64_t src, size_t
vl);
int64_t vmv_x_s_i64m8_i64 (vint64m8_t src);
```



```

vint64m8_t vmv_s_x_i64m8 (vint64m8_t dest, int64_t src, size_t
    vl);
uint8_t vmv_x_s_u8m1_u8 (vuint8m1_t src);
vuint8m1_t vmv_s_x_u8m1 (vuint8m1_t dest, uint8_t src, size_t
    vl);
uint8_t vmv_x_s_u8m2_u8 (vuint8m2_t src);
vuint8m2_t vmv_s_x_u8m2 (vuint8m2_t dest, uint8_t src, size_t
    vl);
uint8_t vmv_x_s_u8m4_u8 (vuint8m4_t src);
vuint8m4_t vmv_s_x_u8m4 (vuint8m4_t dest, uint8_t src, size_t
    vl);
uint8_t vmv_x_s_u8m8_u8 (vuint8m8_t src);
vuint8m8_t vmv_s_x_u8m8 (vuint8m8_t dest, uint8_t src, size_t
    vl);
uint16_t vmv_x_s_u16m1_u16 (vuint16m1_t src);
vuint16m1_t vmv_s_x_u16m1 (vuint16m1_t dest, uint16_t src,
    size_t vl);
uint16_t vmv_x_s_u16m2_u16 (vuint16m2_t src);
vuint16m2_t vmv_s_x_u16m2 (vuint16m2_t dest, uint16_t src,
    size_t vl);
uint16_t vmv_x_s_u16m4_u16 (vuint16m4_t src);
vuint16m4_t vmv_s_x_u16m4 (vuint16m4_t dest, uint16_t src,
    size_t vl);
uint16_t vmv_x_s_u16m8_u16 (vuint16m8_t src);
vuint16m8_t vmv_s_x_u16m8 (vuint16m8_t dest, uint16_t src,
    size_t vl);
uint32_t vmv_x_s_u32m1_u32 (vuint32m1_t src);
vuint32m1_t vmv_s_x_u32m1 (vuint32m1_t dest, uint32_t src,
    size_t vl);
uint32_t vmv_x_s_u32m2_u32 (vuint32m2_t src);
vuint32m2_t vmv_s_x_u32m2 (vuint32m2_t dest, uint32_t src,
    size_t vl);
uint32_t vmv_x_s_u32m4_u32 (vuint32m4_t src);
vuint32m4_t vmv_s_x_u32m4 (vuint32m4_t dest, uint32_t src,
    size_t vl);
uint32_t vmv_x_s_u32m8_u32 (vuint32m8_t src);
vuint32m8_t vmv_s_x_u32m8 (vuint32m8_t dest, uint32_t src,
    size_t vl);
uint64_t vmv_x_s_u64m1_u64 (vuint64m1_t src);
vuint64m1_t vmv_s_x_u64m1 (vuint64m1_t dest, uint64_t src,
    size_t vl);
uint64_t vmv_x_s_u64m2_u64 (vuint64m2_t src);
vuint64m2_t vmv_s_x_u64m2 (vuint64m2_t dest, uint64_t src,
    size_t vl);
uint64_t vmv_x_s_u64m4_u64 (vuint64m4_t src);

```

```

vuint64m4_t vmv_s_x_u64m4 (vuint64m4_t dest, uint64_t src,
    size_t vl);
uint64_t vmv_x_s_u64m8_u64 (vuint64m8_t src);
vuint64m8_t vmv_s_x_u64m8 (vuint64m8_t dest, uint64_t src,
    size_t vl);
float16_t vfmv_f_s_f16m1_f16 (vfloat16m1_t src);
vfloat16m1_t vfmv_s_f_f16m1 (vfloat16m1_t dest, float16_t src,
    size_t vl);
float16_t vfmv_f_s_f16m2_f16 (vfloat16m2_t src);
vfloat16m2_t vfmv_s_f_f16m2 (vfloat16m2_t dest, float16_t src,
    size_t vl);
float16_t vfmv_f_s_f16m4_f16 (vfloat16m4_t src);
vfloat16m4_t vfmv_s_f_f16m4 (vfloat16m4_t dest, float16_t src,
    size_t vl);
float16_t vfmv_f_s_f16m8_f16 (vfloat16m8_t src);
vfloat16m8_t vfmv_s_f_f16m8 (vfloat16m8_t dest, float16_t src,
    size_t vl);
float32_t vfmv_f_s_f32m1_f32 (vfloat32m1_t src);
vfloat32m1_t vfmv_s_f_f32m1 (vfloat32m1_t dest, float32_t src,
    size_t vl);
float32_t vfmv_f_s_f32m2_f32 (vfloat32m2_t src);
vfloat32m2_t vfmv_s_f_f32m2 (vfloat32m2_t dest, float32_t src,
    size_t vl);
float32_t vfmv_f_s_f32m4_f32 (vfloat32m4_t src);
vfloat32m4_t vfmv_s_f_f32m4 (vfloat32m4_t dest, float32_t src,
    size_t vl);
float32_t vfmv_f_s_f32m8_f32 (vfloat32m8_t src);
vfloat32m8_t vfmv_s_f_f32m8 (vfloat32m8_t dest, float32_t src,
    size_t vl);
float64_t vfmv_f_s_f64m1_f64 (vfloat64m1_t src);
vfloat64m1_t vfmv_s_f_f64m1 (vfloat64m1_t dest, float64_t src,
    size_t vl);
float64_t vfmv_f_s_f64m2_f64 (vfloat64m2_t src);
vfloat64m2_t vfmv_s_f_f64m2 (vfloat64m2_t dest, float64_t src,
    size_t vl);
float64_t vfmv_f_s_f64m4_f64 (vfloat64m4_t src);
vfloat64m4_t vfmv_s_f_f64m4 (vfloat64m4_t dest, float64_t src,
    size_t vl);
float64_t vfmv_f_s_f64m8_f64 (vfloat64m8_t src);
vfloat64m8_t vfmv_s_f_f64m8 (vfloat64m8_t dest, float64_t src,
    size_t vl);

```

## Vector Slideup and Slidedown Functions:

### Prototypes:

```

vint8m1_t vslideup_vx_i8m1 (vint8m1_t dest, vint8m1_t src,
    size_t offset, size_t vl);
vint8m2_t vslideup_vx_i8m2 (vint8m2_t dest, vint8m2_t src,
    size_t offset, size_t vl);
vint8m4_t vslideup_vx_i8m4 (vint8m4_t dest, vint8m4_t src,
    size_t offset, size_t vl);
vint8m8_t vslideup_vx_i8m8 (vint8m8_t dest, vint8m8_t src,
    size_t offset, size_t vl);
vint16m1_t vslideup_vx_i16m1 (vint16m1_t dest, vint16m1_t src,
    size_t offset, size_t vl);
vint16m2_t vslideup_vx_i16m2 (vint16m2_t dest, vint16m2_t src,
    size_t offset, size_t vl);
vint16m4_t vslideup_vx_i16m4 (vint16m4_t dest, vint16m4_t src,
    size_t offset, size_t vl);
vint16m8_t vslideup_vx_i16m8 (vint16m8_t dest, vint16m8_t src,
    size_t offset, size_t vl);
vint32m1_t vslideup_vx_i32m1 (vint32m1_t dest, vint32m1_t src,
    size_t offset, size_t vl);
vint32m2_t vslideup_vx_i32m2 (vint32m2_t dest, vint32m2_t src,
    size_t offset, size_t vl);
vint32m4_t vslideup_vx_i32m4 (vint32m4_t dest, vint32m4_t src,
    size_t offset, size_t vl);
vint32m8_t vslideup_vx_i32m8 (vint32m8_t dest, vint32m8_t src,
    size_t offset, size_t vl);
vint64m1_t vslideup_vx_i64m1 (vint64m1_t dest, vint64m1_t src,
    size_t offset, size_t vl);
vint64m2_t vslideup_vx_i64m2 (vint64m2_t dest, vint64m2_t src,
    size_t offset, size_t vl);
vint64m4_t vslideup_vx_i64m4 (vint64m4_t dest, vint64m4_t src,
    size_t offset, size_t vl);
vint64m8_t vslideup_vx_i64m8 (vint64m8_t dest, vint64m8_t src,
    size_t offset, size_t vl);
vuint8m1_t vslideup_vx_u8m1 (vuint8m1_t dest, vuint8m1_t src,
    size_t offset, size_t vl);
vuint8m2_t vslideup_vx_u8m2 (vuint8m2_t dest, vuint8m2_t src,
    size_t offset, size_t vl);
vuint8m4_t vslideup_vx_u8m4 (vuint8m4_t dest, vuint8m4_t src,
    size_t offset, size_t vl);
vuint8m8_t vslideup_vx_u8m8 (vuint8m8_t dest, vuint8m8_t src,
    size_t offset, size_t vl);
vuint16m1_t vslideup_vx_u16m1 (vuint16m1_t dest, vuint16m1_t
    src, size_t offset, size_t vl);
vuint16m2_t vslideup_vx_u16m2 (vuint16m2_t dest, vuint16m2_t
    src, size_t offset, size_t vl);
vuint16m4_t vslideup_vx_u16m4 (vuint16m4_t dest, vuint16m4_t
    src, size_t offset, size_t vl);

```

```

vuint16m8_t vslideup_vx_u16m8 (vuint16m8_t dest, vuint16m8_t
    src, size_t offset, size_t vl);
vuint32m1_t vslideup_vx_u32m1 (vuint32m1_t dest, vuint32m1_t
    src, size_t offset, size_t vl);
vuint32m2_t vslideup_vx_u32m2 (vuint32m2_t dest, vuint32m2_t
    src, size_t offset, size_t vl);
vuint32m4_t vslideup_vx_u32m4 (vuint32m4_t dest, vuint32m4_t
    src, size_t offset, size_t vl);
vuint32m8_t vslideup_vx_u32m8 (vuint32m8_t dest, vuint32m8_t
    src, size_t offset, size_t vl);
vuint64m1_t vslideup_vx_u64m1 (vuint64m1_t dest, vuint64m1_t
    src, size_t offset, size_t vl);
vuint64m2_t vslideup_vx_u64m2 (vuint64m2_t dest, vuint64m2_t
    src, size_t offset, size_t vl);
vuint64m4_t vslideup_vx_u64m4 (vuint64m4_t dest, vuint64m4_t
    src, size_t offset, size_t vl);
vuint64m8_t vslideup_vx_u64m8 (vuint64m8_t dest, vuint64m8_t
    src, size_t offset, size_t vl);
vfloat16m1_t vslideup_vx_f16m1 (vfloat16m1_t dest, vfloat16m1_t
    src, size_t offset, size_t vl);
vfloat16m2_t vslideup_vx_f16m2 (vfloat16m2_t dest, vfloat16m2_t
    src, size_t offset, size_t vl);
vfloat16m4_t vslideup_vx_f16m4 (vfloat16m4_t dest, vfloat16m4_t
    src, size_t offset, size_t vl);
vfloat16m8_t vslideup_vx_f16m8 (vfloat16m8_t dest, vfloat16m8_t
    src, size_t offset, size_t vl);
vfloat32m1_t vslideup_vx_f32m1 (vfloat32m1_t dest, vfloat32m1_t
    src, size_t offset, size_t vl);
vfloat32m2_t vslideup_vx_f32m2 (vfloat32m2_t dest, vfloat32m2_t
    src, size_t offset, size_t vl);
vfloat32m4_t vslideup_vx_f32m4 (vfloat32m4_t dest, vfloat32m4_t
    src, size_t offset, size_t vl);
vfloat32m8_t vslideup_vx_f32m8 (vfloat32m8_t dest, vfloat32m8_t
    src, size_t offset, size_t vl);
vfloat64m1_t vslideup_vx_f64m1 (vfloat64m1_t dest, vfloat64m1_t
    src, size_t offset, size_t vl);
vfloat64m2_t vslideup_vx_f64m2 (vfloat64m2_t dest, vfloat64m2_t
    src, size_t offset, size_t vl);
vfloat64m4_t vslideup_vx_f64m4 (vfloat64m4_t dest, vfloat64m4_t
    src, size_t offset, size_t vl);
vfloat64m8_t vslideup_vx_f64m8 (vfloat64m8_t dest, vfloat64m8_t
    src, size_t offset, size_t vl);
vint8m1_t vslidedown_vx_i8m1 (vint8m1_t dest, vint8m1_t src,
    size_t offset, size_t vl);
vint8m2_t vslidedown_vx_i8m2 (vint8m2_t dest, vint8m2_t src,
    size_t offset, size_t vl);

```

```

vint8m4_t vslidedown_vx_i8m4 (vint8m4_t dest, vint8m4_t src,
    size_t offset, size_t vl);
vint8m8_t vslidedown_vx_i8m8 (vint8m8_t dest, vint8m8_t src,
    size_t offset, size_t vl);
vint16m1_t vslidedown_vx_i16m1 (vint16m1_t dest, vint16m1_t src,
    size_t offset, size_t vl);
vint16m2_t vslidedown_vx_i16m2 (vint16m2_t dest, vint16m2_t src,
    size_t offset, size_t vl);
vint16m4_t vslidedown_vx_i16m4 (vint16m4_t dest, vint16m4_t src,
    size_t offset, size_t vl);
vint16m8_t vslidedown_vx_i16m8 (vint16m8_t dest, vint16m8_t src,
    size_t offset, size_t vl);
vint32m1_t vslidedown_vx_i32m1 (vint32m1_t dest, vint32m1_t src,
    size_t offset, size_t vl);
vint32m2_t vslidedown_vx_i32m2 (vint32m2_t dest, vint32m2_t src,
    size_t offset, size_t vl);
vint32m4_t vslidedown_vx_i32m4 (vint32m4_t dest, vint32m4_t src,
    size_t offset, size_t vl);
vint32m8_t vslidedown_vx_i32m8 (vint32m8_t dest, vint32m8_t src,
    size_t offset, size_t vl);
vint64m1_t vslidedown_vx_i64m1 (vint64m1_t dest, vint64m1_t src,
    size_t offset, size_t vl);
vint64m2_t vslidedown_vx_i64m2 (vint64m2_t dest, vint64m2_t src,
    size_t offset, size_t vl);
vint64m4_t vslidedown_vx_i64m4 (vint64m4_t dest, vint64m4_t src,
    size_t offset, size_t vl);
vint64m8_t vslidedown_vx_i64m8 (vint64m8_t dest, vint64m8_t src,
    size_t offset, size_t vl);
vuint8m1_t vslidedown_vx_u8m1 (vuint8m1_t dest, vuint8m1_t src,
    size_t offset, size_t vl);
vuint8m2_t vslidedown_vx_u8m2 (vuint8m2_t dest, vuint8m2_t src,
    size_t offset, size_t vl);
vuint8m4_t vslidedown_vx_u8m4 (vuint8m4_t dest, vuint8m4_t src,
    size_t offset, size_t vl);
vuint8m8_t vslidedown_vx_u8m8 (vuint8m8_t dest, vuint8m8_t src,
    size_t offset, size_t vl);
vuint16m1_t vslidedown_vx_u16m1 (vuint16m1_t dest, vuint16m1_t
    src, size_t offset, size_t vl);
vuint16m2_t vslidedown_vx_u16m2 (vuint16m2_t dest, vuint16m2_t
    src, size_t offset, size_t vl);
vuint16m4_t vslidedown_vx_u16m4 (vuint16m4_t dest, vuint16m4_t
    src, size_t offset, size_t vl);
vuint16m8_t vslidedown_vx_u16m8 (vuint16m8_t dest, vuint16m8_t
    src, size_t offset, size_t vl);
vuint32m1_t vslidedown_vx_u32m1 (vuint32m1_t dest, vuint32m1_t
    src, size_t offset, size_t vl);

```

```

vuint32m2_t vslidedown_vx_u32m2 (vuint32m2_t dest, vuint32m2_t
    src, size_t offset, size_t vl);
vuint32m4_t vslidedown_vx_u32m4 (vuint32m4_t dest, vuint32m4_t
    src, size_t offset, size_t vl);
vuint32m8_t vslidedown_vx_u32m8 (vuint32m8_t dest, vuint32m8_t
    src, size_t offset, size_t vl);
vuint64m1_t vslidedown_vx_u64m1 (vuint64m1_t dest, vuint64m1_t
    src, size_t offset, size_t vl);
vuint64m2_t vslidedown_vx_u64m2 (vuint64m2_t dest, vuint64m2_t
    src, size_t offset, size_t vl);
vuint64m4_t vslidedown_vx_u64m4 (vuint64m4_t dest, vuint64m4_t
    src, size_t offset, size_t vl);
vuint64m8_t vslidedown_vx_u64m8 (vuint64m8_t dest, vuint64m8_t
    src, size_t offset, size_t vl);
vfloat16m1_t vslidedown_vx_f16m1 (vfloat16m1_t dest,
    vfloat16m1_t src, size_t offset, size_t vl);
vfloat16m2_t vslidedown_vx_f16m2 (vfloat16m2_t dest,
    vfloat16m2_t src, size_t offset, size_t vl);
vfloat16m4_t vslidedown_vx_f16m4 (vfloat16m4_t dest,
    vfloat16m4_t src, size_t offset, size_t vl);
vfloat16m8_t vslidedown_vx_f16m8 (vfloat16m8_t dest,
    vfloat16m8_t src, size_t offset, size_t vl);
vfloat32m1_t vslidedown_vx_f32m1 (vfloat32m1_t dest,
    vfloat32m1_t src, size_t offset, size_t vl);
vfloat32m2_t vslidedown_vx_f32m2 (vfloat32m2_t dest,
    vfloat32m2_t src, size_t offset, size_t vl);
vfloat32m4_t vslidedown_vx_f32m4 (vfloat32m4_t dest,
    vfloat32m4_t src, size_t offset, size_t vl);
vfloat32m8_t vslidedown_vx_f32m8 (vfloat32m8_t dest,
    vfloat32m8_t src, size_t offset, size_t vl);
vfloat64m1_t vslidedown_vx_f64m1 (vfloat64m1_t dest,
    vfloat64m1_t src, size_t offset, size_t vl);
vfloat64m2_t vslidedown_vx_f64m2 (vfloat64m2_t dest,
    vfloat64m2_t src, size_t offset, size_t vl);
vfloat64m4_t vslidedown_vx_f64m4 (vfloat64m4_t dest,
    vfloat64m4_t src, size_t offset, size_t vl);
vfloat64m8_t vslidedown_vx_f64m8 (vfloat64m8_t dest,
    vfloat64m8_t src, size_t offset, size_t vl);
// masked functions
vint8m1_t vslideup_vx_i8m1_m (vbool8_t mask, vint8m1_t dest,
    vint8m1_t src, size_t offset, size_t vl);
vint8m2_t vslideup_vx_i8m2_m (vbool4_t mask, vint8m2_t dest,
    vint8m2_t src, size_t offset, size_t vl);
vint8m4_t vslideup_vx_i8m4_m (vbool2_t mask, vint8m4_t dest,
    vint8m4_t src, size_t offset, size_t vl);

```

```

vint8m8_t vslideup_vx_i8m8_m (vbool1_t mask, vint8m8_t dest,
    vint8m8_t src, size_t offset, size_t vl);
vint16m1_t vslideup_vx_i16m1_m (vbool16_t mask, vint16m1_t dest,
    vint16m1_t src, size_t offset, size_t vl);
vint16m2_t vslideup_vx_i16m2_m (vbool8_t mask, vint16m2_t dest,
    vint16m2_t src, size_t offset, size_t vl);
vint16m4_t vslideup_vx_i16m4_m (vbool4_t mask, vint16m4_t dest,
    vint16m4_t src, size_t offset, size_t vl);
vint16m8_t vslideup_vx_i16m8_m (vbool2_t mask, vint16m8_t dest,
    vint16m8_t src, size_t offset, size_t vl);
vint32m1_t vslideup_vx_i32m1_m (vbool32_t mask, vint32m1_t dest,
    vint32m1_t src, size_t offset, size_t vl);
vint32m2_t vslideup_vx_i32m2_m (vbool16_t mask, vint32m2_t dest,
    vint32m2_t src, size_t offset, size_t vl);
vint32m4_t vslideup_vx_i32m4_m (vbool8_t mask, vint32m4_t dest,
    vint32m4_t src, size_t offset, size_t vl);
vint32m8_t vslideup_vx_i32m8_m (vbool4_t mask, vint32m8_t dest,
    vint32m8_t src, size_t offset, size_t vl);
vint64m1_t vslideup_vx_i64m1_m (vbool64_t mask, vint64m1_t dest,
    vint64m1_t src, size_t offset, size_t vl);
vint64m2_t vslideup_vx_i64m2_m (vbool32_t mask, vint64m2_t dest,
    vint64m2_t src, size_t offset, size_t vl);
vint64m4_t vslideup_vx_i64m4_m (vbool16_t mask, vint64m4_t dest,
    vint64m4_t src, size_t offset, size_t vl);
vint64m8_t vslideup_vx_i64m8_m (vbool8_t mask, vint64m8_t dest,
    vint64m8_t src, size_t offset, size_t vl);
vuint8m1_t vslideup_vx_u8m1_m (vbool8_t mask, vuint8m1_t dest,
    vuint8m1_t src, size_t offset, size_t vl);
vuint8m2_t vslideup_vx_u8m2_m (vbool4_t mask, vuint8m2_t dest,
    vuint8m2_t src, size_t offset, size_t vl);
vuint8m4_t vslideup_vx_u8m4_m (vbool2_t mask, vuint8m4_t dest,
    vuint8m4_t src, size_t offset, size_t vl);
vuint8m8_t vslideup_vx_u8m8_m (vbool1_t mask, vuint8m8_t dest,
    vuint8m8_t src, size_t offset, size_t vl);
vuint16m1_t vslideup_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    dest, vuint16m1_t src, size_t offset, size_t vl);
vuint16m2_t vslideup_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    dest, vuint16m2_t src, size_t offset, size_t vl);
vuint16m4_t vslideup_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    dest, vuint16m4_t src, size_t offset, size_t vl);
vuint16m8_t vslideup_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    dest, vuint16m8_t src, size_t offset, size_t vl);
vuint32m1_t vslideup_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    dest, vuint32m1_t src, size_t offset, size_t vl);
vuint32m2_t vslideup_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    dest, vuint32m2_t src, size_t offset, size_t vl);

```

```

vuint32m4_t vslideup_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    dest, vuint32m4_t src, size_t offset, size_t vl);
vuint32m8_t vslideup_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    dest, vuint32m8_t src, size_t offset, size_t vl);
vuint64m1_t vslideup_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    dest, vuint64m1_t src, size_t offset, size_t vl);
vuint64m2_t vslideup_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    dest, vuint64m2_t src, size_t offset, size_t vl);
vuint64m4_t vslideup_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    dest, vuint64m4_t src, size_t offset, size_t vl);
vuint64m8_t vslideup_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    dest, vuint64m8_t src, size_t offset, size_t vl);
vfloat16m1_t vslideup_vx_f16m1_m (vbool16_t mask, vfloat16m1_t
    dest, vfloat16m1_t src, size_t offset, size_t vl);
vfloat16m2_t vslideup_vx_f16m2_m (vbool8_t mask, vfloat16m2_t
    dest, vfloat16m2_t src, size_t offset, size_t vl);
vfloat16m4_t vslideup_vx_f16m4_m (vbool4_t mask, vfloat16m4_t
    dest, vfloat16m4_t src, size_t offset, size_t vl);
vfloat16m8_t vslideup_vx_f16m8_m (vbool2_t mask, vfloat16m8_t
    dest, vfloat16m8_t src, size_t offset, size_t vl);
vfloat32m1_t vslideup_vx_f32m1_m (vbool32_t mask, vfloat32m1_t
    dest, vfloat32m1_t src, size_t offset, size_t vl);
vfloat32m2_t vslideup_vx_f32m2_m (vbool16_t mask, vfloat32m2_t
    dest, vfloat32m2_t src, size_t offset, size_t vl);
vfloat32m4_t vslideup_vx_f32m4_m (vbool8_t mask, vfloat32m4_t
    dest, vfloat32m4_t src, size_t offset, size_t vl);
vfloat32m8_t vslideup_vx_f32m8_m (vbool4_t mask, vfloat32m8_t
    dest, vfloat32m8_t src, size_t offset, size_t vl);
vfloat64m1_t vslideup_vx_f64m1_m (vbool64_t mask, vfloat64m1_t
    dest, vfloat64m1_t src, size_t offset, size_t vl);
vfloat64m2_t vslideup_vx_f64m2_m (vbool32_t mask, vfloat64m2_t
    dest, vfloat64m2_t src, size_t offset, size_t vl);
vfloat64m4_t vslideup_vx_f64m4_m (vbool16_t mask, vfloat64m4_t
    dest, vfloat64m4_t src, size_t offset, size_t vl);
vfloat64m8_t vslideup_vx_f64m8_m (vbool8_t mask, vfloat64m8_t
    dest, vfloat64m8_t src, size_t offset, size_t vl);
vint8m1_t vslidedown_vx_i8m1_m (vbool8_t mask, vint8m1_t dest,
    vint8m1_t src, size_t offset, size_t vl);
vint8m2_t vslidedown_vx_i8m2_m (vbool4_t mask, vint8m2_t dest,
    vint8m2_t src, size_t offset, size_t vl);
vint8m4_t vslidedown_vx_i8m4_m (vbool2_t mask, vint8m4_t dest,
    vint8m4_t src, size_t offset, size_t vl);
vint8m8_t vslidedown_vx_i8m8_m (vbool1_t mask, vint8m8_t dest,
    vint8m8_t src, size_t offset, size_t vl);
vint16m1_t vslidedown_vx_i16m1_m (vbool16_t mask, vint16m1_t
    dest, vint16m1_t src, size_t offset, size_t vl);

```



```

vint16m2_t vslidedown_vx_i16m2_m (vbool8_t mask, vint16m2_t
    dest, vint16m2_t src, size_t offset, size_t vl);
vint16m4_t vslidedown_vx_i16m4_m (vbool4_t mask, vint16m4_t
    dest, vint16m4_t src, size_t offset, size_t vl);
vint16m8_t vslidedown_vx_i16m8_m (vbool2_t mask, vint16m8_t
    dest, vint16m8_t src, size_t offset, size_t vl);
vint32m1_t vslidedown_vx_i32m1_m (vbool32_t mask, vint32m1_t
    dest, vint32m1_t src, size_t offset, size_t vl);
vint32m2_t vslidedown_vx_i32m2_m (vbool16_t mask, vint32m2_t
    dest, vint32m2_t src, size_t offset, size_t vl);
vint32m4_t vslidedown_vx_i32m4_m (vbool8_t mask, vint32m4_t
    dest, vint32m4_t src, size_t offset, size_t vl);
vint32m8_t vslidedown_vx_i32m8_m (vbool4_t mask, vint32m8_t
    dest, vint32m8_t src, size_t offset, size_t vl);
vint64m1_t vslidedown_vx_i64m1_m (vbool64_t mask, vint64m1_t
    dest, vint64m1_t src, size_t offset, size_t vl);
vint64m2_t vslidedown_vx_i64m2_m (vbool32_t mask, vint64m2_t
    dest, vint64m2_t src, size_t offset, size_t vl);
vint64m4_t vslidedown_vx_i64m4_m (vbool16_t mask, vint64m4_t
    dest, vint64m4_t src, size_t offset, size_t vl);
vint64m8_t vslidedown_vx_i64m8_m (vbool8_t mask, vint64m8_t
    dest, vint64m8_t src, size_t offset, size_t vl);
vuint8m1_t vslidedown_vx_u8m1_m (vbool8_t mask, vuint8m1_t dest,
    vuint8m1_t src, size_t offset, size_t vl);
vuint8m2_t vslidedown_vx_u8m2_m (vbool4_t mask, vuint8m2_t dest,
    vuint8m2_t src, size_t offset, size_t vl);
vuint8m4_t vslidedown_vx_u8m4_m (vbool2_t mask, vuint8m4_t dest,
    vuint8m4_t src, size_t offset, size_t vl);
vuint8m8_t vslidedown_vx_u8m8_m (vbool1_t mask, vuint8m8_t dest,
    vuint8m8_t src, size_t offset, size_t vl);
vuint16m1_t vslidedown_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    dest, vuint16m1_t src, size_t offset, size_t vl);
vuint16m2_t vslidedown_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    dest, vuint16m2_t src, size_t offset, size_t vl);
vuint16m4_t vslidedown_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    dest, vuint16m4_t src, size_t offset, size_t vl);
vuint16m8_t vslidedown_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    dest, vuint16m8_t src, size_t offset, size_t vl);
vuint32m1_t vslidedown_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    dest, vuint32m1_t src, size_t offset, size_t vl);
vuint32m2_t vslidedown_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    dest, vuint32m2_t src, size_t offset, size_t vl);
vuint32m4_t vslidedown_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    dest, vuint32m4_t src, size_t offset, size_t vl);
vuint32m8_t vslidedown_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    dest, vuint32m8_t src, size_t offset, size_t vl);

```

```

vuint64m1_t vslidedown_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    dest, vuint64m1_t src, size_t offset, size_t vl);
vuint64m2_t vslidedown_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    dest, vuint64m2_t src, size_t offset, size_t vl);
vuint64m4_t vslidedown_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    dest, vuint64m4_t src, size_t offset, size_t vl);
vuint64m8_t vslidedown_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    dest, vuint64m8_t src, size_t offset, size_t vl);
vfloat16m1_t vslidedown_vx_f16m1_m (vbool16_t mask, vfloat16m1_t
    dest, vfloat16m1_t src, size_t offset, size_t vl);
vfloat16m2_t vslidedown_vx_f16m2_m (vbool8_t mask, vfloat16m2_t
    dest, vfloat16m2_t src, size_t offset, size_t vl);
vfloat16m4_t vslidedown_vx_f16m4_m (vbool4_t mask, vfloat16m4_t
    dest, vfloat16m4_t src, size_t offset, size_t vl);
vfloat16m8_t vslidedown_vx_f16m8_m (vbool2_t mask, vfloat16m8_t
    dest, vfloat16m8_t src, size_t offset, size_t vl);
vfloat32m1_t vslidedown_vx_f32m1_m (vbool32_t mask, vfloat32m1_t
    dest, vfloat32m1_t src, size_t offset, size_t vl);
vfloat32m2_t vslidedown_vx_f32m2_m (vbool16_t mask, vfloat32m2_t
    dest, vfloat32m2_t src, size_t offset, size_t vl);
vfloat32m4_t vslidedown_vx_f32m4_m (vbool8_t mask, vfloat32m4_t
    dest, vfloat32m4_t src, size_t offset, size_t vl);
vfloat32m8_t vslidedown_vx_f32m8_m (vbool4_t mask, vfloat32m8_t
    dest, vfloat32m8_t src, size_t offset, size_t vl);
vfloat64m1_t vslidedown_vx_f64m1_m (vbool64_t mask, vfloat64m1_t
    dest, vfloat64m1_t src, size_t offset, size_t vl);
vfloat64m2_t vslidedown_vx_f64m2_m (vbool32_t mask, vfloat64m2_t
    dest, vfloat64m2_t src, size_t offset, size_t vl);
vfloat64m4_t vslidedown_vx_f64m4_m (vbool16_t mask, vfloat64m4_t
    dest, vfloat64m4_t src, size_t offset, size_t vl);
vfloat64m8_t vslidedown_vx_f64m8_m (vbool8_t mask, vfloat64m8_t
    dest, vfloat64m8_t src, size_t offset, size_t vl);

```

### Vector Slide1up and Slide1down Functions:

#### Prototypes:

```

vint8m1_t vslide1up_vx_i8m1 (vint8m1_t src, int8_t value, size_t
    vl);
vint8m2_t vslide1up_vx_i8m2 (vint8m2_t src, int8_t value, size_t
    vl);
vint8m4_t vslide1up_vx_i8m4 (vint8m4_t src, int8_t value, size_t
    vl);
vint8m8_t vslide1up_vx_i8m8 (vint8m8_t src, int8_t value, size_t
    vl);

```

```

vint16m1_t vslide1up_vx_i16m1 (vint16m1_t src, int16_t value,
    size_t vl);
vint16m2_t vslide1up_vx_i16m2 (vint16m2_t src, int16_t value,
    size_t vl);
vint16m4_t vslide1up_vx_i16m4 (vint16m4_t src, int16_t value,
    size_t vl);
vint16m8_t vslide1up_vx_i16m8 (vint16m8_t src, int16_t value,
    size_t vl);
vint32m1_t vslide1up_vx_i32m1 (vint32m1_t src, int32_t value,
    size_t vl);
vint32m2_t vslide1up_vx_i32m2 (vint32m2_t src, int32_t value,
    size_t vl);
vint32m4_t vslide1up_vx_i32m4 (vint32m4_t src, int32_t value,
    size_t vl);
vint32m8_t vslide1up_vx_i32m8 (vint32m8_t src, int32_t value,
    size_t vl);
vint64m1_t vslide1up_vx_i64m1 (vint64m1_t src, int64_t value,
    size_t vl);
vint64m2_t vslide1up_vx_i64m2 (vint64m2_t src, int64_t value,
    size_t vl);
vint64m4_t vslide1up_vx_i64m4 (vint64m4_t src, int64_t value,
    size_t vl);
vint64m8_t vslide1up_vx_i64m8 (vint64m8_t src, int64_t value,
    size_t vl);
vuint8m1_t vslide1up_vx_u8m1 (vuint8m1_t src, uint8_t value,
    size_t vl);
vuint8m2_t vslide1up_vx_u8m2 (vuint8m2_t src, uint8_t value,
    size_t vl);
vuint8m4_t vslide1up_vx_u8m4 (vuint8m4_t src, uint8_t value,
    size_t vl);
vuint8m8_t vslide1up_vx_u8m8 (vuint8m8_t src, uint8_t value,
    size_t vl);
vuint16m1_t vslide1up_vx_u16m1 (vuint16m1_t src, uint16_t value,
    size_t vl);
vuint16m2_t vslide1up_vx_u16m2 (vuint16m2_t src, uint16_t value,
    size_t vl);
vuint16m4_t vslide1up_vx_u16m4 (vuint16m4_t src, uint16_t value,
    size_t vl);
vuint16m8_t vslide1up_vx_u16m8 (vuint16m8_t src, uint16_t value,
    size_t vl);
vuint32m1_t vslide1up_vx_u32m1 (vuint32m1_t src, uint32_t value,
    size_t vl);
vuint32m2_t vslide1up_vx_u32m2 (vuint32m2_t src, uint32_t value,
    size_t vl);
vuint32m4_t vslide1up_vx_u32m4 (vuint32m4_t src, uint32_t value,
    size_t vl);

```

```

vuint32m8_t vslide1up_vx_u32m8 (vuint32m8_t src, uint32_t value,
    size_t vl);
vuint64m1_t vslide1up_vx_u64m1 (vuint64m1_t src, uint64_t value,
    size_t vl);
vuint64m2_t vslide1up_vx_u64m2 (vuint64m2_t src, uint64_t value,
    size_t vl);
vuint64m4_t vslide1up_vx_u64m4 (vuint64m4_t src, uint64_t value,
    size_t vl);
vuint64m8_t vslide1up_vx_u64m8 (vuint64m8_t src, uint64_t value,
    size_t vl);
vint8m1_t vslide1down_vx_i8m1 (vint8m1_t src, int8_t value,
    size_t vl);
vint8m2_t vslide1down_vx_i8m2 (vint8m2_t src, int8_t value,
    size_t vl);
vint8m4_t vslide1down_vx_i8m4 (vint8m4_t src, int8_t value,
    size_t vl);
vint8m8_t vslide1down_vx_i8m8 (vint8m8_t src, int8_t value,
    size_t vl);
vint16m1_t vslide1down_vx_i16m1 (vint16m1_t src, int16_t value,
    size_t vl);
vint16m2_t vslide1down_vx_i16m2 (vint16m2_t src, int16_t value,
    size_t vl);
vint16m4_t vslide1down_vx_i16m4 (vint16m4_t src, int16_t value,
    size_t vl);
vint16m8_t vslide1down_vx_i16m8 (vint16m8_t src, int16_t value,
    size_t vl);
vint32m1_t vslide1down_vx_i32m1 (vint32m1_t src, int32_t value,
    size_t vl);
vint32m2_t vslide1down_vx_i32m2 (vint32m2_t src, int32_t value,
    size_t vl);
vint32m4_t vslide1down_vx_i32m4 (vint32m4_t src, int32_t value,
    size_t vl);
vint32m8_t vslide1down_vx_i32m8 (vint32m8_t src, int32_t value,
    size_t vl);
vint64m1_t vslide1down_vx_i64m1 (vint64m1_t src, int64_t value,
    size_t vl);
vint64m2_t vslide1down_vx_i64m2 (vint64m2_t src, int64_t value,
    size_t vl);
vint64m4_t vslide1down_vx_i64m4 (vint64m4_t src, int64_t value,
    size_t vl);
vint64m8_t vslide1down_vx_i64m8 (vint64m8_t src, int64_t value,
    size_t vl);
vuint8m1_t vslide1down_vx_u8m1 (vuint8m1_t src, uint8_t value,
    size_t vl);
vuint8m2_t vslide1down_vx_u8m2 (vuint8m2_t src, uint8_t value,
    size_t vl);

```

```

vuint8m4_t vslide1down_vx_u8m4 (vuint8m4_t src, uint8_t value,
    size_t vl);
vuint8m8_t vslide1down_vx_u8m8 (vuint8m8_t src, uint8_t value,
    size_t vl);
vuint16m1_t vslide1down_vx_u16m1 (vuint16m1_t src, uint16_t
    value, size_t vl);
vuint16m2_t vslide1down_vx_u16m2 (vuint16m2_t src, uint16_t
    value, size_t vl);
vuint16m4_t vslide1down_vx_u16m4 (vuint16m4_t src, uint16_t
    value, size_t vl);
vuint16m8_t vslide1down_vx_u16m8 (vuint16m8_t src, uint16_t
    value, size_t vl);
vuint32m1_t vslide1down_vx_u32m1 (vuint32m1_t src, uint32_t
    value, size_t vl);
vuint32m2_t vslide1down_vx_u32m2 (vuint32m2_t src, uint32_t
    value, size_t vl);
vuint32m4_t vslide1down_vx_u32m4 (vuint32m4_t src, uint32_t
    value, size_t vl);
vuint32m8_t vslide1down_vx_u32m8 (vuint32m8_t src, uint32_t
    value, size_t vl);
vuint64m1_t vslide1down_vx_u64m1 (vuint64m1_t src, uint64_t
    value, size_t vl);
vuint64m2_t vslide1down_vx_u64m2 (vuint64m2_t src, uint64_t
    value, size_t vl);
vuint64m4_t vslide1down_vx_u64m4 (vuint64m4_t src, uint64_t
    value, size_t vl);
vuint64m8_t vslide1down_vx_u64m8 (vuint64m8_t src, uint64_t
    value, size_t vl);
// masked functions
vint8m1_t vslide1up_vx_i8m1_m (vbool8_t mask, vint8m1_t
    maskedoff, vint8m1_t src, int8_t value, size_t vl);
vint8m2_t vslide1up_vx_i8m2_m (vbool4_t mask, vint8m2_t
    maskedoff, vint8m2_t src, int8_t value, size_t vl);
vint8m4_t vslide1up_vx_i8m4_m (vbool2_t mask, vint8m4_t
    maskedoff, vint8m4_t src, int8_t value, size_t vl);
vint8m8_t vslide1up_vx_i8m8_m (vbool1_t mask, vint8m8_t
    maskedoff, vint8m8_t src, int8_t value, size_t vl);
vint16m1_t vslide1up_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t src, int16_t value, size_t vl);
vint16m2_t vslide1up_vx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t src, int16_t value, size_t vl);
vint16m4_t vslide1up_vx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t src, int16_t value, size_t vl);
vint16m8_t vslide1up_vx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t src, int16_t value, size_t vl);

```

```

vint32m1_t vslide1up_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t src, int32_t value, size_t vl);
vint32m2_t vslide1up_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t src, int32_t value, size_t vl);
vint32m4_t vslide1up_vx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t src, int32_t value, size_t vl);
vint32m8_t vslide1up_vx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t src, int32_t value, size_t vl);
vint64m1_t vslide1up_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t src, int64_t value, size_t vl);
vint64m2_t vslide1up_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t src, int64_t value, size_t vl);
vint64m4_t vslide1up_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t src, int64_t value, size_t vl);
vint64m8_t vslide1up_vx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t src, int64_t value, size_t vl);
vuint8m1_t vslide1up_vx_u8m1_m (vbool8_t mask, vuint8m1_t
    maskedoff, vuint8m1_t src, uint8_t value, size_t vl);
vuint8m2_t vslide1up_vx_u8m2_m (vbool4_t mask, vuint8m2_t
    maskedoff, vuint8m2_t src, uint8_t value, size_t vl);
vuint8m4_t vslide1up_vx_u8m4_m (vbool2_t mask, vuint8m4_t
    maskedoff, vuint8m4_t src, uint8_t value, size_t vl);
vuint8m8_t vslide1up_vx_u8m8_m (vbool1_t mask, vuint8m8_t
    maskedoff, vuint8m8_t src, uint8_t value, size_t vl);
vuint16m1_t vslide1up_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t src, uint16_t value, size_t vl);
vuint16m2_t vslide1up_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t src, uint16_t value, size_t vl);
vuint16m4_t vslide1up_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t src, uint16_t value, size_t vl);
vuint16m8_t vslide1up_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t src, uint16_t value, size_t vl);
vuint32m1_t vslide1up_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t src, uint32_t value, size_t vl);
vuint32m2_t vslide1up_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t src, uint32_t value, size_t vl);
vuint32m4_t vslide1up_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t src, uint32_t value, size_t vl);
vuint32m8_t vslide1up_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t src, uint32_t value, size_t vl);
vuint64m1_t vslide1up_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t src, uint64_t value, size_t vl);
vuint64m2_t vslide1up_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t src, uint64_t value, size_t vl);
vuint64m4_t vslide1up_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t src, uint64_t value, size_t vl);

```

```

vuint64m8_t vslide1up_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t src, uint64_t value, size_t vl);
vint8m1_t vslide1down_vx_i8m1_m (vbool8_t mask, vint8m1_t
    maskedoff, vint8m1_t src, int8_t value, size_t vl);
vint8m2_t vslide1down_vx_i8m2_m (vbool4_t mask, vint8m2_t
    maskedoff, vint8m2_t src, int8_t value, size_t vl);
vint8m4_t vslide1down_vx_i8m4_m (vbool2_t mask, vint8m4_t
    maskedoff, vint8m4_t src, int8_t value, size_t vl);
vint8m8_t vslide1down_vx_i8m8_m (vbool1_t mask, vint8m8_t
    maskedoff, vint8m8_t src, int8_t value, size_t vl);
vint16m1_t vslide1down_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t src, int16_t value, size_t vl);
vint16m2_t vslide1down_vx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t src, int16_t value, size_t vl);
vint16m4_t vslide1down_vx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t src, int16_t value, size_t vl);
vint16m8_t vslide1down_vx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t src, int16_t value, size_t vl);
vint32m1_t vslide1down_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t src, int32_t value, size_t vl);
vint32m2_t vslide1down_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t src, int32_t value, size_t vl);
vint32m4_t vslide1down_vx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t src, int32_t value, size_t vl);
vint32m8_t vslide1down_vx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t src, int32_t value, size_t vl);
vint64m1_t vslide1down_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t src, int64_t value, size_t vl);
vint64m2_t vslide1down_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t src, int64_t value, size_t vl);
vint64m4_t vslide1down_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t src, int64_t value, size_t vl);
vint64m8_t vslide1down_vx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t src, int64_t value, size_t vl);
vuint8m1_t vslide1down_vx_u8m1_m (vbool8_t mask, vuint8m1_t
    maskedoff, vuint8m1_t src, uint8_t value, size_t vl);
vuint8m2_t vslide1down_vx_u8m2_m (vbool4_t mask, vuint8m2_t
    maskedoff, vuint8m2_t src, uint8_t value, size_t vl);
vuint8m4_t vslide1down_vx_u8m4_m (vbool2_t mask, vuint8m4_t
    maskedoff, vuint8m4_t src, uint8_t value, size_t vl);
vuint8m8_t vslide1down_vx_u8m8_m (vbool1_t mask, vuint8m8_t
    maskedoff, vuint8m8_t src, uint8_t value, size_t vl);
vuint16m1_t vslide1down_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t src, uint16_t value, size_t vl);
vuint16m2_t vslide1down_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t src, uint16_t value, size_t vl);

```

```

vuint16m4_t vslide1down_vx_u16m4_m (vbool4_t mask, vuint16m4_t
maskedoff, vuint16m4_t src, uint16_t value, size_t vl);
vuint16m8_t vslide1down_vx_u16m8_m (vbool2_t mask, vuint16m8_t
maskedoff, vuint16m8_t src, uint16_t value, size_t vl);
vuint32m1_t vslide1down_vx_u32m1_m (vbool32_t mask, vuint32m1_t
maskedoff, vuint32m1_t src, uint32_t value, size_t vl);
vuint32m2_t vslide1down_vx_u32m2_m (vbool16_t mask, vuint32m2_t
maskedoff, vuint32m2_t src, uint32_t value, size_t vl);
vuint32m4_t vslide1down_vx_u32m4_m (vbool8_t mask, vuint32m4_t
maskedoff, vuint32m4_t src, uint32_t value, size_t vl);
vuint32m8_t vslide1down_vx_u32m8_m (vbool4_t mask, vuint32m8_t
maskedoff, vuint32m8_t src, uint32_t value, size_t vl);
vuint64m1_t vslide1down_vx_u64m1_m (vbool64_t mask, vuint64m1_t
maskedoff, vuint64m1_t src, uint64_t value, size_t vl);
vuint64m2_t vslide1down_vx_u64m2_m (vbool32_t mask, vuint64m2_t
maskedoff, vuint64m2_t src, uint64_t value, size_t vl);
vuint64m4_t vslide1down_vx_u64m4_m (vbool16_t mask, vuint64m4_t
maskedoff, vuint64m4_t src, uint64_t value, size_t vl);
vuint64m8_t vslide1down_vx_u64m8_m (vbool8_t mask, vuint64m8_t
maskedoff, vuint64m8_t src, uint64_t value, size_t vl);

```

## Vector Register Gather Functions:

### Prototypes:

```

vint8m1_t vrgather_vv_i8m1 (vint8m1_t op1, vuint8m1_t index,
size_t vl);
vint8m1_t vrgather_vx_i8m1 (vint8m1_t op1, size_t index, size_t
vl);
vint8m2_t vrgather_vv_i8m2 (vint8m2_t op1, vuint8m2_t index,
size_t vl);
vint8m2_t vrgather_vx_i8m2 (vint8m2_t op1, size_t index, size_t
vl);
vint8m4_t vrgather_vv_i8m4 (vint8m4_t op1, vuint8m4_t index,
size_t vl);
vint8m4_t vrgather_vx_i8m4 (vint8m4_t op1, size_t index, size_t
vl);
vint8m8_t vrgather_vv_i8m8 (vint8m8_t op1, vuint8m8_t index,
size_t vl);
vint8m8_t vrgather_vx_i8m8 (vint8m8_t op1, size_t index, size_t
vl);
vint16m1_t vrgather_vv_i16m1 (vint16m1_t op1, vuint16m1_t index,
size_t vl);
vint16m1_t vrgather_vx_i16m1 (vint16m1_t op1, size_t index,
size_t vl);

```



```

vint16m2_t vrgather_vv_i16m2 (vint16m2_t op1, vuint16m2_t index,
    size_t vl);
vint16m2_t vrgather_vx_i16m2 (vint16m2_t op1, size_t index,
    size_t vl);
vint16m4_t vrgather_vv_i16m4 (vint16m4_t op1, vuint16m4_t index,
    size_t vl);
vint16m4_t vrgather_vx_i16m4 (vint16m4_t op1, size_t index,
    size_t vl);
vint16m8_t vrgather_vv_i16m8 (vint16m8_t op1, vuint16m8_t index,
    size_t vl);
vint16m8_t vrgather_vx_i16m8 (vint16m8_t op1, size_t index,
    size_t vl);
vint32m1_t vrgather_vv_i32m1 (vint32m1_t op1, vuint32m1_t index,
    size_t vl);
vint32m1_t vrgather_vx_i32m1 (vint32m1_t op1, size_t index,
    size_t vl);
vint32m2_t vrgather_vv_i32m2 (vint32m2_t op1, vuint32m2_t index,
    size_t vl);
vint32m2_t vrgather_vx_i32m2 (vint32m2_t op1, size_t index,
    size_t vl);
vint32m4_t vrgather_vv_i32m4 (vint32m4_t op1, vuint32m4_t index,
    size_t vl);
vint32m4_t vrgather_vx_i32m4 (vint32m4_t op1, size_t index,
    size_t vl);
vint32m8_t vrgather_vv_i32m8 (vint32m8_t op1, vuint32m8_t index,
    size_t vl);
vint32m8_t vrgather_vx_i32m8 (vint32m8_t op1, size_t index,
    size_t vl);
vint64m1_t vrgather_vv_i64m1 (vint64m1_t op1, vuint64m1_t index,
    size_t vl);
vint64m1_t vrgather_vx_i64m1 (vint64m1_t op1, size_t index,
    size_t vl);
vint64m2_t vrgather_vv_i64m2 (vint64m2_t op1, vuint64m2_t index,
    size_t vl);
vint64m2_t vrgather_vx_i64m2 (vint64m2_t op1, size_t index,
    size_t vl);
vint64m4_t vrgather_vv_i64m4 (vint64m4_t op1, vuint64m4_t index,
    size_t vl);
vint64m4_t vrgather_vx_i64m4 (vint64m4_t op1, size_t index,
    size_t vl);
vint64m8_t vrgather_vv_i64m8 (vint64m8_t op1, vuint64m8_t index,
    size_t vl);
vint64m8_t vrgather_vx_i64m8 (vint64m8_t op1, size_t index,
    size_t vl);
vuint8m1_t vrgather_vv_u8m1 (vuint8m1_t op1, vuint8m1_t index,
    size_t vl);

```

```

vuint8m1_t vrgather_vx_u8m1 (vuint8m1_t op1, size_t index,
    size_t vl);
vuint8m2_t vrgather_vv_u8m2 (vuint8m2_t op1, vuint8m2_t index,
    size_t vl);
vuint8m2_t vrgather_vx_u8m2 (vuint8m2_t op1, size_t index,
    size_t vl);
vuint8m4_t vrgather_vv_u8m4 (vuint8m4_t op1, vuint8m4_t index,
    size_t vl);
vuint8m4_t vrgather_vx_u8m4 (vuint8m4_t op1, size_t index,
    size_t vl);
vuint8m8_t vrgather_vv_u8m8 (vuint8m8_t op1, vuint8m8_t index,
    size_t vl);
vuint8m8_t vrgather_vx_u8m8 (vuint8m8_t op1, size_t index,
    size_t vl);
vuint16m1_t vrgather_vv_u16m1 (vuint16m1_t op1, vuint16m1_t
    index, size_t vl);
vuint16m1_t vrgather_vx_u16m1 (vuint16m1_t op1, size_t index,
    size_t vl);
vuint16m2_t vrgather_vv_u16m2 (vuint16m2_t op1, vuint16m2_t
    index, size_t vl);
vuint16m2_t vrgather_vx_u16m2 (vuint16m2_t op1, size_t index,
    size_t vl);
vuint16m4_t vrgather_vv_u16m4 (vuint16m4_t op1, vuint16m4_t
    index, size_t vl);
vuint16m4_t vrgather_vx_u16m4 (vuint16m4_t op1, size_t index,
    size_t vl);
vuint16m8_t vrgather_vv_u16m8 (vuint16m8_t op1, vuint16m8_t
    index, size_t vl);
vuint16m8_t vrgather_vx_u16m8 (vuint16m8_t op1, size_t index,
    size_t vl);
vuint32m1_t vrgather_vv_u32m1 (vuint32m1_t op1, vuint32m1_t
    index, size_t vl);
vuint32m1_t vrgather_vx_u32m1 (vuint32m1_t op1, size_t index,
    size_t vl);
vuint32m2_t vrgather_vv_u32m2 (vuint32m2_t op1, vuint32m2_t
    index, size_t vl);
vuint32m2_t vrgather_vx_u32m2 (vuint32m2_t op1, size_t index,
    size_t vl);
vuint32m4_t vrgather_vv_u32m4 (vuint32m4_t op1, vuint32m4_t
    index, size_t vl);
vuint32m4_t vrgather_vx_u32m4 (vuint32m4_t op1, size_t index,
    size_t vl);
vuint32m8_t vrgather_vv_u32m8 (vuint32m8_t op1, vuint32m8_t
    index, size_t vl);
vuint32m8_t vrgather_vx_u32m8 (vuint32m8_t op1, size_t index,
    size_t vl);

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vuint64m1_t vrgather_vv_u64m1 (vuint64m1_t op1, vuint64m1_t
    index, size_t vl);
vuint64m1_t vrgather_vx_u64m1 (vuint64m1_t op1, size_t index,
    size_t vl);
vuint64m2_t vrgather_vv_u64m2 (vuint64m2_t op1, vuint64m2_t
    index, size_t vl);
vuint64m2_t vrgather_vx_u64m2 (vuint64m2_t op1, size_t index,
    size_t vl);
vuint64m4_t vrgather_vv_u64m4 (vuint64m4_t op1, vuint64m4_t
    index, size_t vl);
vuint64m4_t vrgather_vx_u64m4 (vuint64m4_t op1, size_t index,
    size_t vl);
vuint64m8_t vrgather_vv_u64m8 (vuint64m8_t op1, vuint64m8_t
    index, size_t vl);
vuint64m8_t vrgather_vx_u64m8 (vuint64m8_t op1, size_t index,
    size_t vl);
vfloat16m1_t vrgather_vv_f16m1 (vfloat16m1_t op1, vuint16m1_t
    index, size_t vl);
vfloat16m1_t vrgather_vx_f16m1 (vfloat16m1_t op1, size_t index,
    size_t vl);
vfloat16m2_t vrgather_vv_f16m2 (vfloat16m2_t op1, vuint16m2_t
    index, size_t vl);
vfloat16m2_t vrgather_vx_f16m2 (vfloat16m2_t op1, size_t index,
    size_t vl);
vfloat16m4_t vrgather_vv_f16m4 (vfloat16m4_t op1, vuint16m4_t
    index, size_t vl);
vfloat16m4_t vrgather_vx_f16m4 (vfloat16m4_t op1, size_t index,
    size_t vl);
vfloat16m8_t vrgather_vv_f16m8 (vfloat16m8_t op1, vuint16m8_t
    index, size_t vl);
vfloat16m8_t vrgather_vx_f16m8 (vfloat16m8_t op1, size_t index,
    size_t vl);
vfloat32m1_t vrgather_vv_f32m1 (vfloat32m1_t op1, vuint32m1_t
    index, size_t vl);
vfloat32m1_t vrgather_vx_f32m1 (vfloat32m1_t op1, size_t index,
    size_t vl);
vfloat32m2_t vrgather_vv_f32m2 (vfloat32m2_t op1, vuint32m2_t
    index, size_t vl);
vfloat32m2_t vrgather_vx_f32m2 (vfloat32m2_t op1, size_t index,
    size_t vl);
vfloat32m4_t vrgather_vv_f32m4 (vfloat32m4_t op1, vuint32m4_t
    index, size_t vl);
vfloat32m4_t vrgather_vx_f32m4 (vfloat32m4_t op1, size_t index,
    size_t vl);
vfloat32m8_t vrgather_vv_f32m8 (vfloat32m8_t op1, vuint32m8_t
    index, size_t vl);

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vfloat32m8_t vrgather_vx_f32m8 (vfloat32m8_t op1, size_t index,
    size_t vl);
vfloat64m1_t vrgather_vv_f64m1 (vfloat64m1_t op1, vuint64m1_t
    index, size_t vl);
vfloat64m1_t vrgather_vx_f64m1 (vfloat64m1_t op1, size_t index,
    size_t vl);
vfloat64m2_t vrgather_vv_f64m2 (vfloat64m2_t op1, vuint64m2_t
    index, size_t vl);
vfloat64m2_t vrgather_vx_f64m2 (vfloat64m2_t op1, size_t index,
    size_t vl);
vfloat64m4_t vrgather_vv_f64m4 (vfloat64m4_t op1, vuint64m4_t
    index, size_t vl);
vfloat64m4_t vrgather_vx_f64m4 (vfloat64m4_t op1, size_t index,
    size_t vl);
vfloat64m8_t vrgather_vv_f64m8 (vfloat64m8_t op1, vuint64m8_t
    index, size_t vl);
vfloat64m8_t vrgather_vx_f64m8 (vfloat64m8_t op1, size_t index,
    size_t vl);
// masked functions
vint8m1_t vrgather_vv_i8m1_m (vbool8_t mask, vint8m1_t
    maskedoff, vint8m1_t op1, vuint8m1_t index, size_t vl);
vint8m1_t vrgather_vx_i8m1_m (vbool8_t mask, vint8m1_t
    maskedoff, vint8m1_t op1, size_t index, size_t vl);
vint8m2_t vrgather_vv_i8m2_m (vbool4_t mask, vint8m2_t
    maskedoff, vint8m2_t op1, vuint8m2_t index, size_t vl);
vint8m2_t vrgather_vx_i8m2_m (vbool4_t mask, vint8m2_t
    maskedoff, vint8m2_t op1, size_t index, size_t vl);
vint8m4_t vrgather_vv_i8m4_m (vbool2_t mask, vint8m4_t
    maskedoff, vint8m4_t op1, vuint8m4_t index, size_t vl);
vint8m4_t vrgather_vx_i8m4_m (vbool2_t mask, vint8m4_t
    maskedoff, vint8m4_t op1, size_t index, size_t vl);
vint8m8_t vrgather_vv_i8m8_m (vbool1_t mask, vint8m8_t
    maskedoff, vint8m8_t op1, vuint8m8_t index, size_t vl);
vint8m8_t vrgather_vx_i8m8_m (vbool1_t mask, vint8m8_t
    maskedoff, vint8m8_t op1, size_t index, size_t vl);
vint16m1_t vrgather_vv_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, vuint16m1_t index, size_t vl);
vint16m1_t vrgather_vx_i16m1_m (vbool16_t mask, vint16m1_t
    maskedoff, vint16m1_t op1, size_t index, size_t vl);
vint16m2_t vrgather_vv_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, vuint16m2_t index, size_t vl);
vint16m2_t vrgather_vx_i16m2_m (vbool8_t mask, vint16m2_t
    maskedoff, vint16m2_t op1, size_t index, size_t vl);
vint16m4_t vrgather_vv_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, vuint16m4_t index, size_t vl);

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vint16m4_t vrgather_vx_i16m4_m (vbool4_t mask, vint16m4_t
    maskedoff, vint16m4_t op1, size_t index, size_t vl);
vint16m8_t vrgather_vv_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, vuint16m8_t index, size_t vl);
vint16m8_t vrgather_vx_i16m8_m (vbool2_t mask, vint16m8_t
    maskedoff, vint16m8_t op1, size_t index, size_t vl);
vint32m1_t vrgather_vv_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, vuint32m1_t index, size_t vl);
vint32m1_t vrgather_vx_i32m1_m (vbool32_t mask, vint32m1_t
    maskedoff, vint32m1_t op1, size_t index, size_t vl);
vint32m2_t vrgather_vv_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, vuint32m2_t index, size_t vl);
vint32m2_t vrgather_vx_i32m2_m (vbool16_t mask, vint32m2_t
    maskedoff, vint32m2_t op1, size_t index, size_t vl);
vint32m4_t vrgather_vv_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, vuint32m4_t index, size_t vl);
vint32m4_t vrgather_vx_i32m4_m (vbool8_t mask, vint32m4_t
    maskedoff, vint32m4_t op1, size_t index, size_t vl);
vint32m8_t vrgather_vv_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, vuint32m8_t index, size_t vl);
vint32m8_t vrgather_vx_i32m8_m (vbool4_t mask, vint32m8_t
    maskedoff, vint32m8_t op1, size_t index, size_t vl);
vint64m1_t vrgather_vv_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, vuint64m1_t index, size_t vl);
vint64m1_t vrgather_vx_i64m1_m (vbool64_t mask, vint64m1_t
    maskedoff, vint64m1_t op1, size_t index, size_t vl);
vint64m2_t vrgather_vv_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, vuint64m2_t index, size_t vl);
vint64m2_t vrgather_vx_i64m2_m (vbool32_t mask, vint64m2_t
    maskedoff, vint64m2_t op1, size_t index, size_t vl);
vint64m4_t vrgather_vv_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, vuint64m4_t index, size_t vl);
vint64m4_t vrgather_vx_i64m4_m (vbool16_t mask, vint64m4_t
    maskedoff, vint64m4_t op1, size_t index, size_t vl);
vint64m8_t vrgather_vv_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, vuint64m8_t index, size_t vl);
vint64m8_t vrgather_vx_i64m8_m (vbool8_t mask, vint64m8_t
    maskedoff, vint64m8_t op1, size_t index, size_t vl);
vuint8m1_t vrgather_vv_u8m1_m (vbool8_t mask, vuint8m1_t
    maskedoff, vuint8m1_t op1, vuint8m1_t index, size_t vl);
vuint8m1_t vrgather_vx_u8m1_m (vbool8_t mask, vuint8m1_t
    maskedoff, vuint8m1_t op1, size_t index, size_t vl);
vuint8m2_t vrgather_vv_u8m2_m (vbool4_t mask, vuint8m2_t
    maskedoff, vuint8m2_t op1, vuint8m2_t index, size_t vl);
vuint8m2_t vrgather_vx_u8m2_m (vbool4_t mask, vuint8m2_t
    maskedoff, vuint8m2_t op1, size_t index, size_t vl);

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vuint8m4_t vrgather_vv_u8m4_m (vbool12_t mask, vuint8m4_t
    maskedoff, vuint8m4_t op1, vuint8m4_t index, size_t vl);
vuint8m4_t vrgather_vx_u8m4_m (vbool12_t mask, vuint8m4_t
    maskedoff, vuint8m4_t op1, size_t index, size_t vl);
vuint8m8_t vrgather_vv_u8m8_m (vbool11_t mask, vuint8m8_t
    maskedoff, vuint8m8_t op1, vuint8m8_t index, size_t vl);
vuint8m8_t vrgather_vx_u8m8_m (vbool11_t mask, vuint8m8_t
    maskedoff, vuint8m8_t op1, size_t index, size_t vl);
vuint16m1_t vrgather_vv_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, vuint16m1_t index, size_t vl);
vuint16m1_t vrgather_vx_u16m1_m (vbool16_t mask, vuint16m1_t
    maskedoff, vuint16m1_t op1, size_t index, size_t vl);
vuint16m2_t vrgather_vv_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, vuint16m2_t index, size_t vl);
vuint16m2_t vrgather_vx_u16m2_m (vbool8_t mask, vuint16m2_t
    maskedoff, vuint16m2_t op1, size_t index, size_t vl);
vuint16m4_t vrgather_vv_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, vuint16m4_t index, size_t vl);
vuint16m4_t vrgather_vx_u16m4_m (vbool4_t mask, vuint16m4_t
    maskedoff, vuint16m4_t op1, size_t index, size_t vl);
vuint16m8_t vrgather_vv_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, vuint16m8_t index, size_t vl);
vuint16m8_t vrgather_vx_u16m8_m (vbool2_t mask, vuint16m8_t
    maskedoff, vuint16m8_t op1, size_t index, size_t vl);
vuint32m1_t vrgather_vv_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, vuint32m1_t index, size_t vl);
vuint32m1_t vrgather_vx_u32m1_m (vbool32_t mask, vuint32m1_t
    maskedoff, vuint32m1_t op1, size_t index, size_t vl);
vuint32m2_t vrgather_vv_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, vuint32m2_t index, size_t vl);
vuint32m2_t vrgather_vx_u32m2_m (vbool16_t mask, vuint32m2_t
    maskedoff, vuint32m2_t op1, size_t index, size_t vl);
vuint32m4_t vrgather_vv_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, vuint32m4_t index, size_t vl);
vuint32m4_t vrgather_vx_u32m4_m (vbool8_t mask, vuint32m4_t
    maskedoff, vuint32m4_t op1, size_t index, size_t vl);
vuint32m8_t vrgather_vv_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, vuint32m8_t index, size_t vl);
vuint32m8_t vrgather_vx_u32m8_m (vbool4_t mask, vuint32m8_t
    maskedoff, vuint32m8_t op1, size_t index, size_t vl);
vuint64m1_t vrgather_vv_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, vuint64m1_t index, size_t vl);
vuint64m1_t vrgather_vx_u64m1_m (vbool64_t mask, vuint64m1_t
    maskedoff, vuint64m1_t op1, size_t index, size_t vl);
vuint64m2_t vrgather_vv_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, vuint64m2_t index, size_t vl);

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vuint64m2_t vrgather_vx_u64m2_m (vbool32_t mask, vuint64m2_t
    maskedoff, vuint64m2_t op1, size_t index, size_t vl);
vuint64m4_t vrgather_vv_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, vuint64m4_t index, size_t vl);
vuint64m4_t vrgather_vx_u64m4_m (vbool16_t mask, vuint64m4_t
    maskedoff, vuint64m4_t op1, size_t index, size_t vl);
vuint64m8_t vrgather_vv_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, vuint64m8_t index, size_t vl);
vuint64m8_t vrgather_vx_u64m8_m (vbool8_t mask, vuint64m8_t
    maskedoff, vuint64m8_t op1, size_t index, size_t vl);
vfloat16m1_t vrgather_vv_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, vuint16m1_t index, size_t vl);
vfloat16m1_t vrgather_vx_f16m1_m (vbool16_t mask, vfloat16m1_t
    maskedoff, vfloat16m1_t op1, size_t index, size_t vl);
vfloat16m2_t vrgather_vv_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, vuint16m2_t index, size_t vl);
vfloat16m2_t vrgather_vx_f16m2_m (vbool8_t mask, vfloat16m2_t
    maskedoff, vfloat16m2_t op1, size_t index, size_t vl);
vfloat16m4_t vrgather_vv_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, vuint16m4_t index, size_t vl);
vfloat16m4_t vrgather_vx_f16m4_m (vbool4_t mask, vfloat16m4_t
    maskedoff, vfloat16m4_t op1, size_t index, size_t vl);
vfloat16m8_t vrgather_vv_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, vuint16m8_t index, size_t vl);
vfloat16m8_t vrgather_vx_f16m8_m (vbool2_t mask, vfloat16m8_t
    maskedoff, vfloat16m8_t op1, size_t index, size_t vl);
vfloat32m1_t vrgather_vv_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, vuint32m1_t index, size_t vl);
vfloat32m1_t vrgather_vx_f32m1_m (vbool32_t mask, vfloat32m1_t
    maskedoff, vfloat32m1_t op1, size_t index, size_t vl);
vfloat32m2_t vrgather_vv_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, vuint32m2_t index, size_t vl);
vfloat32m2_t vrgather_vx_f32m2_m (vbool16_t mask, vfloat32m2_t
    maskedoff, vfloat32m2_t op1, size_t index, size_t vl);
vfloat32m4_t vrgather_vv_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, vuint32m4_t index, size_t vl);
vfloat32m4_t vrgather_vx_f32m4_m (vbool8_t mask, vfloat32m4_t
    maskedoff, vfloat32m4_t op1, size_t index, size_t vl);
vfloat32m8_t vrgather_vv_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, vuint32m8_t index, size_t vl);
vfloat32m8_t vrgather_vx_f32m8_m (vbool4_t mask, vfloat32m8_t
    maskedoff, vfloat32m8_t op1, size_t index, size_t vl);
vfloat64m1_t vrgather_vv_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, vuint64m1_t index, size_t vl);
vfloat64m1_t vrgather_vx_f64m1_m (vbool64_t mask, vfloat64m1_t
    maskedoff, vfloat64m1_t op1, size_t index, size_t vl);

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vfloat64m2_t vrgather_vv_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, vuint64m2_t index, size_t vl);
vfloat64m2_t vrgather_vx_f64m2_m (vbool32_t mask, vfloat64m2_t
    maskedoff, vfloat64m2_t op1, size_t index, size_t vl);
vfloat64m4_t vrgather_vv_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, vuint64m4_t index, size_t vl);
vfloat64m4_t vrgather_vx_f64m4_m (vbool16_t mask, vfloat64m4_t
    maskedoff, vfloat64m4_t op1, size_t index, size_t vl);
vfloat64m8_t vrgather_vv_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, vuint64m8_t index, size_t vl);
vfloat64m8_t vrgather_vx_f64m8_m (vbool8_t mask, vfloat64m8_t
    maskedoff, vfloat64m8_t op1, size_t index, size_t vl);

```

## Vector Compress Functions:

### Prototypes:

```

vint8m1_t vcompress_vm_i8m1 (vbool8_t mask, vint8m1_t dest,
    vint8m1_t src, size_t vl);
vint8m2_t vcompress_vm_i8m2 (vbool4_t mask, vint8m2_t dest,
    vint8m2_t src, size_t vl);
vint8m4_t vcompress_vm_i8m4 (vbool2_t mask, vint8m4_t dest,
    vint8m4_t src, size_t vl);
vint8m8_t vcompress_vm_i8m8 (vbool1_t mask, vint8m8_t dest,
    vint8m8_t src, size_t vl);
vint16m1_t vcompress_vm_i16m1 (vbool16_t mask, vint16m1_t dest,
    vint16m1_t src, size_t vl);
vint16m2_t vcompress_vm_i16m2 (vbool8_t mask, vint16m2_t dest,
    vint16m2_t src, size_t vl);
vint16m4_t vcompress_vm_i16m4 (vbool4_t mask, vint16m4_t dest,
    vint16m4_t src, size_t vl);
vint16m8_t vcompress_vm_i16m8 (vbool2_t mask, vint16m8_t dest,
    vint16m8_t src, size_t vl);
vint32m1_t vcompress_vm_i32m1 (vbool32_t mask, vint32m1_t dest,
    vint32m1_t src, size_t vl);
vint32m2_t vcompress_vm_i32m2 (vbool16_t mask, vint32m2_t dest,
    vint32m2_t src, size_t vl);
vint32m4_t vcompress_vm_i32m4 (vbool8_t mask, vint32m4_t dest,
    vint32m4_t src, size_t vl);
vint32m8_t vcompress_vm_i32m8 (vbool4_t mask, vint32m8_t dest,
    vint32m8_t src, size_t vl);
vint64m1_t vcompress_vm_i64m1 (vbool64_t mask, vint64m1_t dest,
    vint64m1_t src, size_t vl);
vint64m2_t vcompress_vm_i64m2 (vbool32_t mask, vint64m2_t dest,
    vint64m2_t src, size_t vl);

```



```

vint64m4_t vcompress_vm_i64m4 (vbool16_t mask, vint64m4_t dest,
    vint64m4_t src, size_t vl);
vint64m8_t vcompress_vm_i64m8 (vbool8_t mask, vint64m8_t dest,
    vint64m8_t src, size_t vl);
vuint8m1_t vcompress_vm_u8m1 (vbool8_t mask, vuint8m1_t dest,
    vuint8m1_t src, size_t vl);
vuint8m2_t vcompress_vm_u8m2 (vbool4_t mask, vuint8m2_t dest,
    vuint8m2_t src, size_t vl);
vuint8m4_t vcompress_vm_u8m4 (vbool2_t mask, vuint8m4_t dest,
    vuint8m4_t src, size_t vl);
vuint8m8_t vcompress_vm_u8m8 (vbool1_t mask, vuint8m8_t dest,
    vuint8m8_t src, size_t vl);
vuint16m1_t vcompress_vm_u16m1 (vbool16_t mask, vuint16m1_t
    dest, vuint16m1_t src, size_t vl);
vuint16m2_t vcompress_vm_u16m2 (vbool8_t mask, vuint16m2_t dest,
    vuint16m2_t src, size_t vl);
vuint16m4_t vcompress_vm_u16m4 (vbool4_t mask, vuint16m4_t dest,
    vuint16m4_t src, size_t vl);
vuint16m8_t vcompress_vm_u16m8 (vbool2_t mask, vuint16m8_t dest,
    vuint16m8_t src, size_t vl);
vuint32m1_t vcompress_vm_u32m1 (vbool32_t mask, vuint32m1_t
    dest, vuint32m1_t src, size_t vl);
vuint32m2_t vcompress_vm_u32m2 (vbool16_t mask, vuint32m2_t
    dest, vuint32m2_t src, size_t vl);
vuint32m4_t vcompress_vm_u32m4 (vbool8_t mask, vuint32m4_t dest,
    vuint32m4_t src, size_t vl);
vuint32m8_t vcompress_vm_u32m8 (vbool4_t mask, vuint32m8_t dest,
    vuint32m8_t src, size_t vl);
vuint64m1_t vcompress_vm_u64m1 (vbool64_t mask, vuint64m1_t
    dest, vuint64m1_t src, size_t vl);
vuint64m2_t vcompress_vm_u64m2 (vbool32_t mask, vuint64m2_t
    dest, vuint64m2_t src, size_t vl);
vuint64m4_t vcompress_vm_u64m4 (vbool16_t mask, vuint64m4_t
    dest, vuint64m4_t src, size_t vl);
vuint64m8_t vcompress_vm_u64m8 (vbool8_t mask, vuint64m8_t dest,
    vuint64m8_t src, size_t vl);
vfloat16m1_t vcompress_vm_f16m1 (vbool16_t mask, vfloat16m1_t
    dest, vfloat16m1_t src, size_t vl);
vfloat16m2_t vcompress_vm_f16m2 (vbool8_t mask, vfloat16m2_t
    dest, vfloat16m2_t src, size_t vl);
vfloat16m4_t vcompress_vm_f16m4 (vbool4_t mask, vfloat16m4_t
    dest, vfloat16m4_t src, size_t vl);
vfloat16m8_t vcompress_vm_f16m8 (vbool2_t mask, vfloat16m8_t
    dest, vfloat16m8_t src, size_t vl);
vfloat32m1_t vcompress_vm_f32m1 (vbool32_t mask, vfloat32m1_t
    dest, vfloat32m1_t src, size_t vl);

```

```

vfloat32m2_t vcompress_vm_f32m2 (vbool16_t mask, vfloat32m2_t
    dest, vfloat32m2_t src, size_t vl);
vfloat32m4_t vcompress_vm_f32m4 (vbool8_t mask, vfloat32m4_t
    dest, vfloat32m4_t src, size_t vl);
vfloat32m8_t vcompress_vm_f32m8 (vbool4_t mask, vfloat32m8_t
    dest, vfloat32m8_t src, size_t vl);
vfloat64m1_t vcompress_vm_f64m1 (vbool64_t mask, vfloat64m1_t
    dest, vfloat64m1_t src, size_t vl);
vfloat64m2_t vcompress_vm_f64m2 (vbool32_t mask, vfloat64m2_t
    dest, vfloat64m2_t src, size_t vl);
vfloat64m4_t vcompress_vm_f64m4 (vbool16_t mask, vfloat64m4_t
    dest, vfloat64m4_t src, size_t vl);
vfloat64m8_t vcompress_vm_f64m8 (vbool8_t mask, vfloat64m8_t
    dest, vfloat64m8_t src, size_t vl);

```

## Miscellaneous Vector Functions:

### Reinterpret Cast Conversion Functions:

#### Prototypes:

```

// Reinterpret between different type under the same SEW/LMUL
vuint8m1_t vreinterpret_v_i8m1_u8m1 (vint8m1_t src);
vuint8m2_t vreinterpret_v_i8m2_u8m2 (vint8m2_t src);
vuint8m4_t vreinterpret_v_i8m4_u8m4 (vint8m4_t src);
vuint8m8_t vreinterpret_v_i8m8_u8m8 (vint8m8_t src);
vint8m1_t vreinterpret_v_u8m1_i8m1 (vuint8m1_t src);
vint8m2_t vreinterpret_v_u8m2_i8m2 (vuint8m2_t src);
vint8m4_t vreinterpret_v_u8m4_i8m4 (vuint8m4_t src);
vint8m8_t vreinterpret_v_u8m8_i8m8 (vuint8m8_t src);
vuint16m1_t vreinterpret_v_i16m1_u16m1 (vint16m1_t src);
vuint16m2_t vreinterpret_v_i16m2_u16m2 (vint16m2_t src);
vuint16m4_t vreinterpret_v_i16m4_u16m4 (vint16m4_t src);
vuint16m8_t vreinterpret_v_i16m8_u16m8 (vint16m8_t src);
vint16m1_t vreinterpret_v_u16m1_i16m1 (vuint16m1_t src);
vint16m2_t vreinterpret_v_u16m2_i16m2 (vuint16m2_t src);
vint16m4_t vreinterpret_v_u16m4_i16m4 (vuint16m4_t src);
vint16m8_t vreinterpret_v_u16m8_i16m8 (vuint16m8_t src);
vint16m1_t vreinterpret_v_f16m1_i16m1 (vfloat16m1_t src);
vint16m2_t vreinterpret_v_f16m2_i16m2 (vfloat16m2_t src);
vint16m4_t vreinterpret_v_f16m4_i16m4 (vfloat16m4_t src);
vint16m8_t vreinterpret_v_f16m8_i16m8 (vfloat16m8_t src);
vuint16m1_t vreinterpret_v_f16m1_u16m1 (vfloat16m1_t src);
vuint16m2_t vreinterpret_v_f16m2_u16m2 (vfloat16m2_t src);
vuint16m4_t vreinterpret_v_f16m4_u16m4 (vfloat16m4_t src);
vuint16m8_t vreinterpret_v_f16m8_u16m8 (vfloat16m8_t src);

```

```
vfloat16m1_t vreinterpret_v_i16m1_f16m1 (vint16m1_t src);
vfloat16m2_t vreinterpret_v_i16m2_f16m2 (vint16m2_t src);
vfloat16m4_t vreinterpret_v_i16m4_f16m4 (vint16m4_t src);
vfloat16m8_t vreinterpret_v_i16m8_f16m8 (vint16m8_t src);
vfloat16m1_t vreinterpret_v_u16m1_f16m1 (vuint16m1_t src);
vfloat16m2_t vreinterpret_v_u16m2_f16m2 (vuint16m2_t src);
vfloat16m4_t vreinterpret_v_u16m4_f16m4 (vuint16m4_t src);
vfloat16m8_t vreinterpret_v_u16m8_f16m8 (vuint16m8_t src);
vuint32m1_t vreinterpret_v_i32m1_u32m1 (vint32m1_t src);
vuint32m2_t vreinterpret_v_i32m2_u32m2 (vint32m2_t src);
vuint32m4_t vreinterpret_v_i32m4_u32m4 (vint32m4_t src);
vuint32m8_t vreinterpret_v_i32m8_u32m8 (vint32m8_t src);
vint32m1_t vreinterpret_v_u32m1_i32m1 (vuint32m1_t src);
vint32m2_t vreinterpret_v_u32m2_i32m2 (vuint32m2_t src);
vint32m4_t vreinterpret_v_u32m4_i32m4 (vuint32m4_t src);
vint32m8_t vreinterpret_v_u32m8_i32m8 (vuint32m8_t src);
vfloat32m1_t vreinterpret_v_f32m1_i32m1 (vfloat32m1_t src);
vfloat32m2_t vreinterpret_v_f32m2_i32m2 (vfloat32m2_t src);
vfloat32m4_t vreinterpret_v_f32m4_i32m4 (vfloat32m4_t src);
vfloat32m8_t vreinterpret_v_f32m8_i32m8 (vfloat32m8_t src);
vuint32m1_t vreinterpret_v_f32m1_u32m1 (vfloat32m1_t src);
vuint32m2_t vreinterpret_v_f32m2_u32m2 (vfloat32m2_t src);
vuint32m4_t vreinterpret_v_f32m4_u32m4 (vfloat32m4_t src);
vuint32m8_t vreinterpret_v_f32m8_u32m8 (vfloat32m8_t src);
vfloat32m1_t vreinterpret_v_i32m1_f32m1 (vint32m1_t src);
vfloat32m2_t vreinterpret_v_i32m2_f32m2 (vint32m2_t src);
vfloat32m4_t vreinterpret_v_i32m4_f32m4 (vint32m4_t src);
vfloat32m8_t vreinterpret_v_i32m8_f32m8 (vint32m8_t src);
vfloat32m1_t vreinterpret_v_u32m1_f32m1 (vuint32m1_t src);
vfloat32m2_t vreinterpret_v_u32m2_f32m2 (vuint32m2_t src);
vfloat32m4_t vreinterpret_v_u32m4_f32m4 (vuint32m4_t src);
vfloat32m8_t vreinterpret_v_u32m8_f32m8 (vuint32m8_t src);
vuint64m1_t vreinterpret_v_i64m1_u64m1 (vint64m1_t src);
vuint64m2_t vreinterpret_v_i64m2_u64m2 (vint64m2_t src);
vuint64m4_t vreinterpret_v_i64m4_u64m4 (vint64m4_t src);
vuint64m8_t vreinterpret_v_i64m8_u64m8 (vint64m8_t src);
vint64m1_t vreinterpret_v_u64m1_i64m1 (vuint64m1_t src);
vint64m2_t vreinterpret_v_u64m2_i64m2 (vuint64m2_t src);
vint64m4_t vreinterpret_v_u64m4_i64m4 (vuint64m4_t src);
vint64m8_t vreinterpret_v_u64m8_i64m8 (vuint64m8_t src);
vfloat64m1_t vreinterpret_v_f64m1_i64m1 (vfloat64m1_t src);
vfloat64m2_t vreinterpret_v_f64m2_i64m2 (vfloat64m2_t src);
vfloat64m4_t vreinterpret_v_f64m4_i64m4 (vfloat64m4_t src);
vfloat64m8_t vreinterpret_v_f64m8_i64m8 (vfloat64m8_t src);
vuint64m1_t vreinterpret_v_f64m1_u64m1 (vfloat64m1_t src);
vuint64m2_t vreinterpret_v_f64m2_u64m2 (vfloat64m2_t src);
```

```

vuint64m4_t vreinterpret_v_f64m4_u64m4 (vfloat64m4_t src);
vuint64m8_t vreinterpret_v_f64m8_u64m8 (vfloat64m8_t src);
vfloat64m1_t vreinterpret_v_i64m1_f64m1 (vint64m1_t src);
vfloat64m2_t vreinterpret_v_i64m2_f64m2 (vint64m2_t src);
vfloat64m4_t vreinterpret_v_i64m4_f64m4 (vint64m4_t src);
vfloat64m8_t vreinterpret_v_i64m8_f64m8 (vint64m8_t src);
vfloat64m1_t vreinterpret_v_u64m1_f64m1 (vuint64m1_t src);
vfloat64m2_t vreinterpret_v_u64m2_f64m2 (vuint64m2_t src);
vfloat64m4_t vreinterpret_v_u64m4_f64m4 (vuint64m4_t src);
vfloat64m8_t vreinterpret_v_u64m8_f64m8 (vuint64m8_t src);
// Reinterpret between different SEW under the same LMUL
vint16m1_t vreinterpret_v_i8m1_i16m1 (vint8m1_t src);
vint16m2_t vreinterpret_v_i8m2_i16m2 (vint8m2_t src);
vint16m4_t vreinterpret_v_i8m4_i16m4 (vint8m4_t src);
vint16m8_t vreinterpret_v_i8m8_i16m8 (vint8m8_t src);
vuint16m1_t vreinterpret_v_u8m1_u16m1 (vuint8m1_t src);
vuint16m2_t vreinterpret_v_u8m2_u16m2 (vuint8m2_t src);
vuint16m4_t vreinterpret_v_u8m4_u16m4 (vuint8m4_t src);
vuint16m8_t vreinterpret_v_u8m8_u16m8 (vuint8m8_t src);
vint32m1_t vreinterpret_v_i8m1_i32m1 (vint8m1_t src);
vint32m2_t vreinterpret_v_i8m2_i32m2 (vint8m2_t src);
vint32m4_t vreinterpret_v_i8m4_i32m4 (vint8m4_t src);
vint32m8_t vreinterpret_v_i8m8_i32m8 (vint8m8_t src);
vuint32m1_t vreinterpret_v_u8m1_u32m1 (vuint8m1_t src);
vuint32m2_t vreinterpret_v_u8m2_u32m2 (vuint8m2_t src);
vuint32m4_t vreinterpret_v_u8m4_u32m4 (vuint8m4_t src);
vuint32m8_t vreinterpret_v_u8m8_u32m8 (vuint8m8_t src);
vint64m1_t vreinterpret_v_i8m1_i64m1 (vint8m1_t src);
vint64m2_t vreinterpret_v_i8m2_i64m2 (vint8m2_t src);
vint64m4_t vreinterpret_v_i8m4_i64m4 (vint8m4_t src);
vint64m8_t vreinterpret_v_i8m8_i64m8 (vint8m8_t src);
vuint64m1_t vreinterpret_v_u8m1_u64m1 (vuint8m1_t src);
vuint64m2_t vreinterpret_v_u8m2_u64m2 (vuint8m2_t src);
vuint64m4_t vreinterpret_v_u8m4_u64m4 (vuint8m4_t src);
vuint64m8_t vreinterpret_v_u8m8_u64m8 (vuint8m8_t src);
vint8m1_t vreinterpret_v_i16m1_i8m1 (vint16m1_t src);
vint8m2_t vreinterpret_v_i16m2_i8m2 (vint16m2_t src);
vint8m4_t vreinterpret_v_i16m4_i8m4 (vint16m4_t src);
vint8m8_t vreinterpret_v_i16m8_i8m8 (vint16m8_t src);
vuint8m1_t vreinterpret_v_u16m1_u8m1 (vuint16m1_t src);
vuint8m2_t vreinterpret_v_u16m2_u8m2 (vuint16m2_t src);
vuint8m4_t vreinterpret_v_u16m4_u8m4 (vuint16m4_t src);
vuint8m8_t vreinterpret_v_u16m8_u8m8 (vuint16m8_t src);
vint32m1_t vreinterpret_v_i16m1_i32m1 (vint16m1_t src);
vint32m2_t vreinterpret_v_i16m2_i32m2 (vint16m2_t src);
vint32m4_t vreinterpret_v_i16m4_i32m4 (vint16m4_t src);

```

```
vint32m8_t vreinterpret_v_i16m8_i32m8 (vint16m8_t src);
vuint32m1_t vreinterpret_v_u16m1_u32m1 (vuint16m1_t src);
vuint32m2_t vreinterpret_v_u16m2_u32m2 (vuint16m2_t src);
vuint32m4_t vreinterpret_v_u16m4_u32m4 (vuint16m4_t src);
vuint32m8_t vreinterpret_v_u16m8_u32m8 (vuint16m8_t src);
vint64m1_t vreinterpret_v_i16m1_i64m1 (vint16m1_t src);
vint64m2_t vreinterpret_v_i16m2_i64m2 (vint16m2_t src);
vint64m4_t vreinterpret_v_i16m4_i64m4 (vint16m4_t src);
vint64m8_t vreinterpret_v_i16m8_i64m8 (vint16m8_t src);
vuint64m1_t vreinterpret_v_u16m1_u64m1 (vuint16m1_t src);
vuint64m2_t vreinterpret_v_u16m2_u64m2 (vuint16m2_t src);
vuint64m4_t vreinterpret_v_u16m4_u64m4 (vuint16m4_t src);
vuint64m8_t vreinterpret_v_u16m8_u64m8 (vuint16m8_t src);
vint8m1_t vreinterpret_v_i32m1_i8m1 (vint32m1_t src);
vint8m2_t vreinterpret_v_i32m2_i8m2 (vint32m2_t src);
vint8m4_t vreinterpret_v_i32m4_i8m4 (vint32m4_t src);
vint8m8_t vreinterpret_v_i32m8_i8m8 (vint32m8_t src);
vuint8m1_t vreinterpret_v_u32m1_u8m1 (vuint32m1_t src);
vuint8m2_t vreinterpret_v_u32m2_u8m2 (vuint32m2_t src);
vuint8m4_t vreinterpret_v_u32m4_u8m4 (vuint32m4_t src);
vuint8m8_t vreinterpret_v_u32m8_u8m8 (vuint32m8_t src);
vint16m1_t vreinterpret_v_i32m1_i16m1 (vint32m1_t src);
vint16m2_t vreinterpret_v_i32m2_i16m2 (vint32m2_t src);
vint16m4_t vreinterpret_v_i32m4_i16m4 (vint32m4_t src);
vint16m8_t vreinterpret_v_i32m8_i16m8 (vint32m8_t src);
vuint16m1_t vreinterpret_v_u32m1_u16m1 (vuint32m1_t src);
vuint16m2_t vreinterpret_v_u32m2_u16m2 (vuint32m2_t src);
vuint16m4_t vreinterpret_v_u32m4_u16m4 (vuint32m4_t src);
vuint16m8_t vreinterpret_v_u32m8_u16m8 (vuint32m8_t src);
vint64m1_t vreinterpret_v_i32m1_i64m1 (vint32m1_t src);
vint64m2_t vreinterpret_v_i32m2_i64m2 (vint32m2_t src);
vint64m4_t vreinterpret_v_i32m4_i64m4 (vint32m4_t src);
vint64m8_t vreinterpret_v_i32m8_i64m8 (vint32m8_t src);
vuint64m1_t vreinterpret_v_u32m1_u64m1 (vuint32m1_t src);
vuint64m2_t vreinterpret_v_u32m2_u64m2 (vuint32m2_t src);
vuint64m4_t vreinterpret_v_u32m4_u64m4 (vuint32m4_t src);
vuint64m8_t vreinterpret_v_u32m8_u64m8 (vuint32m8_t src);
vint8m1_t vreinterpret_v_i64m1_i8m1 (vint64m1_t src);
vint8m2_t vreinterpret_v_i64m2_i8m2 (vint64m2_t src);
vint8m4_t vreinterpret_v_i64m4_i8m4 (vint64m4_t src);
vint8m8_t vreinterpret_v_i64m8_i8m8 (vint64m8_t src);
vuint8m1_t vreinterpret_v_u64m1_u8m1 (vuint64m1_t src);
vuint8m2_t vreinterpret_v_u64m2_u8m2 (vuint64m2_t src);
vuint8m4_t vreinterpret_v_u64m4_u8m4 (vuint64m4_t src);
vuint8m8_t vreinterpret_v_u64m8_u8m8 (vuint64m8_t src);
vint16m1_t vreinterpret_v_i64m1_i16m1 (vint64m1_t src);
```

```

vint16m2_t vreinterpret_v_i64m2_i16m2 (vint64m2_t src);
vint16m4_t vreinterpret_v_i64m4_i16m4 (vint64m4_t src);
vint16m8_t vreinterpret_v_i64m8_i16m8 (vint64m8_t src);
vuint16m1_t vreinterpret_v_u64m1_u16m1 (vuint64m1_t src);
vuint16m2_t vreinterpret_v_u64m2_u16m2 (vuint64m2_t src);
vuint16m4_t vreinterpret_v_u64m4_u16m4 (vuint64m4_t src);
vuint16m8_t vreinterpret_v_u64m8_u16m8 (vuint64m8_t src);
vint32m1_t vreinterpret_v_i64m1_i32m1 (vint64m1_t src);
vint32m2_t vreinterpret_v_i64m2_i32m2 (vint64m2_t src);
vint32m4_t vreinterpret_v_i64m4_i32m4 (vint64m4_t src);
vint32m8_t vreinterpret_v_i64m8_i32m8 (vint64m8_t src);
vuint32m1_t vreinterpret_v_u64m1_u32m1 (vuint64m1_t src);
vuint32m2_t vreinterpret_v_u64m2_u32m2 (vuint64m2_t src);
vuint32m4_t vreinterpret_v_u64m4_u32m4 (vuint64m4_t src);
vuint32m8_t vreinterpret_v_u64m8_u32m8 (vuint64m8_t src);

```

## Vector Initialization Functions:

### Prototypes:

```

vint8m1_t vundefined_i8m1 ();
vint8m2_t vundefined_i8m2 ();
vint8m4_t vundefined_i8m4 ();
vint8m8_t vundefined_i8m8 ();
vint16m1_t vundefined_i16m1 ();
vint16m2_t vundefined_i16m2 ();
vint16m4_t vundefined_i16m4 ();
vint16m8_t vundefined_i16m8 ();
vint32m1_t vundefined_i32m1 ();
vint32m2_t vundefined_i32m2 ();
vint32m4_t vundefined_i32m4 ();
vint32m8_t vundefined_i32m8 ();
vint64m1_t vundefined_i64m1 ();
vint64m2_t vundefined_i64m2 ();
vint64m4_t vundefined_i64m4 ();
vint64m8_t vundefined_i64m8 ();
vuint8m1_t vundefined_u8m1 ();
vuint8m2_t vundefined_u8m2 ();
vuint8m4_t vundefined_u8m4 ();
vuint8m8_t vundefined_u8m8 ();
vuint16m1_t vundefined_u16m1 ();
vuint16m2_t vundefined_u16m2 ();
vuint16m4_t vundefined_u16m4 ();
vuint16m8_t vundefined_u16m8 ();
vuint32m1_t vundefined_u32m1 ();
vuint32m2_t vundefined_u32m2 ();

```

```

vuint32m4_t vundefined_u32m4 ();
vuint32m8_t vundefined_u32m8 ();
vuint64m1_t vundefined_u64m1 ();
vuint64m2_t vundefined_u64m2 ();
vuint64m4_t vundefined_u64m4 ();
vuint64m8_t vundefined_u64m8 ();
vfloat16m1_t vundefined_f16m1 ();
vfloat16m2_t vundefined_f16m2 ();
vfloat16m4_t vundefined_f16m4 ();
vfloat16m8_t vundefined_f16m8 ();
vfloat32m1_t vundefined_f32m1 ();
vfloat32m2_t vundefined_f32m2 ();
vfloat32m4_t vundefined_f32m4 ();
vfloat32m8_t vundefined_f32m8 ();
vfloat64m1_t vundefined_f64m1 ();
vfloat64m2_t vundefined_f64m2 ();
vfloat64m4_t vundefined_f64m4 ();
vfloat64m8_t vundefined_f64m8 ();

```

## Vector Insertion Functions:

### Prototypes:

```

vint8m2_t vset_v_i8m1_i8m2 (vint8m2_t dest, const size_t index,
    vint8m1_t val);
vint8m4_t vset_v_i8m1_i8m4 (vint8m4_t dest, const size_t index,
    vint8m1_t val);
vint8m4_t vset_v_i8m2_i8m4 (vint8m4_t dest, const size_t index,
    vint8m2_t val);
vint8m8_t vset_v_i8m1_i8m8 (vint8m8_t dest, const size_t index,
    vint8m1_t val);
vint8m8_t vset_v_i8m2_i8m8 (vint8m8_t dest, const size_t index,
    vint8m2_t val);
vint8m8_t vset_v_i8m4_i8m8 (vint8m8_t dest, const size_t index,
    vint8m4_t val);
vuint8m2_t vset_v_u8m1_u8m2 (vuint8m2_t dest, const size_t
    index, vuint8m1_t val);
vuint8m4_t vset_v_u8m1_u8m4 (vuint8m4_t dest, const size_t
    index, vuint8m1_t val);
vuint8m4_t vset_v_u8m2_u8m4 (vuint8m4_t dest, const size_t
    index, vuint8m2_t val);
vuint8m8_t vset_v_u8m1_u8m8 (vuint8m8_t dest, const size_t
    index, vuint8m1_t val);
vuint8m8_t vset_v_u8m2_u8m8 (vuint8m8_t dest, const size_t
    index, vuint8m2_t val);

```

```

vuint8m8_t vset_v_u8m4_u8m8 (vuint8m8_t dest, const size_t
    index, vuint8m4_t val);
vint16m2_t vset_v_i16m1_i16m2 (vint16m2_t dest, const size_t
    index, vint16m1_t val);
vint16m4_t vset_v_i16m1_i16m4 (vint16m4_t dest, const size_t
    index, vint16m1_t val);
vint16m4_t vset_v_i16m2_i16m4 (vint16m4_t dest, const size_t
    index, vint16m2_t val);
vint16m8_t vset_v_i16m1_i16m8 (vint16m8_t dest, const size_t
    index, vint16m1_t val);
vint16m8_t vset_v_i16m2_i16m8 (vint16m8_t dest, const size_t
    index, vint16m2_t val);
vint16m8_t vset_v_i16m4_i16m8 (vint16m8_t dest, const size_t
    index, vint16m4_t val);
vuint16m2_t vset_v_u16m1_u16m2 (vuint16m2_t dest, const size_t
    index, vuint16m1_t val);
vuint16m4_t vset_v_u16m1_u16m4 (vuint16m4_t dest, const size_t
    index, vuint16m1_t val);
vuint16m4_t vset_v_u16m2_u16m4 (vuint16m4_t dest, const size_t
    index, vuint16m2_t val);
vuint16m8_t vset_v_u16m1_u16m8 (vuint16m8_t dest, const size_t
    index, vuint16m1_t val);
vuint16m8_t vset_v_u16m2_u16m8 (vuint16m8_t dest, const size_t
    index, vuint16m2_t val);
vuint16m8_t vset_v_u16m4_u16m8 (vuint16m8_t dest, const size_t
    index, vuint16m4_t val);
vfloat16m2_t vset_v_f16m1_f16m2 (vfloat16m2_t dest, const size_t
    index, vfloat16m1_t val);
vfloat16m4_t vset_v_f16m1_f16m4 (vfloat16m4_t dest, const size_t
    index, vfloat16m1_t val);
vfloat16m4_t vset_v_f16m2_f16m4 (vfloat16m4_t dest, const size_t
    index, vfloat16m2_t val);
vfloat16m8_t vset_v_f16m1_f16m8 (vfloat16m8_t dest, const size_t
    index, vfloat16m1_t val);
vfloat16m8_t vset_v_f16m2_f16m8 (vfloat16m8_t dest, const size_t
    index, vfloat16m2_t val);
vfloat16m8_t vset_v_f16m4_f16m8 (vfloat16m8_t dest, const size_t
    index, vfloat16m4_t val);
vint32m2_t vset_v_i32m1_i32m2 (vint32m2_t dest, const size_t
    index, vint32m1_t val);
vint32m4_t vset_v_i32m1_i32m4 (vint32m4_t dest, const size_t
    index, vint32m1_t val);
vint32m4_t vset_v_i32m2_i32m4 (vint32m4_t dest, const size_t
    index, vint32m2_t val);
vint32m8_t vset_v_i32m1_i32m8 (vint32m8_t dest, const size_t
    index, vint32m1_t val);

```



```

vint32m8_t vset_v_i32m2_i32m8 (vint32m8_t dest, const size_t
    index, vint32m2_t val);
vint32m8_t vset_v_i32m4_i32m8 (vint32m8_t dest, const size_t
    index, vint32m4_t val);
vuint32m2_t vset_v_u32m1_u32m2 (vuint32m2_t dest, const size_t
    index, vuint32m1_t val);
vuint32m4_t vset_v_u32m1_u32m4 (vuint32m4_t dest, const size_t
    index, vuint32m1_t val);
vuint32m4_t vset_v_u32m2_u32m4 (vuint32m4_t dest, const size_t
    index, vuint32m2_t val);
vuint32m8_t vset_v_u32m1_u32m8 (vuint32m8_t dest, const size_t
    index, vuint32m1_t val);
vuint32m8_t vset_v_u32m2_u32m8 (vuint32m8_t dest, const size_t
    index, vuint32m2_t val);
vuint32m8_t vset_v_u32m4_u32m8 (vuint32m8_t dest, const size_t
    index, vuint32m4_t val);
vfloat32m2_t vset_v_f32m1_f32m2 (vfloat32m2_t dest, const size_t
    index, vfloat32m1_t val);
vfloat32m4_t vset_v_f32m1_f32m4 (vfloat32m4_t dest, const size_t
    index, vfloat32m1_t val);
vfloat32m4_t vset_v_f32m2_f32m4 (vfloat32m4_t dest, const size_t
    index, vfloat32m2_t val);
vfloat32m8_t vset_v_f32m1_f32m8 (vfloat32m8_t dest, const size_t
    index, vfloat32m1_t val);
vfloat32m8_t vset_v_f32m2_f32m8 (vfloat32m8_t dest, const size_t
    index, vfloat32m2_t val);
vfloat32m8_t vset_v_f32m4_f32m8 (vfloat32m8_t dest, const size_t
    index, vfloat32m4_t val);
vint64m2_t vset_v_i64m1_i64m2 (vint64m2_t dest, const size_t
    index, vint64m1_t val);
vint64m4_t vset_v_i64m1_i64m4 (vint64m4_t dest, const size_t
    index, vint64m1_t val);
vint64m4_t vset_v_i64m2_i64m4 (vint64m4_t dest, const size_t
    index, vint64m2_t val);
vint64m8_t vset_v_i64m1_i64m8 (vint64m8_t dest, const size_t
    index, vint64m1_t val);
vint64m8_t vset_v_i64m2_i64m8 (vint64m8_t dest, const size_t
    index, vint64m2_t val);
vint64m8_t vset_v_i64m4_i64m8 (vint64m8_t dest, const size_t
    index, vint64m4_t val);
vuint64m2_t vset_v_u64m1_u64m2 (vuint64m2_t dest, const size_t
    index, vuint64m1_t val);
vuint64m4_t vset_v_u64m1_u64m4 (vuint64m4_t dest, const size_t
    index, vuint64m1_t val);
vuint64m4_t vset_v_u64m2_u64m4 (vuint64m4_t dest, const size_t
    index, vuint64m2_t val);

```

```

vuint64m8_t vset_v_u64m1_u64m8 (vuint64m8_t dest, const size_t
    index, vuint64m1_t val);
vuint64m8_t vset_v_u64m2_u64m8 (vuint64m8_t dest, const size_t
    index, vuint64m2_t val);
vuint64m8_t vset_v_u64m4_u64m8 (vuint64m8_t dest, const size_t
    index, vuint64m4_t val);
vfloat64m2_t vset_v_f64m1_f64m2 (vfloat64m2_t dest, const size_t
    index, vfloat64m1_t val);
vfloat64m4_t vset_v_f64m1_f64m4 (vfloat64m4_t dest, const size_t
    index, vfloat64m1_t val);
vfloat64m4_t vset_v_f64m2_f64m4 (vfloat64m4_t dest, const size_t
    index, vfloat64m2_t val);
vfloat64m8_t vset_v_f64m1_f64m8 (vfloat64m8_t dest, const size_t
    index, vfloat64m1_t val);
vfloat64m8_t vset_v_f64m2_f64m8 (vfloat64m8_t dest, const size_t
    index, vfloat64m2_t val);
vfloat64m8_t vset_v_f64m4_f64m8 (vfloat64m8_t dest, const size_t
    index, vfloat64m4_t val);

```

## Vector Extraction Functions:

### Prototypes:

```

vint8m1_t vget_v_i8m2_i8m1 (vint8m2_t src, const size_t index);
vint8m1_t vget_v_i8m4_i8m1 (vint8m4_t src, const size_t index);
vint8m1_t vget_v_i8m8_i8m1 (vint8m8_t src, const size_t index);
vint8m2_t vget_v_i8m4_i8m2 (vint8m4_t src, const size_t index);
vint8m2_t vget_v_i8m8_i8m2 (vint8m8_t src, const size_t index);
vint8m4_t vget_v_i8m8_i8m4 (vint8m8_t src, const size_t index);
vuint8m1_t vget_v_u8m2_u8m1 (vuint8m2_t src, const size_t index);
vuint8m1_t vget_v_u8m4_u8m1 (vuint8m4_t src, const size_t index);
vuint8m1_t vget_v_u8m8_u8m1 (vuint8m8_t src, const size_t index);
vuint8m2_t vget_v_u8m4_u8m2 (vuint8m4_t src, const size_t index);
vuint8m2_t vget_v_u8m8_u8m2 (vuint8m8_t src, const size_t index);
vuint8m4_t vget_v_u8m8_u8m4 (vuint8m8_t src, const size_t index);
vint16m1_t vget_v_i16m2_i16m1 (vint16m2_t src, const size_t
    index);
vint16m1_t vget_v_i16m4_i16m1 (vint16m4_t src, const size_t
    index);
vint16m1_t vget_v_i16m8_i16m1 (vint16m8_t src, const size_t
    index);
vint16m2_t vget_v_i16m4_i16m2 (vint16m4_t src, const size_t
    index);
vint16m2_t vget_v_i16m8_i16m2 (vint16m8_t src, const size_t
    index);

```

```

vint16m4_t vget_v_i16m8_i16m4 (vint16m8_t src, const size_t
    index);
vuint16m1_t vget_v_u16m2_u16m1 (vuint16m2_t src, const size_t
    index);
vuint16m1_t vget_v_u16m4_u16m1 (vuint16m4_t src, const size_t
    index);
vuint16m1_t vget_v_u16m8_u16m1 (vuint16m8_t src, const size_t
    index);
vuint16m2_t vget_v_u16m4_u16m2 (vuint16m4_t src, const size_t
    index);
vuint16m2_t vget_v_u16m8_u16m2 (vuint16m8_t src, const size_t
    index);
vuint16m4_t vget_v_u16m8_u16m4 (vuint16m8_t src, const size_t
    index);
vfloat16m1_t vget_v_f16m2_f16m1 (vfloat16m2_t src, const size_t
    index);
vfloat16m1_t vget_v_f16m4_f16m1 (vfloat16m4_t src, const size_t
    index);
vfloat16m1_t vget_v_f16m8_f16m1 (vfloat16m8_t src, const size_t
    index);
vfloat16m2_t vget_v_f16m4_f16m2 (vfloat16m4_t src, const size_t
    index);
vfloat16m2_t vget_v_f16m8_f16m2 (vfloat16m8_t src, const size_t
    index);
vfloat16m4_t vget_v_f16m8_f16m4 (vfloat16m8_t src, const size_t
    index);
vint32m1_t vget_v_i32m2_i32m1 (vint32m2_t src, const size_t
    index);
vint32m1_t vget_v_i32m4_i32m1 (vint32m4_t src, const size_t
    index);
vint32m1_t vget_v_i32m8_i32m1 (vint32m8_t src, const size_t
    index);
vint32m2_t vget_v_i32m4_i32m2 (vint32m4_t src, const size_t
    index);
vint32m2_t vget_v_i32m8_i32m2 (vint32m8_t src, const size_t
    index);
vint32m4_t vget_v_i32m8_i32m4 (vint32m8_t src, const size_t
    index);
vuint32m1_t vget_v_u32m2_u32m1 (vuint32m2_t src, const size_t
    index);
vuint32m1_t vget_v_u32m4_u32m1 (vuint32m4_t src, const size_t
    index);
vuint32m1_t vget_v_u32m8_u32m1 (vuint32m8_t src, const size_t
    index);
vuint32m2_t vget_v_u32m4_u32m2 (vuint32m4_t src, const size_t
    index);

```

```

vuint32m2_t vget_v_u32m8_u32m2 (vuint32m8_t src, const size_t
    index);
vuint32m4_t vget_v_u32m8_u32m4 (vuint32m8_t src, const size_t
    index);
vfloat32m1_t vget_v_f32m2_f32m1 (vfloat32m2_t src, const size_t
    index);
vfloat32m1_t vget_v_f32m4_f32m1 (vfloat32m4_t src, const size_t
    index);
vfloat32m1_t vget_v_f32m8_f32m1 (vfloat32m8_t src, const size_t
    index);
vfloat32m2_t vget_v_f32m4_f32m2 (vfloat32m4_t src, const size_t
    index);
vfloat32m2_t vget_v_f32m8_f32m2 (vfloat32m8_t src, const size_t
    index);
vfloat32m4_t vget_v_f32m8_f32m4 (vfloat32m8_t src, const size_t
    index);
vint64m1_t vget_v_i64m2_i64m1 (vint64m2_t src, const size_t
    index);
vint64m1_t vget_v_i64m4_i64m1 (vint64m4_t src, const size_t
    index);
vint64m1_t vget_v_i64m8_i64m1 (vint64m8_t src, const size_t
    index);
vint64m2_t vget_v_i64m4_i64m2 (vint64m4_t src, const size_t
    index);
vint64m2_t vget_v_i64m8_i64m2 (vint64m8_t src, const size_t
    index);
vint64m4_t vget_v_i64m8_i64m4 (vint64m8_t src, const size_t
    index);
vuint64m1_t vget_v_u64m2_u64m1 (vuint64m2_t src, const size_t
    index);
vuint64m1_t vget_v_u64m4_u64m1 (vuint64m4_t src, const size_t
    index);
vuint64m1_t vget_v_u64m8_u64m1 (vuint64m8_t src, const size_t
    index);
vuint64m2_t vget_v_u64m4_u64m2 (vuint64m4_t src, const size_t
    index);
vuint64m2_t vget_v_u64m8_u64m2 (vuint64m8_t src, const size_t
    index);
vuint64m4_t vget_v_u64m8_u64m4 (vuint64m8_t src, const size_t
    index);
vfloat64m1_t vget_v_f64m2_f64m1 (vfloat64m2_t src, const size_t
    index);
vfloat64m1_t vget_v_f64m4_f64m1 (vfloat64m4_t src, const size_t
    index);
vfloat64m1_t vget_v_f64m8_f64m1 (vfloat64m8_t src, const size_t
    index);

```

```

vfloat64m2_t vget_v_f64m4_f64m2 (vfloat64m4_t src, const size_t
    index);
vfloat64m2_t vget_v_f64m8_f64m2 (vfloat64m8_t src, const size_t
    index);
vfloat64m4_t vget_v_f64m8_f64m4 (vfloat64m8_t src, const size_t
    index);

```

## RISC-V Vector Intrinsic Examples

### rvv\_sgemm.c

```

#include <riscv_vector.h>
#include <stddef.h>
#include <stdio.h>
#include <string.h>
#include <math.h>

#define N 32

#define MAX_BLOCKSIZE 32
#define MLEN 4
#define KLEN 8
#define NLEN 4
#define OUTPUT_LEN 16

float a_array[MAX_BLOCKSIZE] = {-0.4325648115282207,
    -1.6655843782380970,
    0.1253323064748307,
    0.2876764203585489,
    -1.1464713506814637,
    1.1909154656429988,
    1.1891642016521031,
    -0.0376332765933176,
    0.3272923614086541,
    0.1746391428209245,
    -0.1867085776814394,
    0.7257905482933027,
    -0.5883165430141887,
    2.1831858181971011,
    -0.1363958830865957,
    0.1139313135208096,
    1.0667682113591888,
    0.0592814605236053,
    -0.0956484054836690,
    -0.8323494636500225,

```

```

0.2944108163926404,
    -1.3361818579378040,
0.7143245518189522,
    1.6235620644462707,
-0.6917757017022868,
    0.8579966728282626,
1.2540014216025324,
    -1.5937295764474768,
-1.4409644319010200,
    0.5711476236581780,
-0.3998855777153632, 0.1};

float b_array[MAX_BLOCKSIZE] = {1.7491401329284098,
    0.1325982188803279,
    0.3252281811989881,
    -0.7938091410349637,
0.3149236145048914,
    -0.5272704888029532,
0.9322666565031119,
    1.1646643544607362,
-2.0456694357357357,
    -0.6443728590041911,
1.7410657940825480,
    0.4867684246821860,
1.0488288293660140,
    1.4885752747099299,
1.2705014969484090,
    -1.8561241921210170,
2.1343209047321410,
    1.4358467535865909,
-0.9173023332875400,
    -1.1060770780029008,
0.8105708062681296,
    0.6985430696369063,
-0.4015827425012831,
    1.2687512030669628,
-0.7836083053674872,
    0.2132664971465569,
0.7878984786088954,
    0.8966819356782295,
-0.1869172943544062,
    1.0131816724341454,
0.2484350696132857, 0.1};

float golden_array[OUTPUT_LEN];
float c_array[OUTPUT_LEN];

```

```

void sgemm_golden() {
    for (size_t i = 0; i < MLEN; ++i)
        for (size_t j = 0; j < NLEN; ++j)
            for (size_t k = 0; k < KLEN; ++k)
                golden_array[i * NLEN + j] += a_array[i * KLEN + k] *
                    b_array[j + k * NLEN];
}

// reference
    https://github.com/riscv/riscv-v-spec/blob/master/example/sgemm.S
// c += a*b (alpha=1, no transpose on input matrices)
// matrices stored in C row-major order
void sgemm_vec(size_t size_m, size_t size_n, size_t size_k,
               const float *a, // m * k matrix
               size_t lda,
               const float *b, // k * n matrix
               size_t ldb,
               float *c, // m * n matrix
               size_t ldc) {
    size_t vl;
    for (size_t m = 0; m < size_m; ++m) {
        const float *b_n_ptr = b;
        float *c_n_ptr = c;
        for (size_t c_n_count = size_n; c_n_count; c_n_count -= vl) {
            vl = vsetvl_e32m1(c_n_count);
            const float *a_k_ptr = a;
            const float *b_k_ptr = b_n_ptr;
            vfloat32m1_t acc = vle32_v_f32m1(c_n_ptr, vl);
            for (size_t k = 0; k < size_k; ++k) {
                vfloat32m1_t b_n_data = vle32_v_f32m1(b_k_ptr, vl);
                acc = vfmacc_vf_f32m1(acc, *a_k_ptr, b_n_data, vl);
                b_k_ptr += ldb;
                a_k_ptr++;
            }
            vse32_v_f32m1(c_n_ptr, acc, vl);
            c_n_ptr += vl;
            b_n_ptr += vl;
        }
        a += lda;
        c += ldc;
    }
}

int fp_eq(float reference, float actual, float relErr)

```

```

{
    // if near zero, do absolute error instead.
    float absErr = relErr * ((fabsf(reference) > relErr) ?
        fabsf(reference) : relErr);
    return fabsf(actual - reference) < absErr;
}

int main() {
    // golden
    memcpy(golden_array, b_array, OUTPUT_LEN * sizeof(float));
    sgemm_golden();
    // vector
    memcpy(c_array, b_array, OUTPUT_LEN * sizeof(float));
    sgemm_vec(MLEN, NLEN, KLEN, a_array, KLEN, b_array, NLEN,
        c_array, NLEN);

    int pass = 1;
    for (int i = 0; i < OUTPUT_LEN; i++) {
        if (!fp_eq(golden_array[i], c_array[i], 1e-5)) {
            printf("index %d failed, %f!=%f\n", i, golden_array[i],
                c_array[i]);
            pass = 0;
        }
    }
    if (pass)
        printf("passed\n");
    return (pass == 0);
}

```

### rvv\_\_saxpy.c

```

#include <riscv_vector.h>
#include <stddef.h>
#include <stdio.h>
#include <math.h>

#define N 31

float input[N] = {-0.4325648115282207, -1.6655843782380970,
    0.1253323064748307,
    0.2876764203585489, -1.1464713506814637,
    1.1909154656429988,
    1.1891642016521031, -0.0376332765933176,
    0.3272923614086541,
    0.1746391428209245, -0.1867085776814394,
    0.7257905482933027,

```



```

-0.5883165430141887, 2.1831858181971011,
  -0.1363958830865957,
0.1139313135208096, 1.0667682113591888,
  0.0592814605236053,
-0.0956484054836690, -0.8323494636500225,
  0.2944108163926404,
-1.3361818579378040, 0.7143245518189522,
  1.6235620644462707,
-0.6917757017022868, 0.8579966728282626,
  1.2540014216025324,
-1.5937295764474768, -1.4409644319010200,
  0.5711476236581780,
-0.3998855777153632};

float output_golden[N] = {
  1.7491401329284098, 0.1325982188803279, 0.3252281811989881,
-0.7938091410349637, 0.3149236145048914,
  -0.5272704888029532,
0.9322666565031119, 1.1646643544607362,
  -2.0456694357357357,
-0.6443728590041911, 1.7410657940825480, 0.4867684246821860,
1.0488288293660140, 1.4885752747099299, 1.2705014969484090,
-1.8561241921210170, 2.1343209047321410, 1.4358467535865909,
-0.9173023332875400, -1.1060770780029008, 0.8105708062681296,
0.6985430696369063, -0.4015827425012831, 1.2687512030669628,
-0.7836083053674872, 0.2132664971465569, 0.7878984786088954,
0.8966819356782295, -0.1869172943544062, 1.0131816724341454,
0.2484350696132857};

float output[N] = {
  1.7491401329284098, 0.1325982188803279, 0.3252281811989881,
-0.7938091410349637, 0.3149236145048914,
  -0.5272704888029532,
0.9322666565031119, 1.1646643544607362,
  -2.0456694357357357,
-0.6443728590041911, 1.7410657940825480, 0.4867684246821860,
1.0488288293660140, 1.4885752747099299, 1.2705014969484090,
-1.8561241921210170, 2.1343209047321410, 1.4358467535865909,
-0.9173023332875400, -1.1060770780029008, 0.8105708062681296,
0.6985430696369063, -0.4015827425012831, 1.2687512030669628,
-0.7836083053674872, 0.2132664971465569, 0.7878984786088954,
0.8966819356782295, -0.1869172943544062, 1.0131816724341454,
0.2484350696132857};

void saxpy_golden(size_t n, const float a, const float *x, float
*y) {

```

```

    for (size_t i = 0; i < n; ++i) {
        y[i] = a * x[i] + y[i];
    }
}

void saxpy_vec(size_t n, const float a, const float *x, float
    *y) {
    size_t l;

    vfloat32m8_t vx, vy;

    for (; n > 0; n -= 1) {
        l = vsetvl_e32m8(n);
        vx = vle32_v_f32m8(x, l);
        x += l;
        vy = vle32_v_f32m8(y, l);
        vy = vfmacv_vf_f32m8(vy, a, vx, l);
        vse32_v_f32m8(y, vy, l);
        y += l;
    }
}

int fp_eq(float reference, float actual, float relErr)
{
    // if near zero, do absolute error instead.
    float absErr = relErr * ((fabsf(reference) > relErr) ?
        fabsf(reference) : relErr);
    return fabsf(actual - reference) < absErr;
}

int main() {
    saxpy_golden(N, 55.66, input, output_golden);
    saxpy_vec(N, 55.66, input, output);
    int pass = 1;
    for (int i = 0; i < N; i++) {
        if (!fp_eq(output_golden[i], output[i], 1e-6)) {
            printf("failed, %f!=%f\n", output_golden[i], output[i]);
            pass = 0;
        }
    }
    if (pass)
        printf("passed\n");
    return (pass == 0);
}

```